

Appendix for 5A_N38A

Product Name: 5G Window CPE

Model No: TITAN 5100

Appendix A: Average Power Output Data

Test Result

Band	SC S	Bandwidth h	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_5A_n38A	30	5+30	DFT-QPSK	M+L	Outer_Full	21.55	PC3	PASS
DC_5A_n38A	30	5+30	DFT-QPSK	M+L	Inner_Full	22.47	PC3	PASS
DC_5A_n38A	30	5+10	DFT-QPSK	M+L	Outer_Full	21.73	PC3	PASS
DC_5A_n38A	30	5+10	DFT-QPSK	M+L	Inner_Full	22.64	PC3	PASS
DC_5A_n38A	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	21.38	PC3	PASS
DC_5A_n38A	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	21.29	PC3	PASS
DC_5A_n38A	30	5+10	DFT-16QAM	M+L	Outer_Full	20.63	PC3	PASS
DC_5A_n38A	30	5+10	DFT-16QAM	M+L	Inner_Full	21.63	PC3	PASS
DC_5A_n38A	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	20.55	PC3	PASS
DC_5A_n38A	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	20.73	PC3	PASS
DC_5A_n38A	30	5+10	DFT-QPSK	M+M	Outer_Full	21.16	PC3	PASS
DC_5A_n38A	30	5+10	DFT-QPSK	M+M	Inner_Full	22.00	PC3	PASS
DC_5A_n38A	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	20.72	PC3	PASS
DC_5A_n38A	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	20.90	PC3	PASS
DC_5A_n38A	30	5+10	DFT-16QAM	M+M	Outer_Full	20.22	PC3	PASS
DC_5A_n38A	30	5+10	DFT-16QAM	M+M	Inner_Full	21.08	PC3	PASS
DC_5A_n38A	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	20.19	PC3	PASS
DC_5A_n38A	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	20.23	PC3	PASS
DC_5A_n38A	30	5+10	DFT-QPSK	M+H	Outer_Full	21.53	PC3	PASS
DC_5A_n38A	30	5+10	DFT-QPSK	M+H	Inner_Full	22.55	PC3	PASS
DC_5A_n38A	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	21.39	PC3	PASS
DC_5A_n38A	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	21.38	PC3	PASS
DC_5A_n38A	30	5+10	DFT-16QAM	M+H	Outer_Full	20.55	PC3	PASS
DC_5A_n38A	30	5+10	DFT-16QAM	M+H	Inner_Full	21.59	PC3	PASS
DC_5A_n38A	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	20.57	PC3	PASS
DC_5A_n38A	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	20.57	PC3	PASS
DC_5A_n38A	30	5+15	DFT-QPSK	M+L	Outer_Full	21.68	PC3	PASS
DC_5A_n38A	30	5+15	DFT-QPSK	M+L	Inner_Full	22.64	PC3	PASS
DC_5A_n38A	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	21.78	PC3	PASS
DC_5A_n38A	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	21.70	PC3	PASS
DC_5A_n38A	30	5+15	DFT-16QAM	M+L	Outer_Full	20.66	PC3	PASS
DC_5A_n38A	30	5+15	DFT-16QAM	M+L	Inner_Full	21.66	PC3	PASS
DC_5A_n38A	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	20.29	PC3	PASS
DC_5A_n38A	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	20.44	PC3	PASS

DC_5A_n38A	30	5+15	DFT-QPSK	M+M	Outer_Full	21.33	PC3	PASS
DC_5A_n38A	30	5+15	DFT-QPSK	M+M	Inner_Full	22.28	PC3	PASS
DC_5A_n38A	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	21.18	PC3	PASS
DC_5A_n38A	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	21.34	PC3	PASS
DC_5A_n38A	30	5+15	DFT-16QAM	M+M	Outer_Full	20.33	PC3	PASS
DC_5A_n38A	30	5+15	DFT-16QAM	M+M	Inner_Full	21.20	PC3	PASS
DC_5A_n38A	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	20.45	PC3	PASS
DC_5A_n38A	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	20.65	PC3	PASS
DC_5A_n38A	30	5+15	DFT-QPSK	M+H	Outer_Full	21.58	PC3	PASS
DC_5A_n38A	30	5+15	DFT-QPSK	M+H	Inner_Full	22.60	PC3	PASS
DC_5A_n38A	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	21.40	PC3	PASS
DC_5A_n38A	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	21.69	PC3	PASS
DC_5A_n38A	30	5+15	DFT-16QAM	M+H	Outer_Full	20.68	PC3	PASS
DC_5A_n38A	30	5+15	DFT-16QAM	M+H	Inner_Full	21.61	PC3	PASS
DC_5A_n38A	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	20.52	PC3	PASS
DC_5A_n38A	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	20.01	PC3	PASS
DC_5A_n38A	30	5+20	DFT-QPSK	M+L	Outer_Full	21.56	PC3	PASS
DC_5A_n38A	30	5+20	DFT-QPSK	M+L	Inner_Full	22.53	PC3	PASS
DC_5A_n38A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	21.33	PC3	PASS
DC_5A_n38A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	21.27	PC3	PASS
DC_5A_n38A	30	5+20	DFT-16QAM	M+L	Outer_Full	20.56	PC3	PASS
DC_5A_n38A	30	5+20	DFT-16QAM	M+L	Inner_Full	21.60	PC3	PASS
DC_5A_n38A	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	20.80	PC3	PASS
DC_5A_n38A	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	20.06	PC3	PASS
DC_5A_n38A	30	5+20	DFT-QPSK	M+M	Outer_Full	21.20	PC3	PASS
DC_5A_n38A	30	5+20	DFT-QPSK	M+M	Inner_Full	22.21	PC3	PASS
DC_5A_n38A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	21.04	PC3	PASS
DC_5A_n38A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	21.26	PC3	PASS
DC_5A_n38A	30	5+20	DFT-16QAM	M+M	Outer_Full	20.29	PC3	PASS
DC_5A_n38A	30	5+20	DFT-16QAM	M+M	Inner_Full	21.23	PC3	PASS
DC_5A_n38A	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	20.39	PC3	PASS
DC_5A_n38A	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	20.35	PC3	PASS
DC_5A_n38A	30	5+20	DFT-QPSK	M+H	Outer_Full	21.47	PC3	PASS
DC_5A_n38A	30	5+20	DFT-QPSK	M+H	Inner_Full	22.57	PC3	PASS
DC_5A_n38A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	21.40	PC3	PASS
DC_5A_n38A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	21.40	PC3	PASS
DC_5A_n38A	30	5+20	DFT-16QAM	M+H	Outer_Full	20.39	PC3	PASS
DC_5A_n38A	30	5+20	DFT-16QAM	M+H	Inner_Full	21.44	PC3	PASS
DC_5A_n38A	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	20.24	PC3	PASS
DC_5A_n38A	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	20.54	PC3	PASS
DC_5A_n38A	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	21.17	PC3	PASS
DC_5A_n38A	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	21.29	PC3	PASS

DC_5A_n38A	30	5+30	DFT-16QAM	M+L	Outer_Full	20.48	PC3	PASS
DC_5A_n38A	30	5+30	DFT-16QAM	M+L	Inner_Full	21.53	PC3	PASS
DC_5A_n38A	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	20.51	PC3	PASS
DC_5A_n38A	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	20.11	PC3	PASS
DC_5A_n38A	30	5+30	DFT-QPSK	M+M	Outer_Full	21.34	PC3	PASS
DC_5A_n38A	30	5+30	DFT-QPSK	M+M	Inner_Full	22.32	PC3	PASS
DC_5A_n38A	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	21.24	PC3	PASS
DC_5A_n38A	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	21.58	PC3	PASS
DC_5A_n38A	30	5+30	DFT-16QAM	M+M	Outer_Full	20.29	PC3	PASS
DC_5A_n38A	30	5+30	DFT-16QAM	M+M	Inner_Full	21.34	PC3	PASS
DC_5A_n38A	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	20.12	PC3	PASS
DC_5A_n38A	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	20.47	PC3	PASS
DC_5A_n38A	30	5+30	DFT-QPSK	M+H	Outer_Full	21.52	PC3	PASS
DC_5A_n38A	30	5+30	DFT-QPSK	M+H	Inner_Full	22.40	PC3	PASS
DC_5A_n38A	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	21.18	PC3	PASS
DC_5A_n38A	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	21.74	PC3	PASS
DC_5A_n38A	30	5+30	DFT-16QAM	M+H	Outer_Full	20.48	PC3	PASS
DC_5A_n38A	30	5+30	DFT-16QAM	M+H	Inner_Full	21.47	PC3	PASS
DC_5A_n38A	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	20.60	PC3	PASS
DC_5A_n38A	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	20.60	PC3	PASS
DC_5A_n38A	30	5+40	DFT-QPSK	M+L	Outer_Full	21.42	PC3	PASS
DC_5A_n38A	30	5+40	DFT-QPSK	M+L	Inner_Full	22.43	PC3	PASS
DC_5A_n38A	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	21.26	PC3	PASS
DC_5A_n38A	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	21.66	PC3	PASS
DC_5A_n38A	30	5+40	DFT-16QAM	M+L	Outer_Full	20.39	PC3	PASS
DC_5A_n38A	30	5+40	DFT-16QAM	M+L	Inner_Full	21.21	PC3	PASS
DC_5A_n38A	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	20.31	PC3	PASS
DC_5A_n38A	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	20.78	PC3	PASS
DC_5A_n38A	30	5+40	DFT-QPSK	M+M	Outer_Full	21.51	PC3	PASS
DC_5A_n38A	30	5+40	DFT-QPSK	M+M	Inner_Full	22.32	PC3	PASS
DC_5A_n38A	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	21.37	PC3	PASS
DC_5A_n38A	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	21.76	PC3	PASS
DC_5A_n38A	30	5+40	DFT-16QAM	M+M	Outer_Full	20.47	PC3	PASS
DC_5A_n38A	30	5+40	DFT-16QAM	M+M	Inner_Full	21.39	PC3	PASS
DC_5A_n38A	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	20.27	PC3	PASS
DC_5A_n38A	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	20.18	PC3	PASS
DC_5A_n38A	30	5+40	DFT-QPSK	M+H	Outer_Full	21.44	PC3	PASS
DC_5A_n38A	30	5+40	DFT-QPSK	M+H	Inner_Full	22.27	PC3	PASS
DC_5A_n38A	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	21.35	PC3	PASS
DC_5A_n38A	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	21.65	PC3	PASS
DC_5A_n38A	30	5+40	DFT-16QAM	M+H	Outer_Full	20.40	PC3	PASS
DC_5A_n38A	30	5+40	DFT-16QAM	M+H	Inner_Full	21.43	PC3	PASS

DC_5A_n38A	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	20.22	PC3	PASS
DC_5A_n38A	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	20.38	PC3	PASS

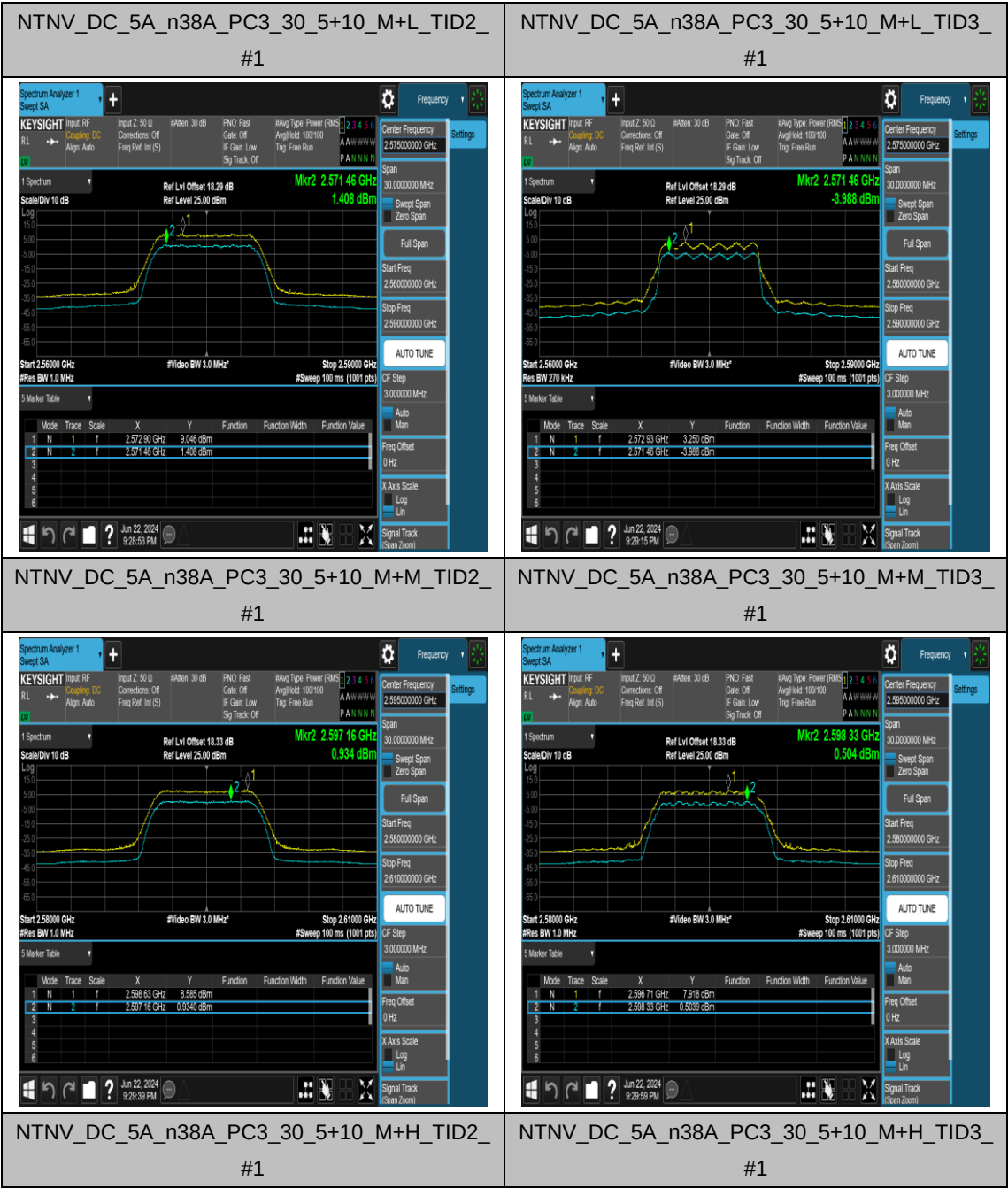
Appendix B: Peak-to-Average Ratio for NSA

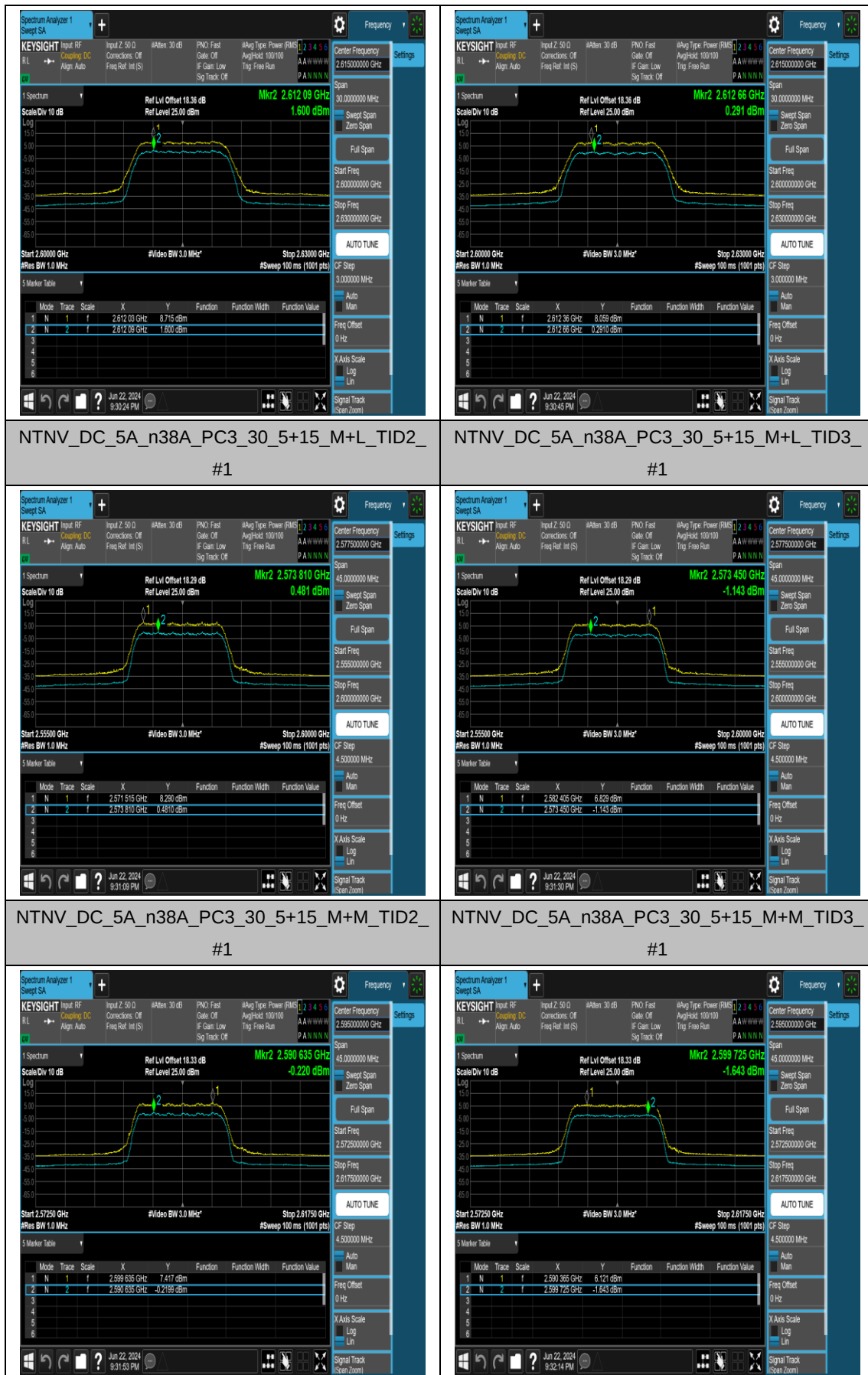
Peak-to-Average Ratio(PAPR)

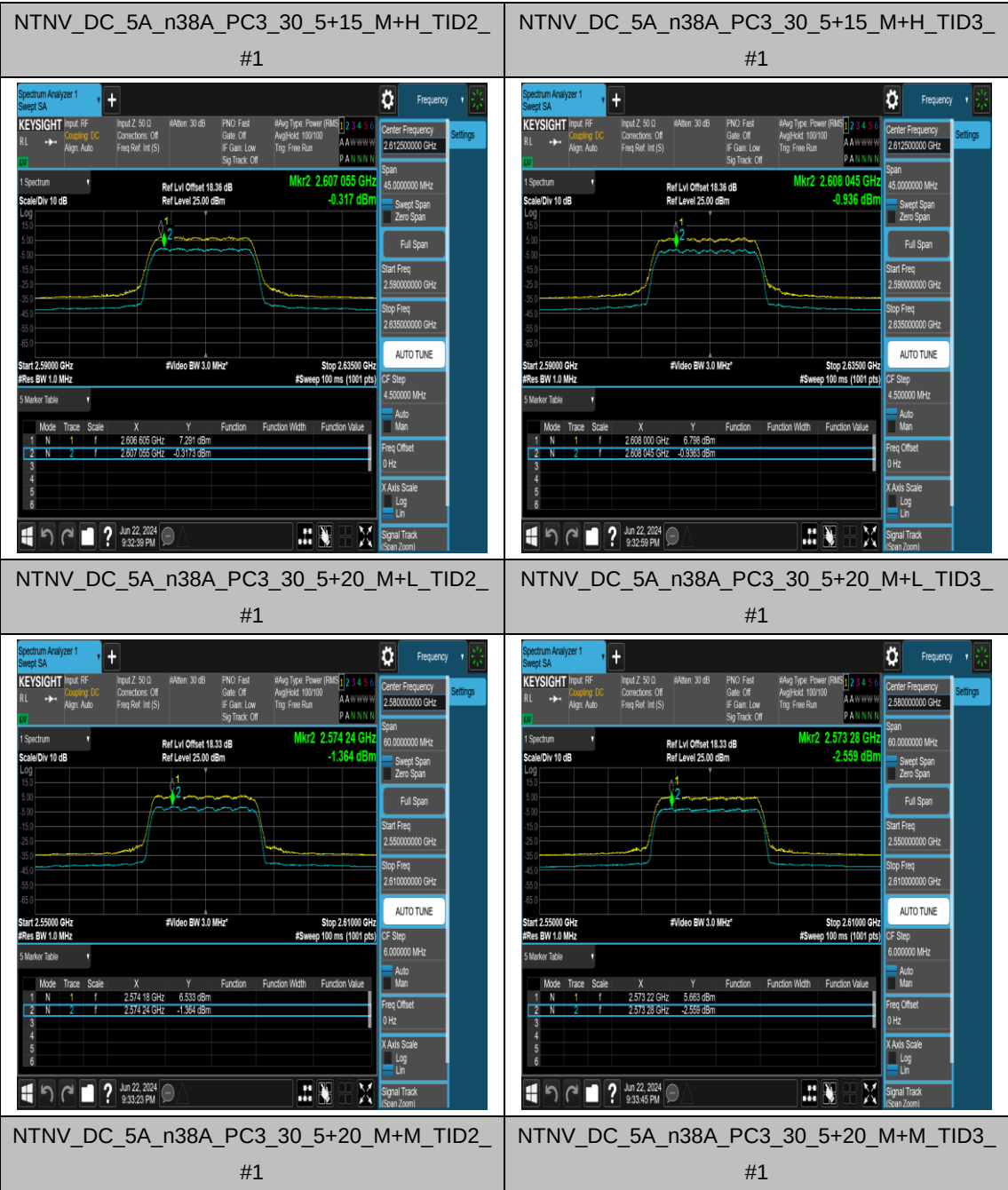
Test Result

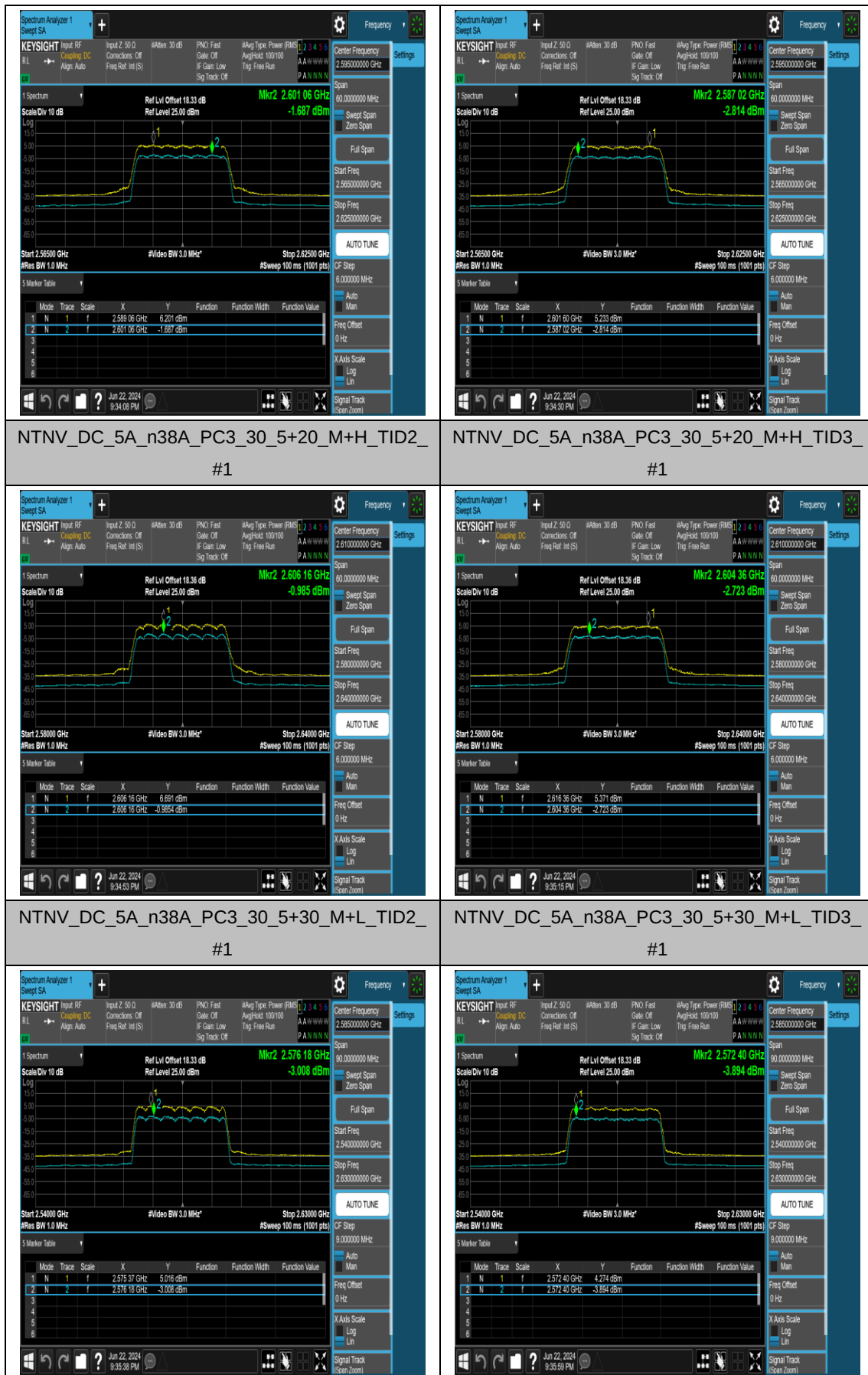
Band	SCS	Bandwidth	Modulation	Channel	RB Config	DutyCycle	Factor	Result	Limit	Verdict
DC_5A_n38A	30	5+10	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.64	≤13	PASS
DC_5A_n38A	30	5+10	DFT-16QAM	M+L	Outer_Full	100%	0.00	7.24	≤13	PASS
DC_5A_n38A	30	5+10	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.66	≤13	PASS
DC_5A_n38A	30	5+10	DFT-16QAM	M+M	Outer_Full	100%	0.00	7.42	≤13	PASS
DC_5A_n38A	30	5+10	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.12	≤13	PASS
DC_5A_n38A	30	5+10	DFT-16QAM	M+H	Outer_Full	100%	0.00	7.77	≤13	PASS
DC_5A_n38A	30	5+15	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.81	≤13	PASS
DC_5A_n38A	30	5+15	DFT-16QAM	M+L	Outer_Full	100%	0.00	7.97	≤13	PASS
DC_5A_n38A	30	5+15	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.64	≤13	PASS
DC_5A_n38A	30	5+15	DFT-16QAM	M+M	Outer_Full	100%	0.00	7.76	≤13	PASS
DC_5A_n38A	30	5+15	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.61	≤13	PASS
DC_5A_n38A	30	5+15	DFT-16QAM	M+H	Outer_Full	100%	0.00	7.74	≤13	PASS
DC_5A_n38A	30	5+20	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.89	≤13	PASS
DC_5A_n38A	30	5+20	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.22	≤13	PASS
DC_5A_n38A	30	5+20	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.89	≤13	PASS
DC_5A_n38A	30	5+20	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.04	≤13	PASS
DC_5A_n38A	30	5+20	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.68	≤13	PASS
DC_5A_n38A	30	5+20	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.09	≤13	PASS
DC_5A_n38A	30	5+30	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.03	≤13	PASS
DC_5A_n38A	30	5+30	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.16	≤13	PASS
DC_5A_n38A	30	5+30	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.11	≤13	PASS
DC_5A_n38A	30	5+30	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.09	≤13	PASS
DC_5A_n38A	30	5+30	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.04	≤13	PASS
DC_5A_n38A	30	5+30	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.17	≤13	PASS
DC_5A_n38A	30	5+40	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.97	≤13	PASS
DC_5A_n38A	30	5+40	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.30	≤13	PASS
DC_5A_n38A	30	5+40	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.40	≤13	PASS
DC_5A_n38A	30	5+40	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.24	≤13	PASS
DC_5A_n38A	30	5+40	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.89	≤13	PASS
DC_5A_n38A	30	5+40	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.28	≤13	PASS

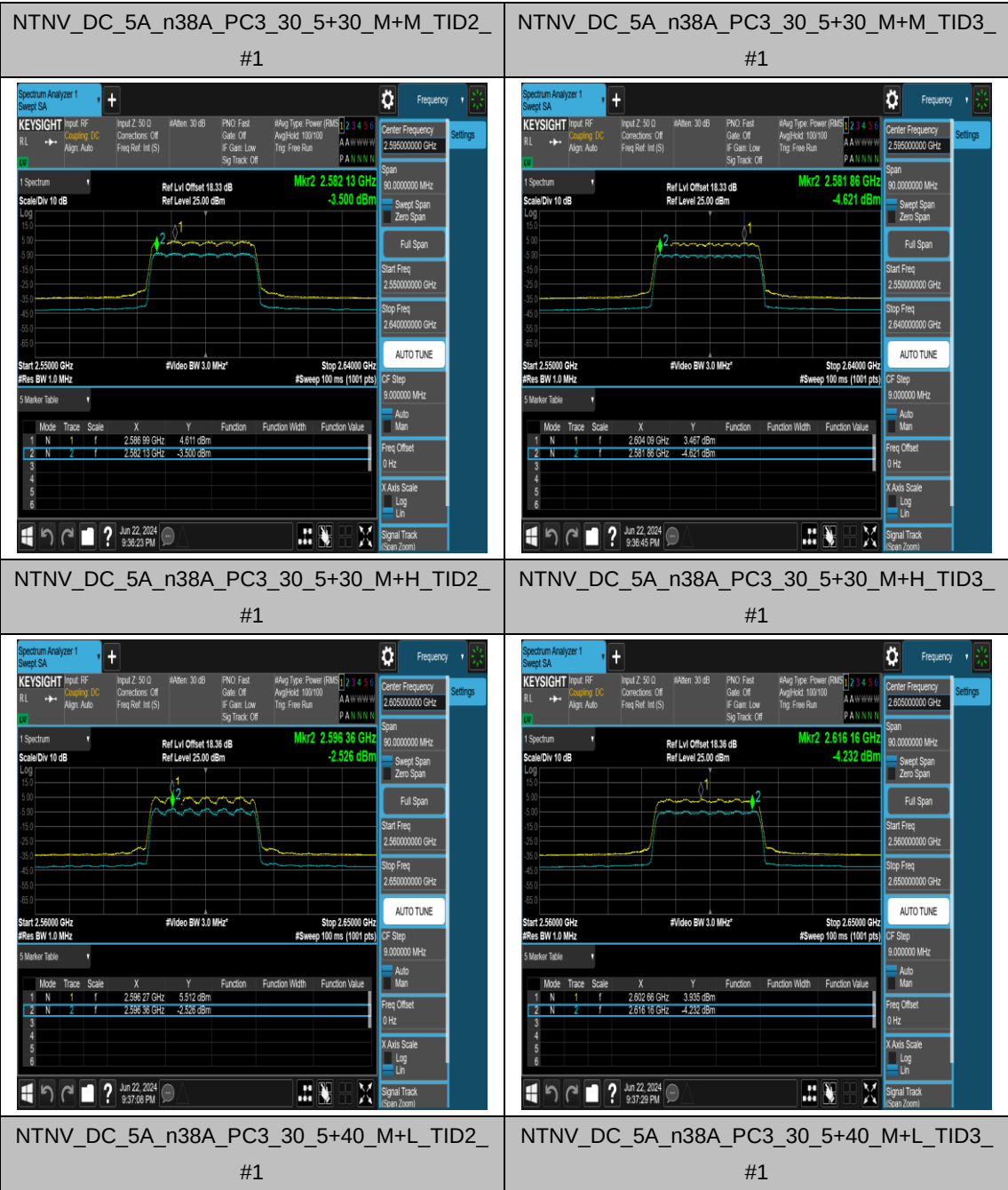
Test Graphs

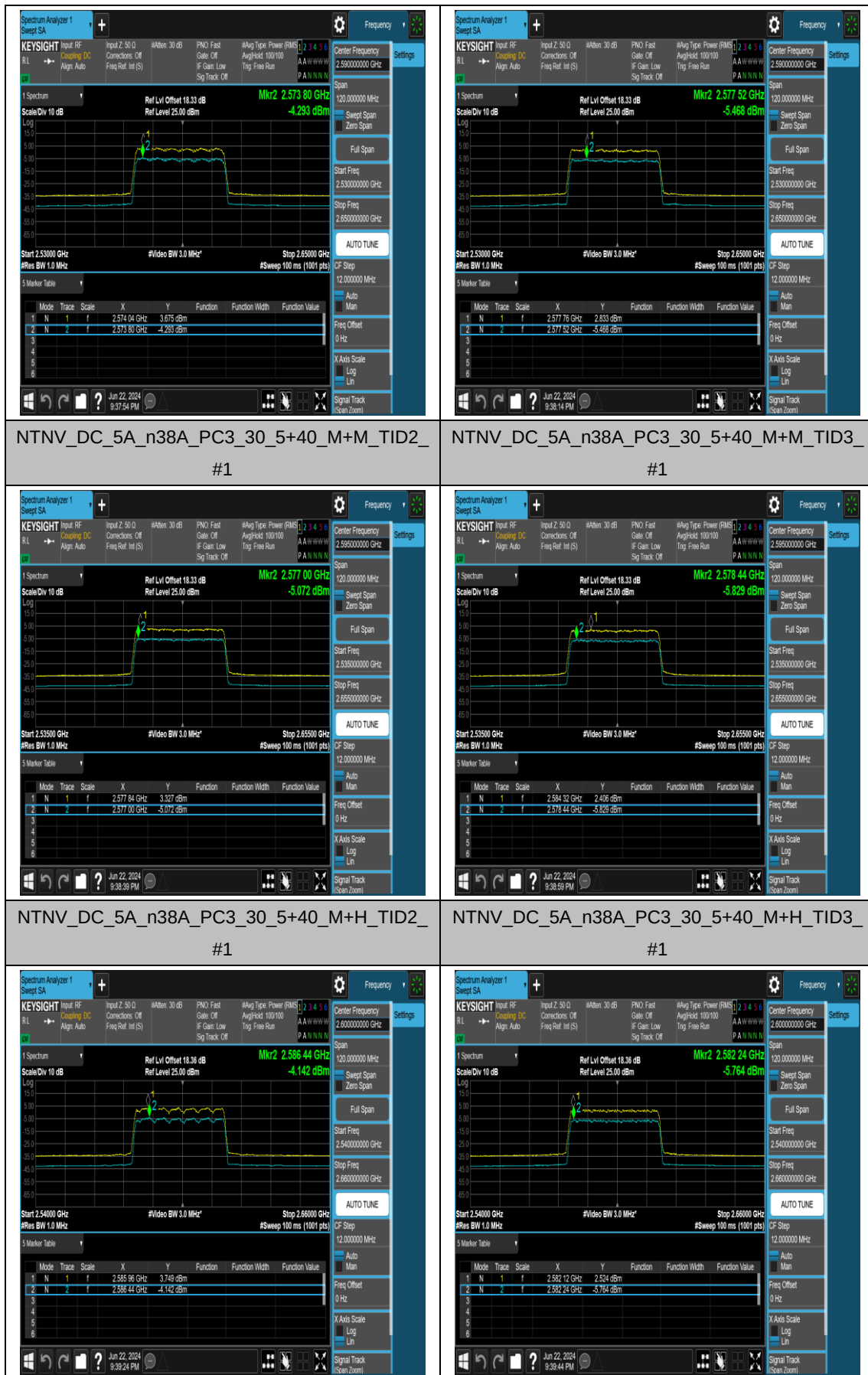










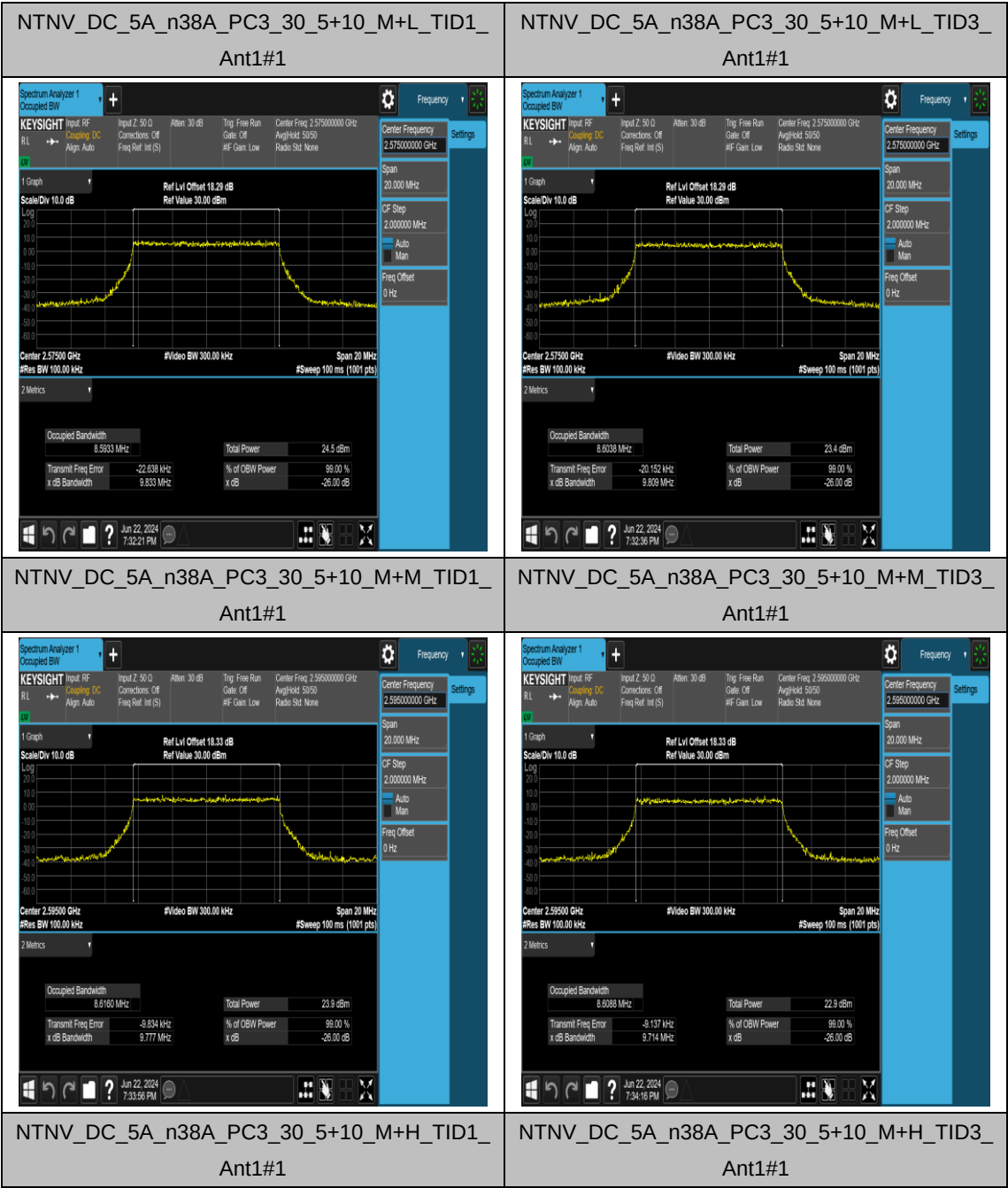


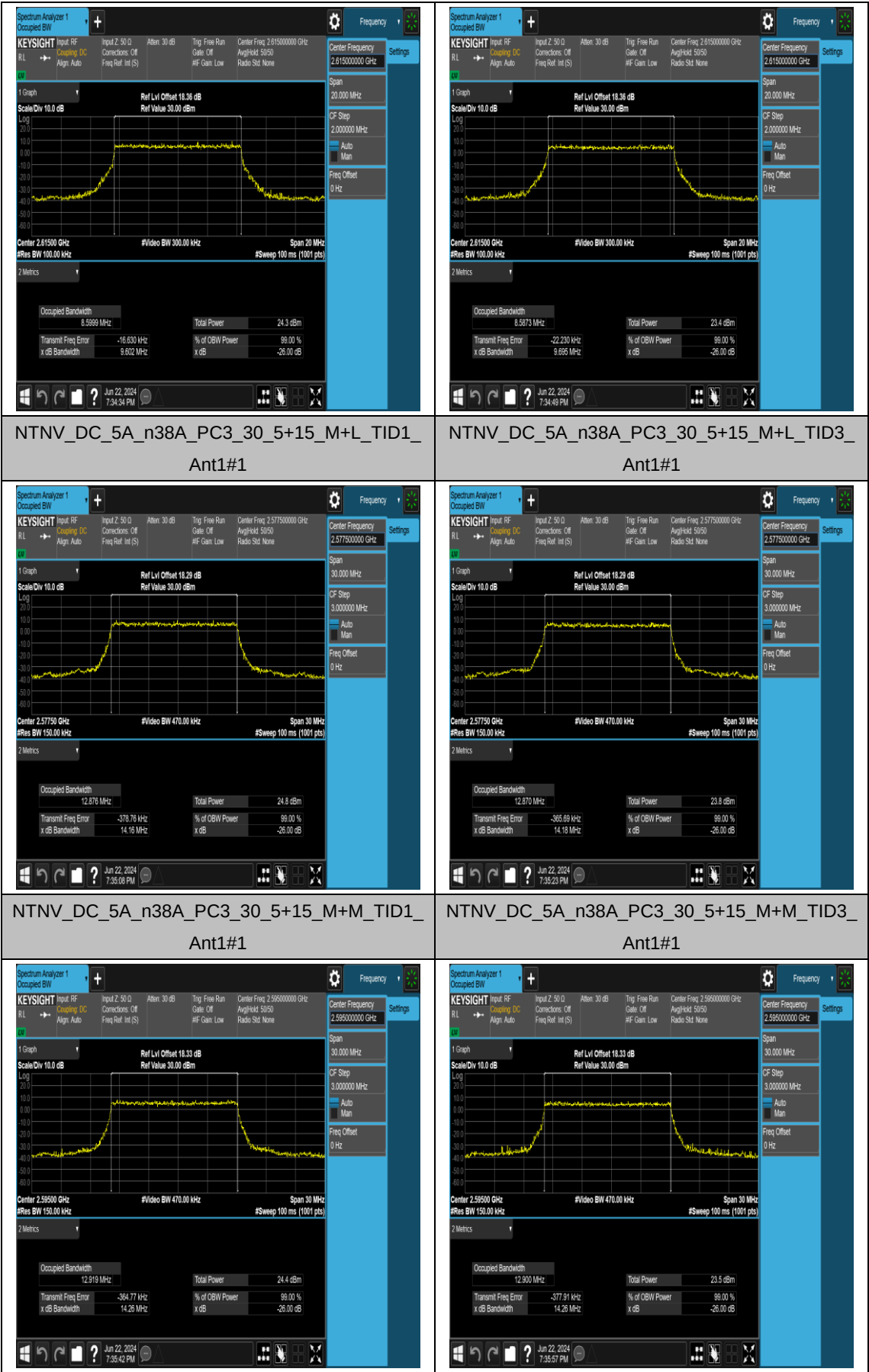
Appendix C: 26dB Bandwidth and Occupied Bandwidth for NSA

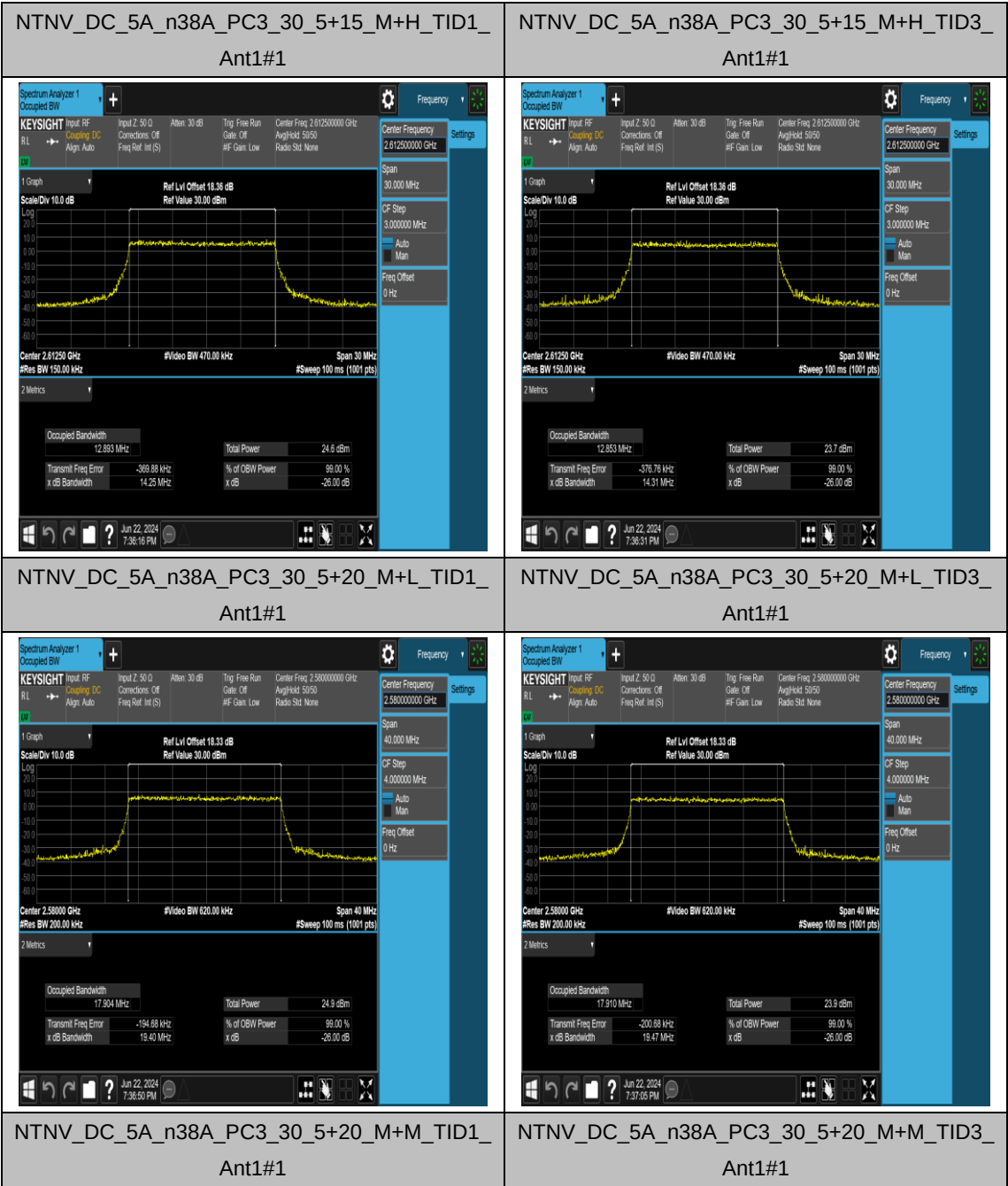
Test Result

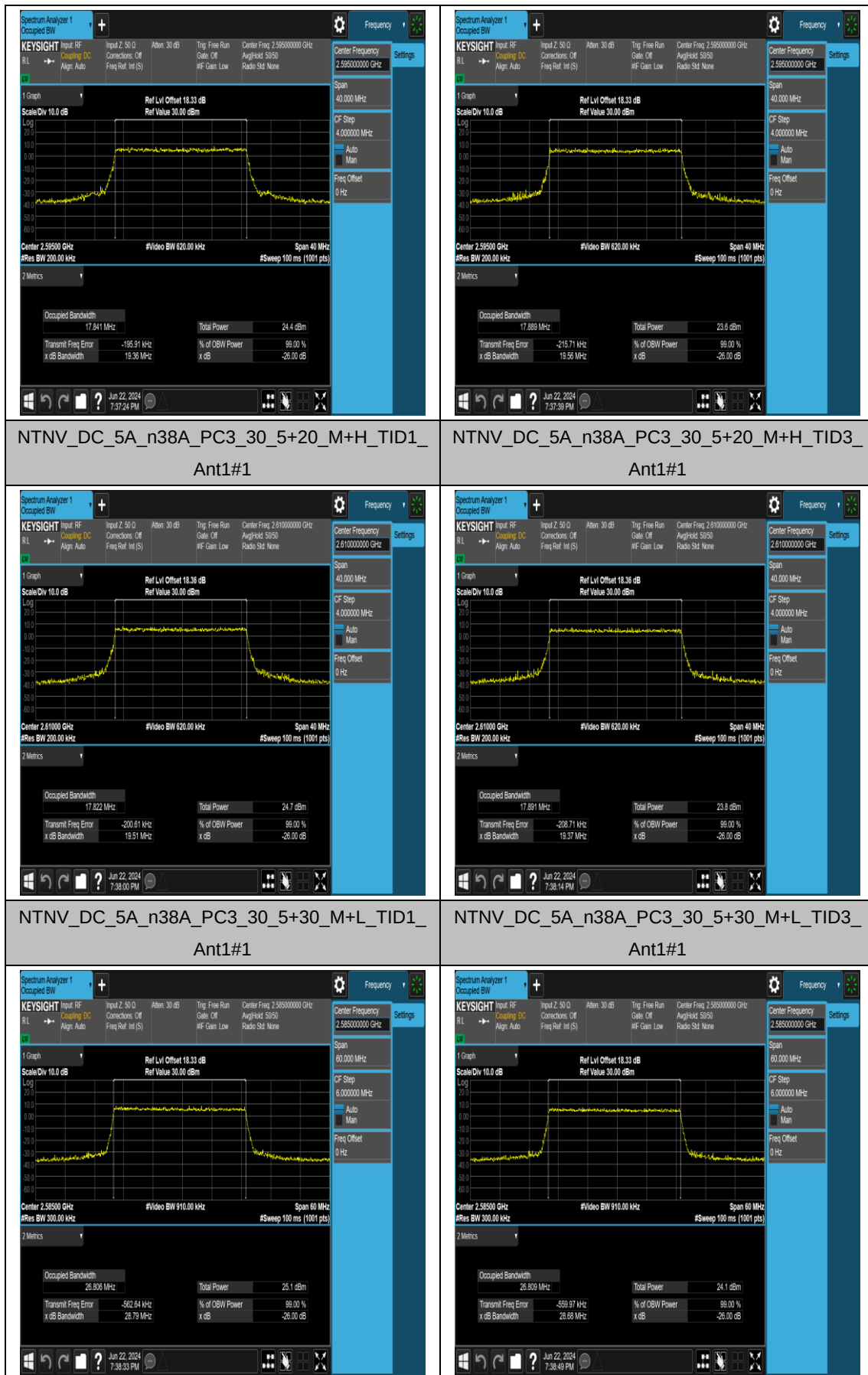
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_5A_n38A	30	5+10	DFT-QPSK	M+L	Outer_Full	8.5933	9.833	PASS
DC_5A_n38A	30	5+10	DFT-16QAM	M+L	Outer_Full	8.6038	9.809	PASS
DC_5A_n38A	30	5+10	DFT-QPSK	M+M	Outer_Full	8.6160	9.777	PASS
DC_5A_n38A	30	5+10	DFT-16QAM	M+M	Outer_Full	8.6088	9.714	PASS
DC_5A_n38A	30	5+10	DFT-QPSK	M+H	Outer_Full	8.5999	9.602	PASS
DC_5A_n38A	30	5+10	DFT-16QAM	M+H	Outer_Full	8.5873	9.695	PASS
DC_5A_n38A	30	5+15	DFT-QPSK	M+L	Outer_Full	12.876	14.16	PASS
DC_5A_n38A	30	5+15	DFT-16QAM	M+L	Outer_Full	12.870	14.18	PASS
DC_5A_n38A	30	5+15	DFT-QPSK	M+M	Outer_Full	12.919	14.26	PASS
DC_5A_n38A	30	5+15	DFT-16QAM	M+M	Outer_Full	12.900	14.26	PASS
DC_5A_n38A	30	5+15	DFT-QPSK	M+H	Outer_Full	12.893	14.25	PASS
DC_5A_n38A	30	5+15	DFT-16QAM	M+H	Outer_Full	12.853	14.31	PASS
DC_5A_n38A	30	5+20	DFT-QPSK	M+L	Outer_Full	17.904	19.40	PASS
DC_5A_n38A	30	5+20	DFT-16QAM	M+L	Outer_Full	17.910	19.47	PASS
DC_5A_n38A	30	5+20	DFT-QPSK	M+M	Outer_Full	17.841	19.36	PASS
DC_5A_n38A	30	5+20	DFT-16QAM	M+M	Outer_Full	17.889	19.56	PASS
DC_5A_n38A	30	5+20	DFT-QPSK	M+H	Outer_Full	17.822	19.51	PASS
DC_5A_n38A	30	5+20	DFT-16QAM	M+H	Outer_Full	17.891	19.37	PASS
DC_5A_n38A	30	5+30	DFT-QPSK	M+L	Outer_Full	26.806	28.79	PASS
DC_5A_n38A	30	5+30	DFT-16QAM	M+L	Outer_Full	26.809	28.68	PASS
DC_5A_n38A	30	5+30	DFT-QPSK	M+M	Outer_Full	26.805	28.70	PASS
DC_5A_n38A	30	5+30	DFT-16QAM	M+M	Outer_Full	26.821	28.58	PASS
DC_5A_n38A	30	5+30	DFT-QPSK	M+H	Outer_Full	26.817	28.79	PASS
DC_5A_n38A	30	5+30	DFT-16QAM	M+H	Outer_Full	26.810	28.72	PASS
DC_5A_n38A	30	5+40	DFT-QPSK	M+L	Outer_Full	35.717	37.76	PASS
DC_5A_n38A	30	5+40	DFT-16QAM	M+L	Outer_Full	35.692	37.79	PASS
DC_5A_n38A	30	5+40	DFT-QPSK	M+M	Outer_Full	35.695	37.68	PASS
DC_5A_n38A	30	5+40	DFT-16QAM	M+M	Outer_Full	35.706	37.68	PASS
DC_5A_n38A	30	5+40	DFT-QPSK	M+H	Outer_Full	35.727	37.71	PASS
DC_5A_n38A	30	5+40	DFT-16QAM	M+H	Outer_Full	35.768	37.67	PASS

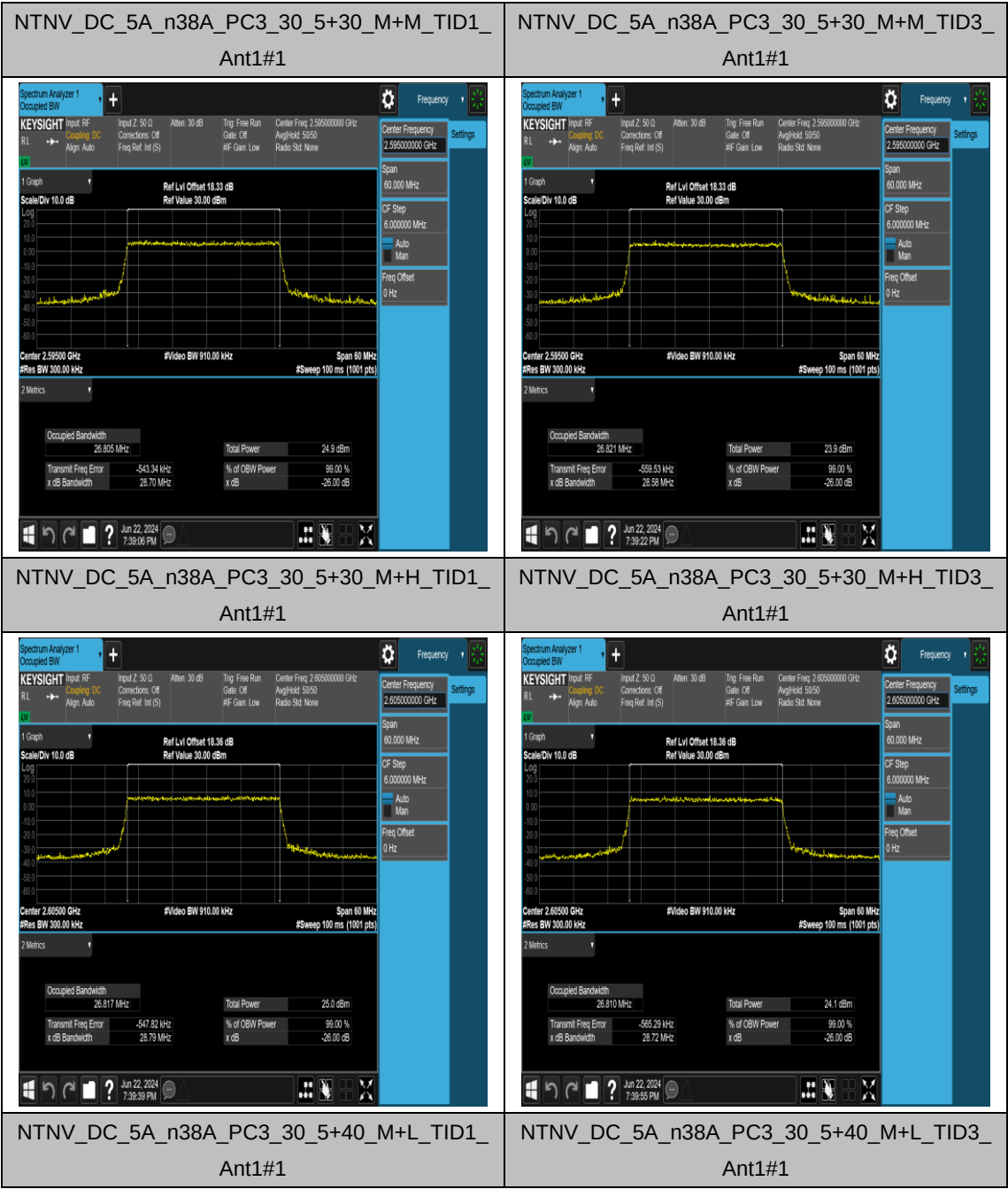
Test Graphs

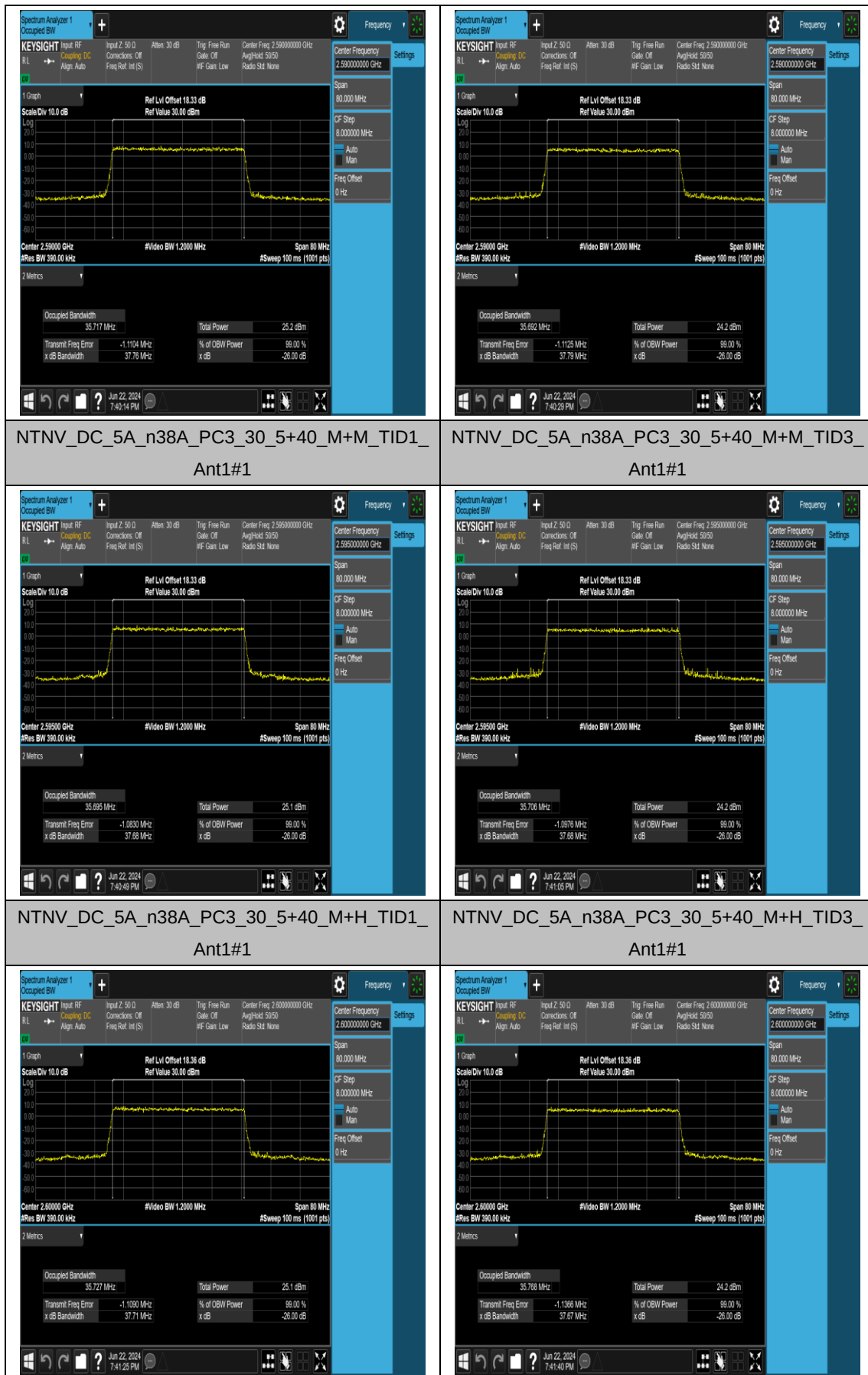












Appendix D: Band Edge for NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Verdict
DC_5A_n38A	30	5+10	DFT-QPSK	M+L	Outer_Full	PASS
DC_5A_n38A	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_5A_n38A	30	5+10	DFT-QPSK	M+H	Outer_Full	PASS
DC_5A_n38A	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	PASS
DC_5A_n38A	30	5+15	DFT-QPSK	M+L	Outer_Full	PASS
DC_5A_n38A	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_5A_n38A	30	5+15	DFT-QPSK	M+H	Outer_Full	PASS
DC_5A_n38A	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	PASS
DC_5A_n38A	30	5+20	DFT-QPSK	M+L	Outer_Full	PASS
DC_5A_n38A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_5A_n38A	30	5+20	DFT-QPSK	M+H	Outer_Full	PASS
DC_5A_n38A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	PASS
DC_5A_n38A	30	5+40	DFT-QPSK	M+L	Outer_Full	PASS
DC_5A_n38A	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_5A_n38A	30	5+40	DFT-QPSK	M+H	Outer_Full	PASS
DC_5A_n38A	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	PASS

Test Graphs

