

Appendix for 2A_n71A

Product Name: TITAN 5400

Model No: G5G-X62O

Appendix A: Average Power Output Data for NSA

Test Result

Band	SC S	Bandwidth h	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_2A_n71A	15	5+5	DFT-QPSK	M+L	Outer_Full	23.45	PC3	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+L	Inner_Full	24.60	PC3	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Left	23.54	PC3	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Right	23.23	PC3	PASS
DC_2A_n71A	15	5+5	DFT-16QAM	M+L	Outer_Full	22.57	PC3	PASS
DC_2A_n71A	15	5+5	DFT-16QAM	M+L	Inner_Full	23.41	PC3	PASS
DC_2A_n71A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Left	22.15	PC3	PASS
DC_2A_n71A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Right	22.16	PC3	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+M	Outer_Full	23.67	PC3	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+M	Inner_Full	24.68	PC3	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Left	23.61	PC3	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Right	23.65	PC3	PASS
DC_2A_n71A	15	5+5	DFT-16QAM	M+M	Outer_Full	22.67	PC3	PASS
DC_2A_n71A	15	5+5	DFT-16QAM	M+M	Inner_Full	23.56	PC3	PASS
DC_2A_n71A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Left	22.08	PC3	PASS
DC_2A_n71A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Right	22.92	PC3	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+H	Outer_Full	23.63	PC3	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+H	Inner_Full	24.68	PC3	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Left	23.53	PC3	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Right	23.44	PC3	PASS
DC_2A_n71A	15	5+5	DFT-16QAM	M+H	Outer_Full	22.71	PC3	PASS
DC_2A_n71A	15	5+5	DFT-16QAM	M+H	Inner_Full	23.80	PC3	PASS
DC_2A_n71A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Left	22.82	PC3	PASS
DC_2A_n71A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Right	22.43	PC3	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+L	Outer_Full	23.54	PC3	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+L	Inner_Full	24.50	PC3	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Left	23.90	PC3	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Right	23.71	PC3	PASS
DC_2A_n71A	15	5+10	DFT-16QAM	M+L	Outer_Full	22.54	PC3	PASS
DC_2A_n71A	15	5+10	DFT-16QAM	M+L	Inner_Full	23.46	PC3	PASS
DC_2A_n71A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Left	22.82	PC3	PASS
DC_2A_n71A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Right	22.90	PC3	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+M	Outer_Full	23.67	PC3	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+M	Inner_Full	24.62	PC3	PASS

DC_2A_n71A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Left	23.85	PC3	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Right	23.89	PC3	PASS
DC_2A_n71A	15	5+10	DFT-16QAM	M+M	Outer_Full	22.64	PC3	PASS
DC_2A_n71A	15	5+10	DFT-16QAM	M+M	Inner_Full	23.71	PC3	PASS
DC_2A_n71A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Left	22.81	PC3	PASS
DC_2A_n71A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Right	22.80	PC3	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+H	Outer_Full	23.68	PC3	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+H	Inner_Full	24.66	PC3	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Left	23.92	PC3	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Right	23.89	PC3	PASS
DC_2A_n71A	15	5+10	DFT-16QAM	M+H	Outer_Full	22.62	PC3	PASS
DC_2A_n71A	15	5+10	DFT-16QAM	M+H	Inner_Full	23.59	PC3	PASS
DC_2A_n71A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Left	23.06	PC3	PASS
DC_2A_n71A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Right	23.18	PC3	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+L	Outer_Full	23.62	PC3	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+L	Inner_Full	24.72	PC3	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Left	23.10	PC3	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Right	23.69	PC3	PASS
DC_2A_n71A	15	5+15	DFT-16QAM	M+L	Outer_Full	22.55	PC3	PASS
DC_2A_n71A	15	5+15	DFT-16QAM	M+L	Inner_Full	23.82	PC3	PASS
DC_2A_n71A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Left	22.32	PC3	PASS
DC_2A_n71A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Right	22.62	PC3	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+M	Outer_Full	23.79	PC3	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+M	Inner_Full	24.76	PC3	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Left	23.60	PC3	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Right	23.77	PC3	PASS
DC_2A_n71A	15	5+15	DFT-16QAM	M+M	Outer_Full	22.80	PC3	PASS
DC_2A_n71A	15	5+15	DFT-16QAM	M+M	Inner_Full	23.81	PC3	PASS
DC_2A_n71A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Left	22.18	PC3	PASS
DC_2A_n71A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Right	22.83	PC3	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+H	Outer_Full	23.76	PC3	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+H	Inner_Full	24.74	PC3	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Left	23.81	PC3	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Right	23.63	PC3	PASS
DC_2A_n71A	15	5+15	DFT-16QAM	M+H	Outer_Full	22.74	PC3	PASS
DC_2A_n71A	15	5+15	DFT-16QAM	M+H	Inner_Full	23.69	PC3	PASS
DC_2A_n71A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Left	23.19	PC3	PASS
DC_2A_n71A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Right	22.76	PC3	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+L	Outer_Full	23.72	PC3	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+L	Inner_Full	24.78	PC3	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	23.52	PC3	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Right	23.78	PC3	PASS

DC_2A_n71A	15	5+20	DFT-16QAM	M+L	Outer_Full	22.65	PC3	PASS
DC_2A_n71A	15	5+20	DFT-16QAM	M+L	Inner_Full	23.64	PC3	PASS
DC_2A_n71A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Left	22.93	PC3	PASS
DC_2A_n71A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Right	22.49	PC3	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+M	Outer_Full	23.82	PC3	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+M	Inner_Full	24.86	PC3	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Left	23.61	PC3	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Right	23.71	PC3	PASS
DC_2A_n71A	15	5+20	DFT-16QAM	M+M	Outer_Full	22.83	PC3	PASS
DC_2A_n71A	15	5+20	DFT-16QAM	M+M	Inner_Full	23.85	PC3	PASS
DC_2A_n71A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Left	22.97	PC3	PASS
DC_2A_n71A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Right	22.83	PC3	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+H	Outer_Full	23.80	PC3	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+H	Inner_Full	24.77	PC3	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Left	24.00	PC3	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	23.76	PC3	PASS
DC_2A_n71A	15	5+20	DFT-16QAM	M+H	Outer_Full	22.81	PC3	PASS
DC_2A_n71A	15	5+20	DFT-16QAM	M+H	Inner_Full	23.82	PC3	PASS
DC_2A_n71A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Left	22.81	PC3	PASS
DC_2A_n71A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Right	22.83	PC3	PASS

Appendix B: Peak-to-Average Ratio for NSA

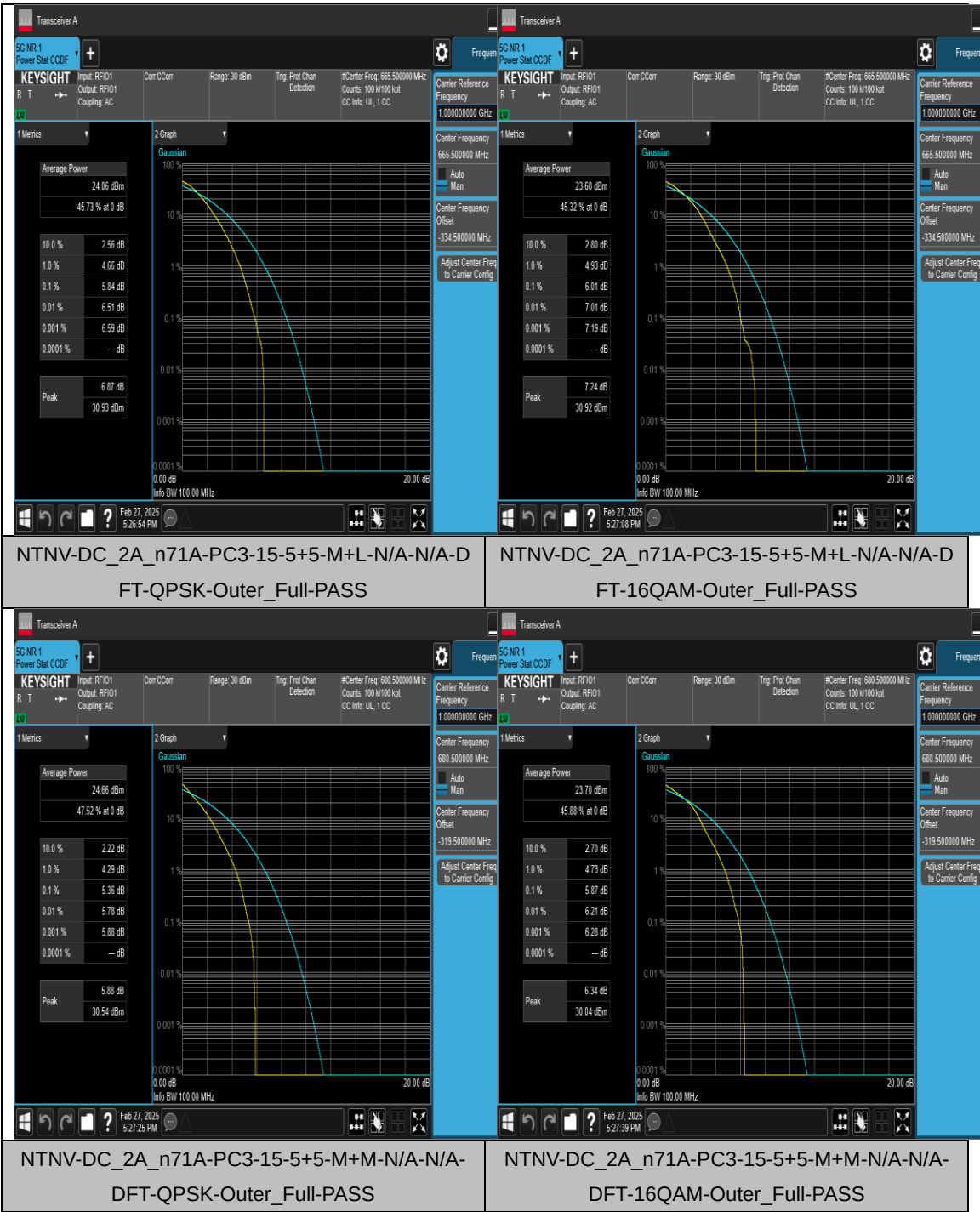
Peak-to-Average Ratio(CCDF)

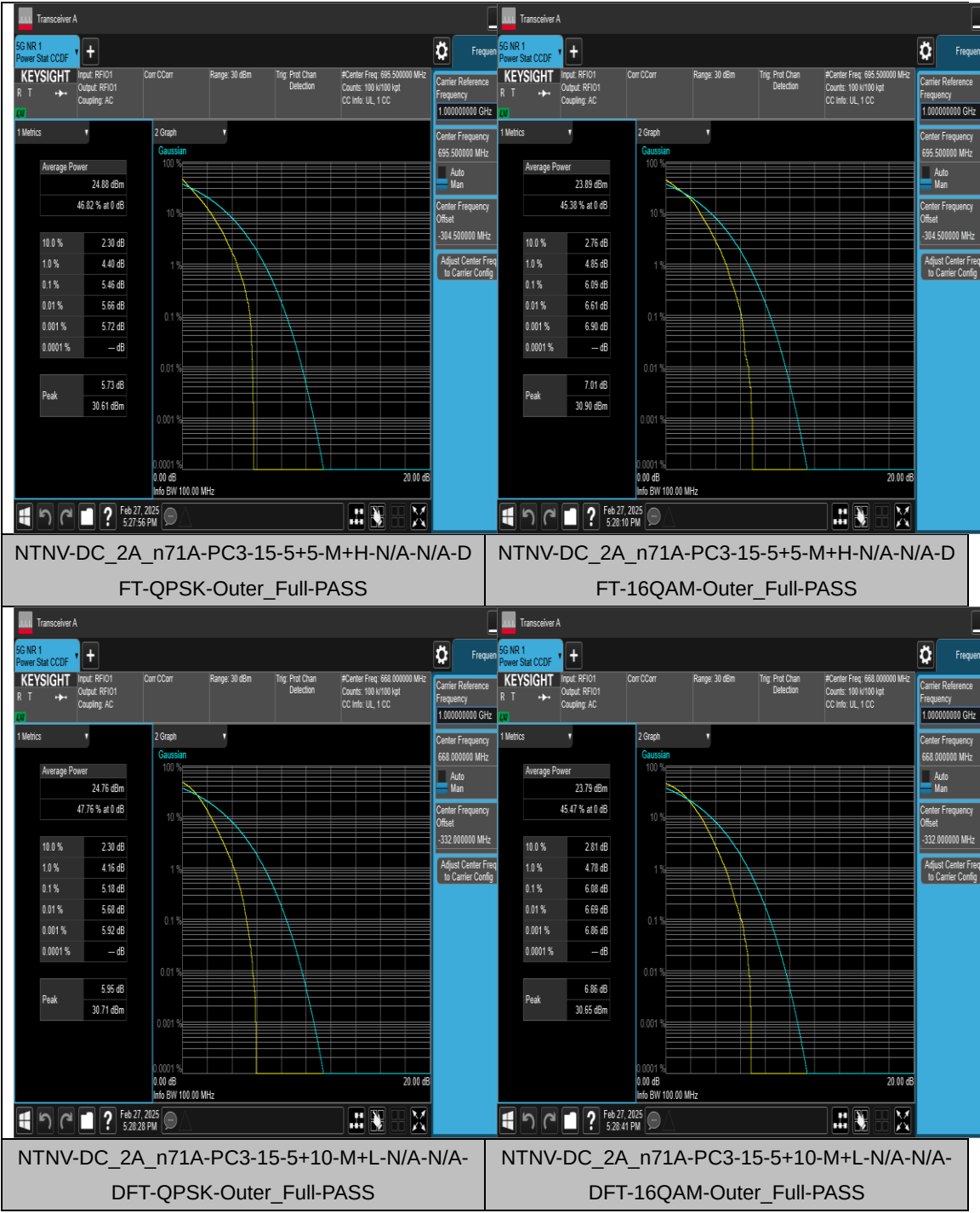
Test Result

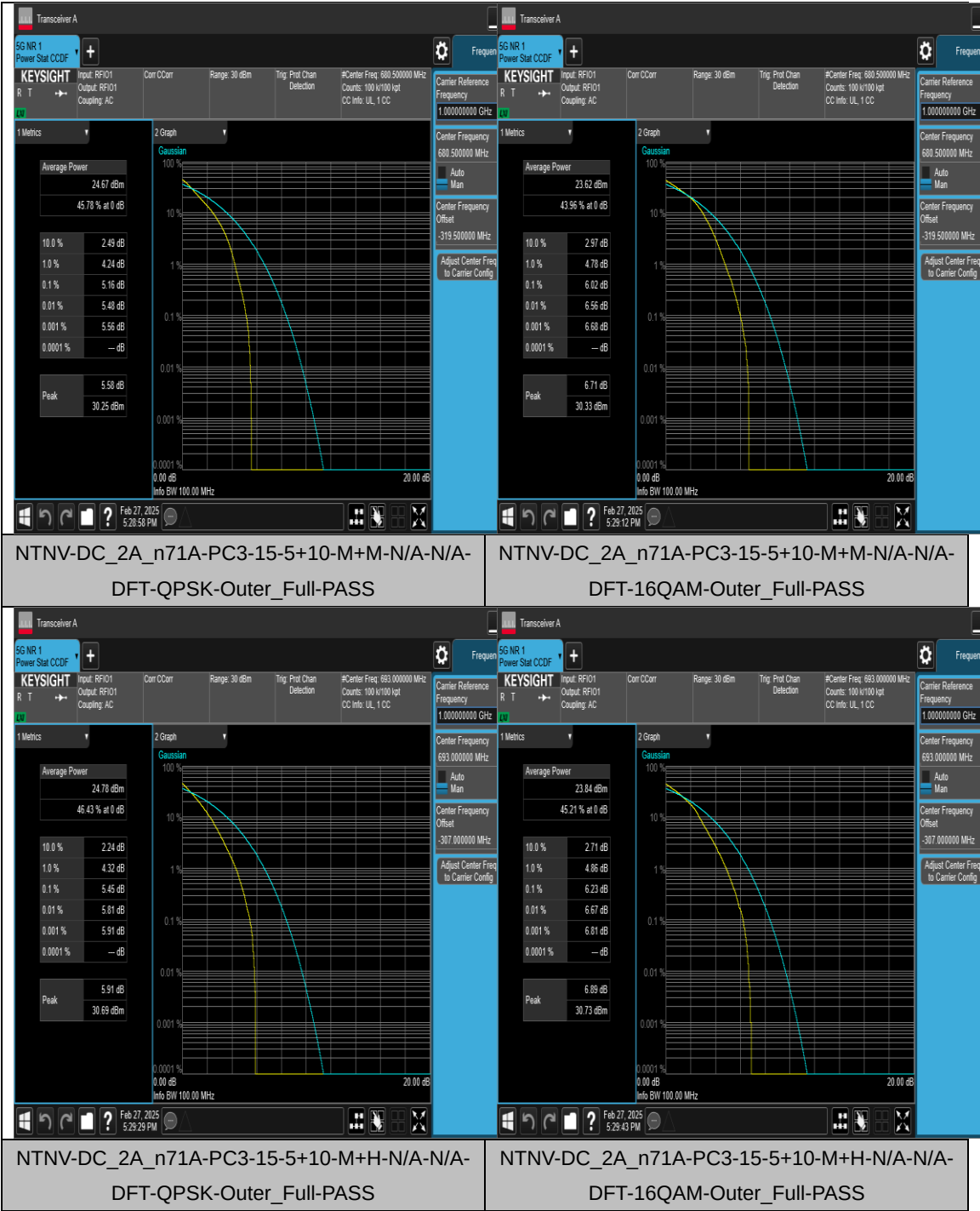
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
DC_2A_n7 1A	15	5+5	DFT-QPSK	M+L	Outer_Full	5.84	≤13	PASS
DC_2A_n7 1A	15	5+5	DFT-16QAM	M+L	Outer_Full	6.01	≤13	PASS
DC_2A_n7 1A	15	5+5	DFT-QPSK	M+M	Outer_Full	5.36	≤13	PASS
DC_2A_n7 1A	15	5+5	DFT-16QAM	M+M	Outer_Full	5.87	≤13	PASS
DC_2A_n7 1A	15	5+5	DFT-QPSK	M+H	Outer_Full	5.46	≤13	PASS
DC_2A_n7 1A	15	5+5	DFT-16QAM	M+H	Outer_Full	6.09	≤13	PASS
DC_2A_n7 1A	15	5+10	DFT-QPSK	M+L	Outer_Full	5.18	≤13	PASS
DC_2A_n7 1A	15	5+10	DFT-16QAM	M+L	Outer_Full	6.08	≤13	PASS
DC_2A_n7 1A	15	5+10	DFT-QPSK	M+M	Outer_Full	5.16	≤13	PASS
DC_2A_n7 1A	15	5+10	DFT-16QAM	M+M	Outer_Full	6.02	≤13	PASS
DC_2A_n7 1A	15	5+10	DFT-QPSK	M+H	Outer_Full	5.45	≤13	PASS
DC_2A_n7 1A	15	5+10	DFT-16QAM	M+H	Outer_Full	6.23	≤13	PASS
DC_2A_n7 1A	15	5+15	DFT-QPSK	M+L	Outer_Full	5.46	≤13	PASS
DC_2A_n7 1A	15	5+15	DFT-16QAM	M+L	Outer_Full	6.25	≤13	PASS
DC_2A_n7 1A	15	5+15	DFT-QPSK	M+M	Outer_Full	5.24	≤13	PASS
DC_2A_n7 1A	15	5+15	DFT-16QAM	M+M	Outer_Full	5.97	≤13	PASS
DC_2A_n7	15	5+15	DFT-QPSK	M+H	Outer_Full	5.63	≤13	PASS

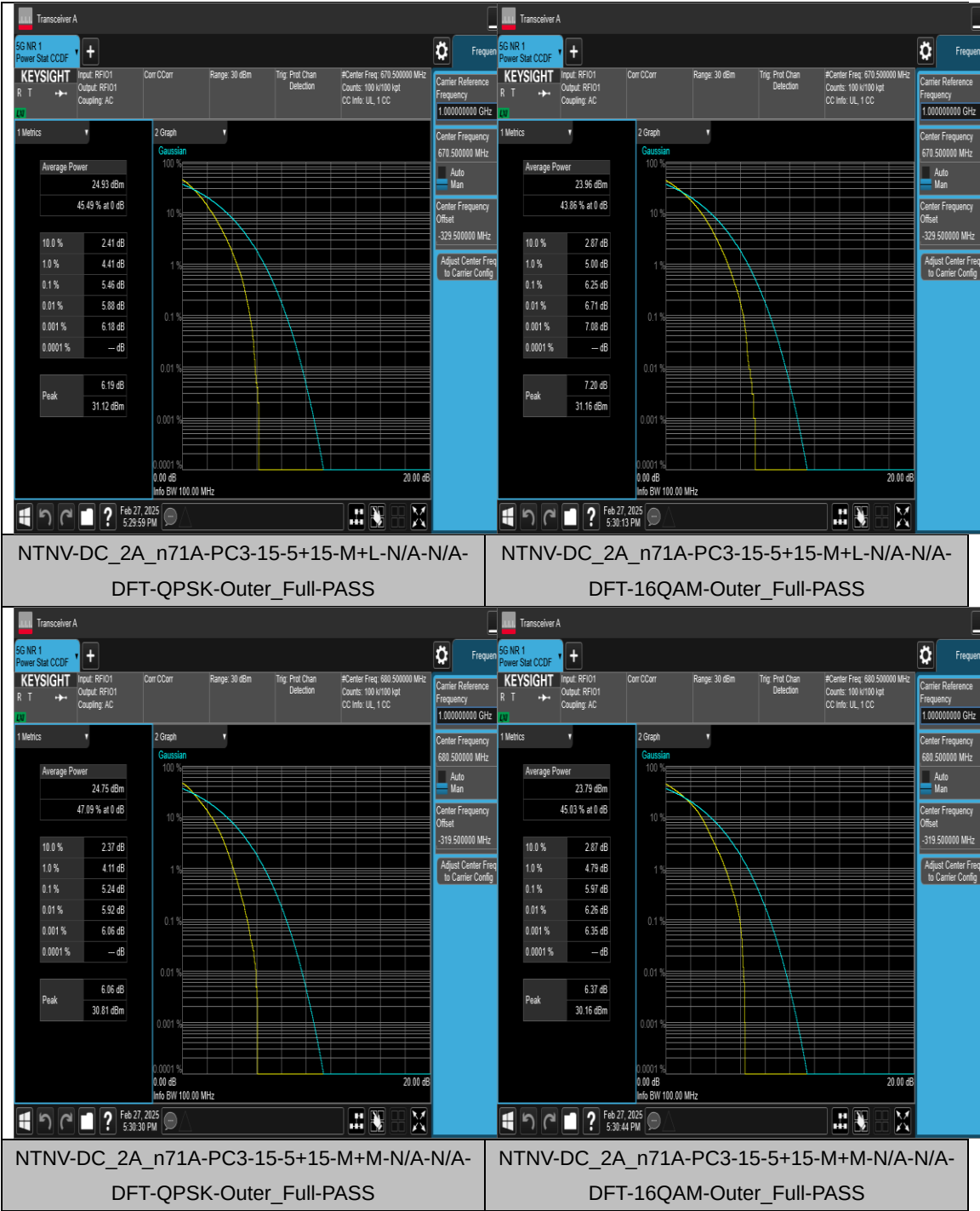
1A								
DC_2A_n7 1A	15	5+15	DFT-16QAM	M+H	Outer_Full	6.31	≤13	PASS
DC_2A_n7 1A	15	5+20	DFT-QPSK	M+L	Outer_Full	5.69	≤13	PASS
DC_2A_n7 1A	15	5+20	DFT-16QAM	M+L	Outer_Full	6.47	≤13	PASS
DC_2A_n7 1A	15	5+20	DFT-QPSK	M+M	Outer_Full	5.16	≤13	PASS
DC_2A_n7 1A	15	5+20	DFT-16QAM	M+M	Outer_Full	6.03	≤13	PASS
DC_2A_n7 1A	15	5+20	DFT-QPSK	M+H	Outer_Full	5.52	≤13	PASS
DC_2A_n7 1A	15	5+20	DFT-16QAM	M+H	Outer_Full	6.32	≤13	PASS

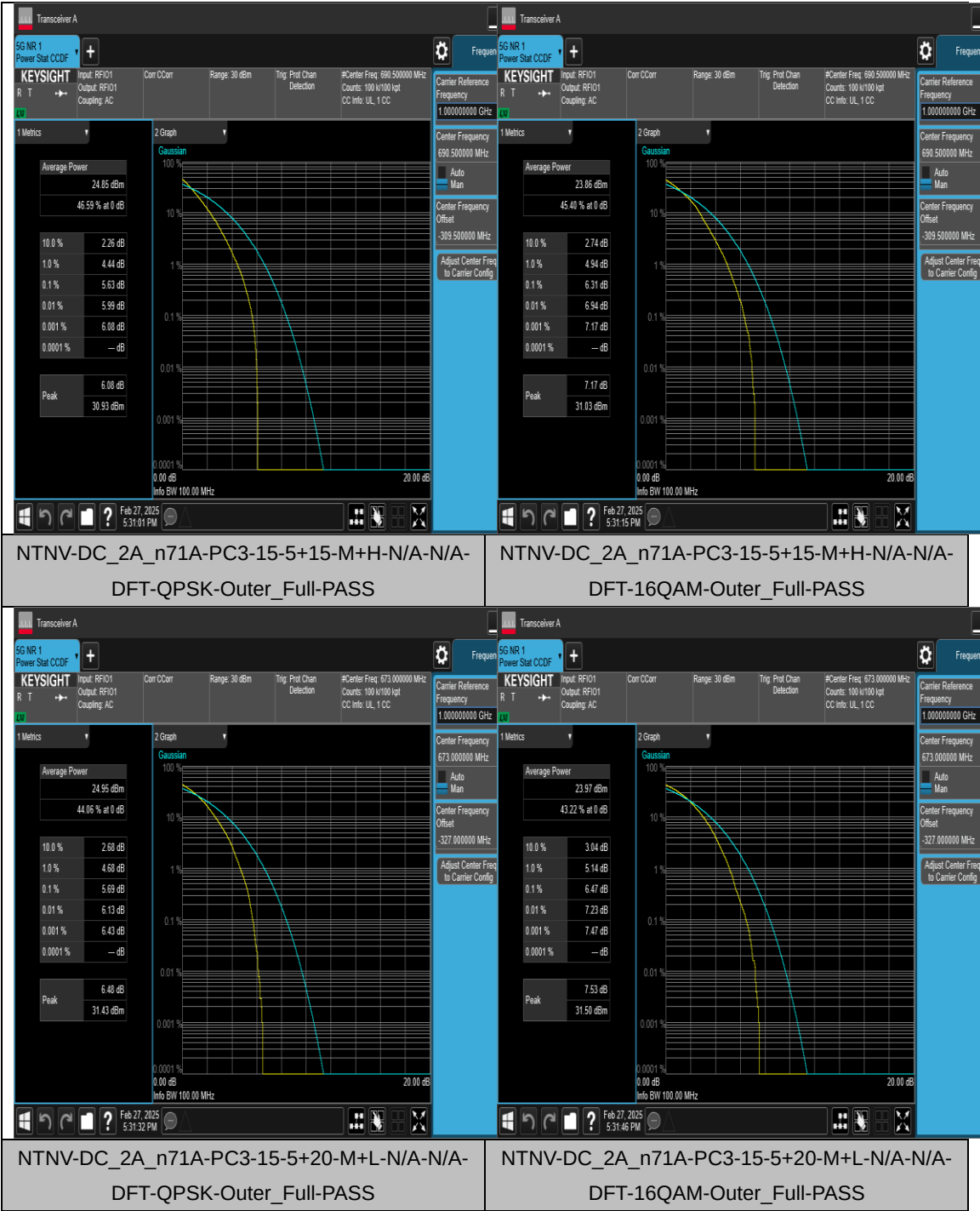
Test Graphs

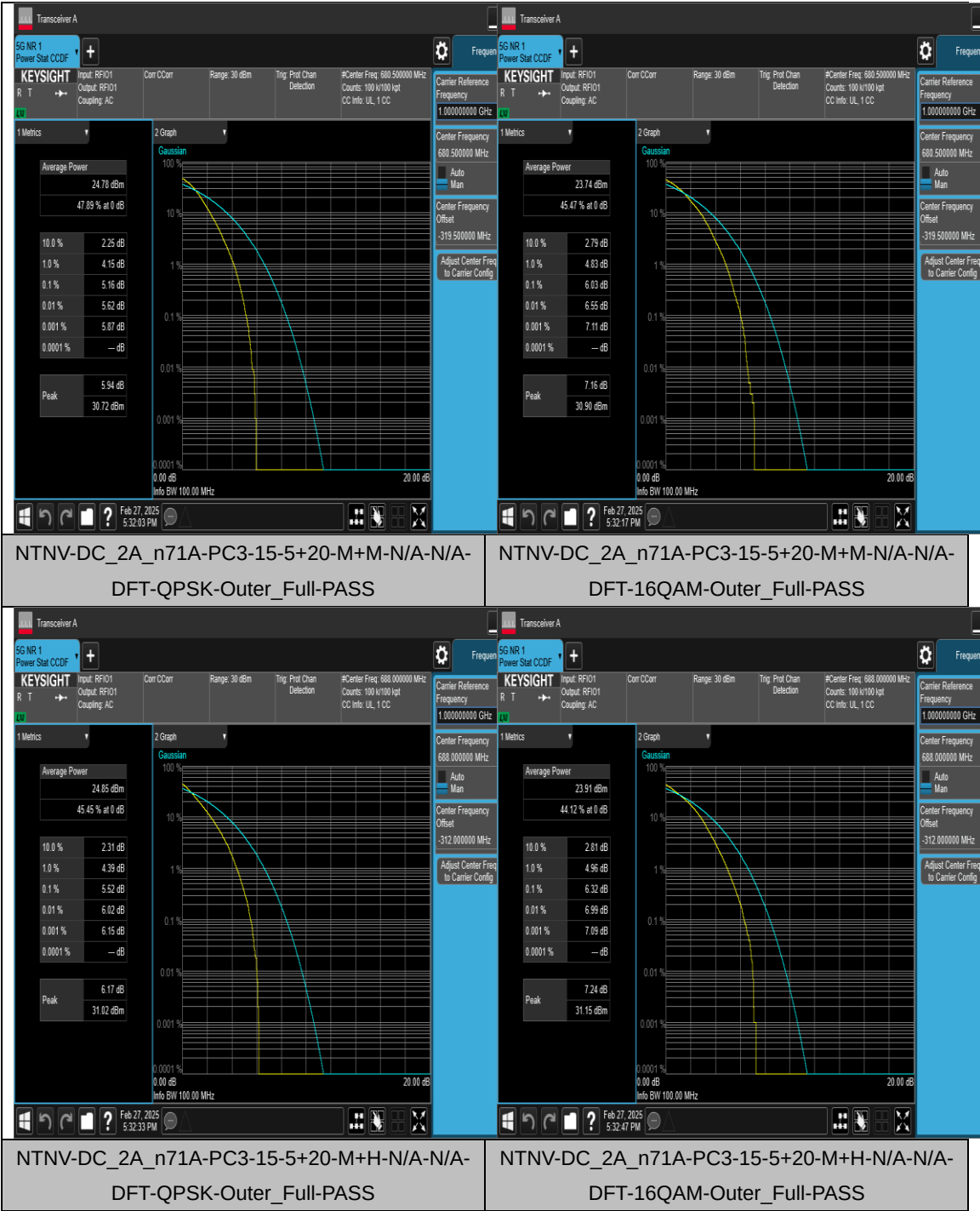












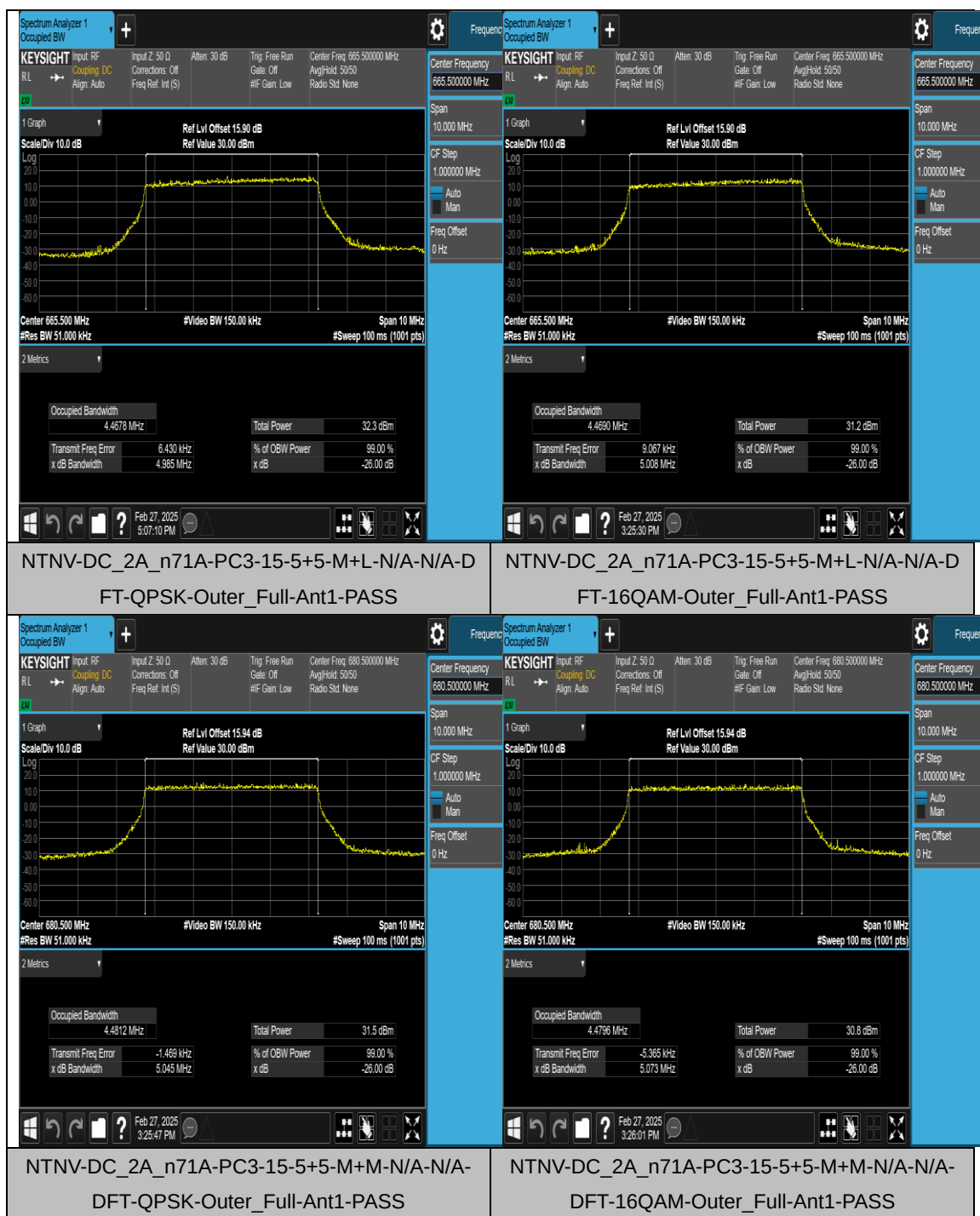
Appendix C: 26dB Bandwidth and Occupied Bandwidth for

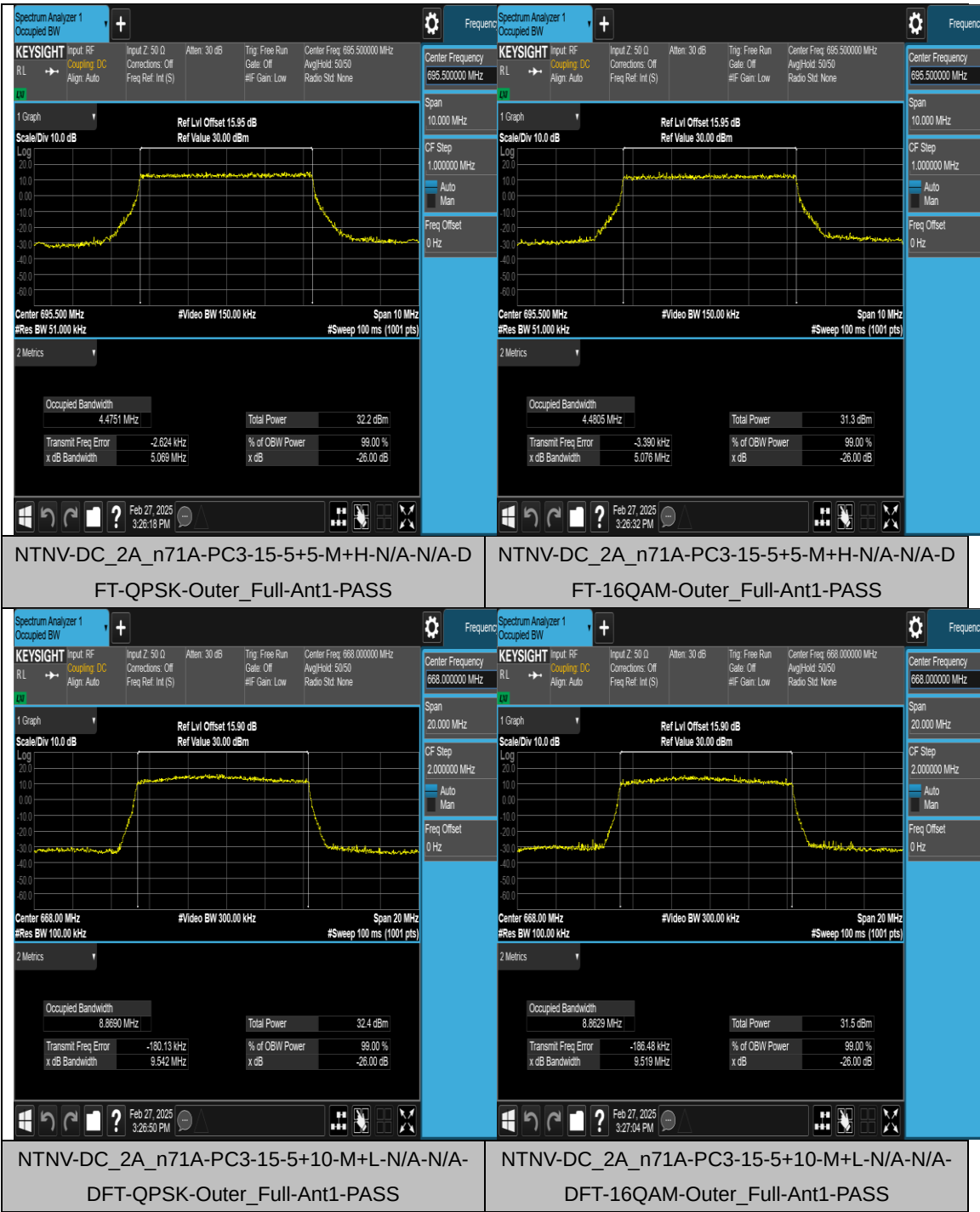
NSA

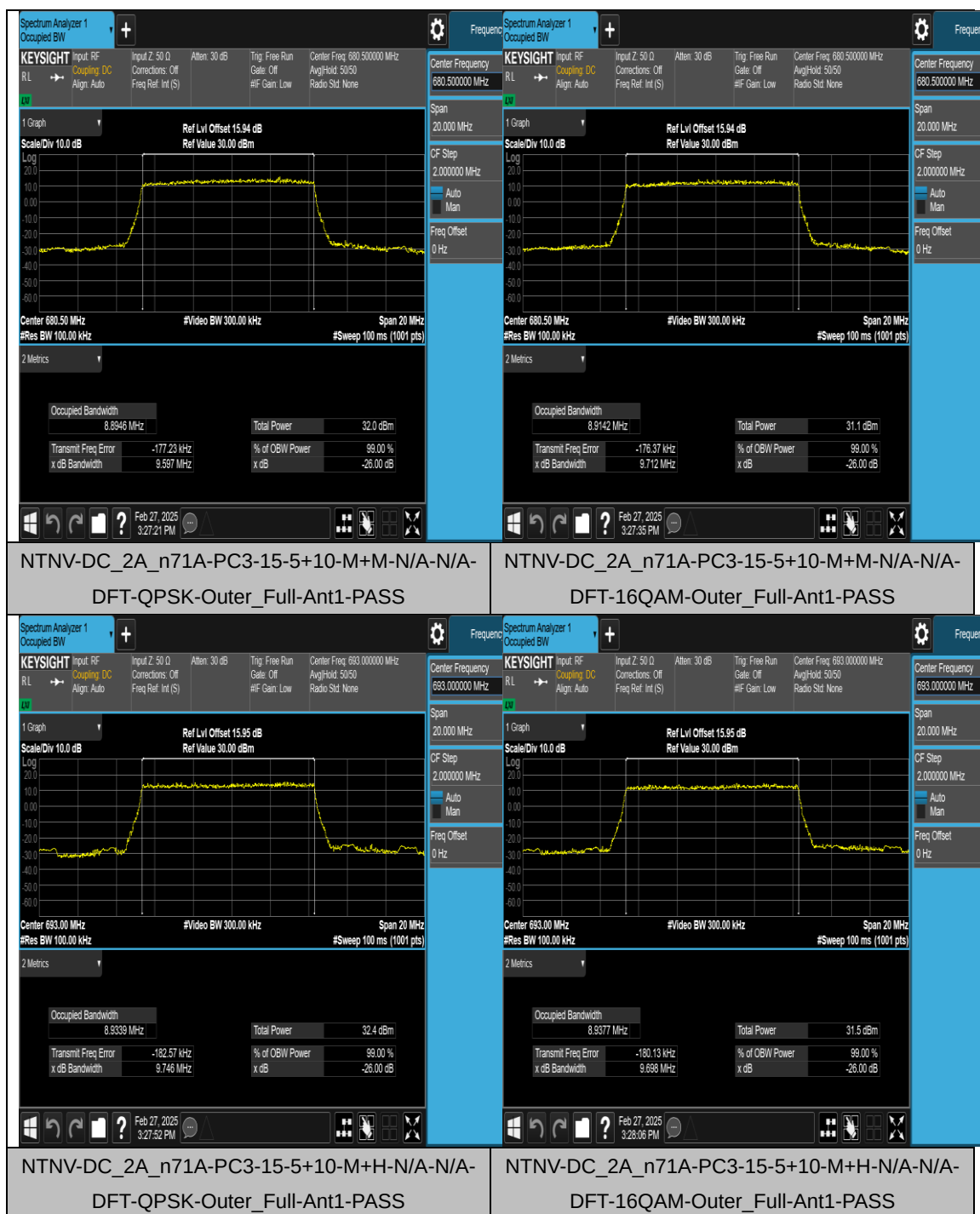
Test Result

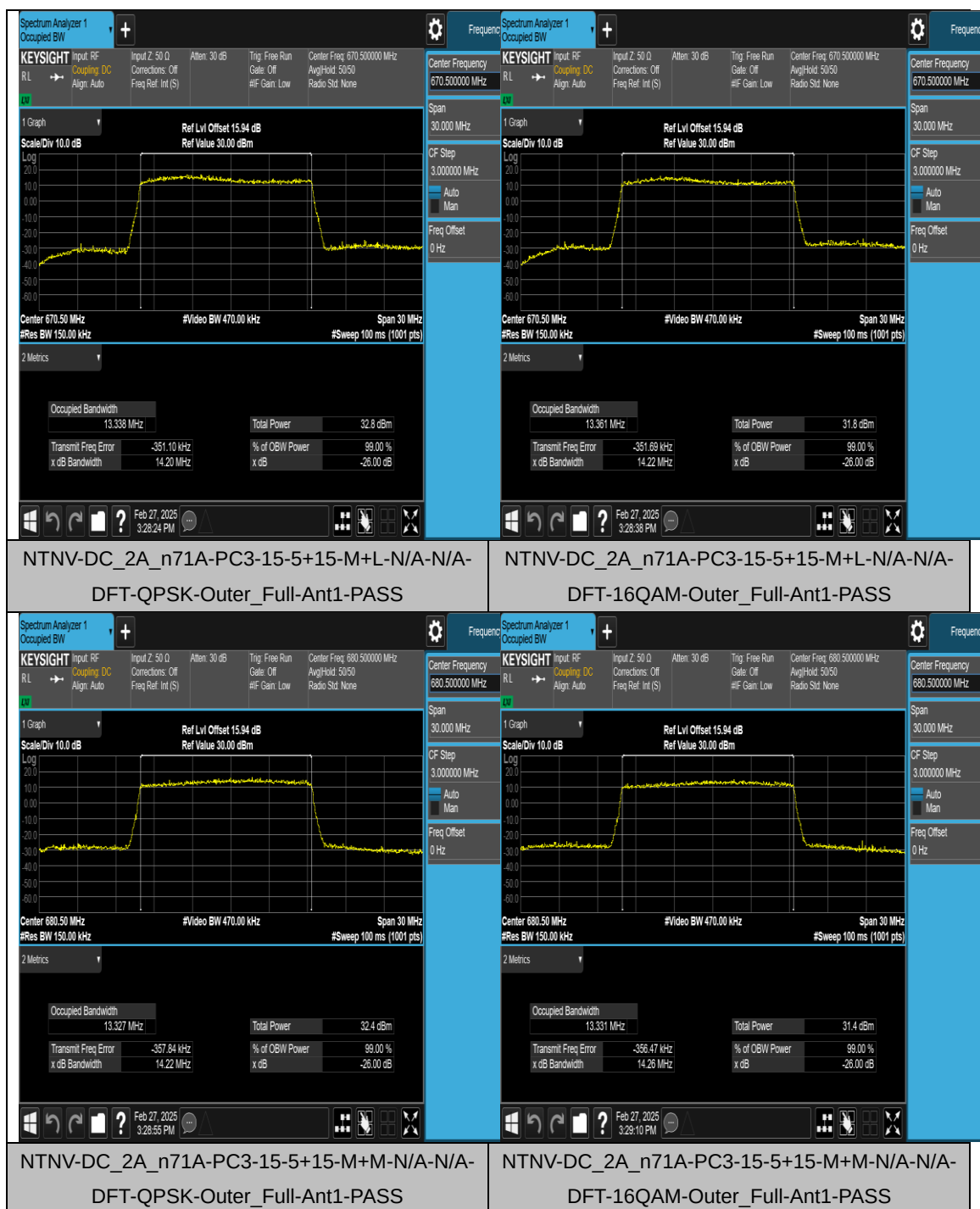
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_2A_n71A	15	5+5	DFT-QPSK	M+L	Outer_Full	4.4678	4.985	PASS
DC_2A_n71A	15	5+5	DFT-16QAM	M+L	Outer_Full	4.4690	5.008	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+M	Outer_Full	4.4812	5.045	PASS
DC_2A_n71A	15	5+5	DFT-16QAM	M+M	Outer_Full	4.4796	5.073	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+H	Outer_Full	4.4751	5.069	PASS
DC_2A_n71A	15	5+5	DFT-16QAM	M+H	Outer_Full	4.4805	5.076	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+L	Outer_Full	8.8690	9.542	PASS
DC_2A_n71A	15	5+10	DFT-16QAM	M+L	Outer_Full	8.8629	9.519	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+M	Outer_Full	8.8946	9.597	PASS
DC_2A_n71A	15	5+10	DFT-16QAM	M+M	Outer_Full	8.9142	9.712	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+H	Outer_Full	8.9339	9.746	PASS
DC_2A_n71A	15	5+10	DFT-16QAM	M+H	Outer_Full	8.9377	9.698	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+L	Outer_Full	13.338	14.20	PASS
DC_2A_n71A	15	5+15	DFT-16QAM	M+L	Outer_Full	13.361	14.22	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+M	Outer_Full	13.327	14.22	PASS
DC_2A_n71A	15	5+15	DFT-16QAM	M+M	Outer_Full	13.331	14.26	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+H	Outer_Full	13.397	14.38	PASS
DC_2A_n71A	15	5+15	DFT-16QAM	M+H	Outer_Full	13.429	14.37	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+L	Outer_Full	17.807	18.82	PASS
DC_2A_n71A	15	5+20	DFT-16QAM	M+L	Outer_Full	17.818	18.84	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+M	Outer_Full	17.771	18.87	PASS
DC_2A_n71A	15	5+20	DFT-16QAM	M+M	Outer_Full	17.786	18.84	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+H	Outer_Full	17.842	18.96	PASS
DC_2A_n71A	15	5+20	DFT-16QAM	M+H	Outer_Full	17.838	18.89	PASS

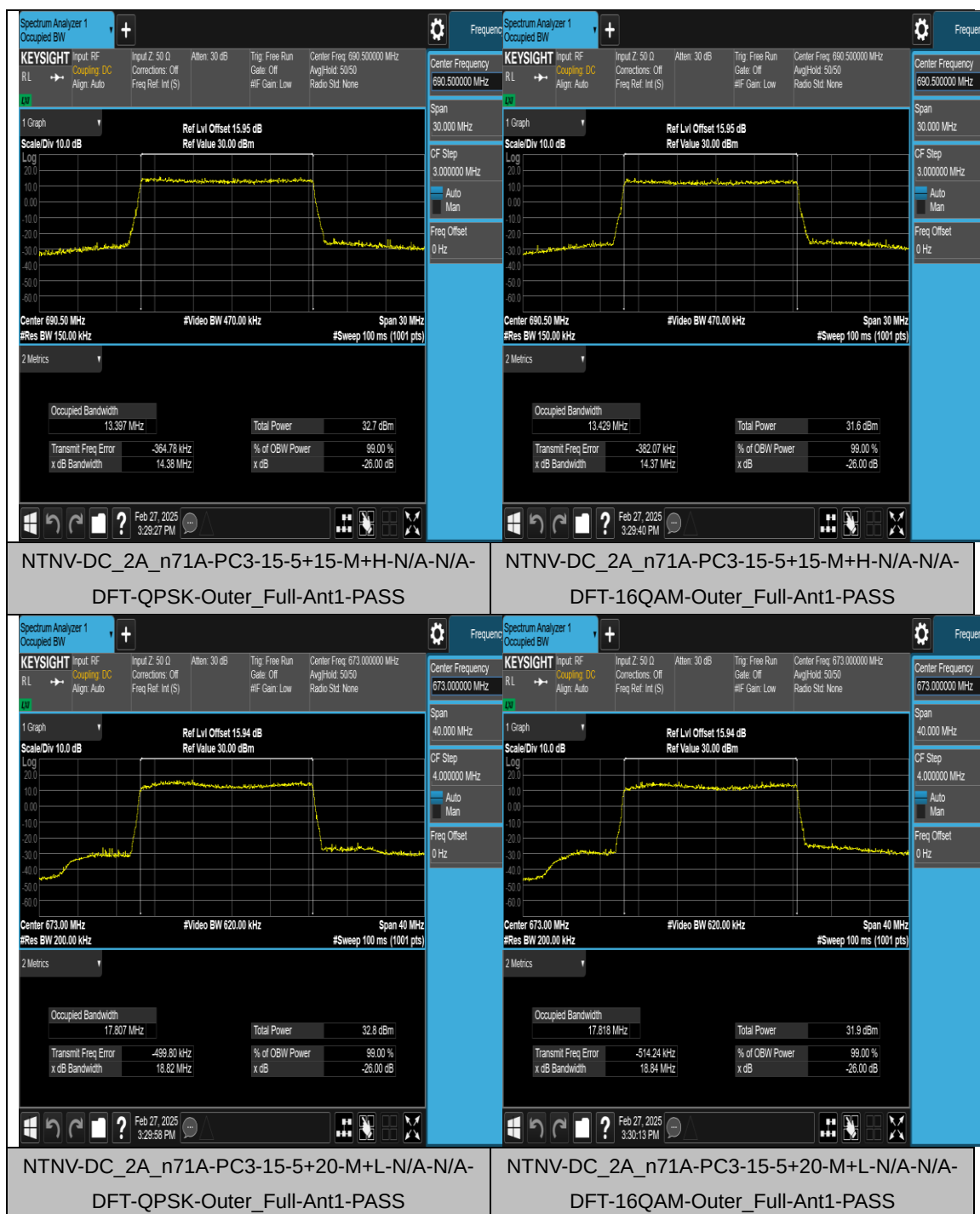
Test Graphs

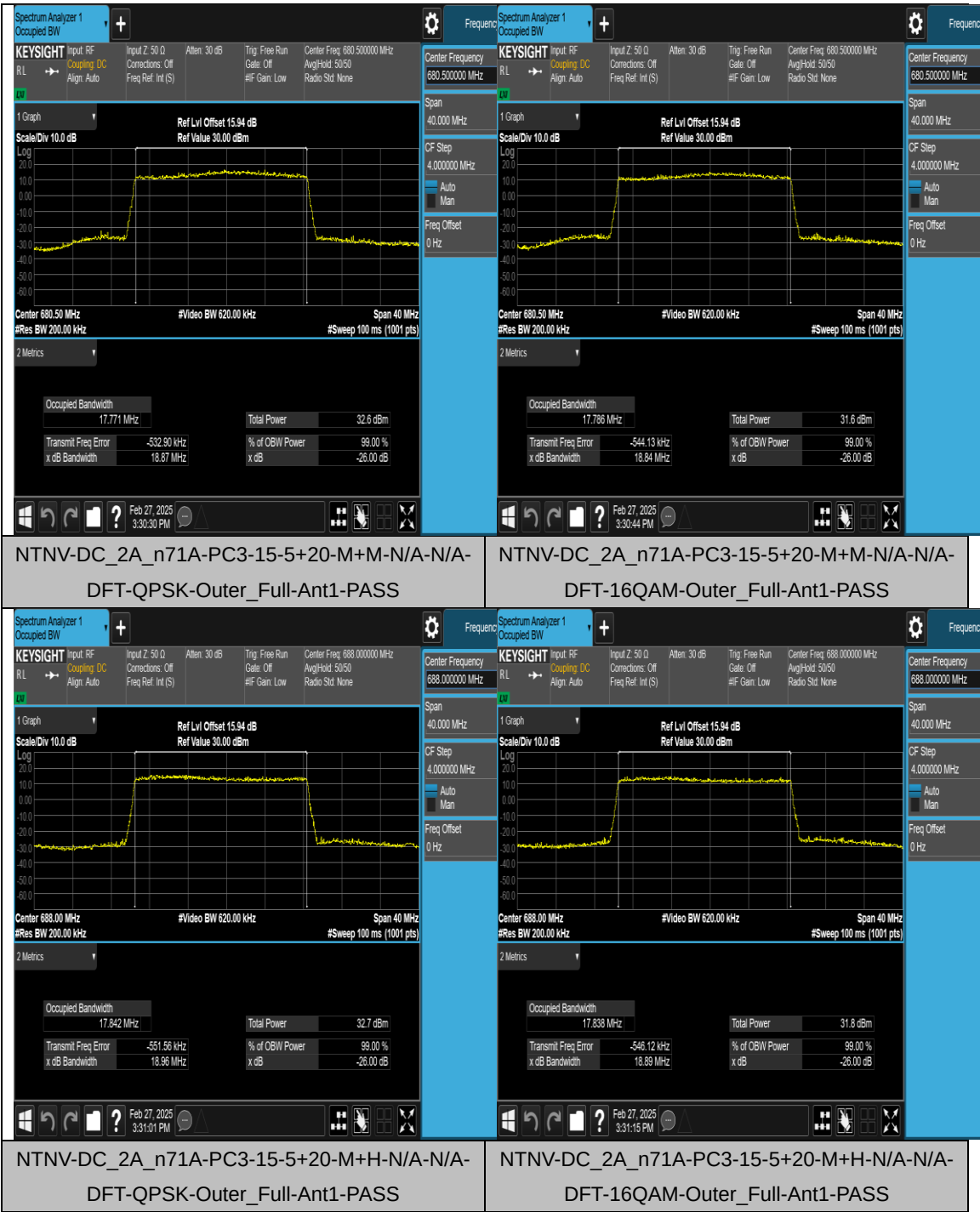










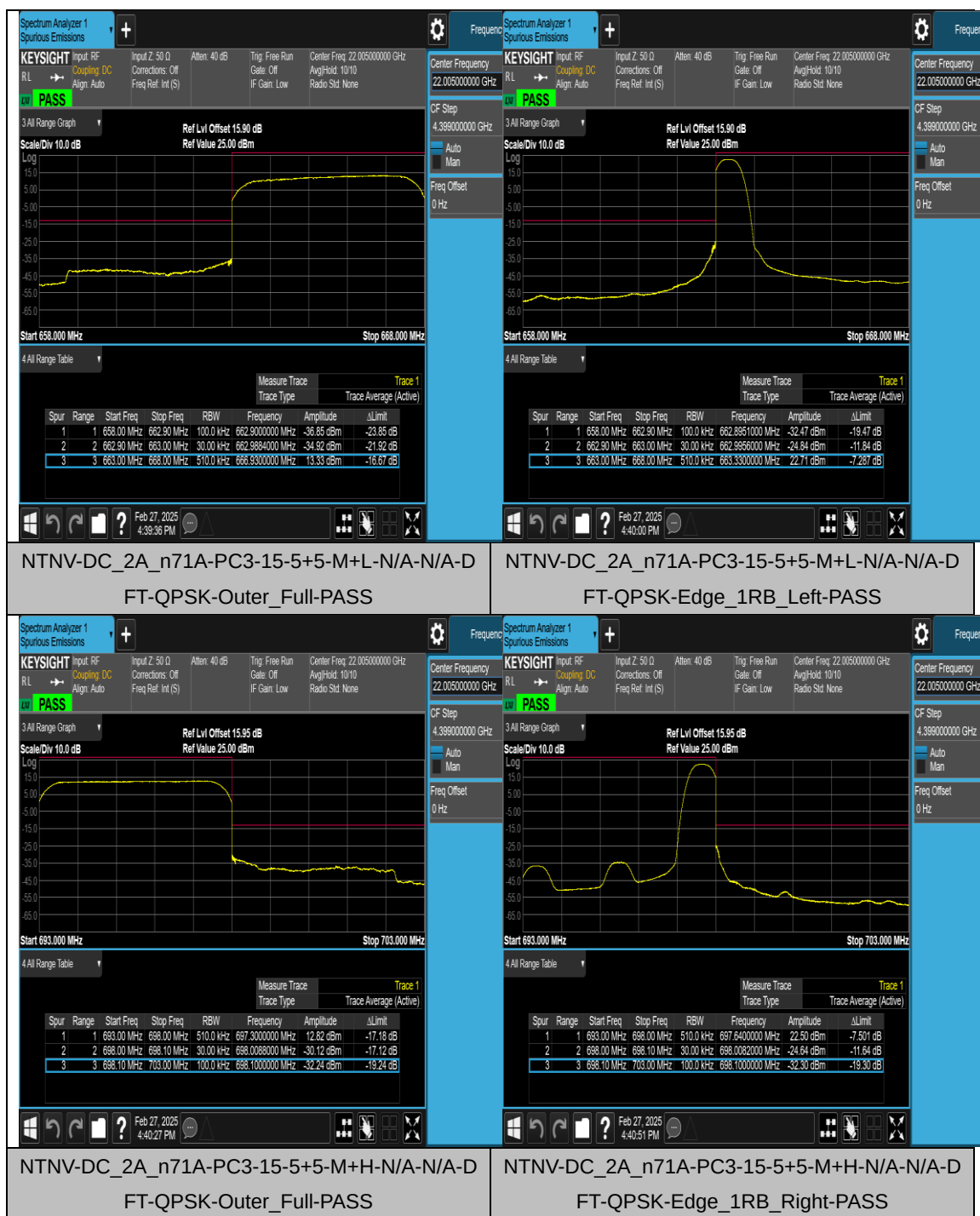


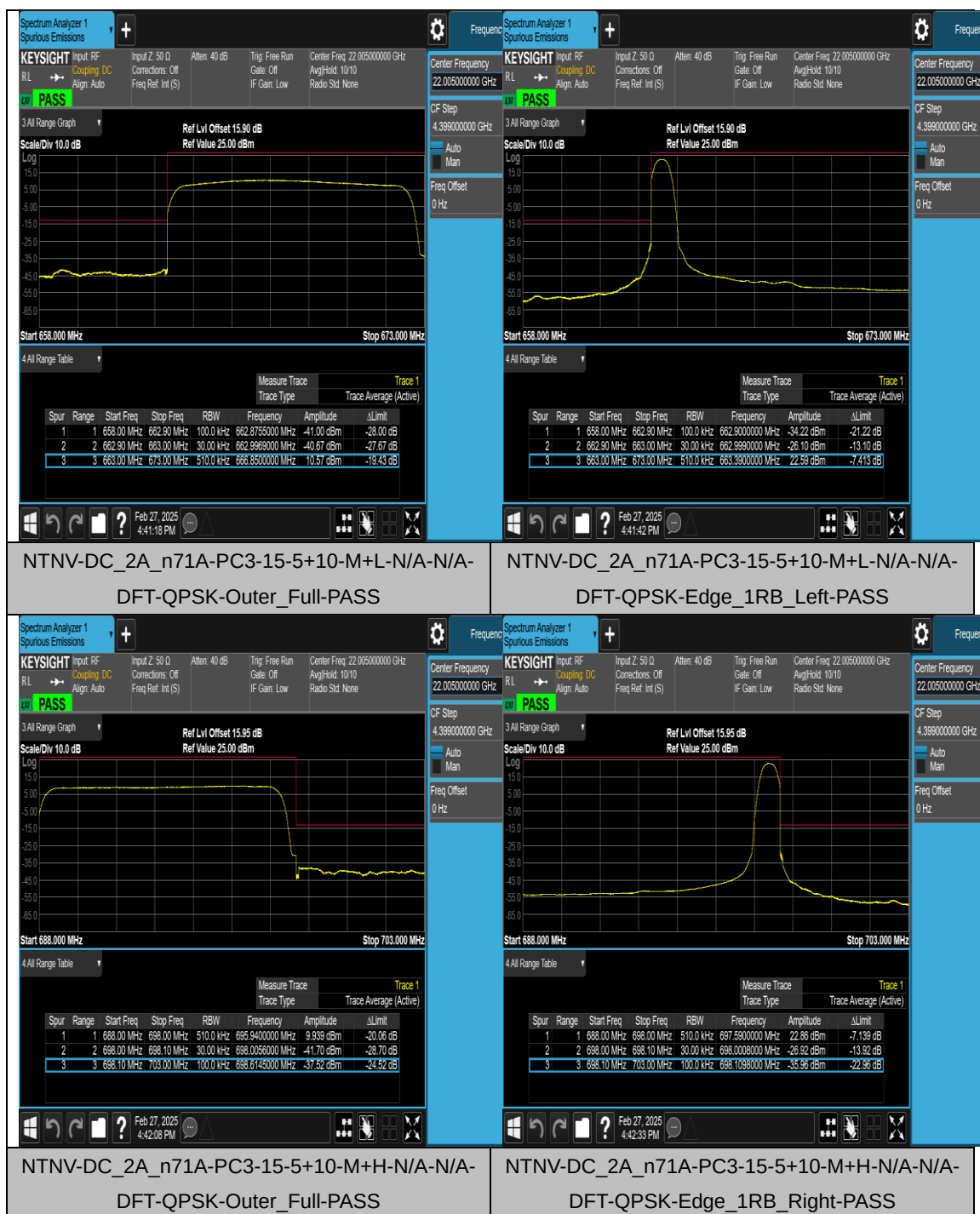
Appendix D: Band Edge for NSA

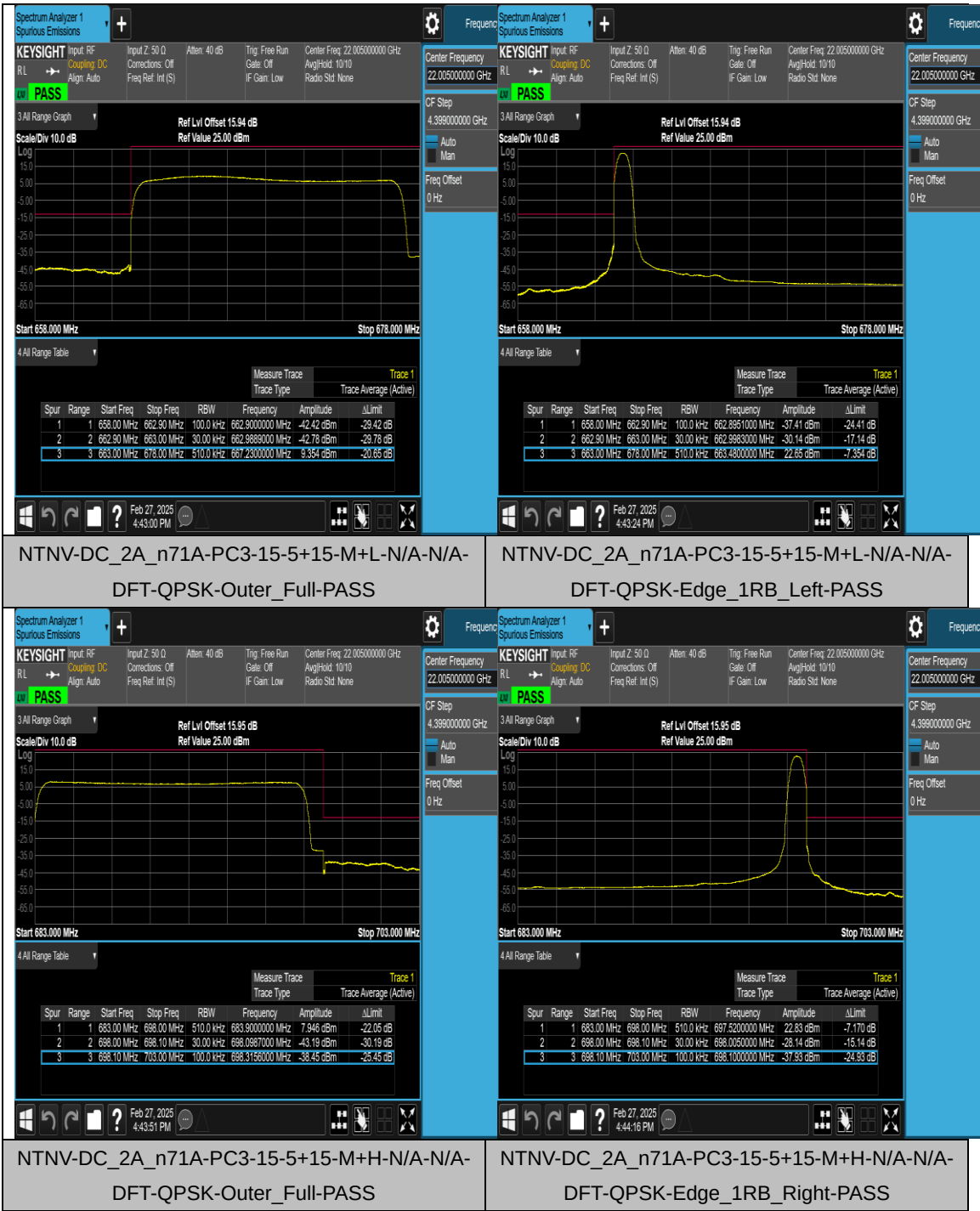
Test Result

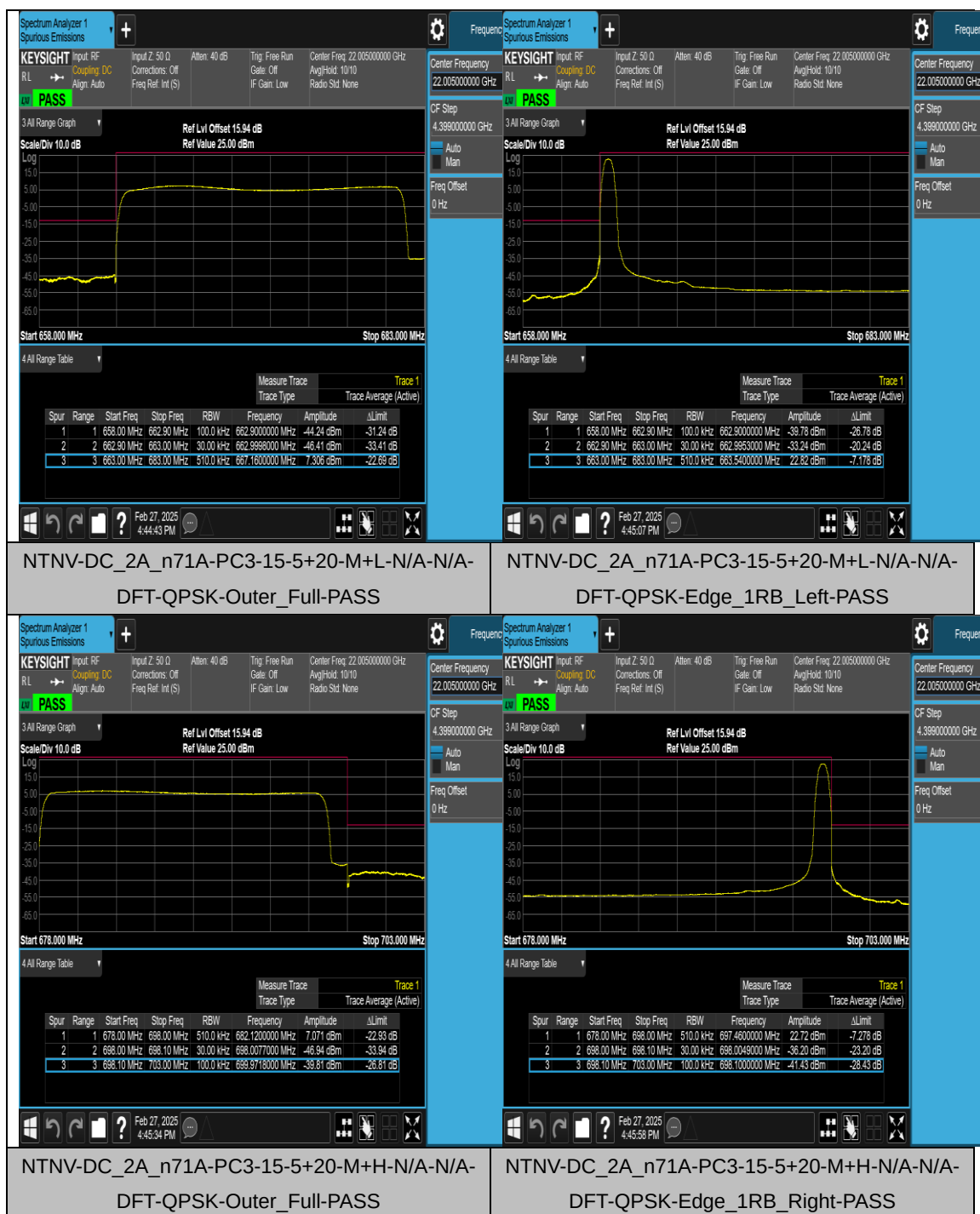
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Margin	Verdict
DC_2A_n71A	15	5+5	DFT-QPSK	M+L	Outer_Full	-34.92	21.92	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Left	-24.84	11.84	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+H	Outer_Full	-30.12	17.12	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Right	-24.64	11.64	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+L	Outer_Full	-40.67	27.67	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Left	-26.10	13.10	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+H	Outer_Full	-37.52	24.52	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Right	-26.92	13.92	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+L	Outer_Full	-42.42	29.42	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Left	-30.14	17.14	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+H	Outer_Full	-38.45	25.45	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Right	-28.14	15.14	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+L	Outer_Full	-44.24	31.24	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	-33.24	20.24	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+H	Outer_Full	-39.81	26.81	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	-36.20	23.20	PASS

Test Graphs









Appendix E: Conducted Spurious Emission for NSA

Test Result

Band	SC S	Band width	Modulation	Chan nel	RB Config	StartFreq	StopFreq	Result	Limit	Verdict
DC_2A_n71A	15	5+5	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-65.81	-33	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+L	Outer_Full	0.15	30	-63.00	-23	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+L	Outer_Full	30	1000	-47.54	-13	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+L	Outer_Full	1000	3000	-38.95	-13	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+L	Outer_Full	3000	12000	-45.83	-13	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+L	Outer_Full	12000	20000	-47.45	-13	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-64.13	-33	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-63.12	-23	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-55.27	-13	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-38.75	-13	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Left	3000	12000	-45.99	-13	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Left	12000	20000	-47.44	-13	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-64.48	-33	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-62.08	-23	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-55.21	-13	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-34.09	-13	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Right	3000	12000	-45.92	-13	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Right	12000	20000	-47.36	-13	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-67.32	-33	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+M	Outer_Full	0.15	30	-67.09	-23	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+M	Outer_Full	30	1000	-55.04	-13	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+M	Outer_Full	1000	3000	-37.44	-13	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+M	Outer_Full	3000	12000	-46.09	-13	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+M	Outer_Full	12000	20000	-47.24	-13	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-66.07	-33	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-62.94	-23	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-55.18	-13	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-36.90	-13	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Left	3000	12000	-45.99	-13	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Left	12000	20000	-47.46	-13	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-66.09	-33	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-62.06	-23	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-54.94	-13	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-38.46	-13	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Right	3000	12000	-46.01	-13	PASS

DC_2A_n71A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Right	12000	20000	-47.30	-13	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-65.11	-33	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+H	Outer_Full	0.15	30	-63.70	-23	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+H	Outer_Full	30	1000	-40.93	-13	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+H	Outer_Full	1000	3000	-37.10	-13	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+H	Outer_Full	3000	12000	-45.97	-13	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+H	Outer_Full	12000	20000	-47.37	-13	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-65.94	-33	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-62.67	-23	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-54.97	-13	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-36.35	-13	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Left	3000	12000	-46.03	-13	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Left	12000	20000	-47.39	-13	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-66.67	-33	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-61.07	-23	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-55.20	-13	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-37.48	-13	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Right	3000	12000	-45.73	-13	PASS
DC_2A_n71A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Right	12000	20000	-47.49	-13	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-67.16	-33	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+L	Outer_Full	0.15	30	-60.27	-23	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+L	Outer_Full	30	1000	-42.28	-13	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+L	Outer_Full	1000	3000	-34.52	-13	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+L	Outer_Full	3000	12000	-45.94	-13	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+L	Outer_Full	12000	20000	-47.51	-13	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-66.20	-33	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-62.48	-23	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-54.84	-13	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-38.50	-13	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Left	3000	12000	-45.97	-13	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Left	12000	20000	-47.40	-13	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-66.27	-33	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-64.35	-23	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-54.87	-13	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-35.99	-13	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Right	3000	12000	-46.08	-13	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Right	12000	20000	-47.48	-13	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-65.59	-33	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+M	Outer_Full	0.15	30	-62.99	-23	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+M	Outer_Full	30	1000	-53.52	-13	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+M	Outer_Full	1000	3000	-34.65	-13	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+M	Outer_Full	3000	12000	-46.01	-13	PASS

DC_2A_n71A	15	5+10	DFT-QPSK	M+M	Outer_Full	12000	20000	-47.38	-13	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-65.10	-33	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-61.70	-23	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-55.09	-13	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-34.61	-13	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Left	3000	12000	-45.97	-13	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Left	12000	20000	-47.19	-13	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-66.68	-33	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-61.70	-23	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-55.01	-13	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-38.90	-13	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Right	3000	12000	-45.96	-13	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Right	12000	20000	-47.38	-13	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-66.24	-33	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+H	Outer_Full	0.15	30	-65.05	-23	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+H	Outer_Full	30	1000	-35.52	-13	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+H	Outer_Full	1000	3000	-38.90	-13	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+H	Outer_Full	3000	12000	-45.94	-13	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+H	Outer_Full	12000	20000	-47.17	-13	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-64.54	-33	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-62.53	-23	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-55.07	-13	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-38.81	-13	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Left	3000	12000	-46.05	-13	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Left	12000	20000	-47.43	-13	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-62.58	-33	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-62.40	-23	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-54.81	-13	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-38.81	-13	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Right	3000	12000	-46.02	-13	PASS
DC_2A_n71A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Right	12000	20000	-47.42	-13	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-66.02	-33	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+L	Outer_Full	0.15	30	-65.39	-23	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+L	Outer_Full	30	1000	-44.28	-13	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+L	Outer_Full	1000	3000	-38.77	-13	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+L	Outer_Full	3000	12000	-46.07	-13	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+L	Outer_Full	12000	20000	-47.33	-13	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-63.67	-33	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-63.19	-23	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-55.11	-13	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-24.39	-13	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Left	3000	12000	-46.03	-13	PASS

DC_2A_n71A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Left	12000	20000	-47.44	-13	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-64.56	-33	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-63.71	-23	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-55.00	-13	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-38.88	-13	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Right	3000	12000	-46.08	-13	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Right	12000	20000	-47.45	-13	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-66.75	-33	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+M	Outer_Full	0.15	30	-62.57	-23	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+M	Outer_Full	30	1000	-46.49	-13	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+M	Outer_Full	1000	3000	-38.87	-13	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+M	Outer_Full	3000	12000	-45.84	-13	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+M	Outer_Full	12000	20000	-47.46	-13	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-62.81	-33	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-64.07	-23	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-55.19	-13	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-38.93	-13	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Left	3000	12000	-45.88	-13	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Left	12000	20000	-47.35	-13	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-64.22	-33	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-63.43	-23	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-55.31	-13	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-38.94	-13	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Right	3000	12000	-46.02	-13	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Right	12000	20000	-47.38	-13	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-66.35	-33	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+H	Outer_Full	0.15	30	-67.64	-23	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+H	Outer_Full	30	1000	-39.23	-13	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+H	Outer_Full	1000	3000	-38.95	-13	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+H	Outer_Full	3000	12000	-45.86	-13	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+H	Outer_Full	12000	20000	-47.39	-13	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-66.09	-33	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-62.84	-23	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-55.40	-13	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-38.96	-13	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Left	3000	12000	-45.85	-13	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Left	12000	20000	-47.42	-13	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-64.76	-33	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-65.46	-23	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-54.81	-13	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-38.92	-13	PASS
DC_2A_n71A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Right	3000	12000	-46.05	-13	PASS

DC_2A_n71A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Right	12000	20000	-47.34	-13	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-66.26	-33	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+L	Outer_Full	0.15	30	-59.10	-23	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+L	Outer_Full	30	1000	-44.19	-13	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+L	Outer_Full	1000	3000	-38.97	-13	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+L	Outer_Full	3000	12000	-46.04	-13	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+L	Outer_Full	12000	20000	-47.48	-13	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-65.09	-33	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-60.60	-23	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-55.31	-13	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-15.54	-13	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	3000	12000	-45.75	-13	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	12000	20000	-47.54	-13	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-65.79	-33	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-63.39	-23	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-55.37	-13	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-38.96	-13	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Right	3000	12000	-45.83	-13	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Right	12000	20000	-47.37	-13	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-66.62	-33	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+M	Outer_Full	0.15	30	-64.53	-23	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+M	Outer_Full	30	1000	-42.38	-13	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+M	Outer_Full	1000	3000	-38.91	-13	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+M	Outer_Full	3000	12000	-45.95	-13	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+M	Outer_Full	12000	20000	-47.45	-13	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-64.28	-33	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-63.79	-23	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-54.97	-13	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-38.94	-13	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Left	3000	12000	-46.07	-13	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Left	12000	20000	-47.43	-13	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-65.11	-33	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-61.35	-23	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-55.25	-13	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-38.89	-13	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Right	3000	12000	-45.96	-13	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Right	12000	20000	-47.30	-13	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-65.87	-33	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+H	Outer_Full	0.15	30	-67.11	-23	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+H	Outer_Full	30	1000	-39.69	-13	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+H	Outer_Full	1000	3000	-38.98	-13	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+H	Outer_Full	3000	12000	-46.06	-13	PASS

DC_2A_n71A	15	5+20	DFT-QPSK	M+H	Outer_Full	12000	20000	-47.52	-13	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-66.42	-33	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-63.40	-23	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-55.03	-13	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-38.88	-13	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Left	3000	12000	-46.00	-13	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Left	12000	20000	-47.37	-13	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-65.75	-33	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-62.15	-23	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-54.66	-13	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-38.95	-13	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	3000	12000	-45.93	-13	PASS
DC_2A_n71A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	12000	20000	-47.39	-13	PASS