Appendix for 41A_n77A(3450-3550MHz)

Product Name: TITAN 5400
Model No: G5G-X62O

Appendix A: Average Power Output Data for NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_41A_n77A-3 450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	24.63	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+10	DFT-QPSK	M+L	Inner_Full	25.63	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	24.70	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	24.52	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+10	DFT-16QAM	M+L	Outer_Full	23.57	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+10	DFT-16QAM	M+L	Inner_Full	24.65	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	23.27	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	23.56	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	25.18	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+10	DFT-QPSK	M+M	Inner_Full	26.17	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	25.14	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	25.17	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+10	DFT-16QAM	M+M	Outer_Full	24.19	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+10	DFT-16QAM	M+M	Inner_Full	25.15	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	24.10	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	24.42	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	25.24	PC2	PASS
DC_41A_n77A-3	30	5+10	DFT-QPSK	M+H	Inner_Full	26.22	PC2	PASS

450-3550								
DC_41A_n77A-3 450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	25.08	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	25.15	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+10	DFT-16QAM	M+H	Outer_Full	24.10	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+10	DFT-16QAM	M+H	Inner_Full	25.08	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	24.05	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	24.07	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	24.80	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+15	DFT-QPSK	M+L	Inner_Full	25.82	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	24.93	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	25.26	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+15	DFT-16QAM	M+L	Outer_Full	23.83	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+15	DFT-16QAM	M+L	Inner_Full	24.71	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	23.61	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	24.12	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	25.37	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+15	DFT-QPSK	M+M	Inner_Full	26.37	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	25.24	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	25.39	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+15	DFT-16QAM	M+M	Outer_Full	24.38	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+15	DFT-16QAM	M+M	Inner_Full	25.35	PC2	PASS
DC_41A_n77A-3	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	24.25	PC2	PASS

450-3550								
DC_41A_n77A-3 450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	24.64	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	25.22	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+15	DFT-QPSK	M+H	Inner_Full	26.25	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	25.29	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	25.21	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+15	DFT-16QAM	M+H	Outer_Full	24.18	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+15	DFT-16QAM	M+H	Inner_Full	25.29	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	24.55	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	24.47	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	24.90	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+20	DFT-QPSK	M+L	Inner_Full	25.84	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	24.82	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	25.12	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	23.81	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+20	DFT-16QAM	M+L	Inner_Full	24.73	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	23.63	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	23.67	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	25.40	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+20	DFT-QPSK	M+M	Inner_Full	26.35	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	25.14	PC2	PASS
DC_41A_n77A-3	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	25.25	PC2	PASS

450-3550								
DC_41A_n77A-3 450-3550	30	5+20	DFT-16QAM	M+M	Outer_Full	24.40	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+20	DFT-16QAM	M+M	Inner_Full	25.39	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	24.13	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	24.52	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	25.35	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+20	DFT-QPSK	M+H	Inner_Full	26.34	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	25.07	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	25.35	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	24.26	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+20	DFT-16QAM	M+H	Inner_Full	25.41	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	24.41	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	24.35	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	25.08	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+30	DFT-QPSK	M+L	Inner_Full	26.01	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	24.75	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	25.36	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+30	DFT-16QAM	M+L	Outer_Full	24.01	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+30	DFT-16QAM	M+L	Inner_Full	24.93	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	23.71	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	24.43	PC2	PASS
DC_41A_n77A-3	30	5+30	DFT-QPSK	M+M	Outer_Full	25.37	PC2	PASS

450-3550								
DC_41A_n77A-3 450-3550	30	5+30	DFT-QPSK	M+M	Inner_Full	26.28	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	25.13	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	25.40	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+30	DFT-16QAM	M+M	Outer_Full	24.39	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+30	DFT-16QAM	M+M	Inner_Full	25.36	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	24.02	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	24.22	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	25.25	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+30	DFT-QPSK	M+H	Inner_Full	26.12	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	25.39	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	25.21	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+30	DFT-16QAM	M+H	Outer_Full	24.23	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+30	DFT-16QAM	M+H	Inner_Full	25.13	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	24.28	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	24.26	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	25.03	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+40	DFT-QPSK	M+L	Inner_Full	26.02	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	24.80	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	25.17	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+40	DFT-16QAM	M+L	Outer_Full	24.05	PC2	PASS
DC_41A_n77A-3	30	5+40	DFT-16QAM	M+L	Inner_Full	25.00	PC2	PASS

450-3550								
DC_41A_n77A-3 450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	23.99	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	24.27	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	25.44	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+40	DFT-QPSK	M+M	Inner_Full	26.32	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	25.43	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	25.62	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+40	DFT-16QAM	M+M	Outer_Full	24.45	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+40	DFT-16QAM	M+M	Inner_Full	25.32	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	24.47	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	24.39	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	25.34	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+40	DFT-QPSK	M+H	Inner_Full	26.18	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	25.30	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	25.43	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+40	DFT-16QAM	M+H	Outer_Full	24.21	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+40	DFT-16QAM	M+H	Inner_Full	25.22	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	24.25	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	24.03	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	25.12	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+50	DFT-QPSK	M+L	Inner_Full	26.13	PC2	PASS
DC_41A_n77A-3	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	24.67	PC2	PASS

450-3550								
DC_41A_n77A-3 450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	24.90	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+50	DFT-16QAM	M+L	Outer_Full	23.94	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+50	DFT-16QAM	M+L	Inner_Full	25.09	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	23.54	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	23.87	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	25.18	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+50	DFT-QPSK	M+M	Inner_Full	26.18	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	25.36	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	24.98	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	24.15	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+50	DFT-16QAM	M+M	Inner_Full	25.22	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	24.51	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	23.82	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	25.02	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+50	DFT-QPSK	M+H	Inner_Full	26.02	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	25.13	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	25.02	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+50	DFT-16QAM	M+H	Outer_Full	24.01	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+50	DFT-16QAM	M+H	Inner_Full	25.07	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	24.19	PC2	PASS
DC_41A_n77A-3	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	24.06	PC2	PASS

450-3550								
DC_41A_n77A-3 450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	25.18	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+60	DFT-QPSK	M+L	Inner_Full	26.20	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	24.56	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	25.15	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+60	DFT-16QAM	M+L	Outer_Full	24.10	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+60	DFT-16QAM	M+L	Inner_Full	25.17	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	23.60	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	24.26	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	25.22	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+60	DFT-QPSK	M+M	Inner_Full	26.16	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	25.15	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	25.14	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+60	DFT-16QAM	M+M	Outer_Full	24.25	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+60	DFT-16QAM	M+M	Inner_Full	25.17	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	24.03	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	23.84	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	25.14	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+60	DFT-QPSK	M+H	Inner_Full	26.25	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	24.98	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	25.10	PC2	PASS
DC_41A_n77A-3	30	5+60	DFT-16QAM	M+H	Outer_Full	24.12	PC2	PASS

450-3550								
DC_41A_n77A-3 450-3550	30	5+60	DFT-16QAM	M+H	Inner_Full	25.16	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	24.17	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	24.08	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	24.96	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+70	DFT-QPSK	M+L	Inner_Full	26.03	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	24.47	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	25.10	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+70	DFT-16QAM	M+L	Outer_Full	23.88	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+70	DFT-16QAM	M+L	Inner_Full	25.02	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	23.37	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	24.05	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	25.21	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+70	DFT-QPSK	M+M	Inner_Full	26.09	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	24.93	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	25.05	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+70	DFT-16QAM	M+M	Outer_Full	24.12	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+70	DFT-16QAM	M+M	Inner_Full	25.13	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	23.68	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	23.92	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	25.22	PC2	PASS
DC_41A_n77A-3	30	5+70	DFT-QPSK	M+H	Inner_Full	26.06	PC2	PASS

450-3550								
DC_41A_n77A-3 450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	25.12	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	25.16	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+70	DFT-16QAM	M+H	Outer_Full	24.15	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+70	DFT-16QAM	M+H	Inner_Full	25.15	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	24.02	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	24.27	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	25.02	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+80	DFT-QPSK	M+L	Inner_Full	26.07	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	24.46	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	24.98	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+80	DFT-16QAM	M+L	Outer_Full	24.03	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+80	DFT-16QAM	M+L	Inner_Full	25.18	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	23.34	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	23.96	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	25.01	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+80	DFT-QPSK	M+M	Inner_Full	26.13	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	24.56	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	25.08	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+80	DFT-16QAM	M+M	Outer_Full	24.01	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+80	DFT-16QAM	M+M	Inner_Full	25.16	PC2	PASS
DC_41A_n77A-3	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	23.76	PC2	PASS

450-3550								
DC_41A_n77A-3 450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	24.01	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	25.03	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+80	DFT-QPSK	M+H	Inner_Full	26.06	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	24.93	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	25.05	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+80	DFT-16QAM	M+H	Outer_Full	24.06	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+80	DFT-16QAM	M+H	Inner_Full	25.01	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	23.94	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	24.02	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	25.04	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+90	DFT-QPSK	M+L	Inner_Full	26.09	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	24.43	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	25.04	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+90	DFT-16QAM	M+L	Outer_Full	24.03	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+90	DFT-16QAM	M+L	Inner_Full	25.09	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	23.17	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	24.33	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	25.07	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+90	DFT-QPSK	M+M	Inner_Full	26.08	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	24.41	PC2	PASS
DC_41A_n77A-3	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	25.18	PC2	PASS

450-3550								
DC_41A_n77A-3 450-3550	30	5+90	DFT-16QAM	M+M	Outer_Full	24.05	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+90	DFT-16QAM	M+M	Inner_Full	25.15	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	23.47	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	24.10	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	25.02	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+90	DFT-QPSK	M+H	Inner_Full	26.09	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	24.57	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	25.16	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+90	DFT-16QAM	M+H	Outer_Full	24.03	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+90	DFT-16QAM	M+H	Inner_Full	25.03	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	23.59	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	24.12	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	25.06	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+100	DFT-QPSK	M+L	Inner_Full	26.12	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	24.38	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	24.96	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+100	DFT-16QAM	M+L	Outer_Full	24.06	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+100	DFT-16QAM	M+L	Inner_Full	25.18	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	23.61	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	24.27	PC2	PASS
DC_41A_n77A-3	30	5+100	DFT-QPSK	M+M	Outer_Full	25.08	PC2	PASS

450-3550								
DC_41A_n77A-3 450-3550	30	5+100	DFT-QPSK	M+M	Inner_Full	26.21	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	24.31	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	25.02	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+100	DFT-16QAM	M+M	Outer_Full	24.00	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+100	DFT-16QAM	M+M	Inner_Full	25.18	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	23.54	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	23.90	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+100	DFT-QPSK	M+H	Outer_Full	25.09	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+100	DFT-QPSK	M+H	Inner_Full	26.13	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	24.35	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	24.96	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+100	DFT-16QAM	M+H	Outer_Full	24.05	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+100	DFT-16QAM	M+H	Inner_Full	25.17	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	23.31	PC2	PASS
DC_41A_n77A-3 450-3550	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	23.83	PC2	PASS

Appendix B: Peak-to-Average Ratio for NSA

Peak-to-Average Ratio(CCDF)

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
DC_41A_n77A-3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	5.19	≤13	PASS
DC_41A_n77A-3450-3550	30	5+10	DFT-16QAM	M+L	Outer_Full	6.00	≤13	PASS
DC_41A_n77A-3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	5.46	≤13	PASS
DC_41A_n77A-3450-3550	30	5+10	DFT-16QAM	M+M	Outer_Full	6.15	≤13	PASS
DC_41A_n77A-3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	5.30	≤13	PASS
DC_41A_n77A-3450-3550	30	5+10	DFT-16QAM	M+H	Outer_Full	5.85	≤13	PASS
DC_41A_n77A-3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	4.95	≤13	PASS
DC_41A_n77A-3450-3550	30	5+15	DFT-16QAM	M+L	Outer_Full	5.87	≤13	PASS
DC_41A_n77A-3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	5.29	≤13	PASS
DC_41A_n77A-3450-3550	30	5+15	DFT-16QAM	M+M	Outer_Full	6.08	≤13	PASS
DC_41A_n77A-3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	5.29	≤13	PASS
DC_41A_n	30	5+15	DFT-16QAM	M+H	Outer_Full	5.97	≤13	PASS

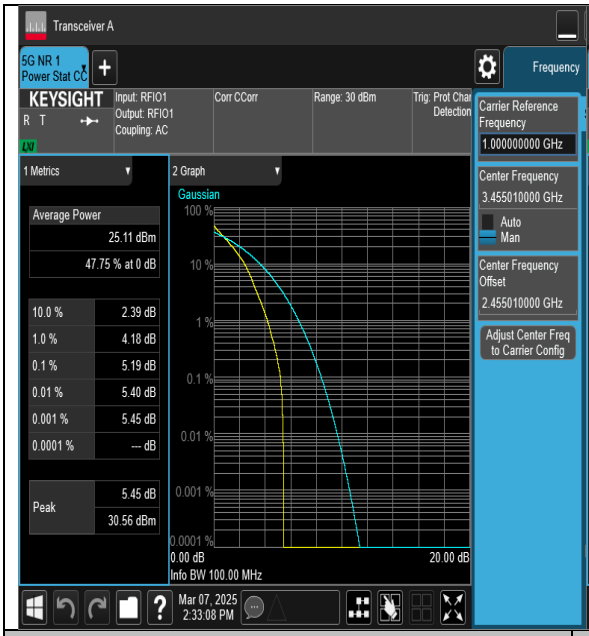
77A-3450-3550								
DC_41A_n77A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	5.32	≤13	PASS
DC_41A_n77A-3450-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	5.96	≤13	PASS
DC_41A_n77A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	5.32	≤13	PASS
DC_41A_n77A-3450-3550	30	5+20	DFT-16QAM	M+M	Outer_Full	5.94	≤13	PASS
DC_41A_n77A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	5.14	≤13	PASS
DC_41A_n77A-3450-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	5.99	≤13	PASS
DC_41A_n77A-3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	5.39	≤13	PASS
DC_41A_n77A-3450-3550	30	5+30	DFT-16QAM	M+L	Outer_Full	5.99	≤13	PASS
DC_41A_n77A-3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	5.19	≤13	PASS
DC_41A_n77A-3450-3550	30	5+30	DFT-16QAM	M+M	Outer_Full	5.93	≤13	PASS
DC_41A_n77A-3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	5.13	≤13	PASS
DC_41A_n77A-3450-3550	30	5+30	DFT-16QAM	M+H	Outer_Full	6.01	≤13	PASS
DC_41A_n77A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	5.24	≤13	PASS
DC_41A_n	30	5+40	DFT-16QAM	M+L	Outer_Full	6.07	≤13	PASS

77A-3450-3550								
DC_41A_n77A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	5.17	≤13	PASS
DC_41A_n77A-3450-3550	30	5+40	DFT-16QAM	M+M	Outer_Full	5.94	≤13	PASS
DC_41A_n77A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	5.12	≤13	PASS
DC_41A_n77A-3450-3550	30	5+40	DFT-16QAM	M+H	Outer_Full	5.94	≤13	PASS
DC_41A_n77A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	5.36	≤13	PASS
DC_41A_n77A-3450-3550	30	5+50	DFT-16QAM	M+L	Outer_Full	6.05	≤13	PASS
DC_41A_n77A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	5.19	≤13	PASS
DC_41A_n77A-3450-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	6.11	≤13	PASS
DC_41A_n77A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	5.22	≤13	PASS
DC_41A_n77A-3450-3550	30	5+50	DFT-16QAM	M+H	Outer_Full	5.88	≤13	PASS
DC_41A_n77A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	5.30	≤13	PASS
DC_41A_n77A-3450-3550	30	5+60	DFT-16QAM	M+L	Outer_Full	6.09	≤13	PASS
DC_41A_n77A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	5.15	≤13	PASS
DC_41A_n	30	5+60	DFT-16QAM	M+M	Outer_Full	5.97	≤13	PASS

77A-3450-3550								
DC_41A_n77A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	5.30	≤13	PASS
DC_41A_n77A-3450-3550	30	5+60	DFT-16QAM	M+H	Outer_Full	6.08	≤13	PASS
DC_41A_n77A-3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	5.42	≤13	PASS
DC_41A_n77A-3450-3550	30	5+70	DFT-16QAM	M+L	Outer_Full	6.02	≤13	PASS
DC_41A_n77A-3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	5.41	≤13	PASS
DC_41A_n77A-3450-3550	30	5+70	DFT-16QAM	M+M	Outer_Full	6.13	≤13	PASS
DC_41A_n77A-3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	5.80	≤13	PASS
DC_41A_n77A-3450-3550	30	5+70	DFT-16QAM	M+H	Outer_Full	6.16	≤13	PASS
DC_41A_n77A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	5.37	≤13	PASS
DC_41A_n77A-3450-3550	30	5+80	DFT-16QAM	M+L	Outer_Full	6.15	≤13	PASS
DC_41A_n77A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	5.34	≤13	PASS
DC_41A_n77A-3450-3550	30	5+80	DFT-16QAM	M+M	Outer_Full	6.09	≤13	PASS
DC_41A_n77A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	5.36	≤13	PASS
DC_41A_n	30	5+80	DFT-16QAM	M+H	Outer_Full	6.10	≤13	PASS

77A-3450-3550								
DC_41A_n77A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	5.37	≤13	PASS
DC_41A_n77A-3450-3550	30	5+90	DFT-16QAM	M+L	Outer_Full	6.10	≤13	PASS
DC_41A_n77A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	5.29	≤13	PASS
DC_41A_n77A-3450-3550	30	5+90	DFT-16QAM	M+M	Outer_Full	6.11	≤13	PASS
DC_41A_n77A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	5.37	≤13	PASS
DC_41A_n77A-3450-3550	30	5+90	DFT-16QAM	M+H	Outer_Full	6.09	≤13	PASS
DC_41A_n77A-3450-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	5.22	≤13	PASS
DC_41A_n77A-3450-3550	30	5+100	DFT-16QAM	M+L	Outer_Full	6.10	≤13	PASS
DC_41A_n77A-3450-3550	30	5+100	DFT-QPSK	M+M	Outer_Full	5.37	≤13	PASS
DC_41A_n77A-3450-3550	30	5+100	DFT-16QAM	M+M	Outer_Full	6.15	≤13	PASS
DC_41A_n77A-3450-3550	30	5+100	DFT-QPSK	M+H	Outer_Full	5.33	≤13	PASS
DC_41A_n77A-3450-3550	30	5+100	DFT-16QAM	M+H	Outer_Full	6.13	≤13	PASS

Test Graphs



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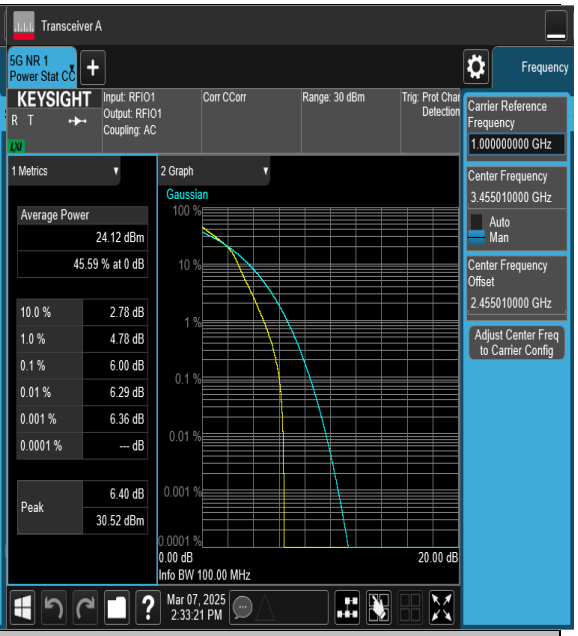

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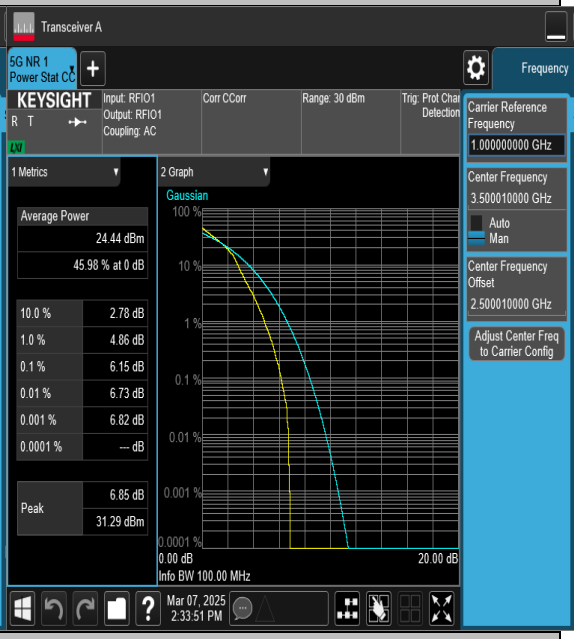

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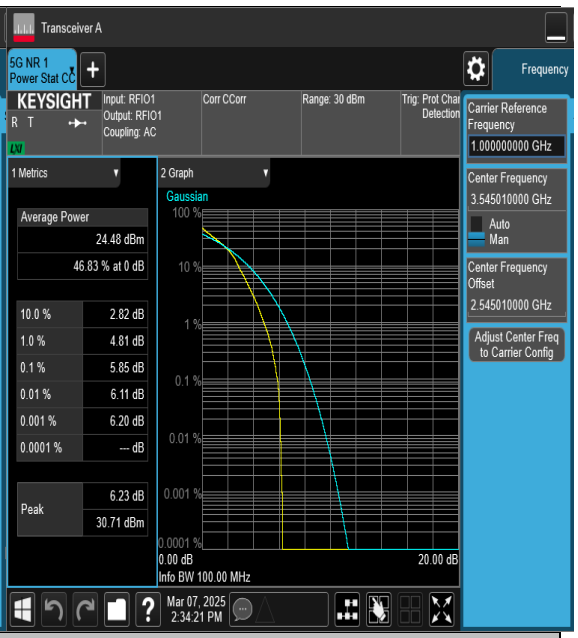

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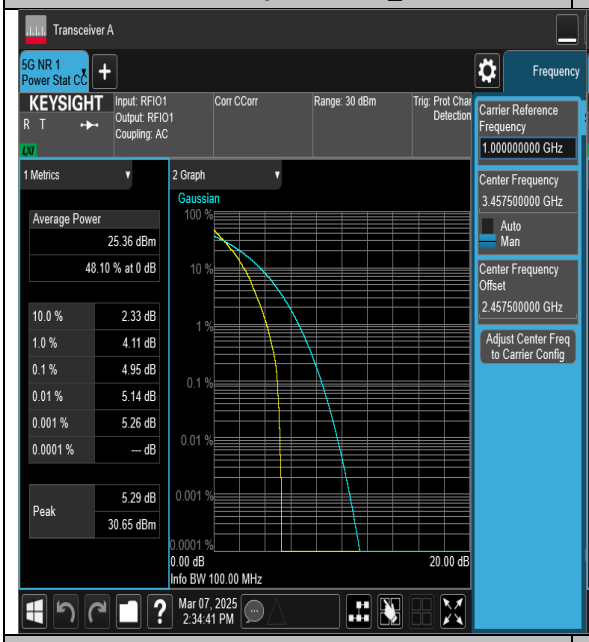

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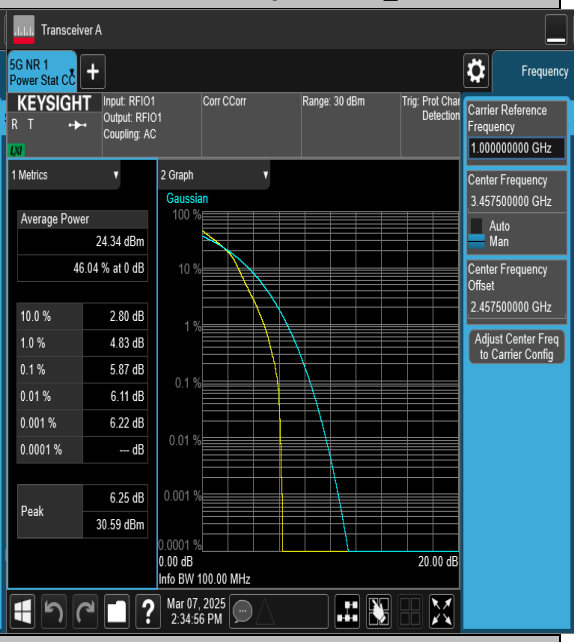

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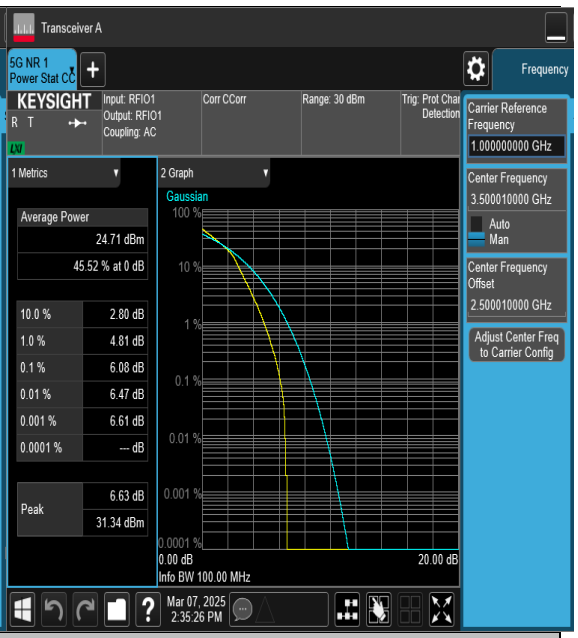

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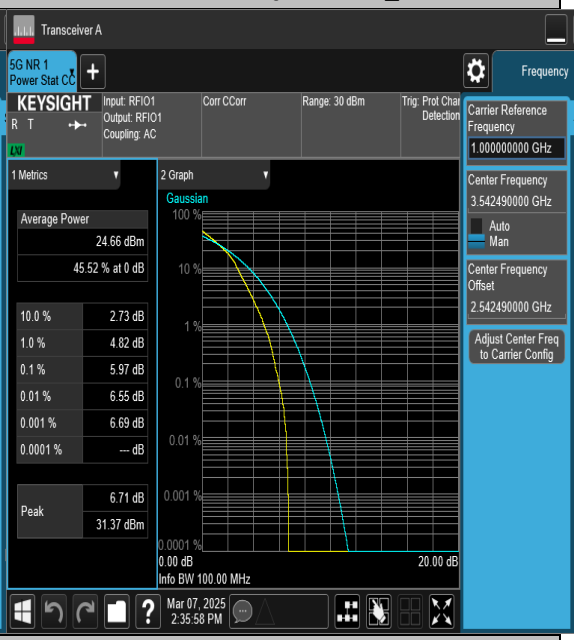

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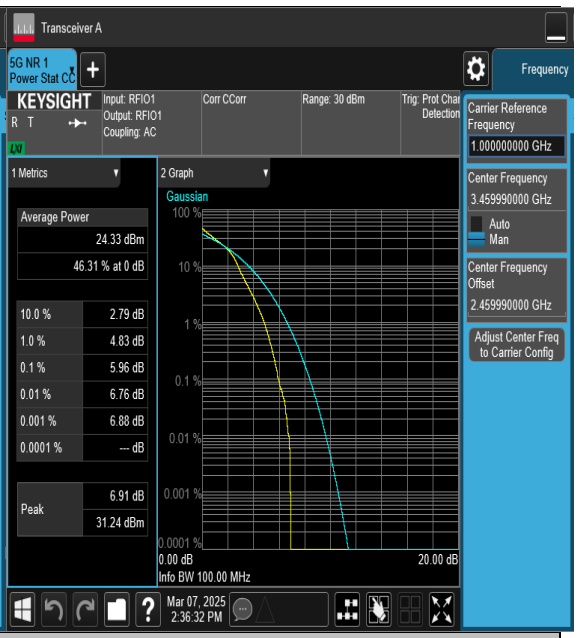

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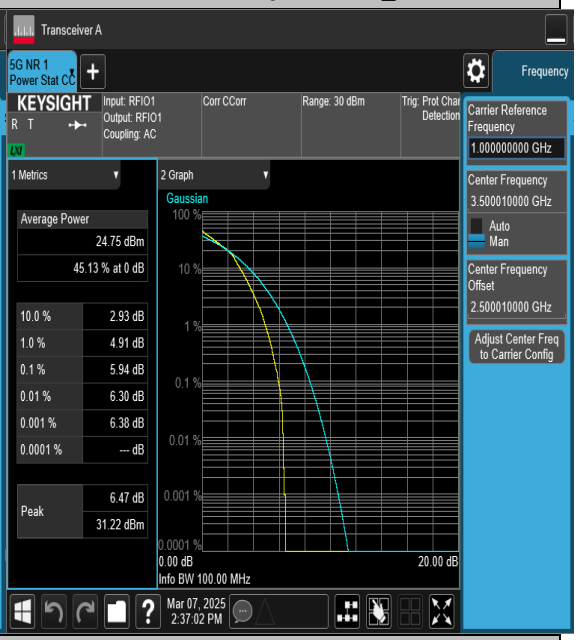

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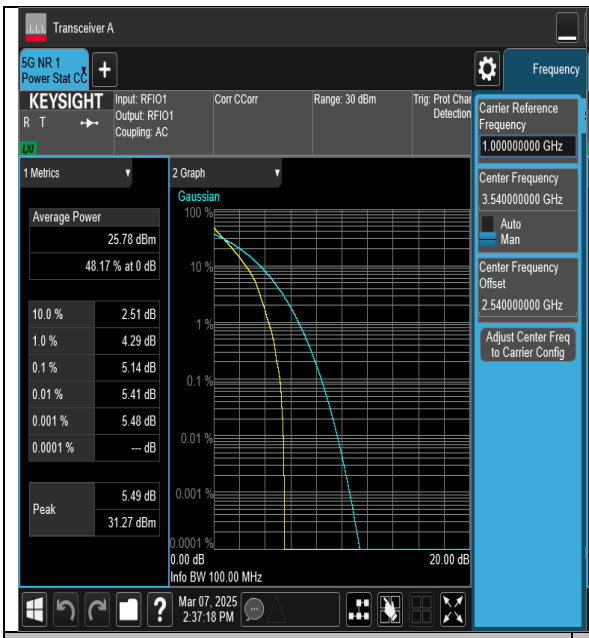

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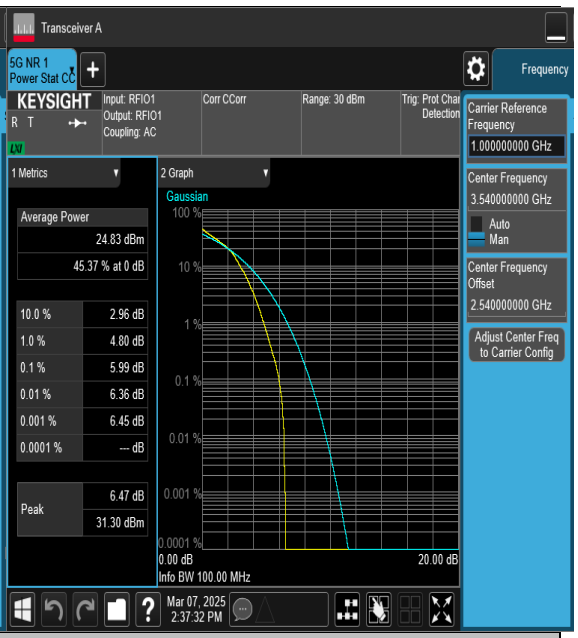

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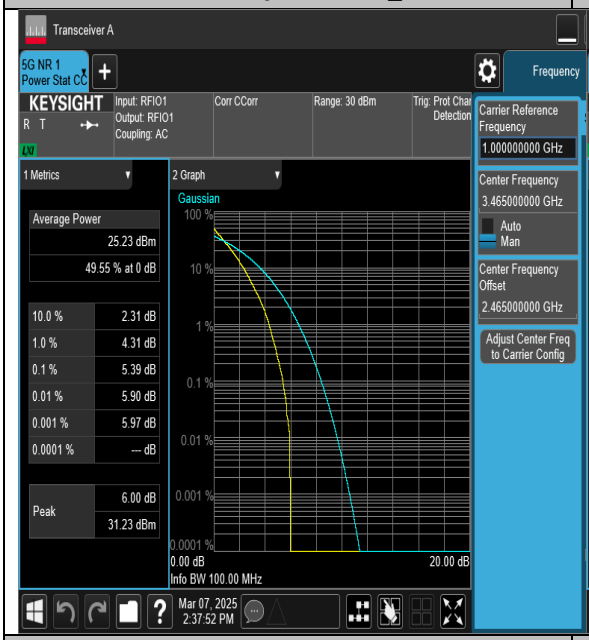

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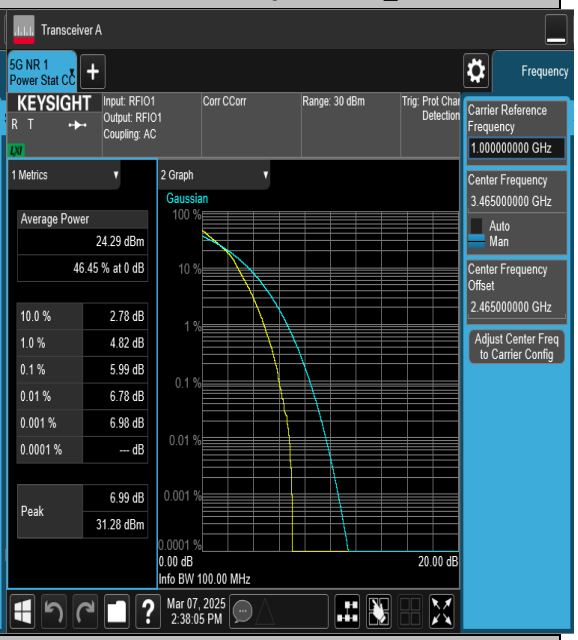

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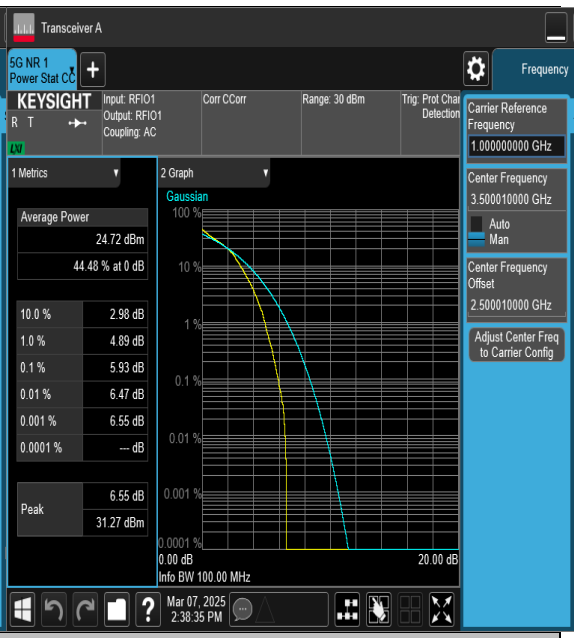

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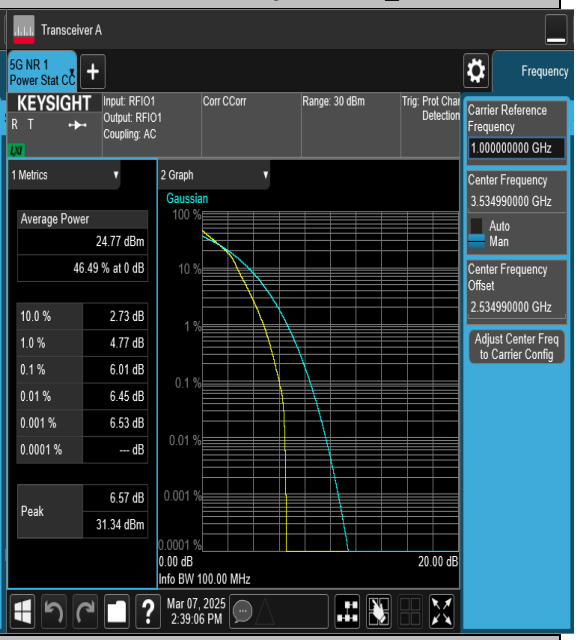

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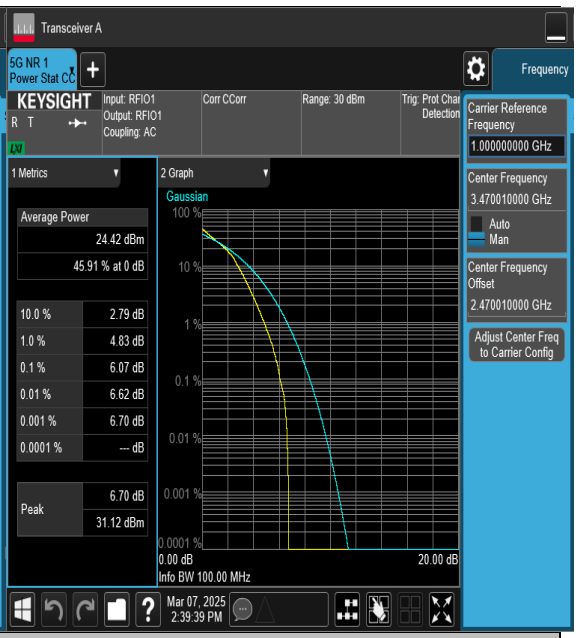

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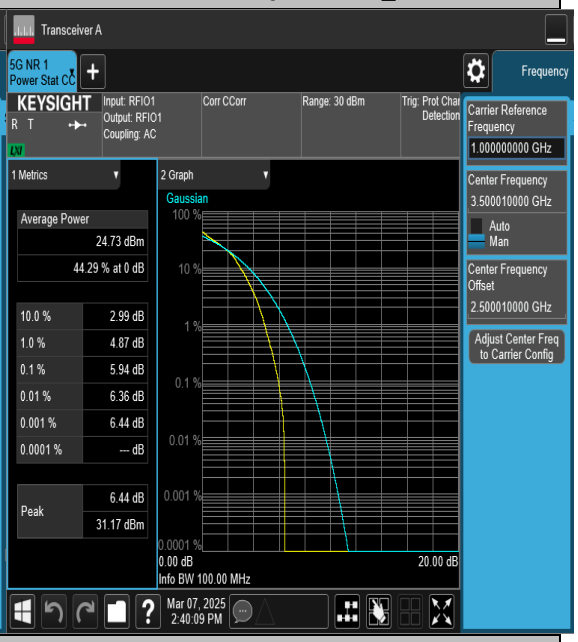

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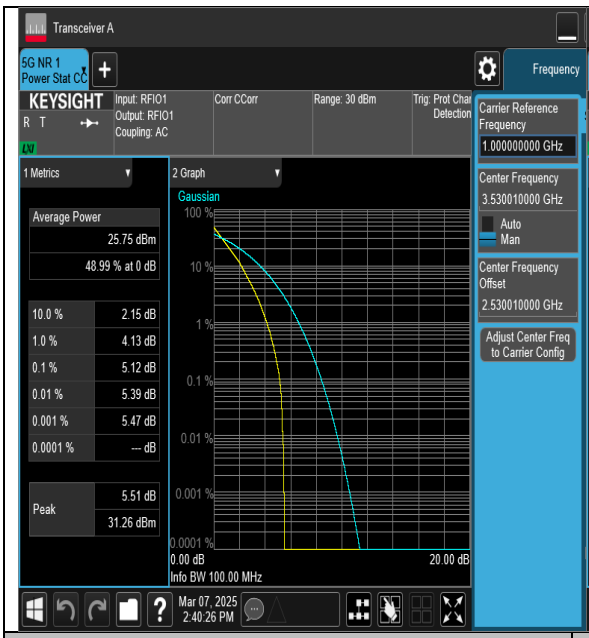

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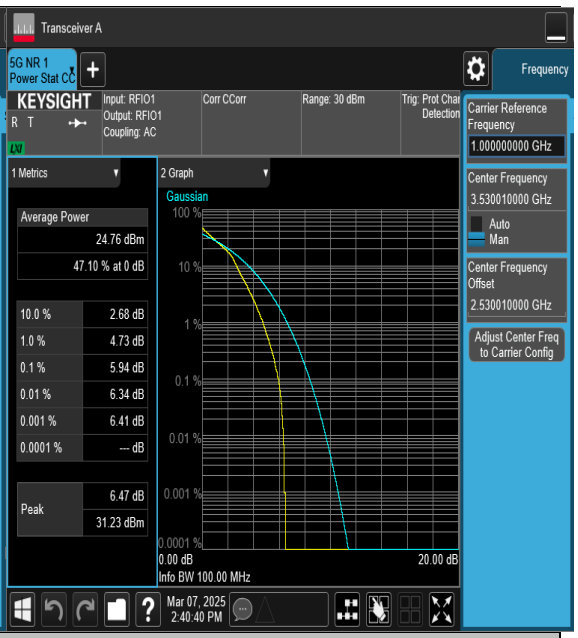

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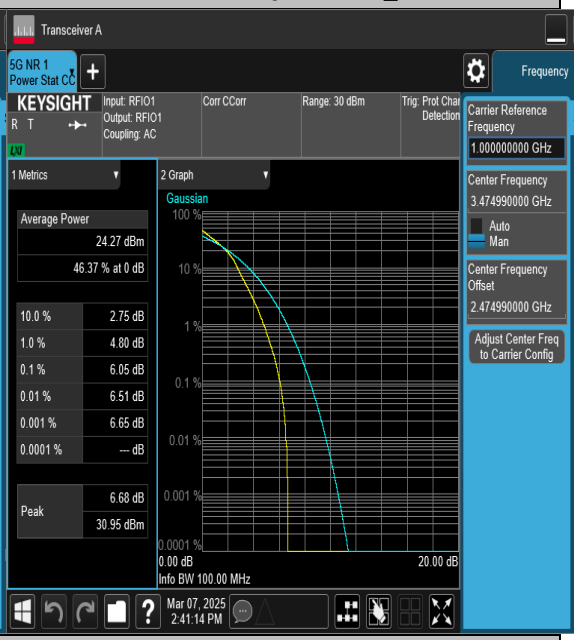

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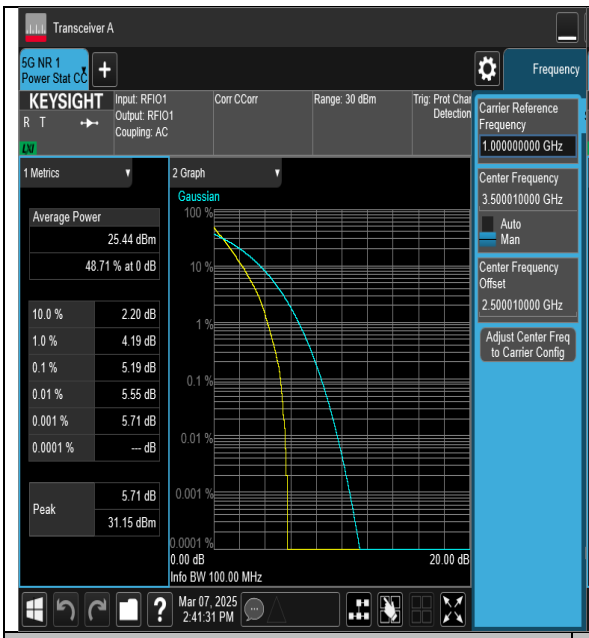

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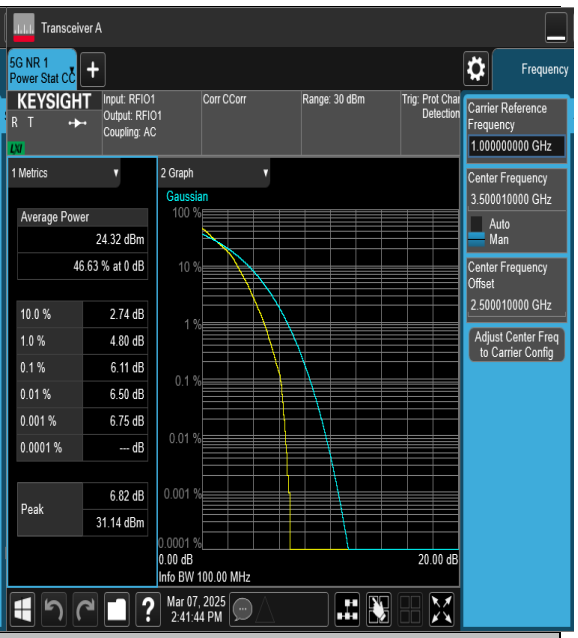

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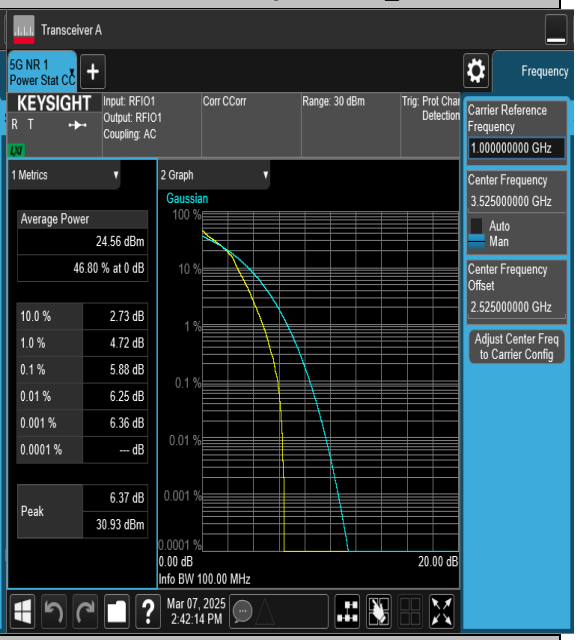

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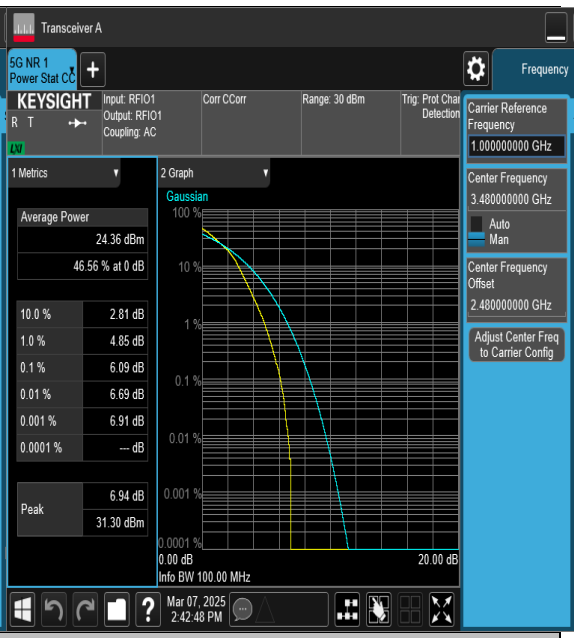

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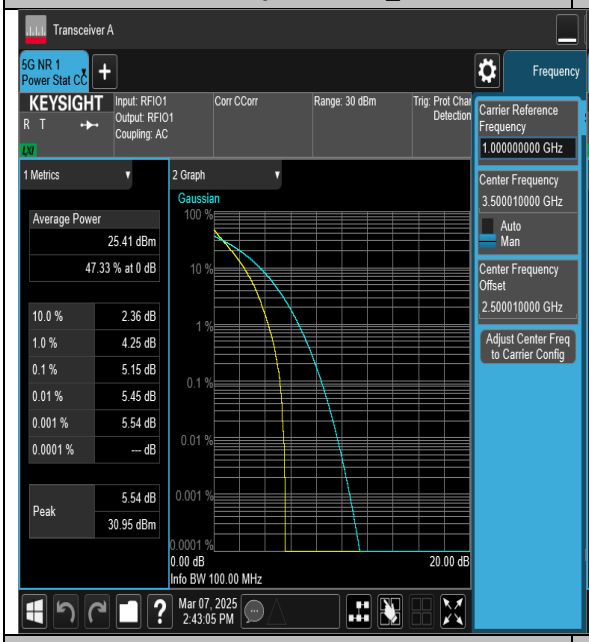

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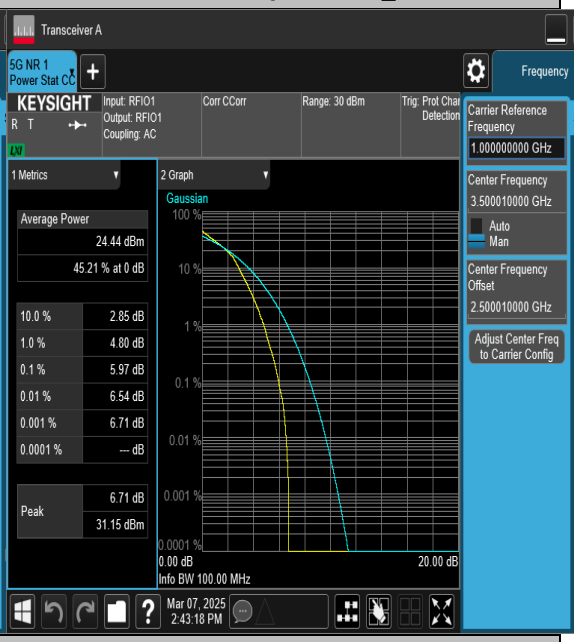

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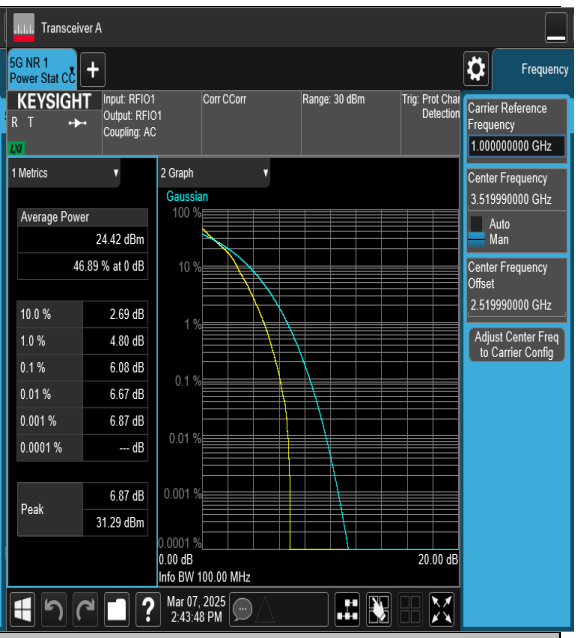

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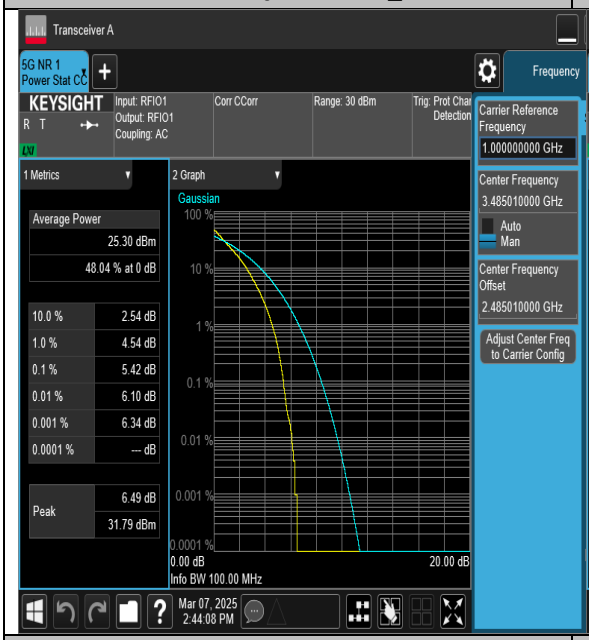

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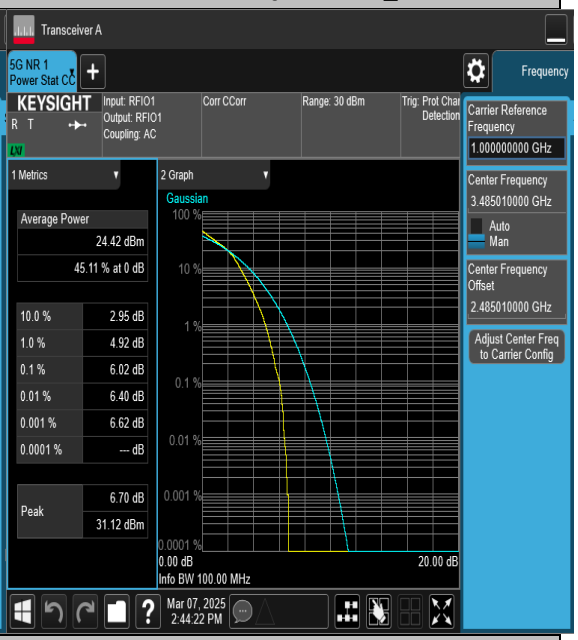

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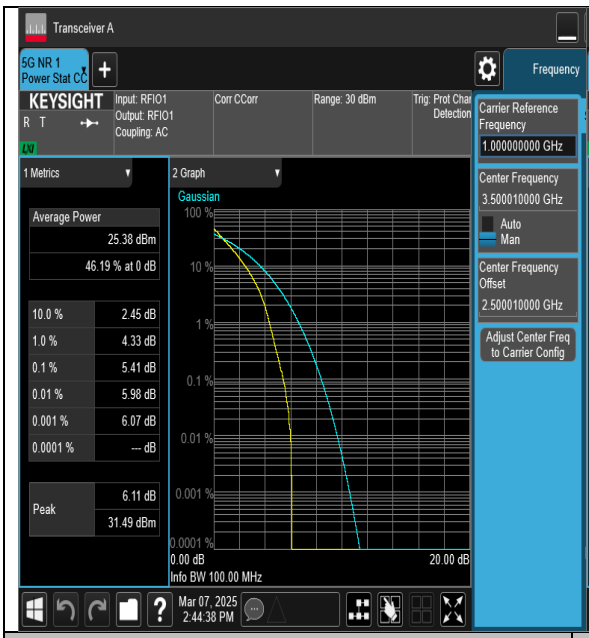

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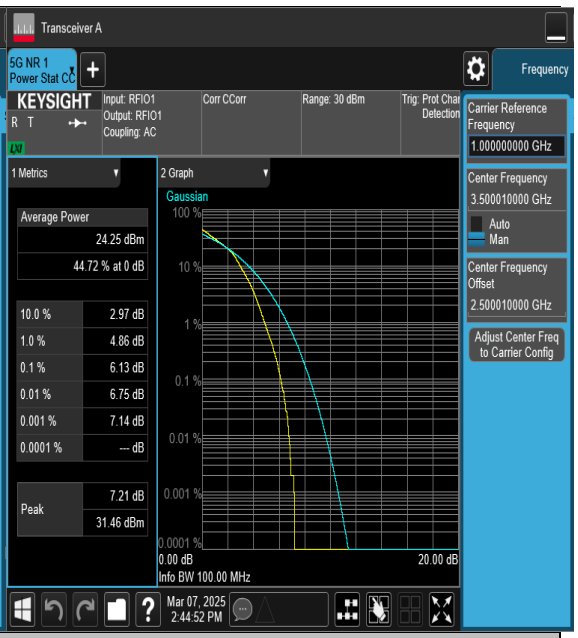

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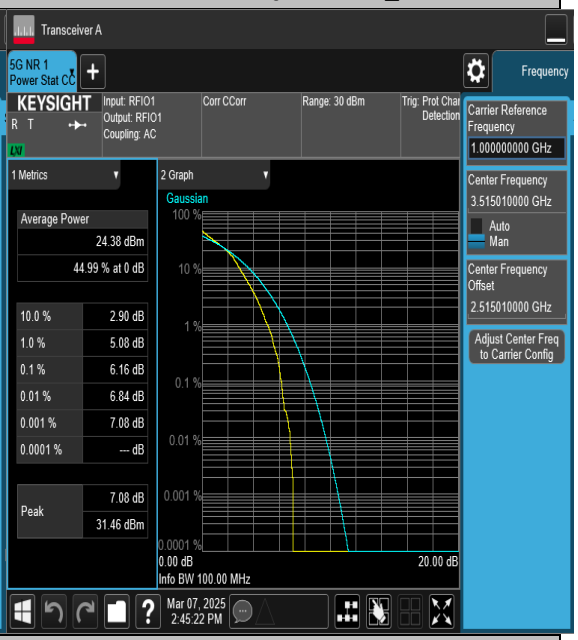

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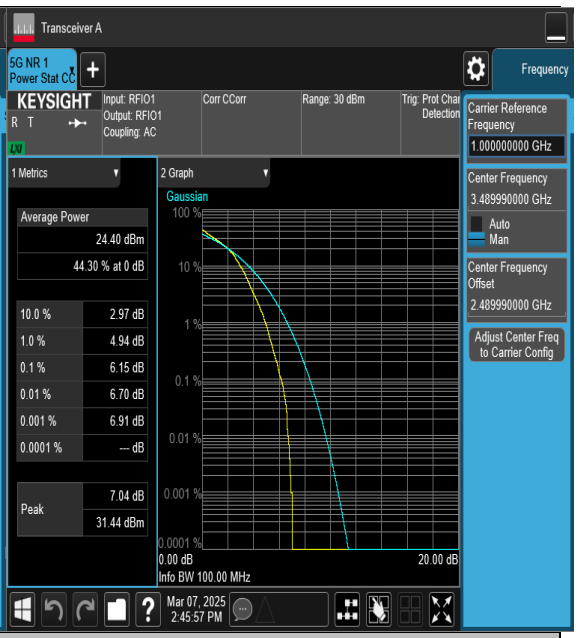

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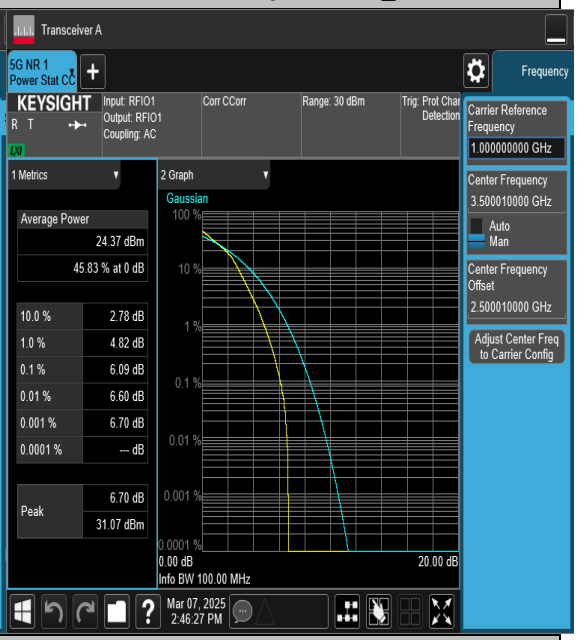

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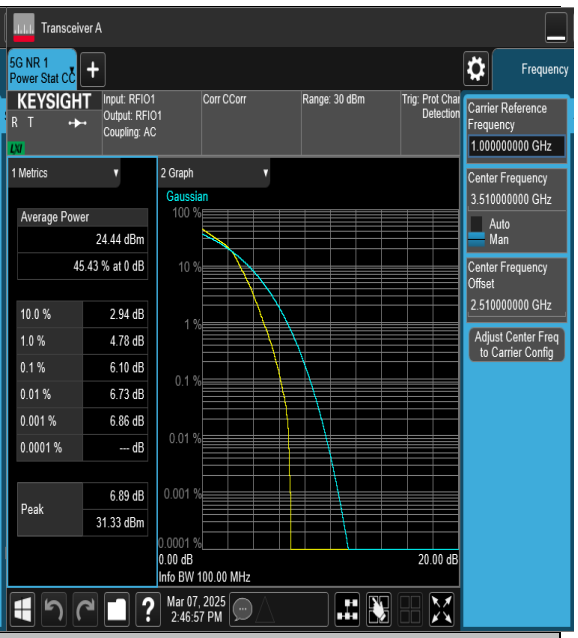

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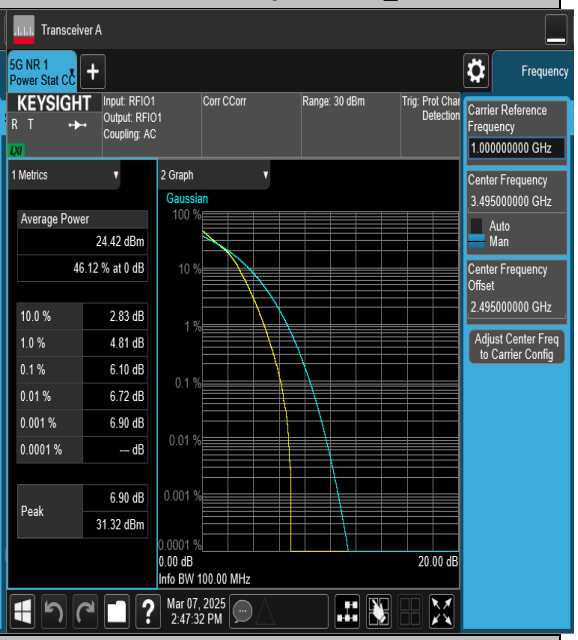

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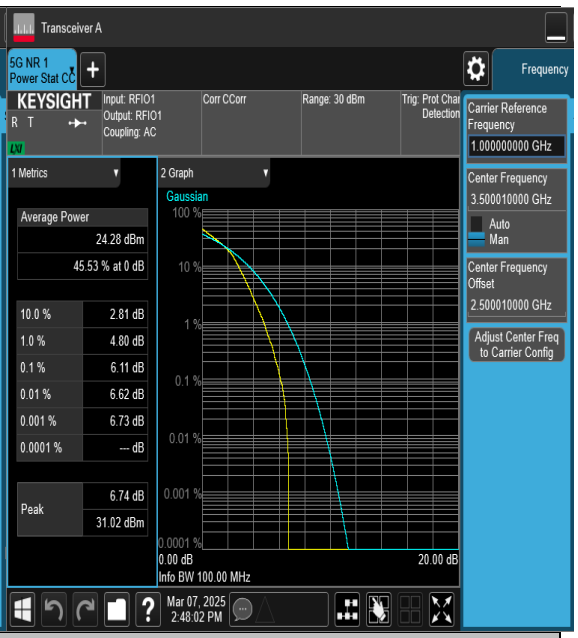

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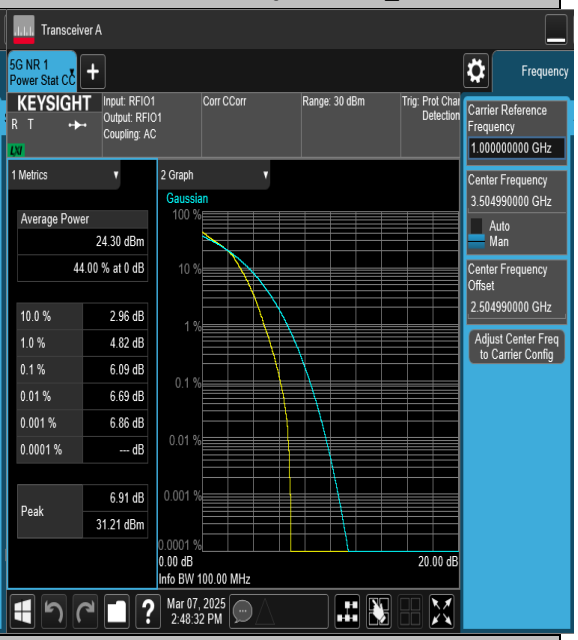

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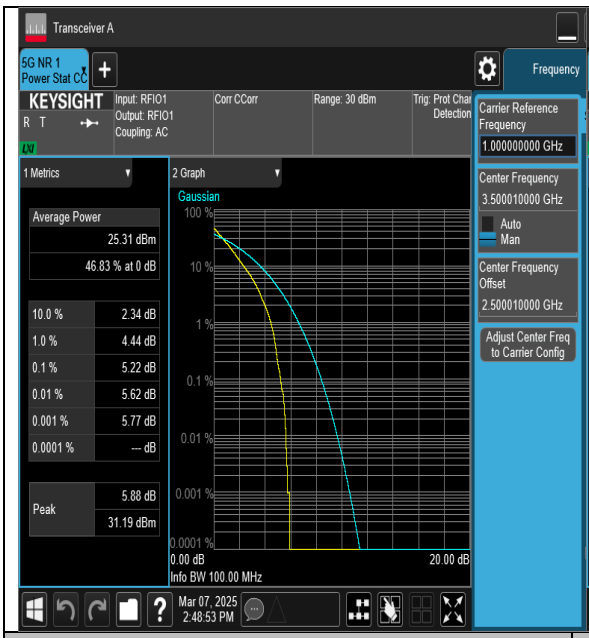

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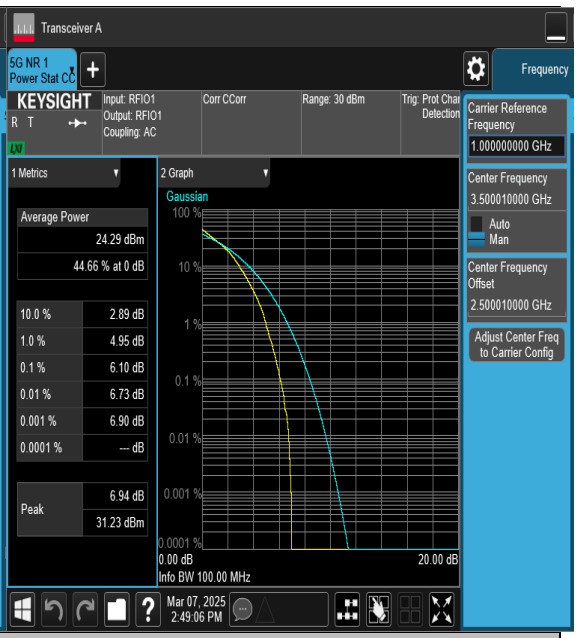

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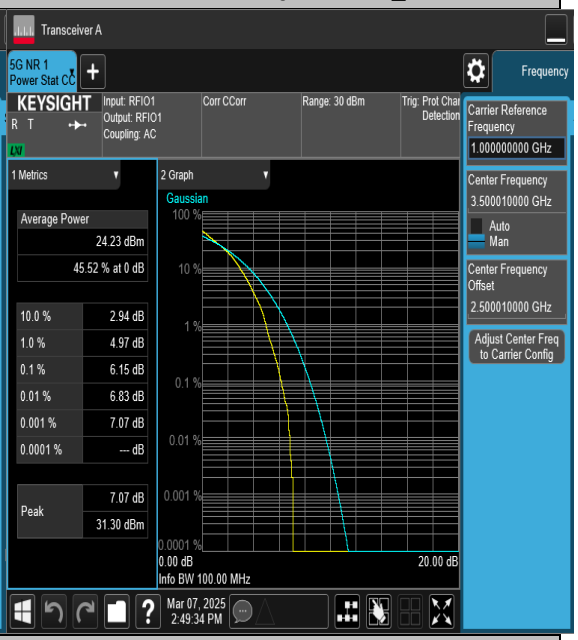

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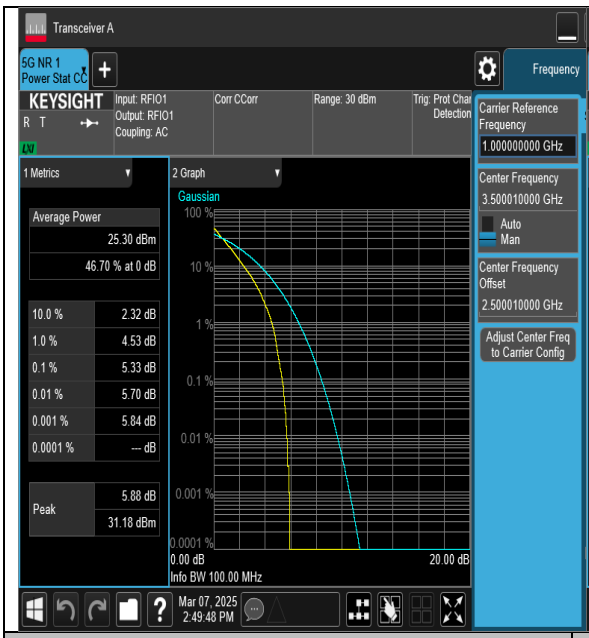

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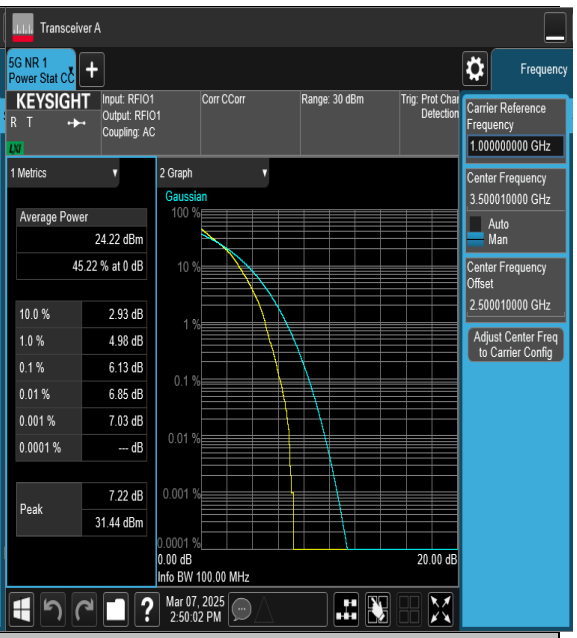


NTNV-DC_41A_n77A-3450-3550-PC2-30-5+100-M+H-N/A-N/A-DFT-16QAM-Outer_Full-PASS

Appendix C: 26dB Bandwidth and Occupied Bandwidth for NSA

Test Result

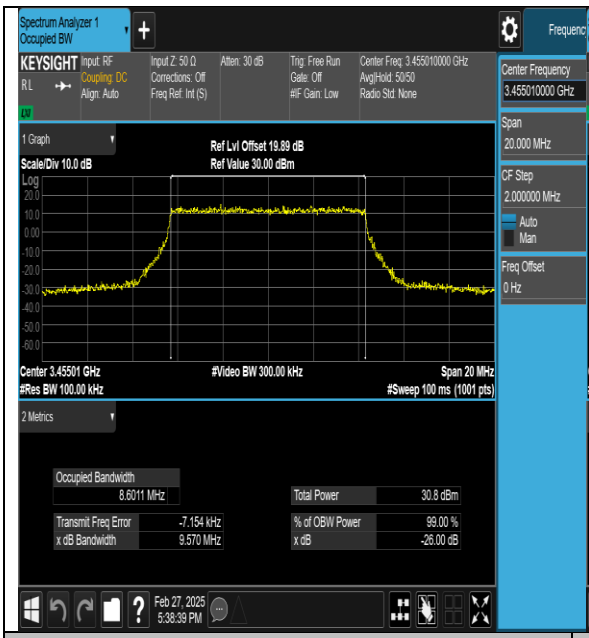
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_41A_n77A-3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	8.6011	9.570	PASS
DC_41A_n77A-3450-3550	30	5+10	DFT-16QAM	M+L	Outer_Full	8.5966	9.696	PASS
DC_41A_n77A-3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	8.6058	9.636	PASS
DC_41A_n77A-3450-3550	30	5+10	DFT-16QAM	M+M	Outer_Full	8.6181	9.641	PASS
DC_41A_n77A-3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	8.5806	9.765	PASS
DC_41A_n77A-3450-3550	30	5+10	DFT-16QAM	M+H	Outer_Full	8.5885	9.888	PASS
DC_41A_n77A-3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	12.884	14.18	PASS
DC_41A_n77A-3450-3550	30	5+15	DFT-16QAM	M+L	Outer_Full	12.877	14.26	PASS
DC_41A_n77A-3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	12.896	14.18	PASS
DC_41A_n77A-3450-3550	30	5+15	DFT-16QAM	M+M	Outer_Full	12.885	14.33	PASS
DC_41A_n77A-3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	12.883	14.38	PASS
DC_41A_n77A-3450-3550	30	5+15	DFT-16QAM	M+H	Outer_Full	12.884	14.38	PASS
DC_41A_n77A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	17.864	19.28	PASS
DC_41A_n77A-3450-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	17.890	19.32	PASS
DC_41A_n77A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	17.857	19.37	PASS
DC_41A_n77A-3450-3550	30	5+20	DFT-16QAM	M+M	Outer_Full	17.849	19.24	PASS
DC_41A_n77A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	17.840	19.31	PASS

DC_41A_n77A-3450-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	17.852	19.48	PASS
DC_41A_n77A-3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	26.803	28.64	PASS
DC_41A_n77A-3450-3550	30	5+30	DFT-16QAM	M+L	Outer_Full	26.766	28.60	PASS
DC_41A_n77A-3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	26.781	28.63	PASS
DC_41A_n77A-3450-3550	30	5+30	DFT-16QAM	M+M	Outer_Full	26.795	28.55	PASS
DC_41A_n77A-3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	26.825	28.59	PASS
DC_41A_n77A-3450-3550	30	5+30	DFT-16QAM	M+H	Outer_Full	26.813	28.74	PASS
DC_41A_n77A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	35.657	37.69	PASS
DC_41A_n77A-3450-3550	30	5+40	DFT-16QAM	M+L	Outer_Full	35.781	38.02	PASS
DC_41A_n77A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	35.735	37.63	PASS
DC_41A_n77A-3450-3550	30	5+40	DFT-16QAM	M+M	Outer_Full	35.712	37.59	PASS
DC_41A_n77A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	35.681	37.74	PASS
DC_41A_n77A-3450-3550	30	5+40	DFT-16QAM	M+H	Outer_Full	35.733	37.55	PASS
DC_41A_n77A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	45.697	47.94	PASS
DC_41A_n77A-3450-3550	30	5+50	DFT-16QAM	M+L	Outer_Full	45.675	47.88	PASS
DC_41A_n77A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	45.649	48.17	PASS
DC_41A_n77A-3450-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	45.615	47.94	PASS
DC_41A_n77A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	45.686	48.12	PASS
DC_41A_n77A-3450-3550	30	5+50	DFT-16QAM	M+H	Outer_Full	45.694	47.78	PASS
DC_41A_n77A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	57.795	60.37	PASS
DC_41A_n77A-3450-3550	30	5+60	DFT-16QAM	M+L	Outer_Full	57.781	60.35	PASS

DC_41A_n77A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	57.747	60.15	PASS
DC_41A_n77A-3450-3550	30	5+60	DFT-16QAM	M+M	Outer_Full	57.740	60.35	PASS
DC_41A_n77A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	57.760	60.22	PASS
DC_41A_n77A-3450-3550	30	5+60	DFT-16QAM	M+H	Outer_Full	57.692	60.48	PASS
DC_41A_n77A-3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	64.140	67.08	PASS
DC_41A_n77A-3450-3550	30	5+70	DFT-16QAM	M+L	Outer_Full	64.239	67.11	PASS
DC_41A_n77A-3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	64.233	66.95	PASS
DC_41A_n77A-3450-3550	30	5+70	DFT-16QAM	M+M	Outer_Full	64.172	67.05	PASS
DC_41A_n77A-3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	64.148	67.04	PASS
DC_41A_n77A-3450-3550	30	5+70	DFT-16QAM	M+H	Outer_Full	64.189	66.88	PASS
DC_41A_n77A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	77.056	80.22	PASS
DC_41A_n77A-3450-3550	30	5+80	DFT-16QAM	M+L	Outer_Full	76.999	79.96	PASS
DC_41A_n77A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	76.964	80.02	PASS
DC_41A_n77A-3450-3550	30	5+80	DFT-16QAM	M+M	Outer_Full	77.100	80.27	PASS
DC_41A_n77A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	76.969	79.97	PASS
DC_41A_n77A-3450-3550	30	5+80	DFT-16QAM	M+H	Outer_Full	77.055	80.16	PASS
DC_41A_n77A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	86.572	90.21	PASS
DC_41A_n77A-3450-3550	30	5+90	DFT-16QAM	M+L	Outer_Full	86.654	90.08	PASS
DC_41A_n77A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	86.697	90.24	PASS
DC_41A_n77A-3450-3550	30	5+90	DFT-16QAM	M+M	Outer_Full	86.700	90.13	PASS
DC_41A_n77A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	86.740	90.00	PASS

DC_41A_n77A-3450-3550	30	5+90	DFT-16QAM	M+H	Outer_Full	86.610	90.04	PASS
DC_41A_n77A-3450-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	96.272	99.99	PASS
DC_41A_n77A-3450-3550	30	5+100	DFT-16QAM	M+L	Outer_Full	96.297	99.84	PASS
DC_41A_n77A-3450-3550	30	5+100	DFT-QPSK	M+M	Outer_Full	96.303	99.95	PASS
DC_41A_n77A-3450-3550	30	5+100	DFT-16QAM	M+M	Outer_Full	96.215	100.0	PASS
DC_41A_n77A-3450-3550	30	5+100	DFT-QPSK	M+H	Outer_Full	96.248	99.85	PASS
DC_41A_n77A-3450-3550	30	5+100	DFT-16QAM	M+H	Outer_Full	96.235	99.88	PASS

Test Graphs



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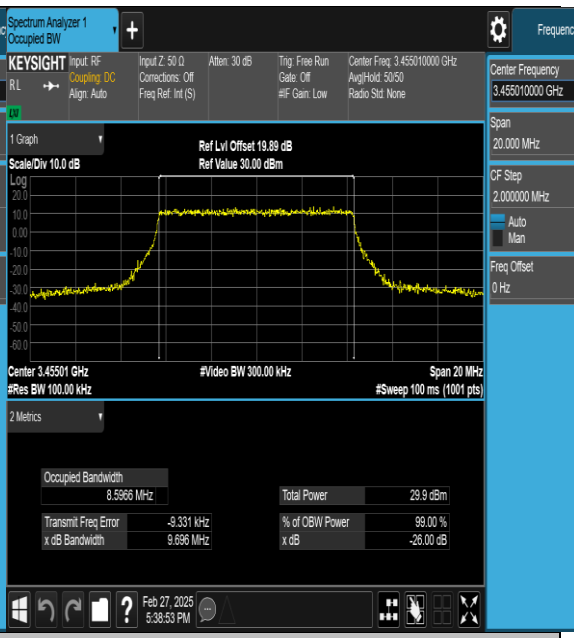

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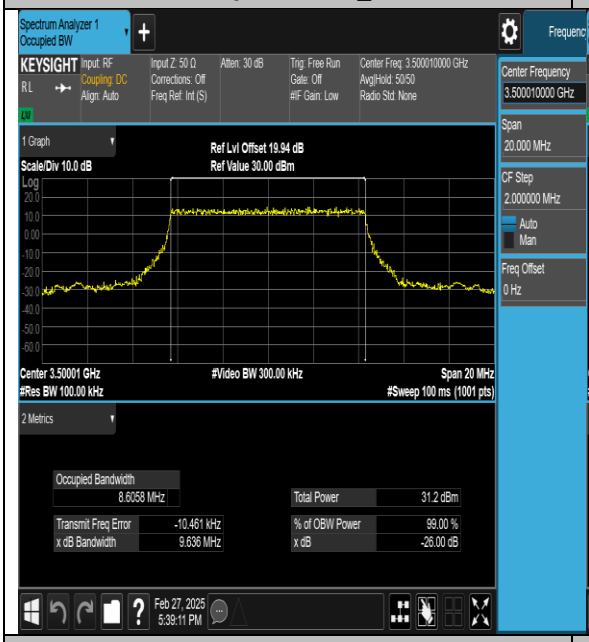

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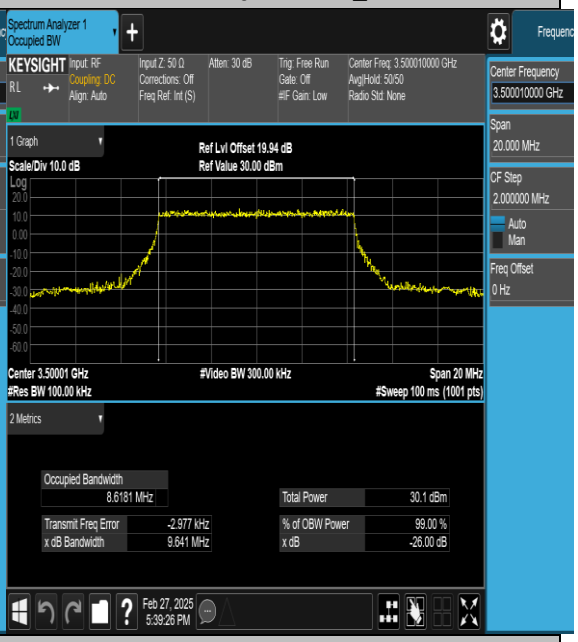

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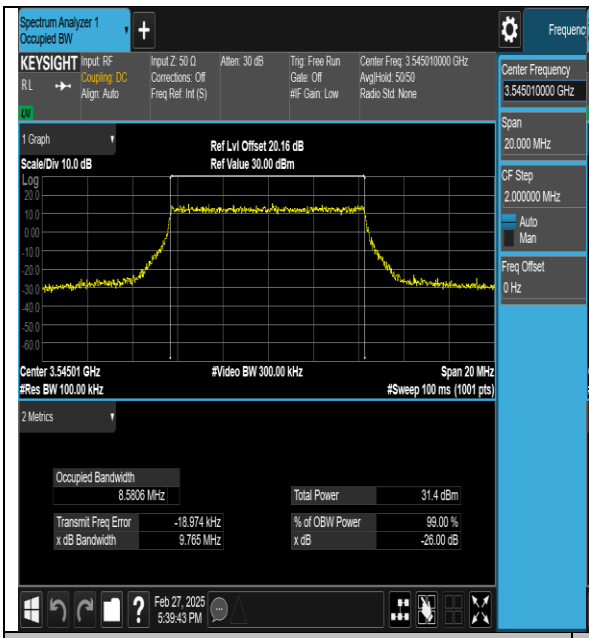

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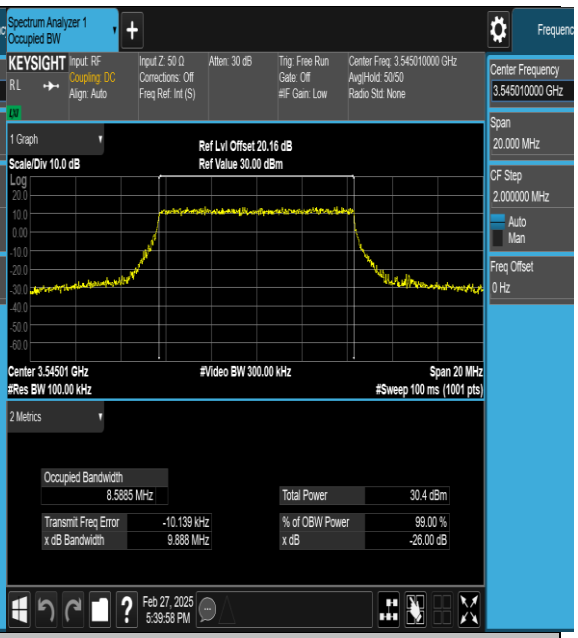

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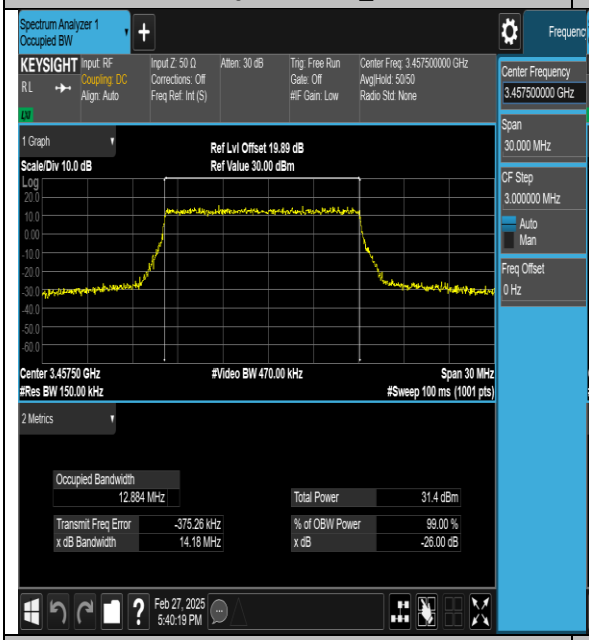

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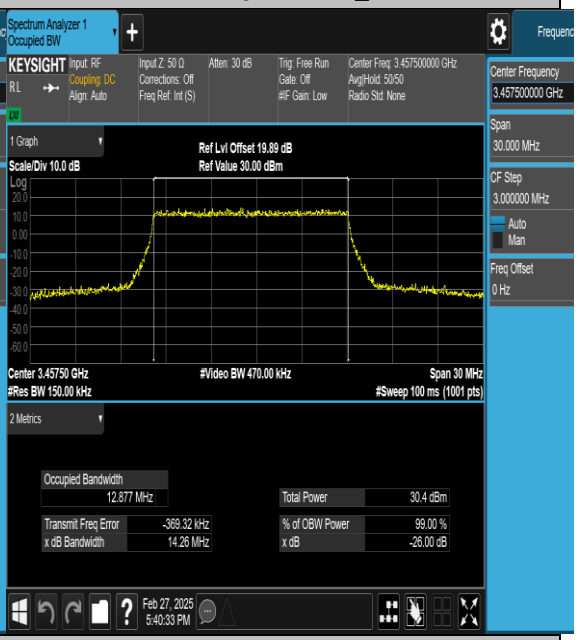

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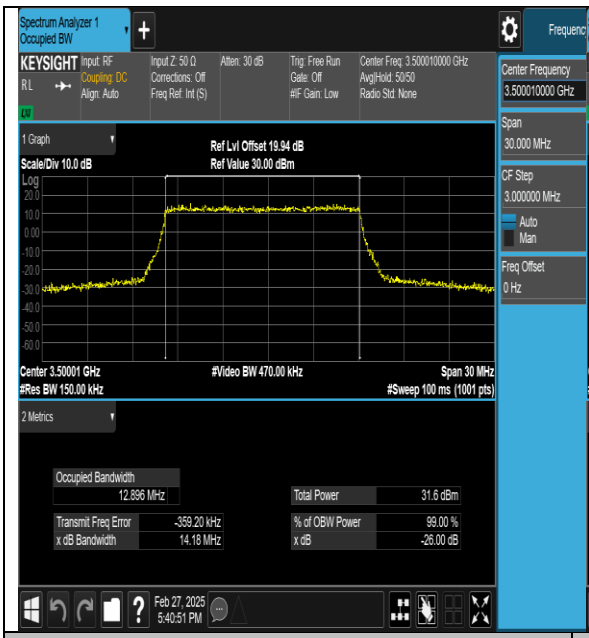

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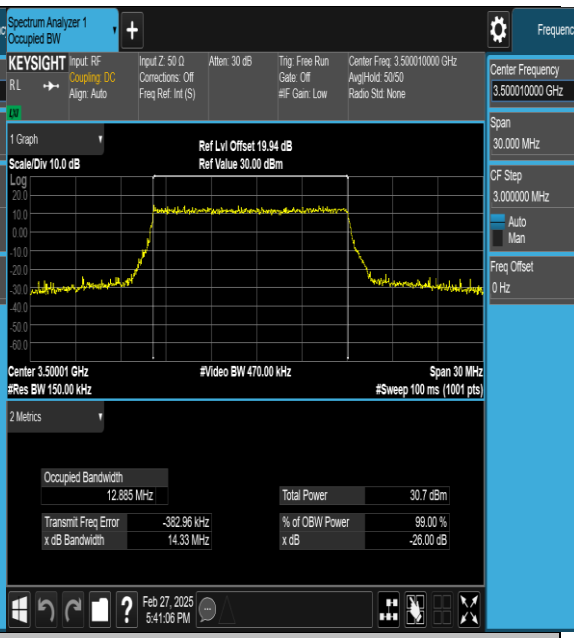

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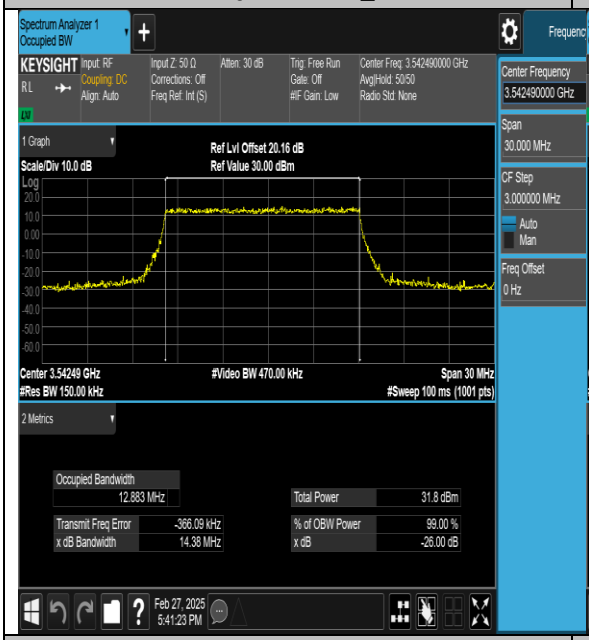

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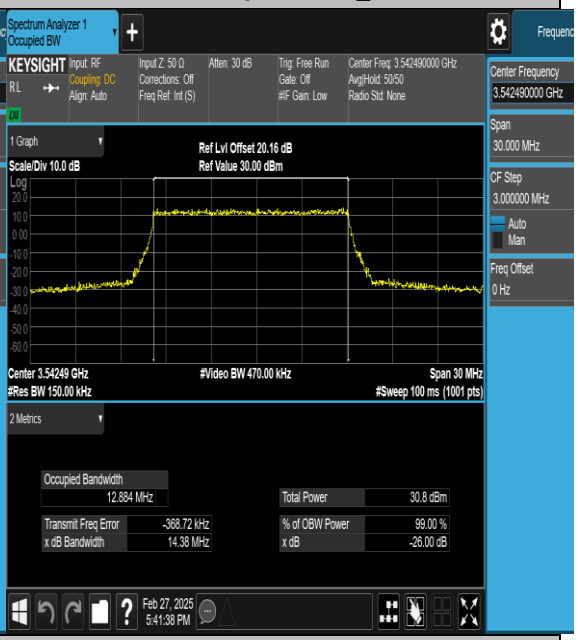

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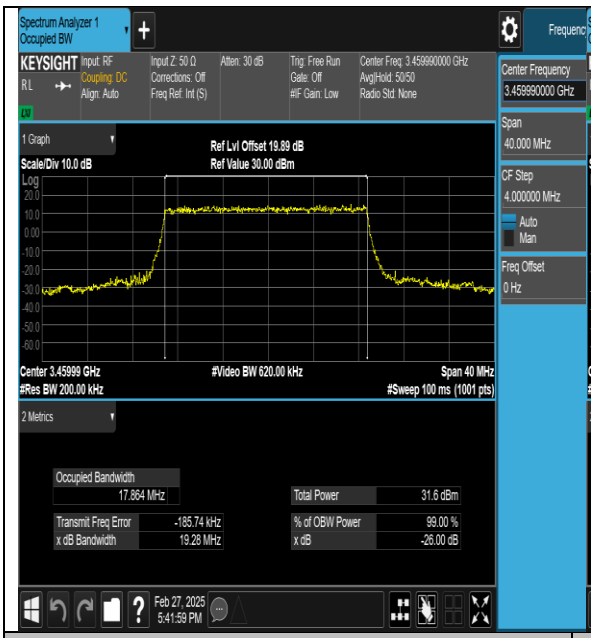

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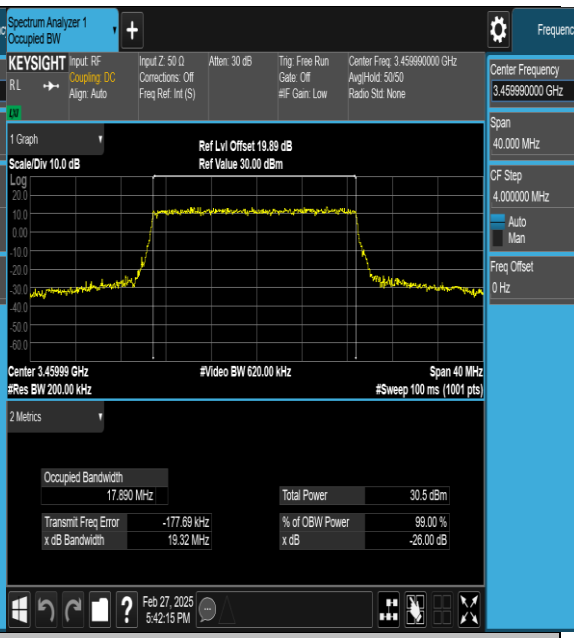

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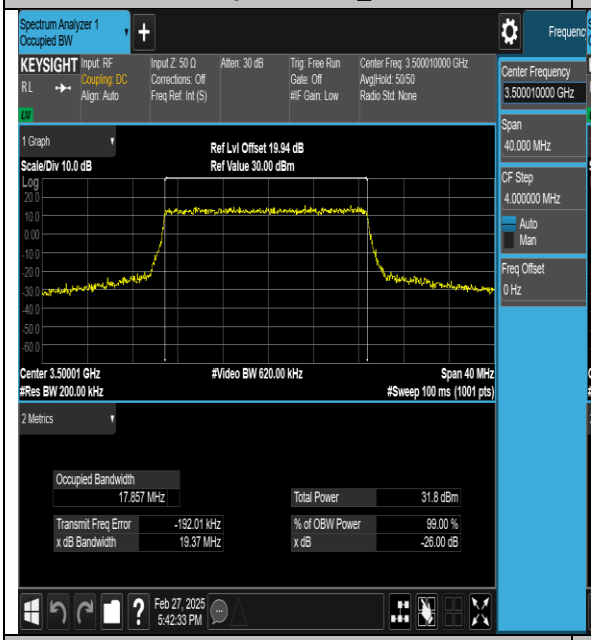

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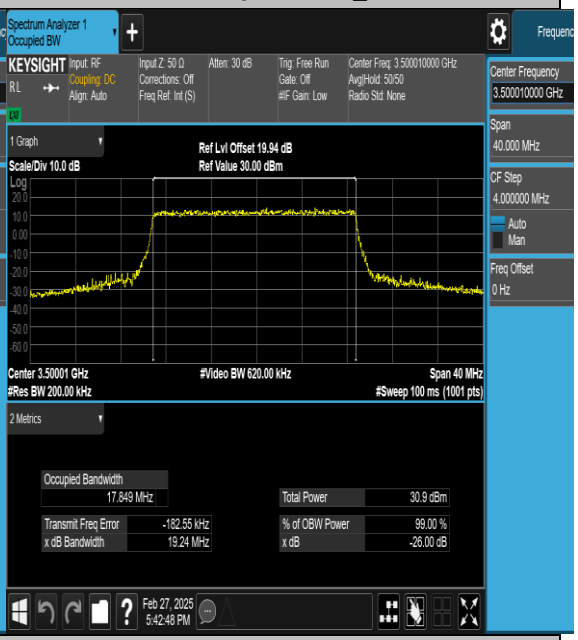

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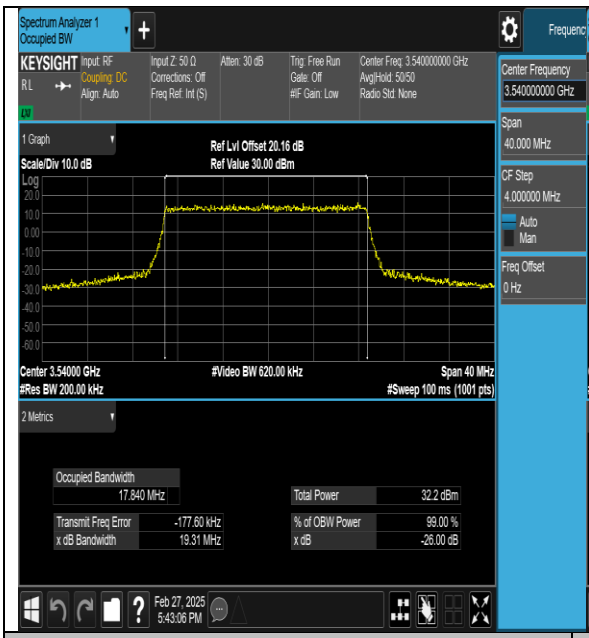

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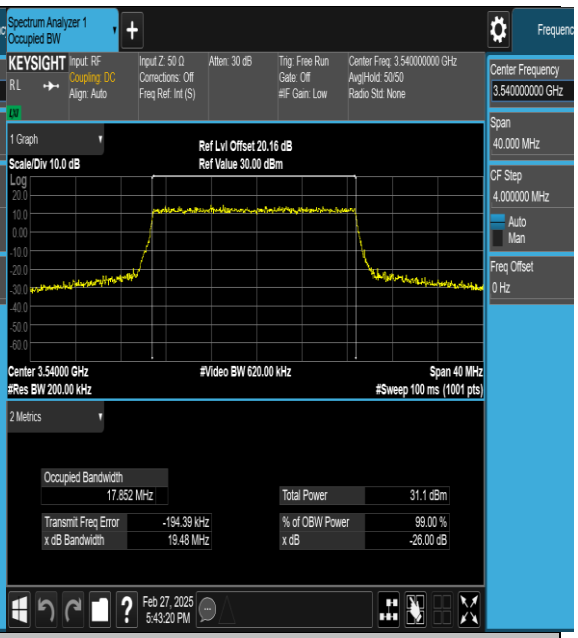

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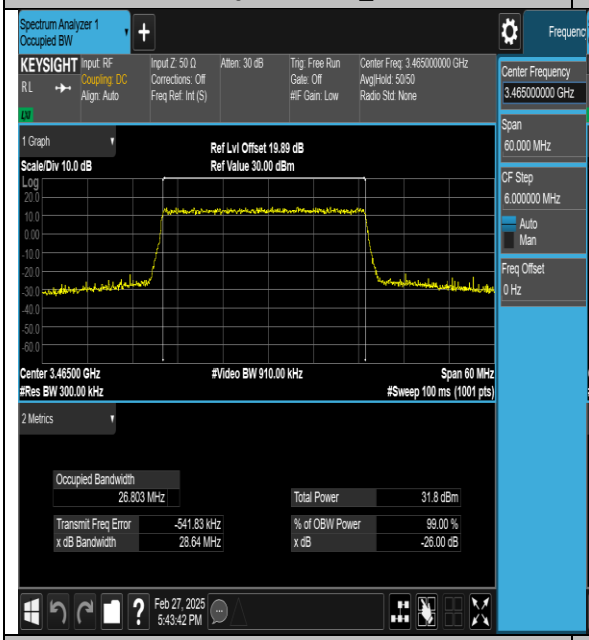

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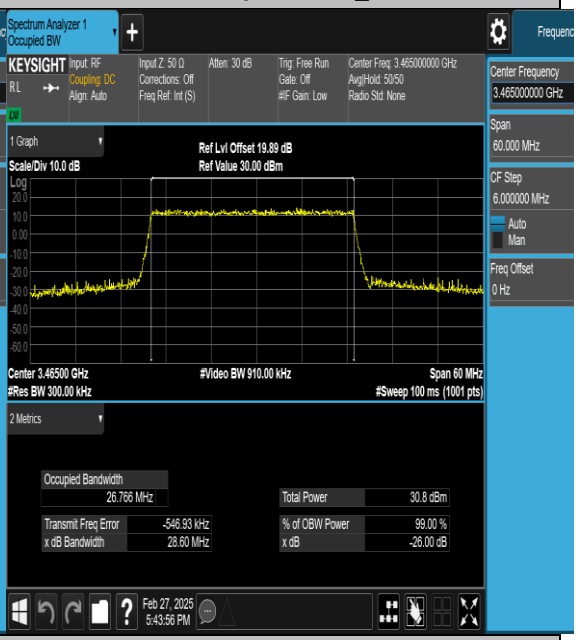

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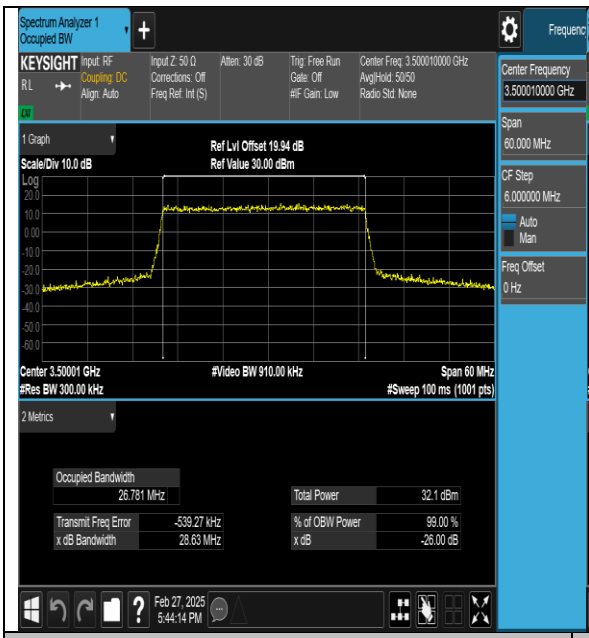

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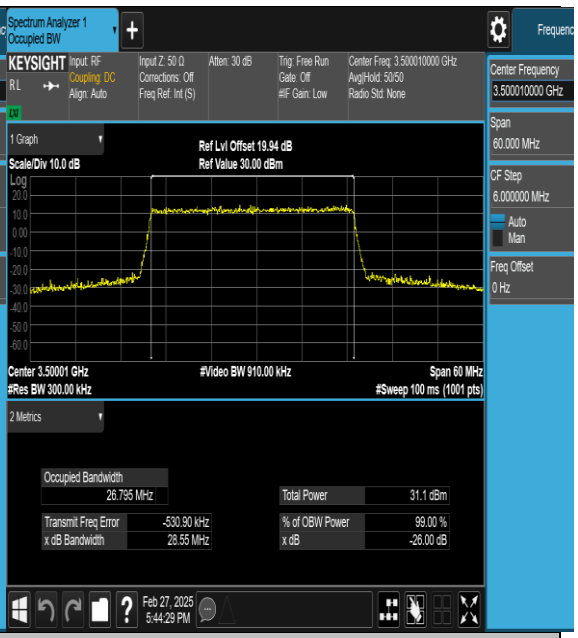

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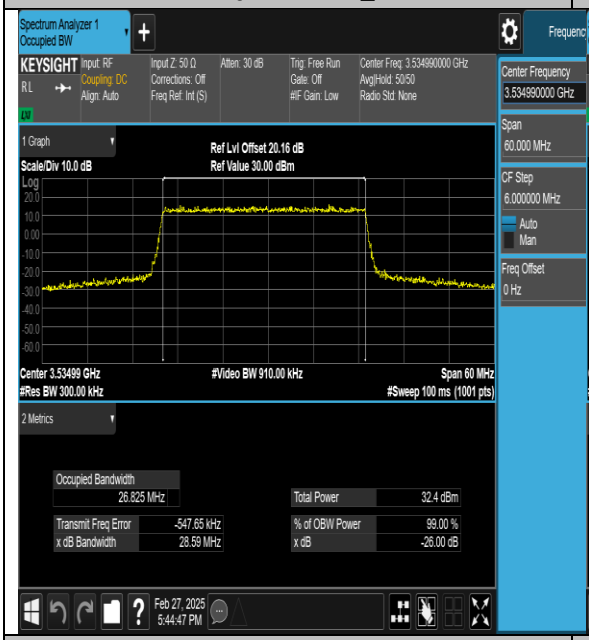

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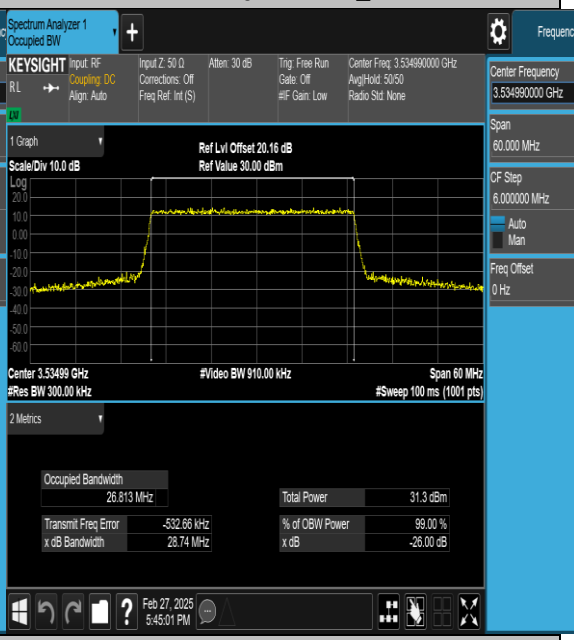

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