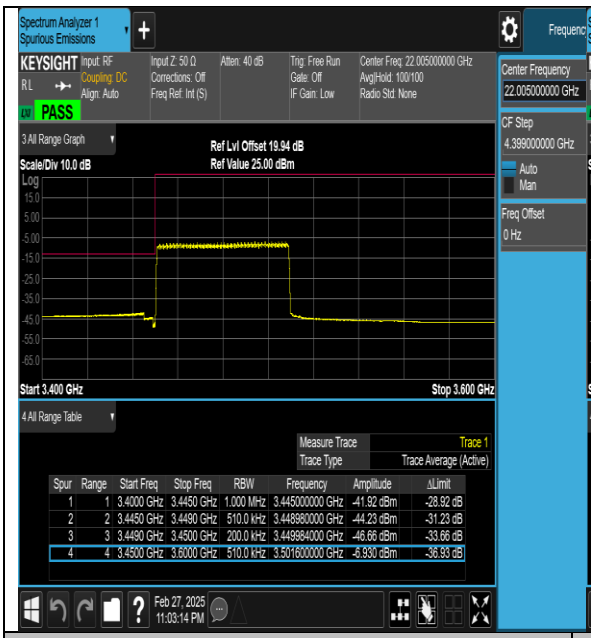

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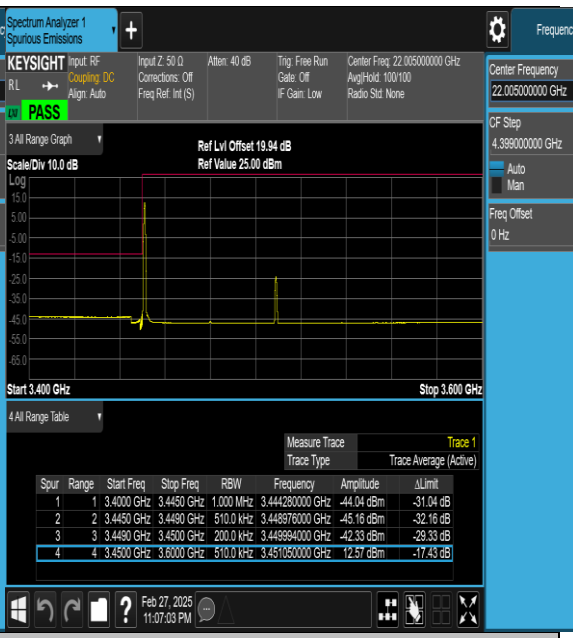

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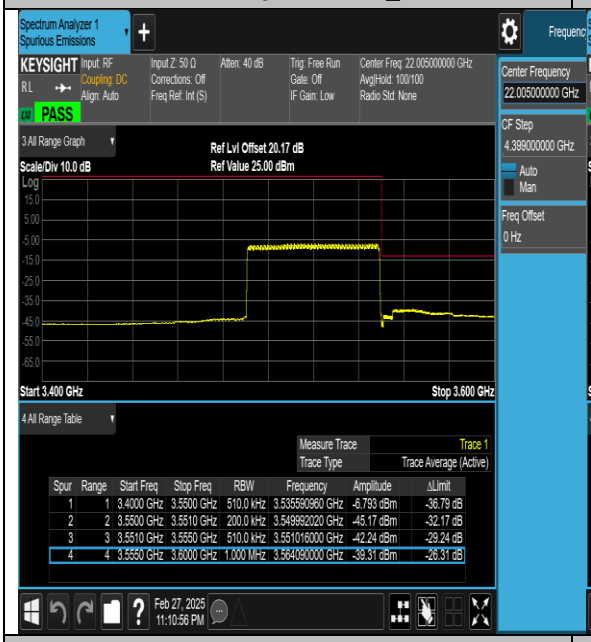

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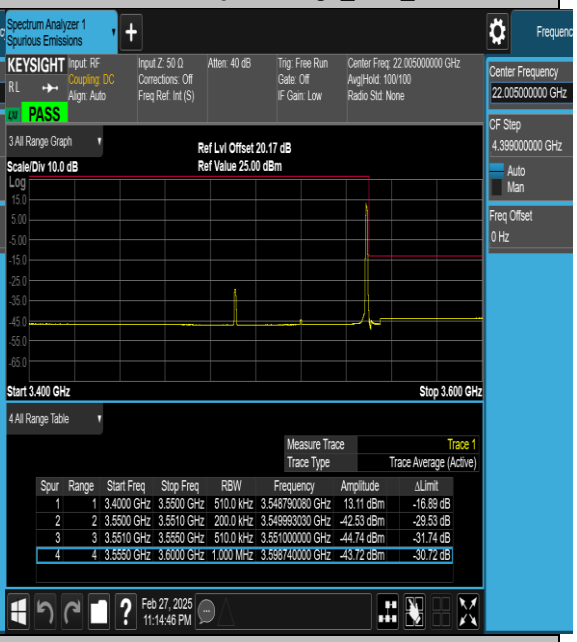

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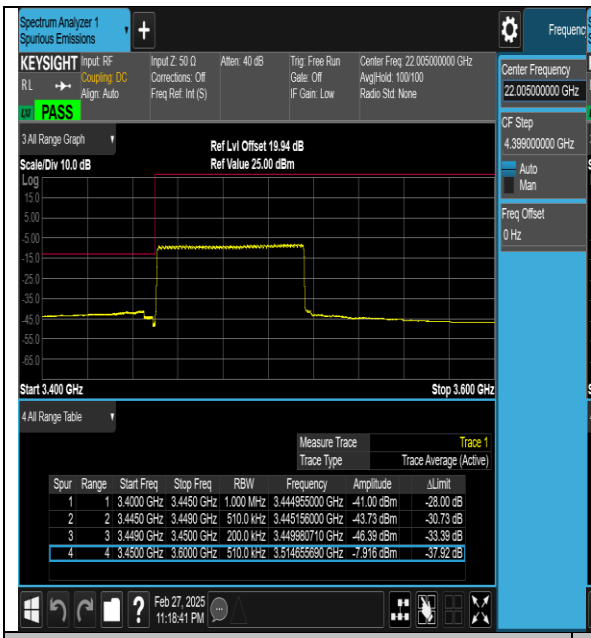

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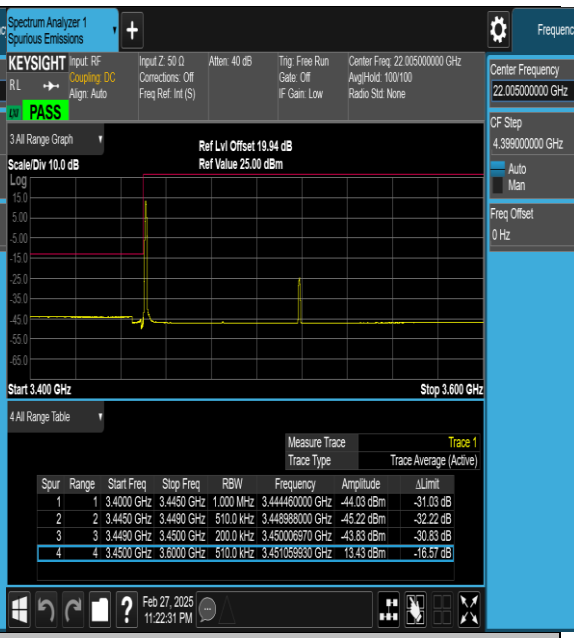

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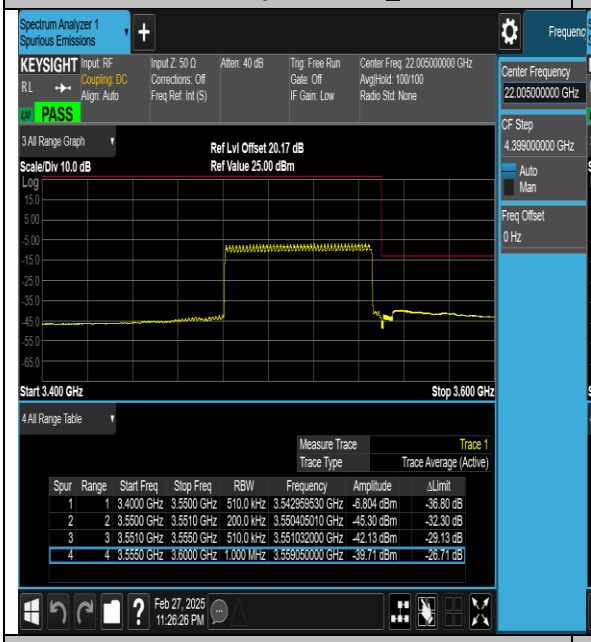

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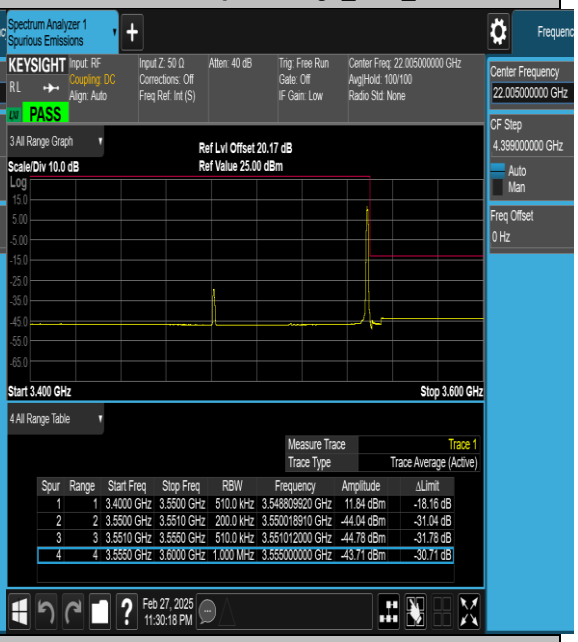

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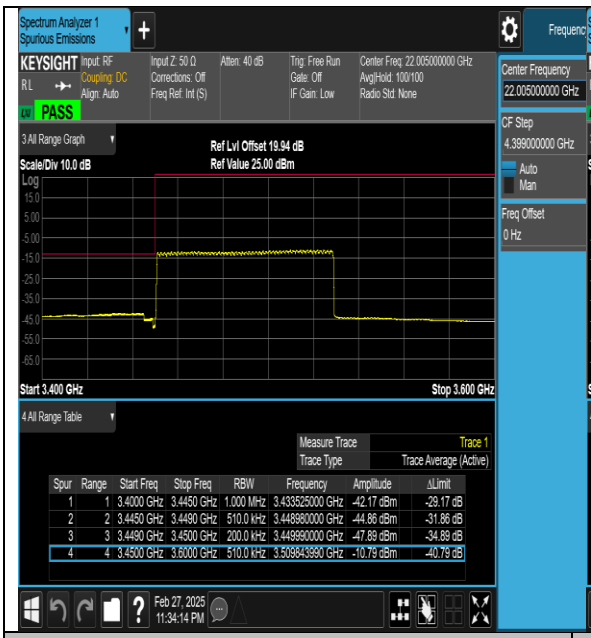

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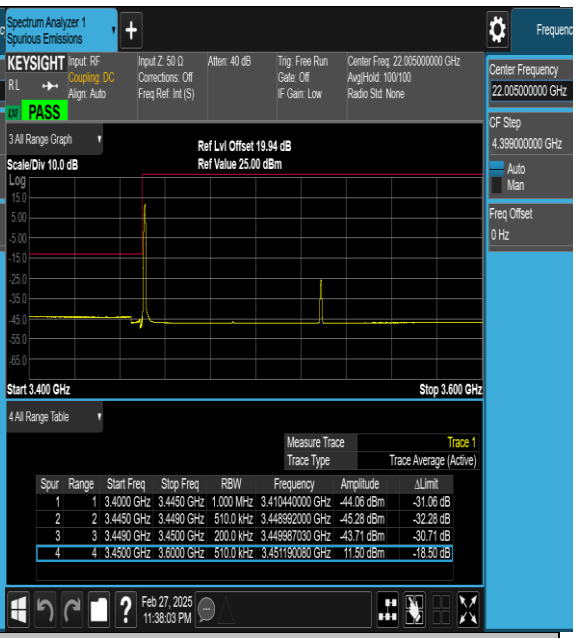

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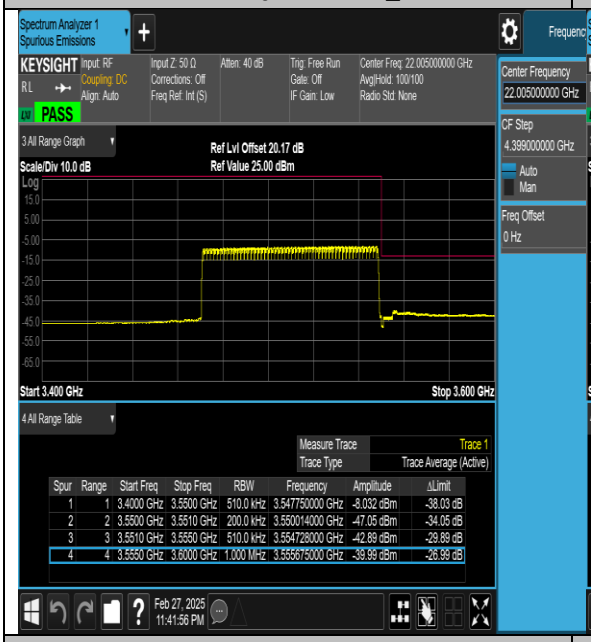

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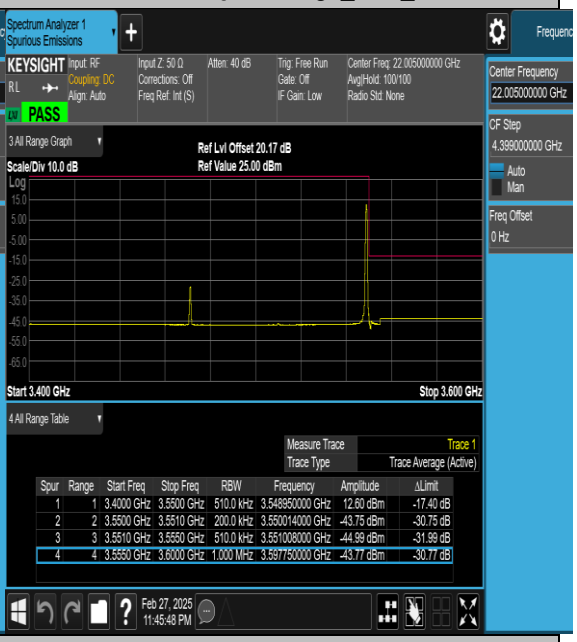
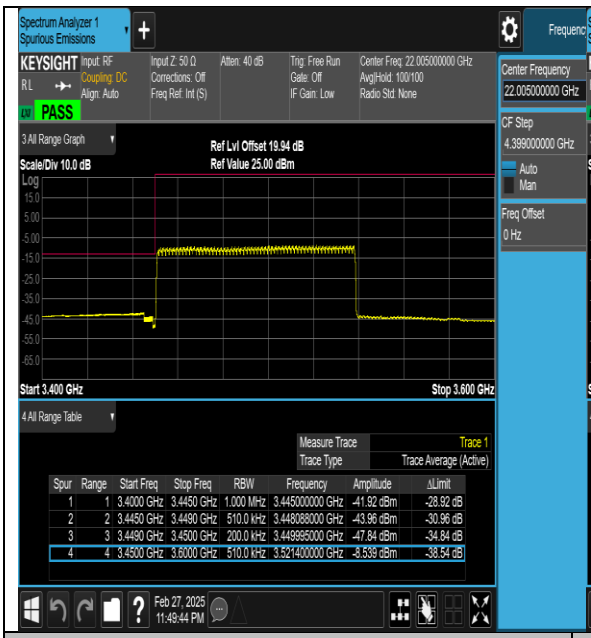


NTNV-DC_41A_n77A-3450-3550-PC2-30-5+80-M+H-N/A-N/A-DFT-QPSK-Edge_1RB_Right-PASS



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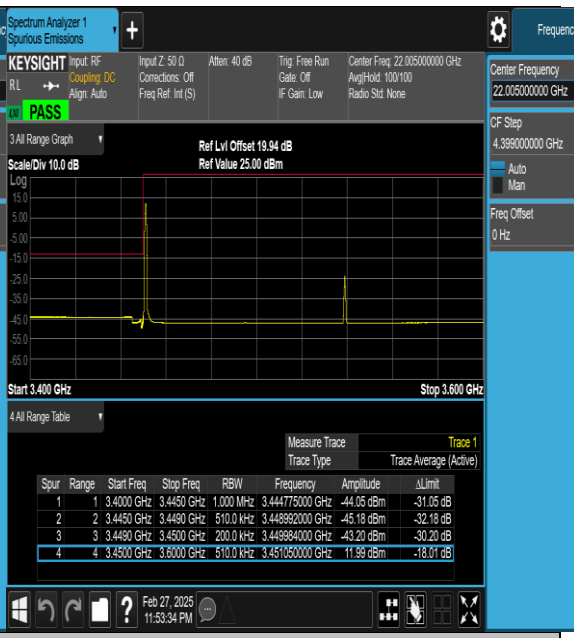

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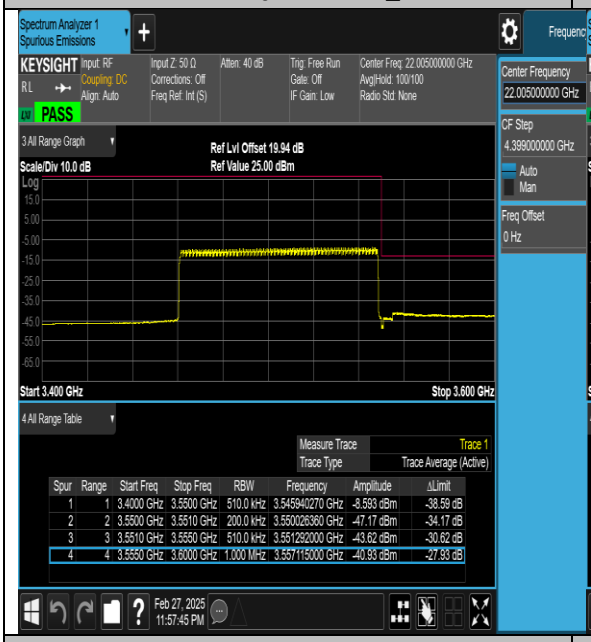

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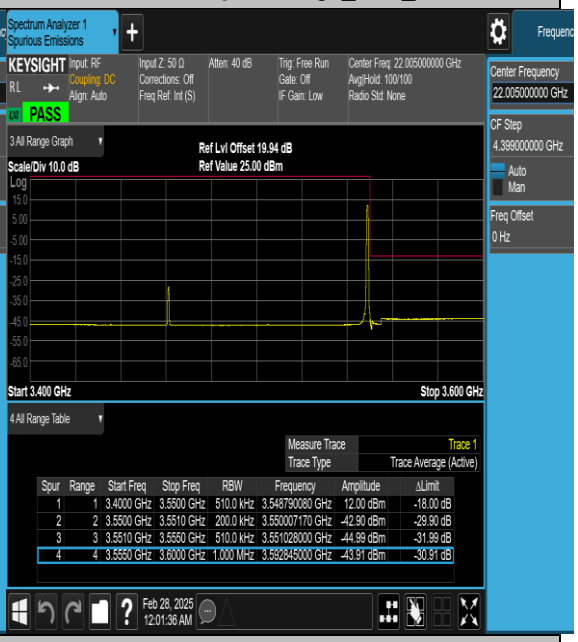
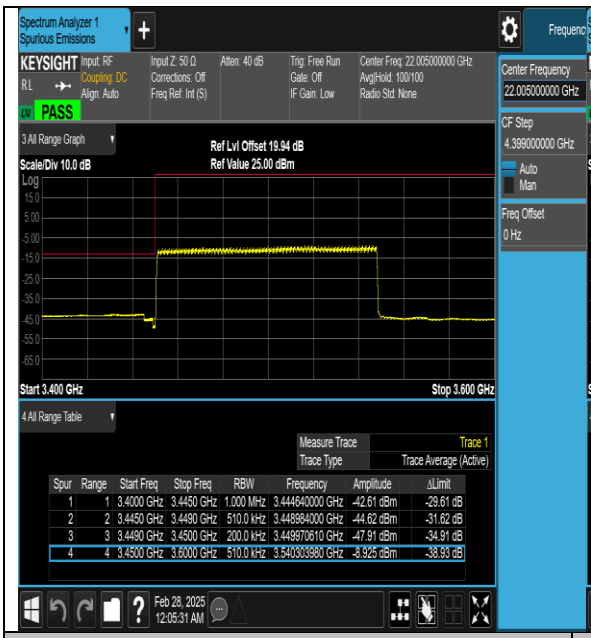


NTNV-DC_41A_n77A-3450-3550-PC2-30-5+90-M+H-N/A-N/A-DFT-QPSK-Edge_1RB_Right-PASS



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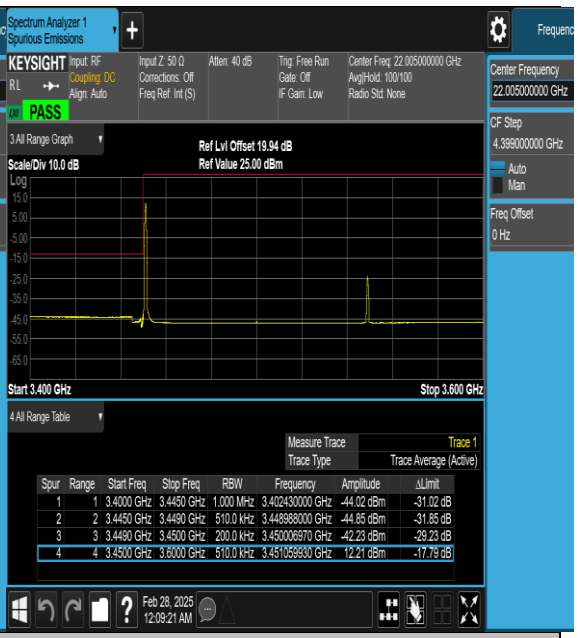

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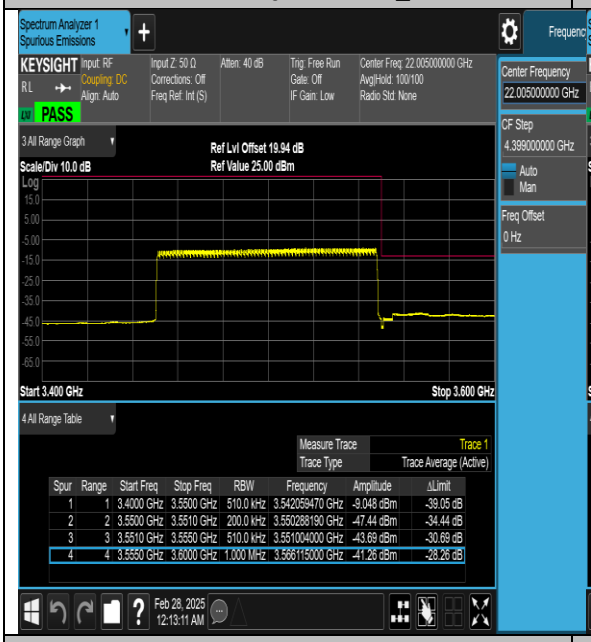

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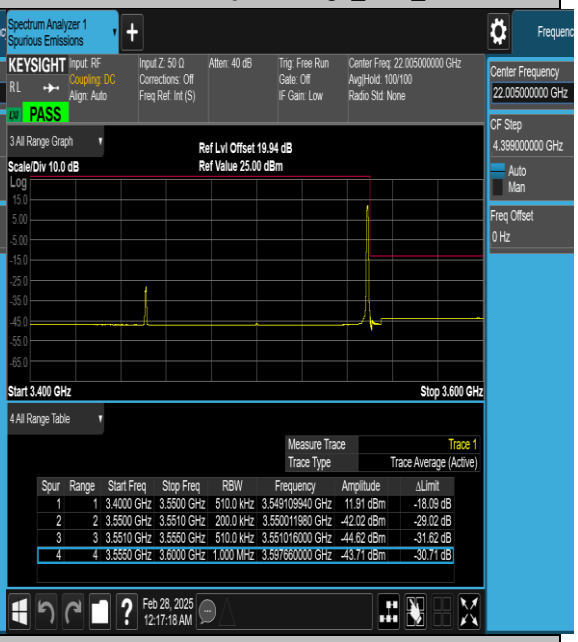

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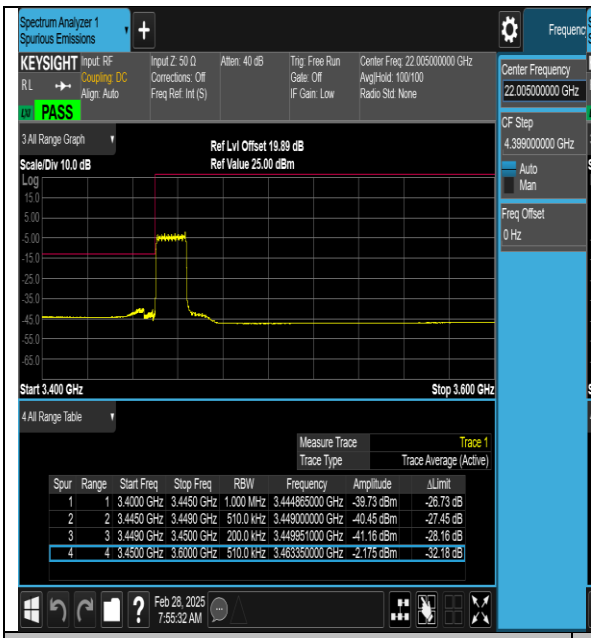

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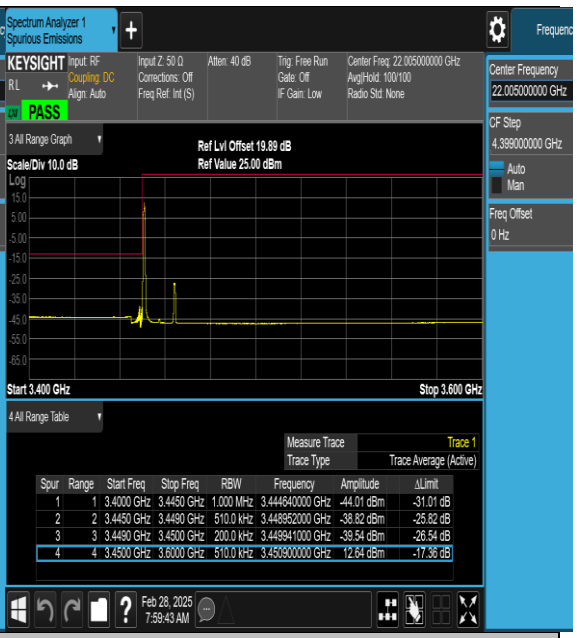

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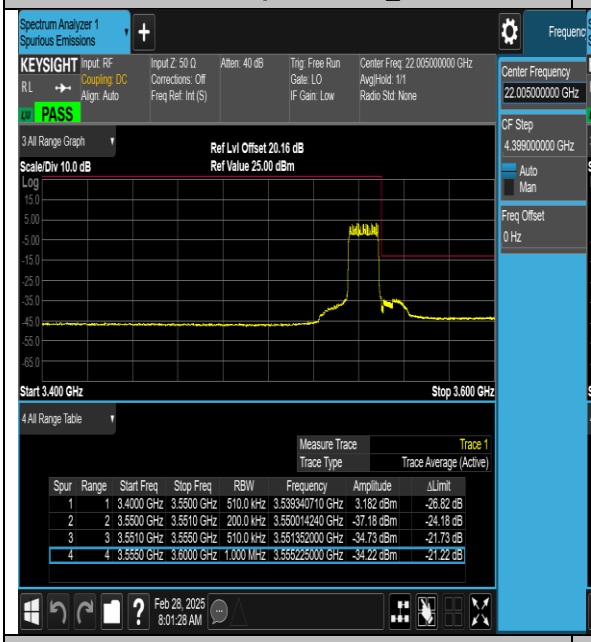
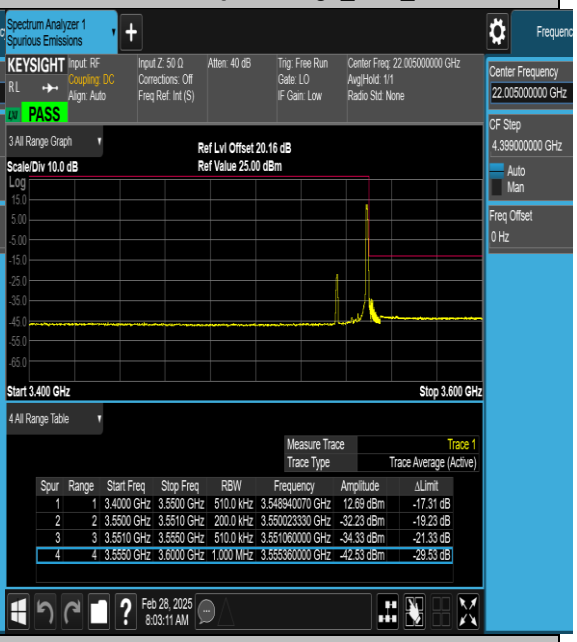


NTNV-DC_41A_n77A-3450-3550-PC2-30-5+15-M +H-N/A-N/A-DFT-QPSK-Outer_Full-PASS



NTNV-DC_41A_n77A-3450-3550-PC2-30-5+15-M +H-N/A-N/A-DFT-QPSK-Edge_1RB_Right-PASS

Appendix E: Conducted Spurious Emission for NSA

Test Result

Band	SCS	Band width	Modulation	Channel	RB Config	StartFreq	StopFreq	Result	Limit	Verdict
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-66.61	-43	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	0.15	30	-66.13	-33	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	30	1000	-55.29	-23	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	1000	3000	-40.02	-13	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	3000	6000	-32.92	-13	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	6000	26000	-36.68	-13	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	26000	40000	-40.92	-13	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-65.76	-43	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-67.15	-33	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-55.09	-23	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-39.87	-13	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-32.60	-13	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-36.51	-13	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-40.81	-13	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-65.96	-43	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-67.32	-33	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-55.17	-23	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-39.96	-13	PASS

DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-32.75	-13	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-36.51	-13	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-40.85	-13	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-66.37	-43	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	0.15	30	-64.96	-33	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	30	1000	-55.14	-23	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	1000	3000	-40.03	-13	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	3000	6000	-32.69	-13	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	6000	26000	-36.65	-13	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	26000	40000	-40.78	-13	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-67.44	-43	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-67.42	-33	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-55.21	-23	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-39.88	-13	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-32.77	-13	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-36.60	-13	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-40.75	-13	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-66.50	-43	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-67.35	-33	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-55.14	-23	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-39.96	-13	PASS

DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-32.78	-13	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-36.47	-13	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-40.67	-13	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-66.35	-43	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	0.15	30	-65.72	-33	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	30	1000	-55.16	-23	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	1000	3000	-39.98	-13	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	3000	6000	-32.75	-13	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	6000	26000	-36.66	-13	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	26000	40000	-40.78	-13	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-66.97	-43	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-65.47	-33	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-55.02	-23	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-39.94	-13	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-32.83	-13	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-36.61	-13	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-40.91	-13	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-66.53	-43	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-66.81	-33	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-54.82	-23	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.07	-13	PASS

DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-32.91	-13	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-36.64	-13	PASS
DC_41A_n77A -3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-40.81	-13	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-66.64	-43	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	0.15	30	-66.14	-33	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	30	1000	-54.91	-23	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	1000	3000	-39.91	-13	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	3000	6000	-32.70	-13	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	6000	26000	-36.60	-13	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	26000	40000	-40.91	-13	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-66.74	-43	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-67.18	-33	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-55.08	-23	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-40.04	-13	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-32.83	-13	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-36.59	-13	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-40.85	-13	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-67.18	-43	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-65.67	-33	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-55.16	-23	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-39.99	-13	PASS

DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-32.65	-13	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-36.64	-13	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-40.73	-13	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-66.80	-43	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	0.15	30	-68.22	-33	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	30	1000	-55.36	-23	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	1000	3000	-40.02	-13	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	3000	6000	-32.80	-13	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	6000	26000	-36.74	-13	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	26000	40000	-40.85	-13	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-66.24	-43	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-66.50	-33	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-55.24	-23	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-40.02	-13	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-32.69	-13	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-36.58	-13	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-40.67	-13	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-67.29	-43	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-65.90	-33	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-55.37	-23	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-39.98	-13	PASS

DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-32.67	-13	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-36.71	-13	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-40.82	-13	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-66.42	-43	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	0.15	30	-67.64	-33	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	30	1000	-55.17	-23	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	1000	3000	-40.03	-13	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	3000	6000	-32.60	-13	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	6000	26000	-36.42	-13	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	26000	40000	-40.65	-13	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-65.77	-43	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-67.03	-33	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-55.53	-23	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-39.89	-13	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-32.72	-13	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-36.65	-13	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-41.01	-13	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-66.77	-43	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-66.72	-33	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-55.57	-23	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.06	-13	PASS

DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-32.69	-13	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-36.70	-13	PASS
DC_41A_n77A -3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-40.60	-13	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-67.12	-43	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	0.15	30	-66.28	-33	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	30	1000	-55.39	-23	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	1000	3000	-40.01	-13	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	3000	6000	-32.78	-13	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	6000	26000	-36.45	-13	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	26000	40000	-40.84	-13	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-65.98	-43	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-65.96	-33	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-55.28	-23	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-40.02	-13	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-32.64	-13	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-36.40	-13	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-40.93	-13	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-66.38	-43	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-66.48	-33	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-55.30	-23	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-40.04	-13	PASS

DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-32.68	-13	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-36.55	-13	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-40.71	-13	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-66.39	-43	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	0.15	30	-66.58	-33	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	30	1000	-55.21	-23	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	1000	3000	-39.97	-13	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	3000	6000	-32.63	-13	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	6000	26000	-36.57	-13	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	26000	40000	-40.80	-13	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-66.50	-43	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-66.89	-33	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-55.25	-23	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-40.05	-13	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-32.71	-13	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-36.63	-13	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-40.81	-13	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-66.77	-43	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-66.97	-33	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-55.06	-23	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.03	-13	PASS

DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-32.69	-13	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-36.42	-13	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-40.74	-13	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-66.21	-43	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	0.15	30	-67.86	-33	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	30	1000	-55.37	-23	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	1000	3000	-40.06	-13	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	3000	6000	-32.57	-13	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	6000	26000	-36.57	-13	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	26000	40000	-40.75	-13	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-65.73	-43	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-67.57	-33	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-55.23	-23	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.06	-13	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-32.82	-13	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-36.60	-13	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-40.67	-13	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-67.33	-43	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-67.73	-33	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-55.33	-23	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.06	-13	PASS

DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-32.77	-13	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-36.51	-13	PASS
DC_41A_n77A -3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-40.75	-13	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-66.62	-43	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	0.15	30	-66.12	-33	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	30	1000	-55.55	-23	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	1000	3000	-39.94	-13	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	3000	6000	-32.70	-13	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	6000	26000	-36.62	-13	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	26000	40000	-40.78	-13	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-63.65	-43	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-66.84	-33	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-55.25	-23	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-39.94	-13	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-32.62	-13	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-36.50	-13	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-40.73	-13	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-66.19	-43	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-67.03	-33	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-54.93	-23	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-40.00	-13	PASS

DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-32.71	-13	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-36.52	-13	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-40.74	-13	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-66.07	-43	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	0.15	30	-67.10	-33	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	30	1000	-55.35	-23	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	1000	3000	-40.05	-13	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	3000	6000	-32.79	-13	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	6000	26000	-36.34	-13	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	26000	40000	-40.74	-13	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-66.44	-43	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-67.70	-33	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-55.33	-23	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-39.96	-13	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-32.50	-13	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-36.41	-13	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-40.86	-13	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-66.91	-43	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-67.79	-33	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-55.46	-23	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.00	-13	PASS

DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-32.76	-13	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-36.70	-13	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-40.95	-13	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-65.04	-43	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	0.15	30	-67.36	-33	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	30	1000	-55.47	-23	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	1000	3000	-40.03	-13	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	3000	6000	-32.76	-13	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	6000	26000	-36.72	-13	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	26000	40000	-40.89	-13	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-68.02	-43	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-67.06	-33	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-55.35	-23	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.11	-13	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-32.60	-13	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-36.62	-13	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-40.82	-13	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-67.10	-43	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-67.69	-33	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-55.36	-23	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-34.92	-13	PASS

DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-32.84	-13	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-36.80	-13	PASS
DC_41A_n77A -3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-40.81	-13	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-67.04	-43	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	0.15	30	-68.30	-33	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	30	1000	-55.39	-23	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	1000	3000	-40.05	-13	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	3000	6000	-32.75	-13	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	6000	26000	-36.74	-13	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	26000	40000	-40.89	-13	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-66.89	-43	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-66.92	-33	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-55.41	-23	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-39.82	-13	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-32.73	-13	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-36.62	-13	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-41.00	-13	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-66.80	-43	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-65.31	-33	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-55.43	-23	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-40.04	-13	PASS

DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-32.78	-13	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-36.67	-13	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-40.99	-13	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-66.14	-43	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	0.15	30	-67.09	-33	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	30	1000	-55.33	-23	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	1000	3000	-40.03	-13	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	3000	6000	-32.76	-13	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	6000	26000	-36.72	-13	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	26000	40000	-40.97	-13	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-66.04	-43	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-67.44	-33	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-55.26	-23	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-15.77	-13	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-32.81	-13	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-36.71	-13	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-40.80	-13	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-67.02	-43	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-67.13	-33	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-55.00	-23	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.08	-13	PASS

DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-32.67	-13	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-36.77	-13	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-40.95	-13	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-67.02	-43	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	0.15	30	-66.57	-33	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	30	1000	-55.31	-23	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	1000	3000	-40.11	-13	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	3000	6000	-32.75	-13	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	6000	26000	-36.69	-13	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	26000	40000	-40.69	-13	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-66.59	-43	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-66.79	-33	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-55.23	-23	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.06	-13	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-32.73	-13	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-36.78	-13	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-40.92	-13	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-67.01	-43	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-67.47	-33	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-55.19	-23	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.11	-13	PASS

DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-32.85	-13	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-36.66	-13	PASS
DC_41A_n77A -3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-40.68	-13	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-66.99	-43	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	0.15	30	-67.75	-33	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	30	1000	-55.40	-23	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	1000	3000	-40.02	-13	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	3000	6000	-32.85	-13	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	6000	26000	-36.65	-13	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	26000	40000	-40.94	-13	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-67.04	-43	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-64.57	-33	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-55.15	-23	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-40.03	-13	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-32.76	-13	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-36.74	-13	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-40.86	-13	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-66.20	-43	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-66.51	-33	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-55.52	-23	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-40.00	-13	PASS

DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-32.86	-13	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-36.80	-13	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-40.95	-13	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-66.56	-43	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	0.15	30	-67.22	-33	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	30	1000	-55.24	-23	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	1000	3000	-39.99	-13	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	3000	6000	-32.84	-13	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	6000	26000	-36.78	-13	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	26000	40000	-40.79	-13	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-66.17	-43	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-67.08	-33	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-55.57	-23	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-40.06	-13	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-32.77	-13	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-36.69	-13	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-40.95	-13	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-66.58	-43	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-66.75	-33	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-55.47	-23	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.01	-13	PASS

DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-32.76	-13	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-36.78	-13	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-40.75	-13	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-66.55	-43	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	0.15	30	-67.57	-33	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	30	1000	-55.30	-23	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	1000	3000	-40.12	-13	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	3000	6000	-32.60	-13	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	6000	26000	-36.44	-13	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	26000	40000	-40.97	-13	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-66.33	-43	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-66.80	-33	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-55.24	-23	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.15	-13	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-32.78	-13	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-36.74	-13	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-40.94	-13	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-65.87	-43	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-67.15	-33	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-55.25	-23	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.07	-13	PASS

DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-32.80	-13	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-36.76	-13	PASS
DC_41A_n77A -3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-40.72	-13	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-65.82	-43	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	0.15	30	-67.36	-33	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	30	1000	-55.23	-23	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	1000	3000	-39.75	-13	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	3000	6000	-32.93	-13	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	6000	26000	-36.72	-13	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	26000	40000	-40.68	-13	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-67.34	-43	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-66.99	-33	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-55.63	-23	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-39.76	-13	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-32.81	-13	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-36.66	-13	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-40.88	-13	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-66.56	-43	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-67.38	-33	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-55.13	-23	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-39.74	-13	PASS

DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-32.55	-13	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-36.56	-13	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-40.61	-13	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-67.26	-43	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	0.15	30	-68.45	-33	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	30	1000	-55.43	-23	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	1000	3000	-39.78	-13	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	3000	6000	-32.80	-13	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	6000	26000	-36.66	-13	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	26000	40000	-40.82	-13	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-66.68	-43	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-67.93	-33	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-55.37	-23	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-38.40	-13	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-32.84	-13	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-36.66	-13	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-40.96	-13	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-66.99	-43	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-66.72	-33	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-55.48	-23	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-17.49	-13	PASS

DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-32.71	-13	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-36.63	-13	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-40.90	-13	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-66.20	-43	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	0.15	30	-66.17	-33	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	30	1000	-55.47	-23	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	1000	3000	-39.75	-13	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	3000	6000	-32.79	-13	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	6000	26000	-36.50	-13	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	26000	40000	-40.78	-13	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-65.94	-43	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-67.19	-33	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-55.46	-23	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-39.67	-13	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-32.85	-13	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-36.60	-13	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-40.80	-13	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-66.90	-43	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-68.26	-33	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-55.43	-23	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-39.79	-13	PASS

DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-32.86	-13	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-36.69	-13	PASS
DC_41A_n77A -3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-40.87	-13	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-65.64	-43	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	0.15	30	-67.73	-33	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	30	1000	-55.57	-23	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	1000	3000	-39.68	-13	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	3000	6000	-32.84	-13	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	6000	26000	-36.62	-13	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	26000	40000	-40.97	-13	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-67.12	-43	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-66.77	-33	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-55.12	-23	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-39.68	-13	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-32.73	-13	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-36.69	-13	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-40.93	-13	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-66.66	-43	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-65.87	-33	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-55.49	-23	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-39.80	-13	PASS

DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-32.80	-13	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-36.45	-13	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-40.94	-13	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-66.78	-43	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	0.15	30	-67.07	-33	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	30	1000	-55.35	-23	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	1000	3000	-39.71	-13	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	3000	6000	-32.82	-13	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	6000	26000	-36.75	-13	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	26000	40000	-40.78	-13	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-67.09	-43	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-66.77	-33	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-55.45	-23	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-39.74	-13	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-32.79	-13	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-36.55	-13	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-40.77	-13	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-66.82	-43	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-66.15	-33	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-55.33	-23	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-39.85	-13	PASS

DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-32.56	-13	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-36.39	-13	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-40.93	-13	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-66.37	-43	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	0.15	30	-66.95	-33	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	30	1000	-55.33	-23	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	1000	3000	-39.80	-13	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	3000	6000	-32.73	-13	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	6000	26000	-36.66	-13	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	26000	40000	-40.95	-13	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-66.03	-43	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-67.74	-33	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-55.31	-23	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-39.74	-13	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-32.73	-13	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-36.52	-13	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-40.90	-13	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-67.24	-43	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-67.51	-33	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-55.36	-23	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-39.77	-13	PASS

DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-32.80	-13	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-36.84	-13	PASS
DC_41A_n77A -3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-40.91	-13	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-65.77	-43	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	0.15	30	-65.82	-33	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	30	1000	-55.33	-23	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	1000	3000	-39.77	-13	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	3000	6000	-32.81	-13	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	6000	26000	-36.60	-13	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	26000	40000	-40.88	-13	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-66.76	-43	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-66.26	-33	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-55.25	-23	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-32.67	-13	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-32.68	-13	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-36.64	-13	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-40.85	-13	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-66.89	-43	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-66.88	-33	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-55.30	-23	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-39.76	-13	PASS

DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-32.74	-13	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-36.80	-13	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-40.74	-13	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-67.31	-43	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	0.15	30	-67.41	-33	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	30	1000	-55.53	-23	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	1000	3000	-39.70	-13	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	3000	6000	-32.87	-13	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	6000	26000	-36.59	-13	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	26000	40000	-40.94	-13	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-66.34	-43	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-67.29	-33	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-55.74	-23	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-39.70	-13	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-32.69	-13	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-36.64	-13	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-40.84	-13	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-66.32	-43	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-67.65	-33	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-55.43	-23	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-39.78	-13	PASS

DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-32.72	-13	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-36.71	-13	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-40.80	-13	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-65.25	-43	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	0.15	30	-68.11	-33	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	30	1000	-55.37	-23	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	1000	3000	-39.84	-13	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	3000	6000	-32.61	-13	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	6000	26000	-36.55	-13	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	26000	40000	-40.91	-13	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-66.82	-43	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-66.34	-33	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-55.58	-23	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-39.72	-13	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-32.81	-13	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-36.68	-13	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-40.83	-13	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-65.48	-43	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-66.71	-33	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-55.43	-23	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-39.74	-13	PASS

DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-32.73	-13	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-36.50	-13	PASS
DC_41A_n77A -3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-40.95	-13	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-66.67	-43	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	0.15	30	-66.10	-33	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	30	1000	-55.38	-23	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	1000	3000	-39.80	-13	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	3000	6000	-32.79	-13	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	6000	26000	-36.74	-13	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	26000	40000	-40.73	-13	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-65.79	-43	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-66.58	-33	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-55.29	-23	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-39.72	-13	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-32.69	-13	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-36.64	-13	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-40.66	-13	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-67.28	-43	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-66.03	-33	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-55.44	-23	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-39.69	-13	PASS

DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-32.90	-13	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-36.70	-13	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-40.96	-13	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-67.35	-43	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	0.15	30	-66.93	-33	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	30	1000	-55.53	-23	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	1000	3000	-39.70	-13	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	3000	6000	-32.72	-13	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	6000	26000	-36.65	-13	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	26000	40000	-40.89	-13	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-67.36	-43	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-67.60	-33	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-55.50	-23	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-39.78	-13	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-32.74	-13	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-36.77	-13	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-40.86	-13	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-67.31	-43	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-67.28	-33	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-55.65	-23	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-39.79	-13	PASS

DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-32.83	-13	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-36.72	-13	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-40.79	-13	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-66.15	-43	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	0.15	30	-66.30	-33	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	30	1000	-55.14	-23	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	1000	3000	-39.79	-13	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	3000	6000	-32.81	-13	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	6000	26000	-36.72	-13	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	26000	40000	-41.01	-13	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-65.92	-43	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-66.90	-33	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-55.54	-23	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-39.79	-13	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-32.84	-13	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-36.81	-13	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-41.05	-13	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-65.50	-43	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-67.61	-33	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-55.31	-23	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-39.78	-13	PASS

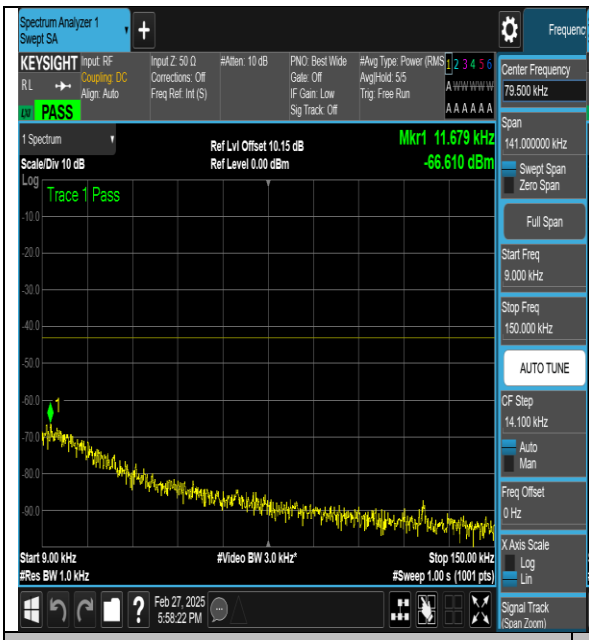
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-32.82	-13	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-36.61	-13	PASS
DC_41A_n77A -3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-40.96	-13	PASS
DC_41A_n77A -3450-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-66.91	-43	PASS
DC_41A_n77A -3450-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	0.15	30	-66.14	-33	PASS
DC_41A_n77A -3450-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	30	1000	-55.33	-23	PASS
DC_41A_n77A -3450-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	1000	3000	-39.77	-13	PASS
DC_41A_n77A -3450-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	3000	6000	-32.78	-13	PASS
DC_41A_n77A -3450-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	6000	26000	-36.71	-13	PASS
DC_41A_n77A -3450-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	26000	40000	-40.79	-13	PASS
DC_41A_n77A -3450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-66.92	-43	PASS
DC_41A_n77A -3450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-66.12	-33	PASS
DC_41A_n77A -3450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-55.44	-23	PASS
DC_41A_n77A -3450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-39.76	-13	PASS
DC_41A_n77A -3450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-32.79	-13	PASS
DC_41A_n77A -3450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-36.83	-13	PASS
DC_41A_n77A -3450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-40.80	-13	PASS
DC_41A_n77A -3450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-65.60	-43	PASS
DC_41A_n77A -3450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-66.35	-33	PASS
DC_41A_n77A -3450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-55.55	-23	PASS
DC_41A_n77A -3450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-39.78	-13	PASS

DC_41A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-32.72	-13	PASS
DC_41A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-36.78	-13	PASS
DC_41A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-41.01	-13	PASS
DC_41A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-66.24	-43	PASS
DC_41A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	0.15	30	-65.43	-33	PASS
DC_41A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	30	1000	-55.53	-23	PASS
DC_41A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	1000	3000	-39.74	-13	PASS
DC_41A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	3000	6000	-32.81	-13	PASS
DC_41A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	6000	26000	-36.85	-13	PASS
DC_41A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	26000	40000	-40.85	-13	PASS
DC_41A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-66.17	-43	PASS
DC_41A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-65.93	-33	PASS
DC_41A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-55.51	-23	PASS
DC_41A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-39.70	-13	PASS
DC_41A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-32.81	-13	PASS
DC_41A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-36.61	-13	PASS
DC_41A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-40.88	-13	PASS
DC_41A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-66.11	-43	PASS
DC_41A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-65.67	-33	PASS
DC_41A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-55.69	-23	PASS
DC_41A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-39.69	-13	PASS

DC_41A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-32.62	-13	PASS
DC_41A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-36.63	-13	PASS
DC_41A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-40.76	-13	PASS
DC_41A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-66.74	-43	PASS
DC_41A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	0.15	30	-66.80	-33	PASS
DC_41A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	30	1000	-55.05	-23	PASS
DC_41A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	1000	3000	-39.78	-13	PASS
DC_41A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	3000	6000	-32.89	-13	PASS
DC_41A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	6000	26000	-36.70	-13	PASS
DC_41A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	26000	40000	-40.81	-13	PASS
DC_41A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-66.32	-43	PASS
DC_41A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-67.77	-33	PASS
DC_41A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-55.23	-23	PASS
DC_41A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-39.83	-13	PASS
DC_41A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-32.51	-13	PASS
DC_41A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-36.60	-13	PASS
DC_41A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-40.97	-13	PASS
DC_41A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-66.71	-43	PASS
DC_41A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-65.67	-33	PASS
DC_41A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-55.22	-23	PASS
DC_41A_n77A -3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-39.81	-13	PASS

DC_41A_n77A -3450-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-32.89	-13	PASS
DC_41A_n77A -3450-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-36.78	-13	PASS
DC_41A_n77A -3450-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-41.02	-13	PASS

Test Graphs



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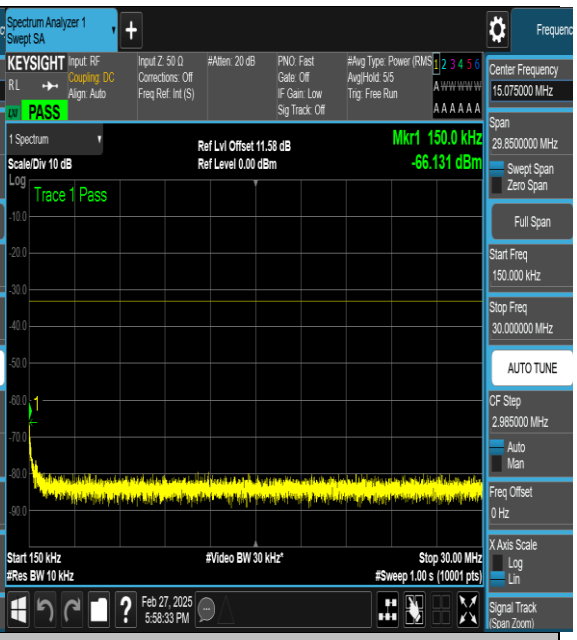

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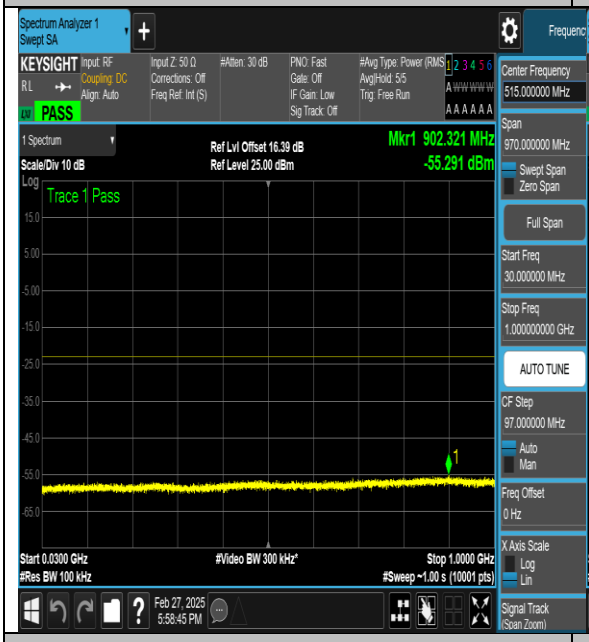

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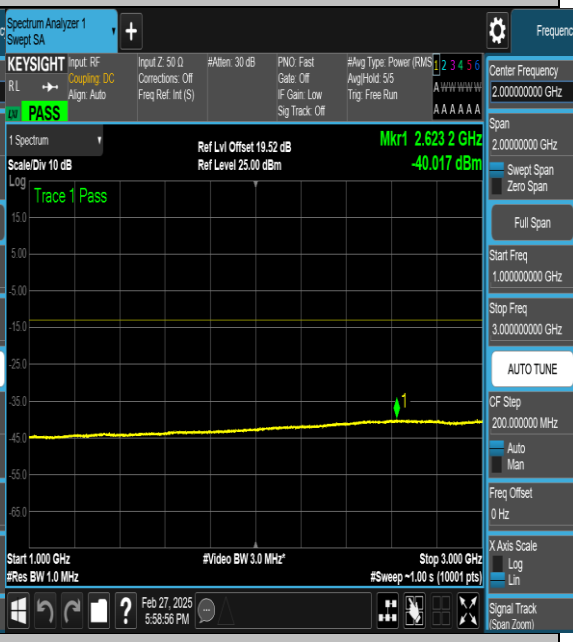

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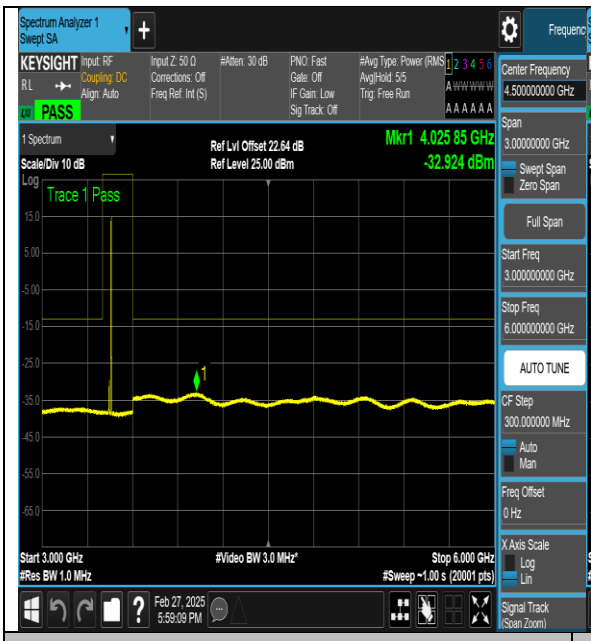

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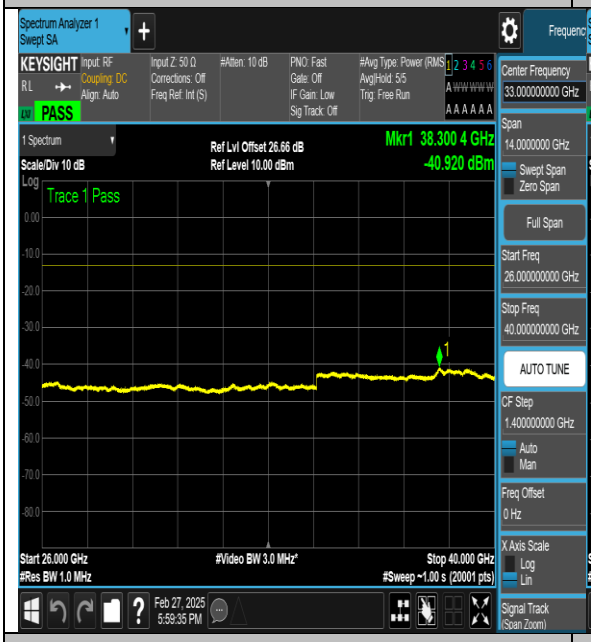

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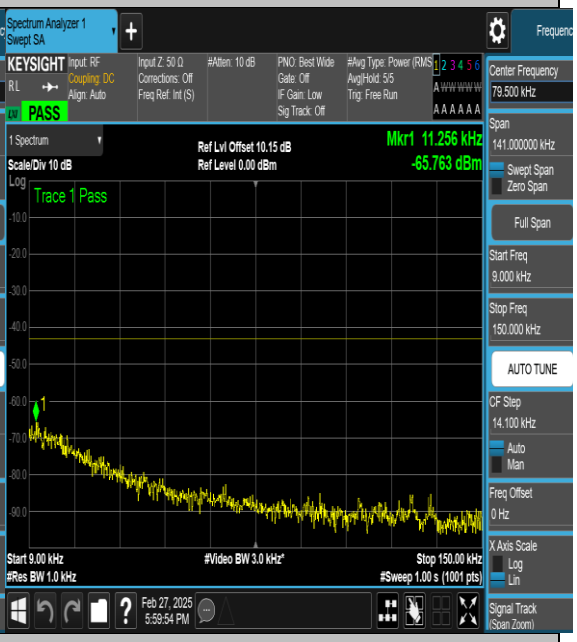

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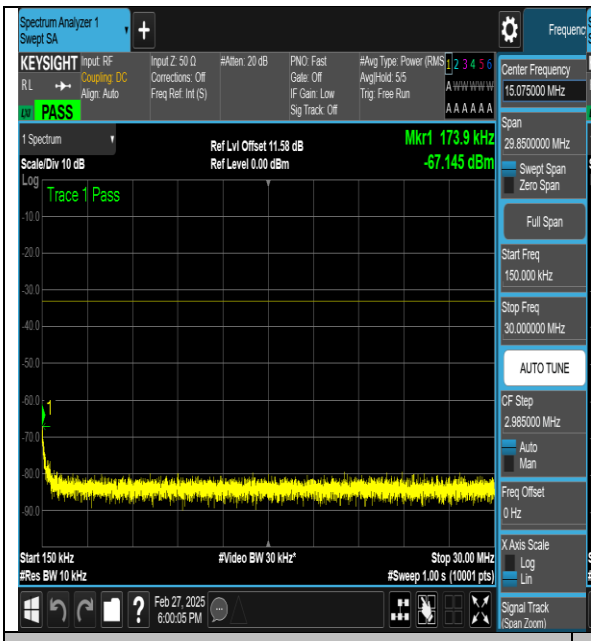

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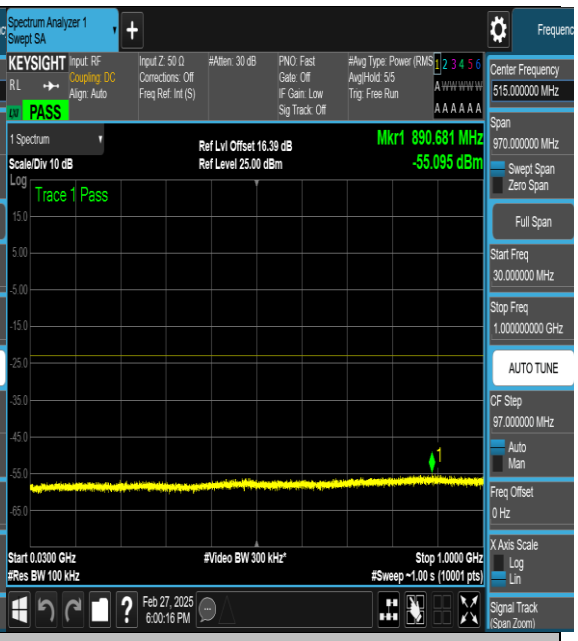

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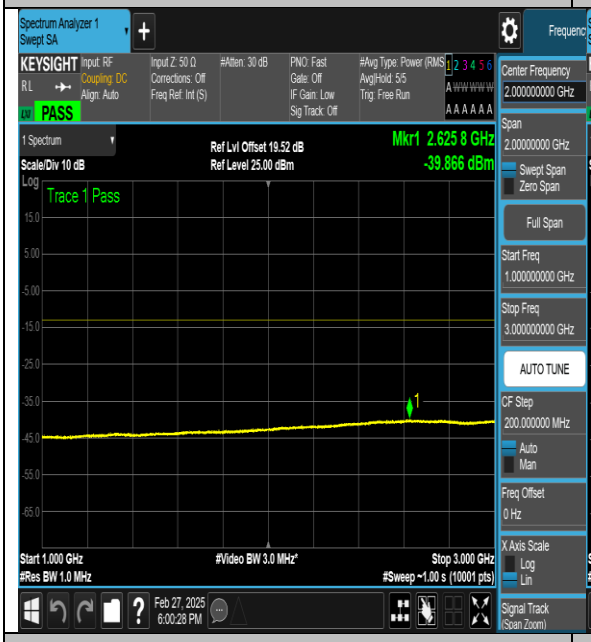

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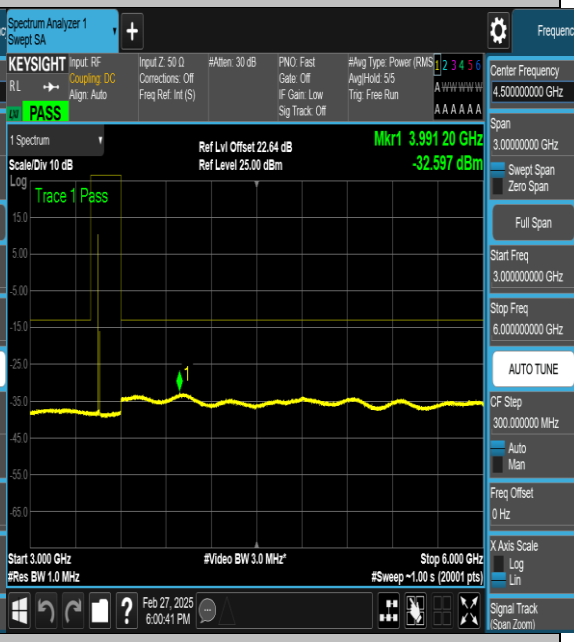

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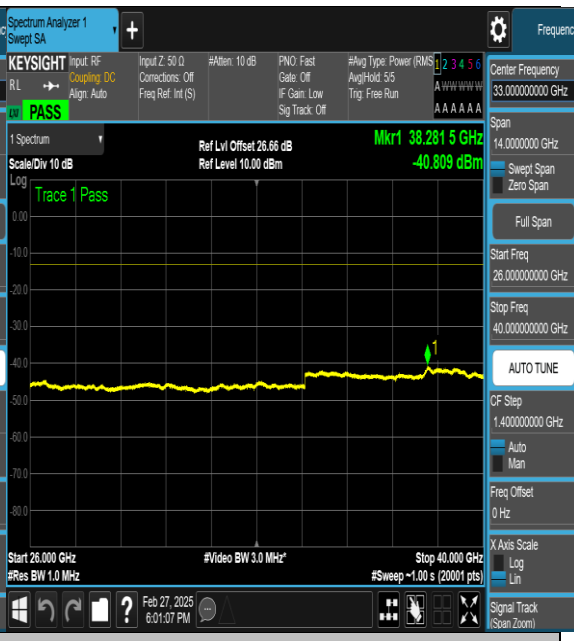

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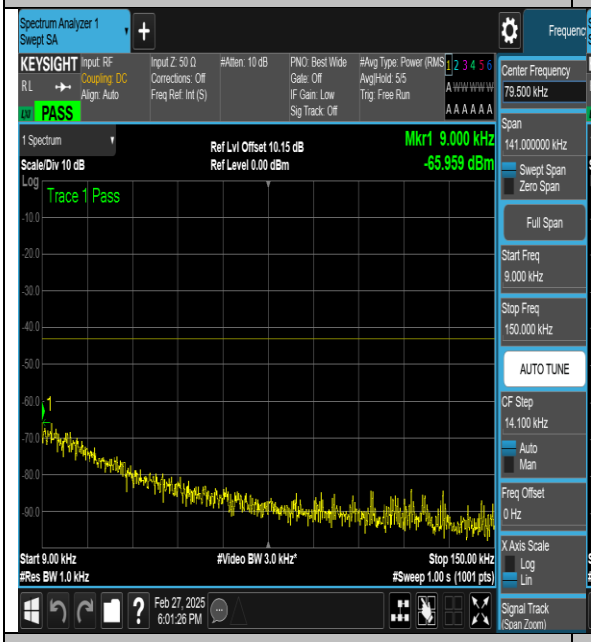

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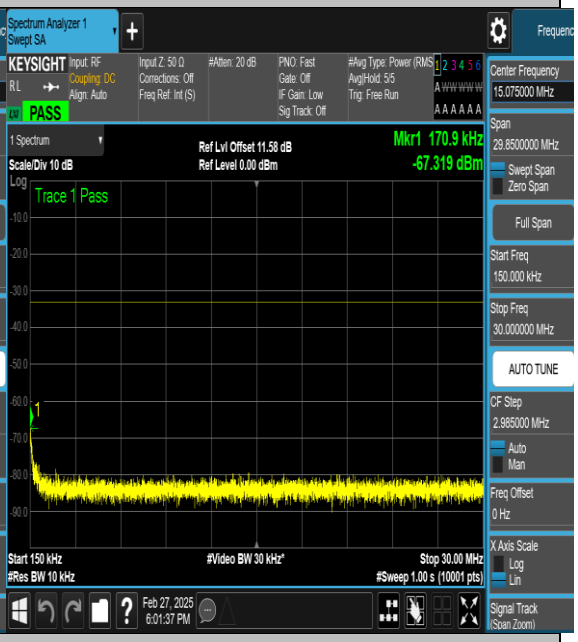

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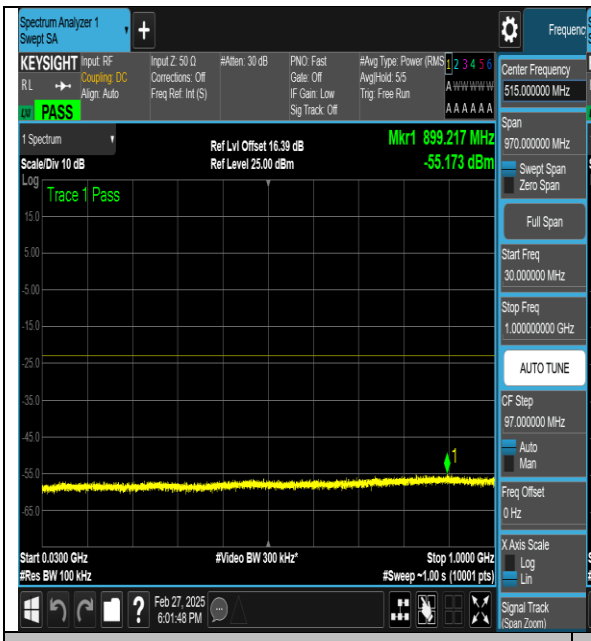

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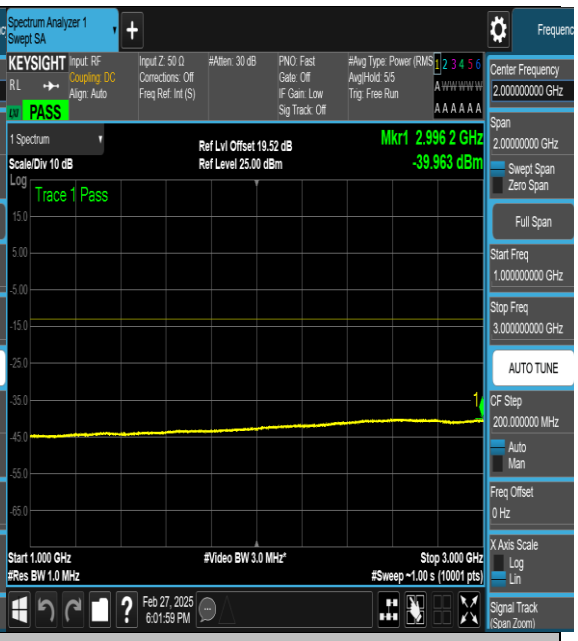

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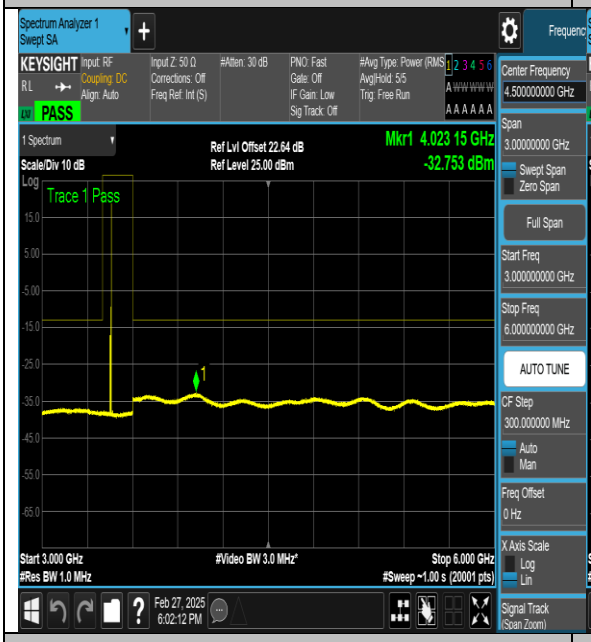

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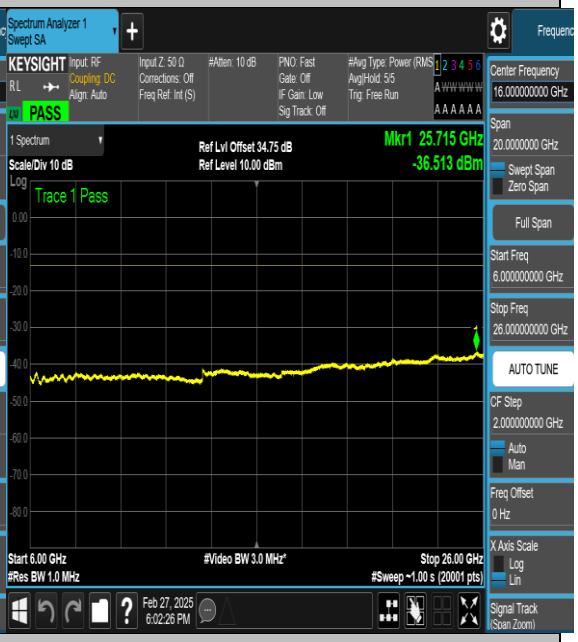

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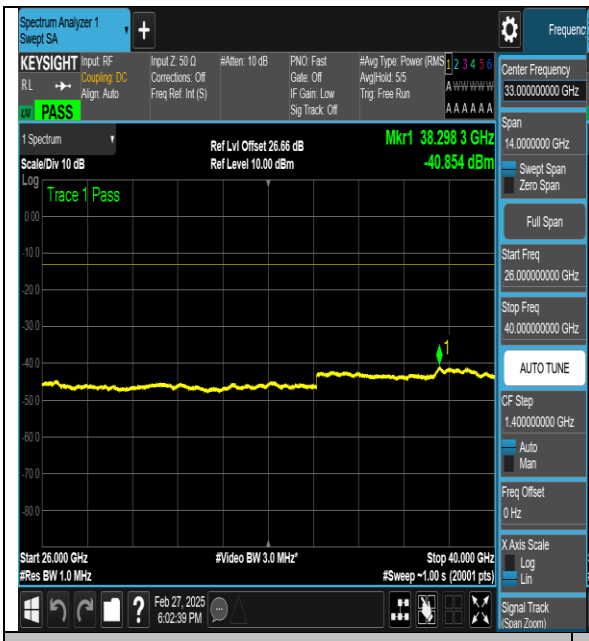

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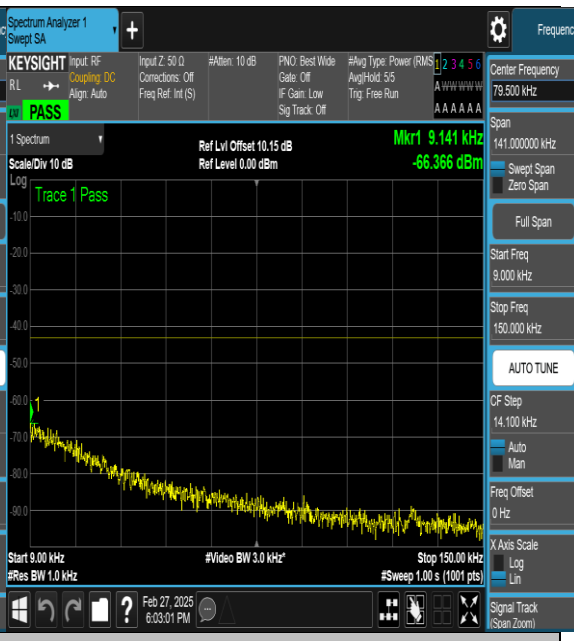

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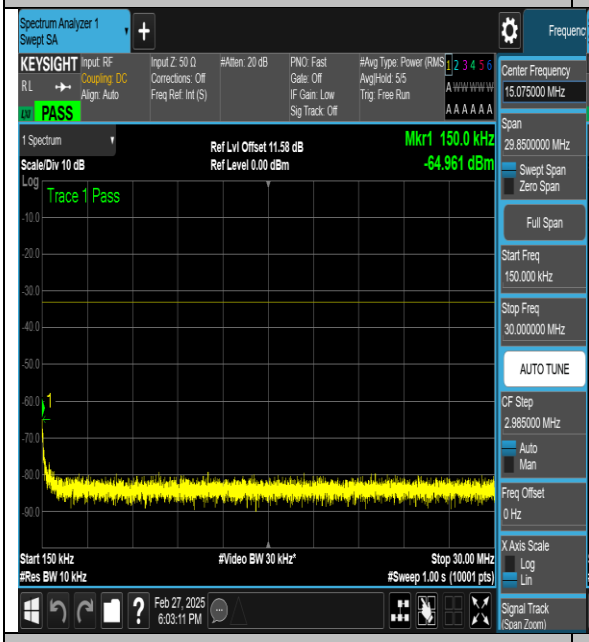

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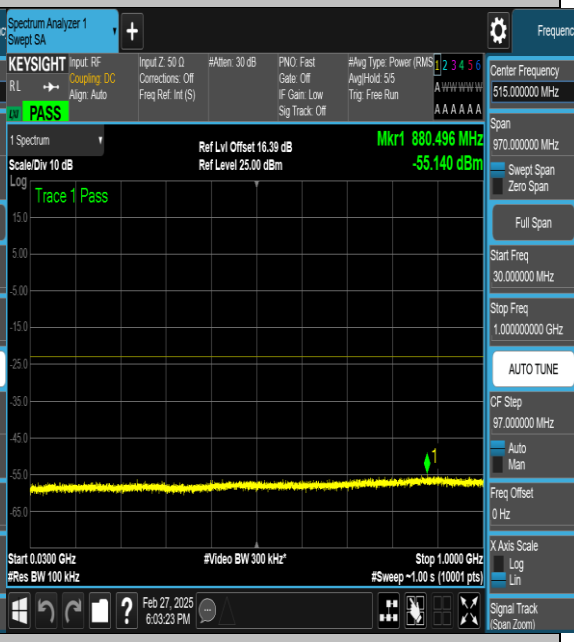

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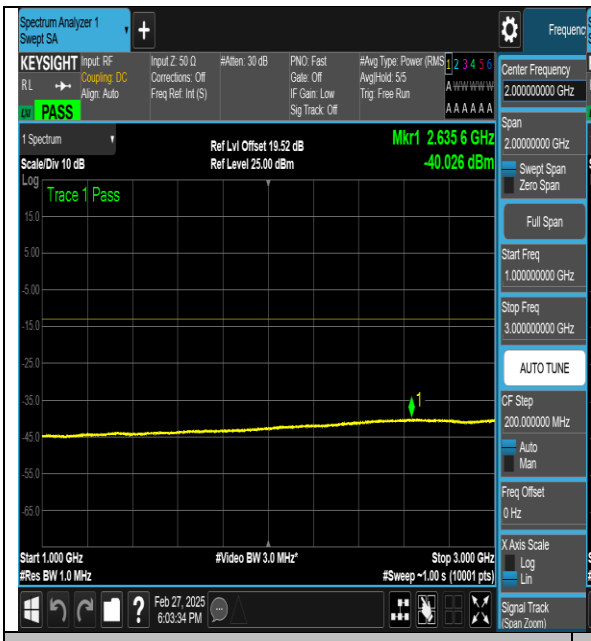

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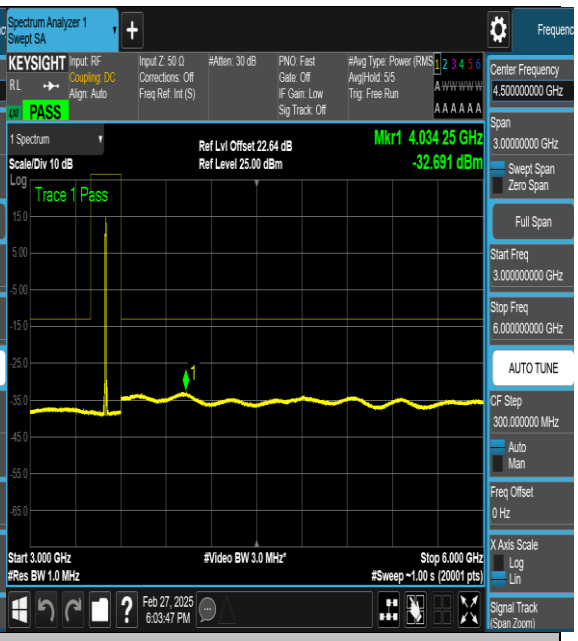

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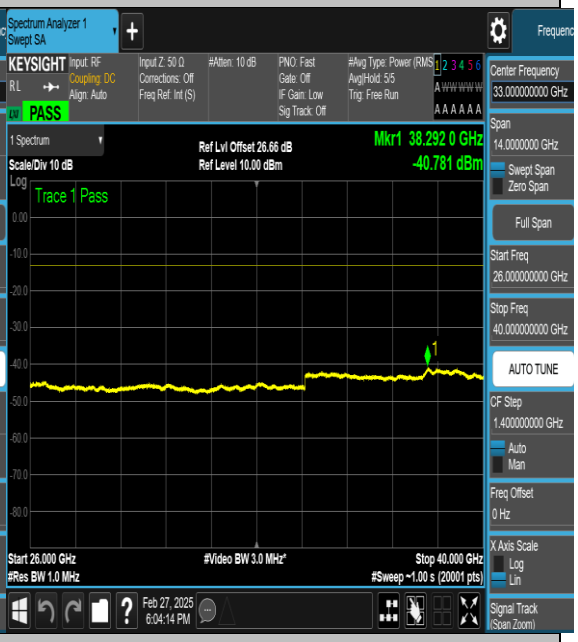

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