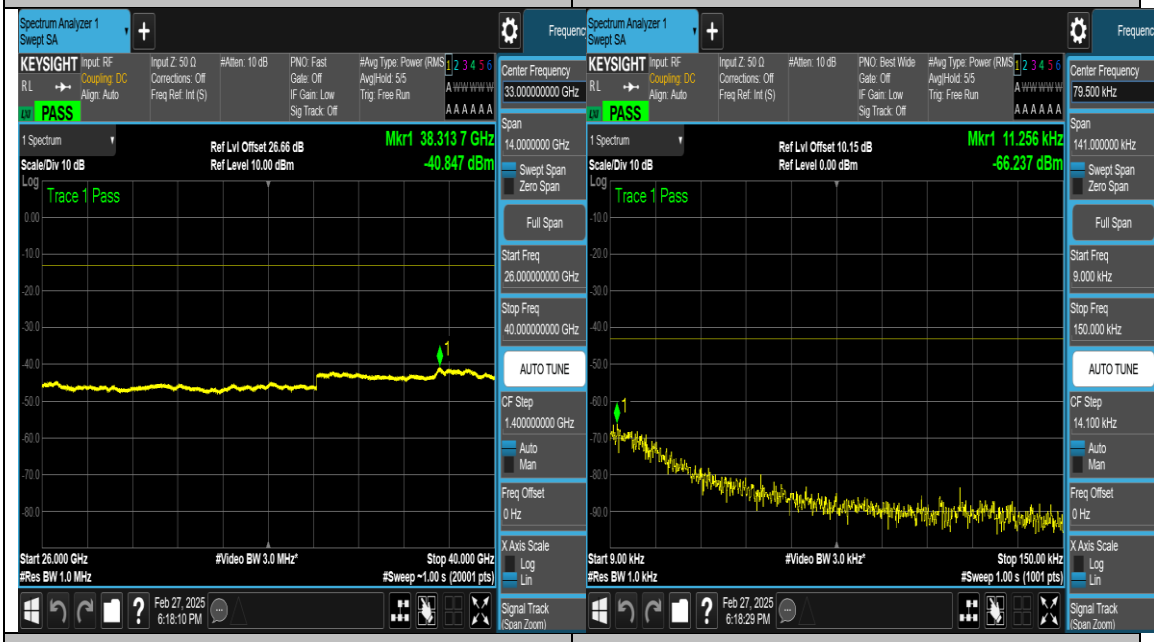


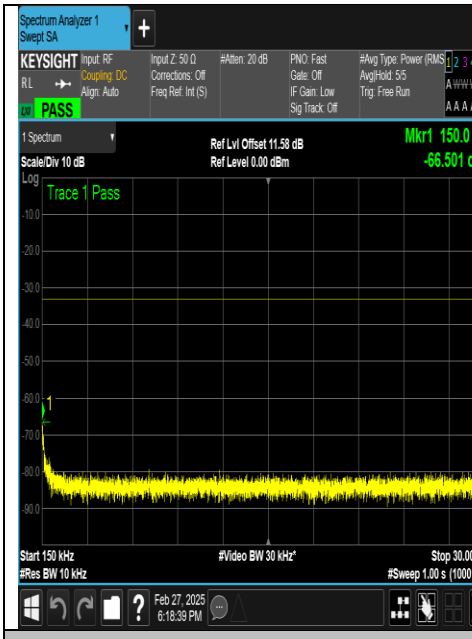
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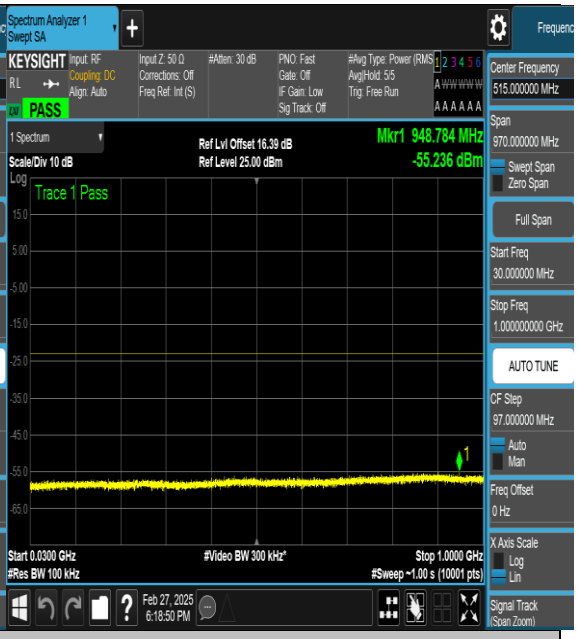


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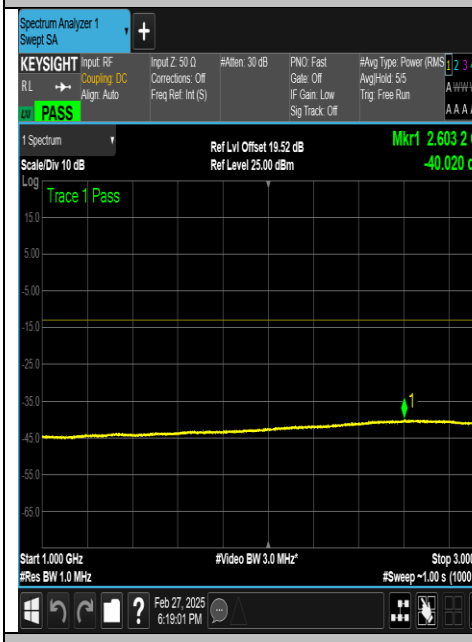
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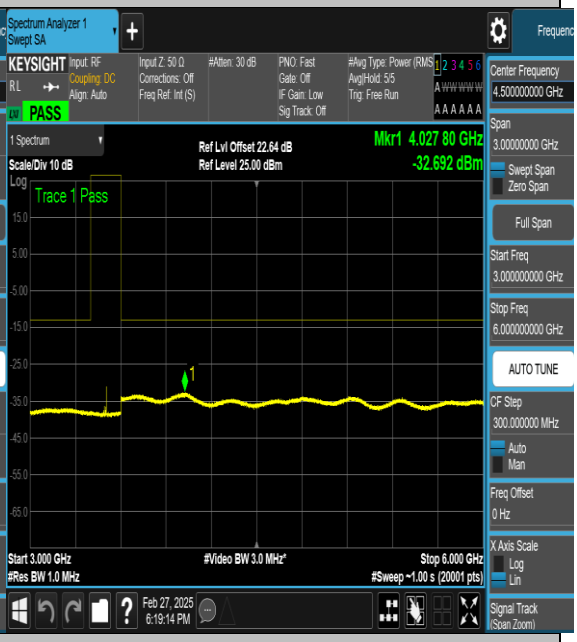
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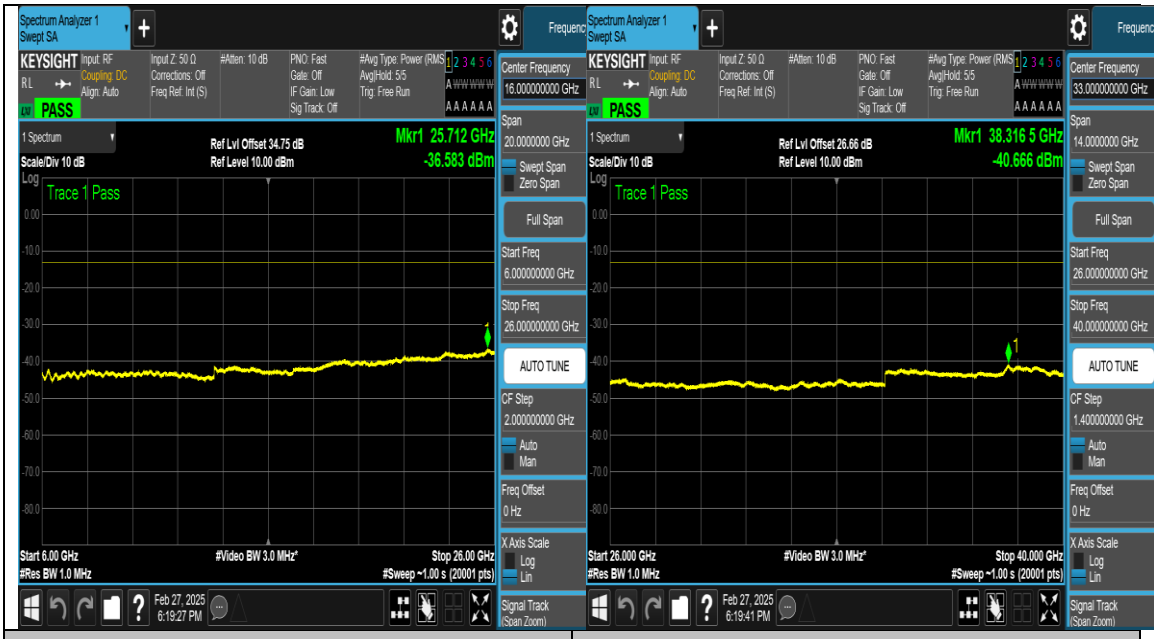
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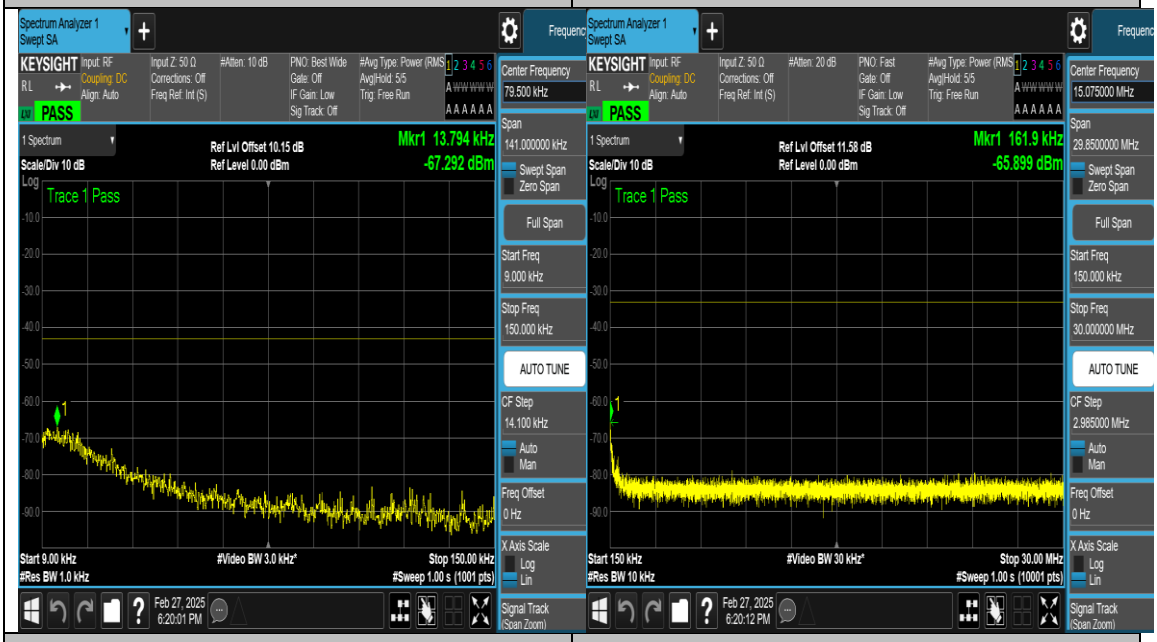


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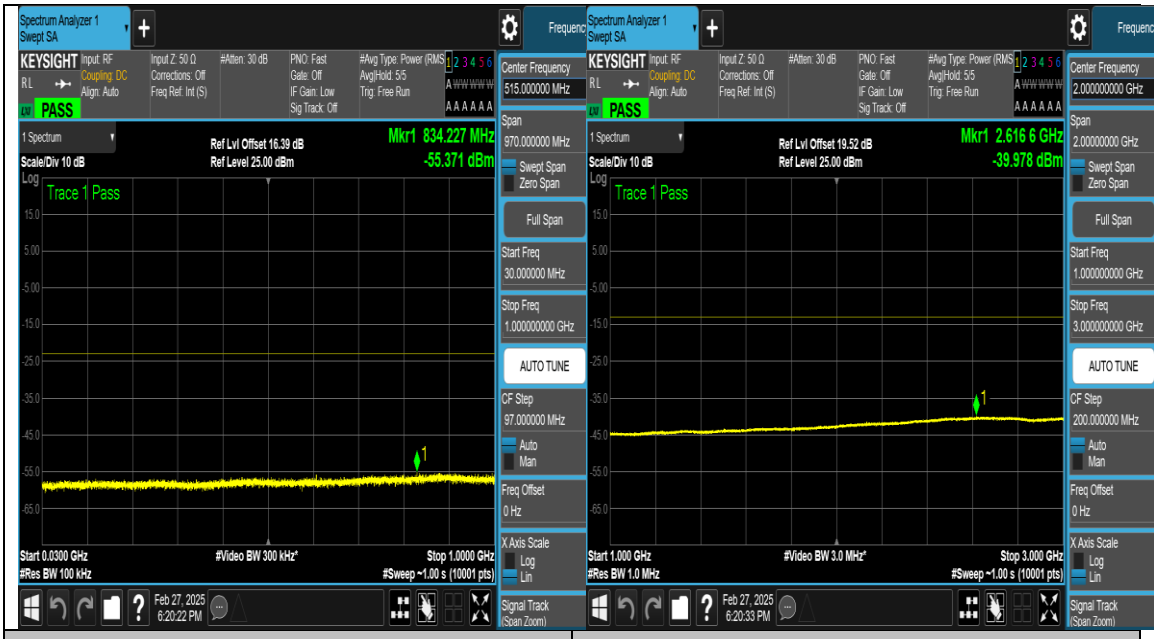
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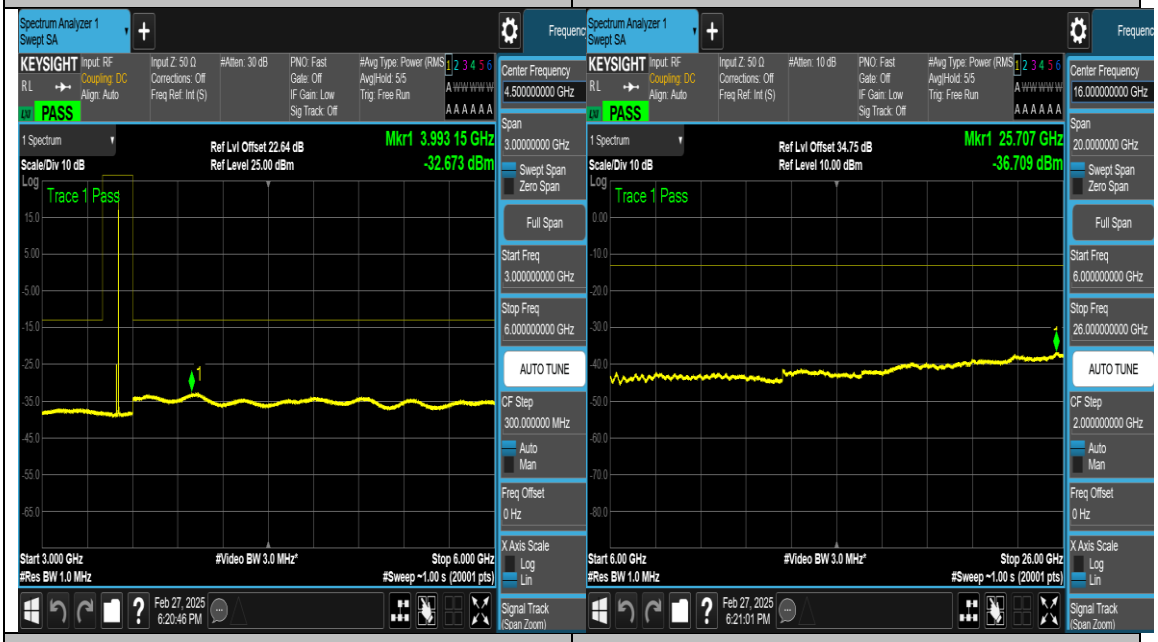
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NTNV-DC_41A_n77A-3450-3550-PC2-30-5+15-M+M-N/A-N/A-DFT-QPSK-Edge_1RB_Right-0.15-30-PASS



NTNV-DC_41A_n77A-3450-3550-PC2-30-5+15-M+M-N/A-N/A-DFT-QPSK-Edge_1RB_Right-30-1000-PASS

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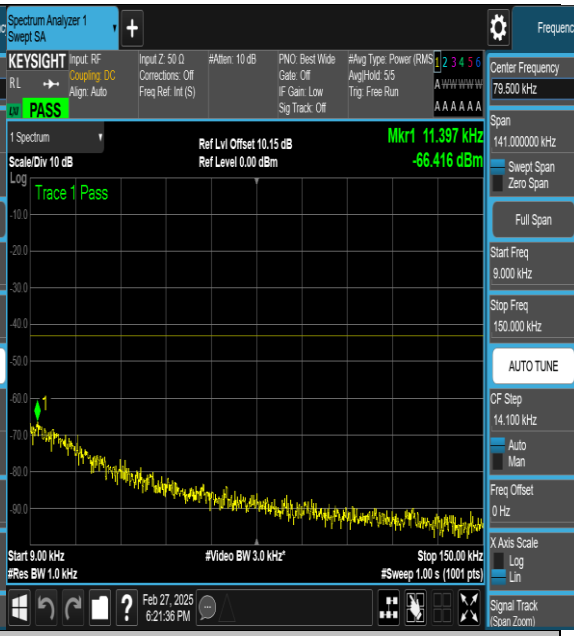


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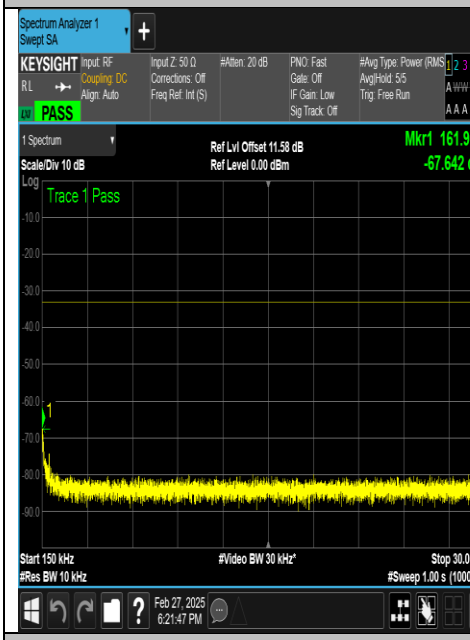
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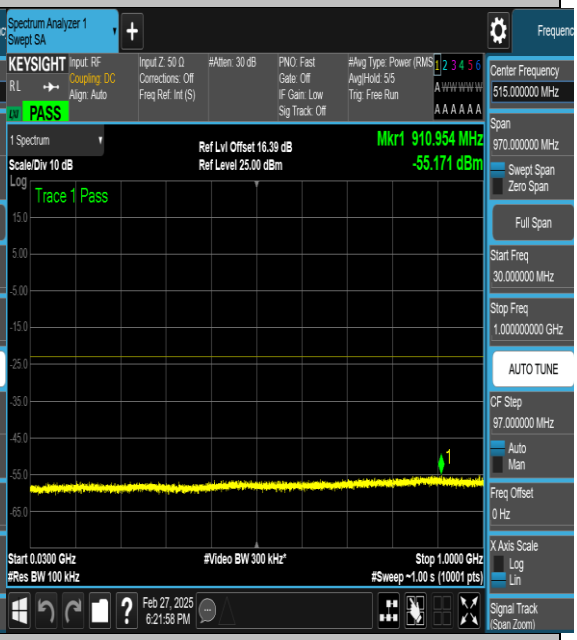
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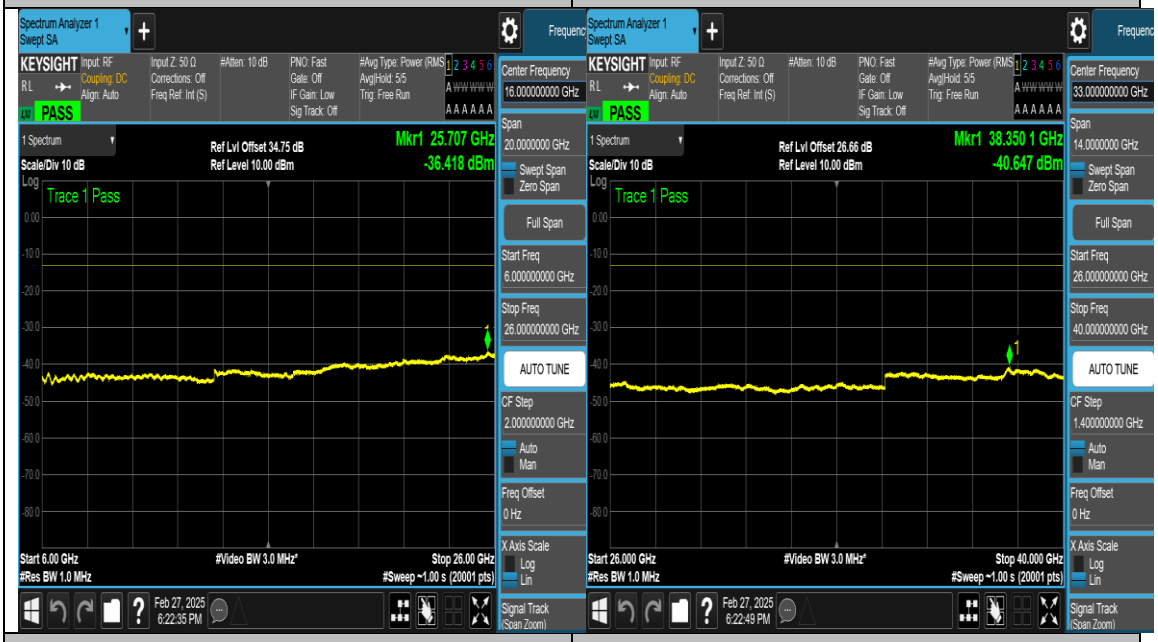
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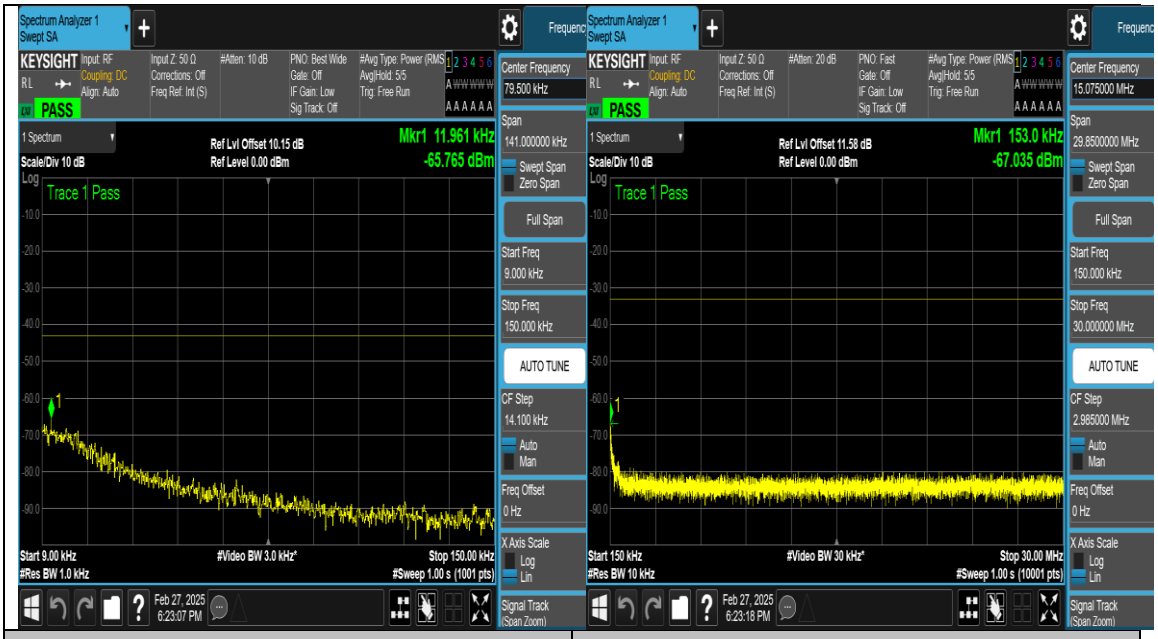
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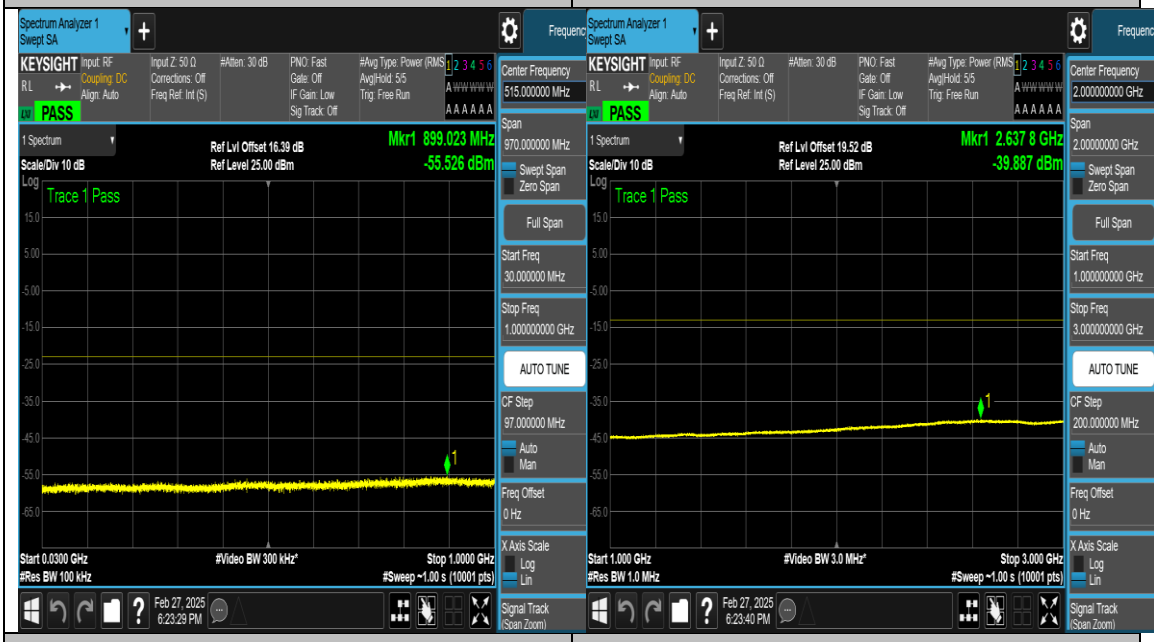
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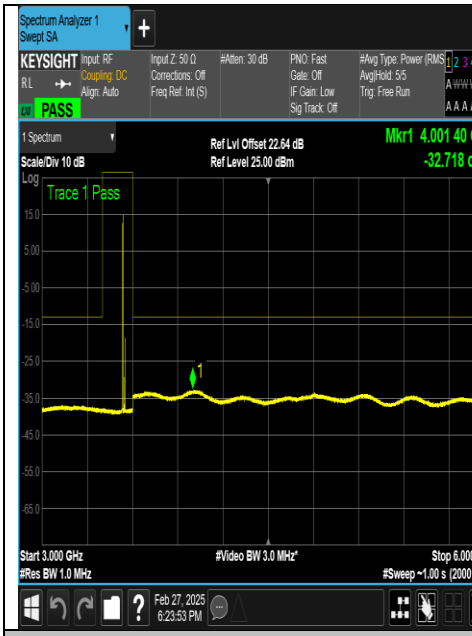
NTNV-DC_41A_n77A-3450-3550-PC2-30-5+15-M+H-N/A-N/A-DFT-QPSK-Outer_Full-6000-26000-PASS	NTNV-DC_41A_n77A-3450-3550-PC2-30-5+15-M+H-N/A-N/A-DFT-QPSK-Outer_Full-26000-40000-PASS



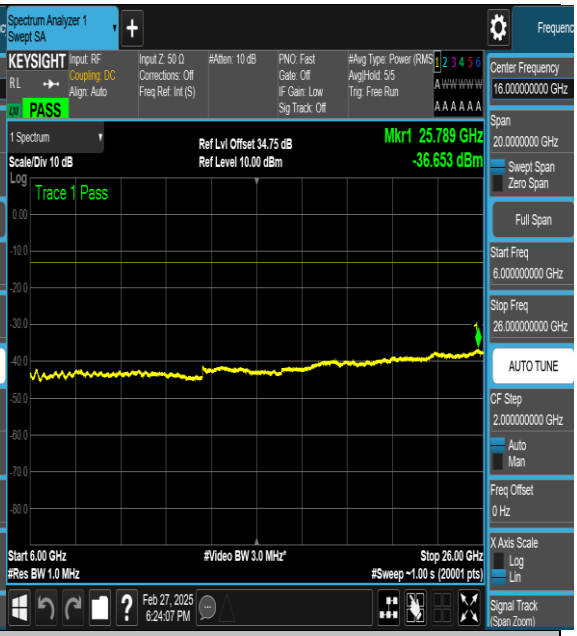
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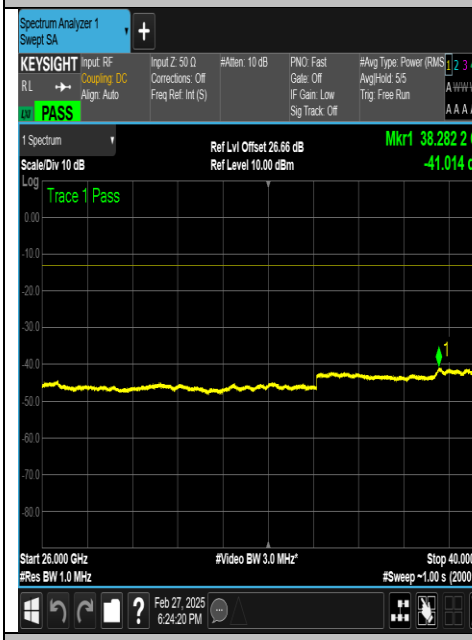
NTNV-DC_41A_n77A-3450-3550-PC2-30-5+15-M+H-N/A-N/A-DFT-QPSK-Edge_1RB_Left-30-1000-PASS	NTNV-DC_41A_n77A-3450-3550-PC2-30-5+15-M+H-N/A-N/A-DFT-QPSK-Edge_1RB_Left-1000-3000-PASS



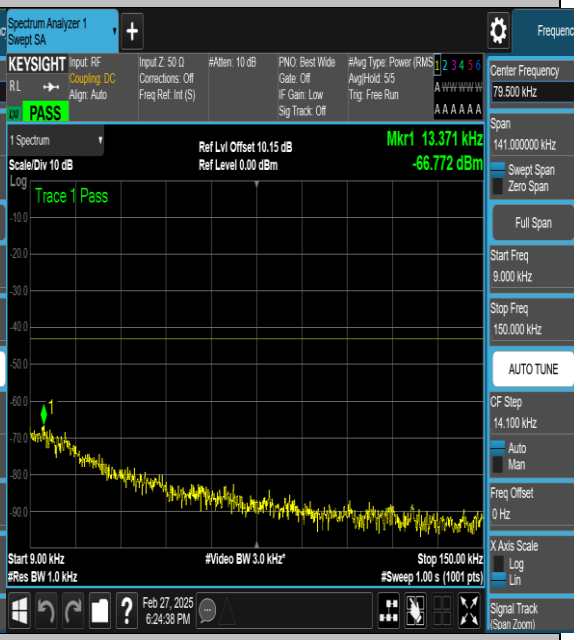
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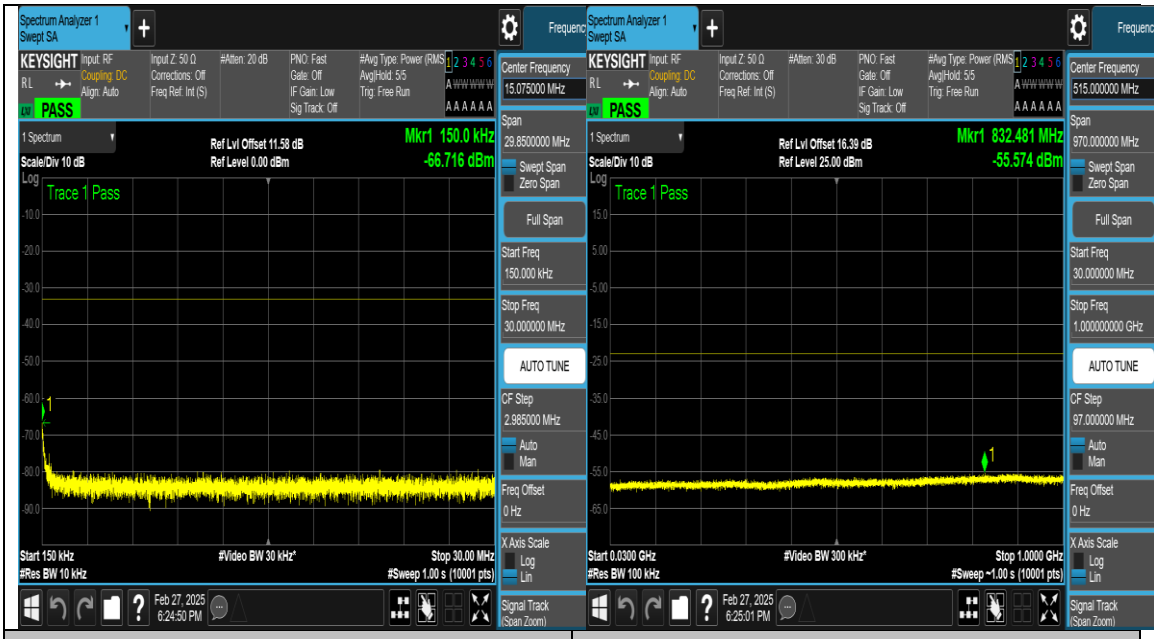
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NTNV-DC_41A_n77A-3450-3550-PC2-30-5+15-M+H-N/A-N/A-DFT-QPSK-Edge_1RB_Left-26000-40000-PASS

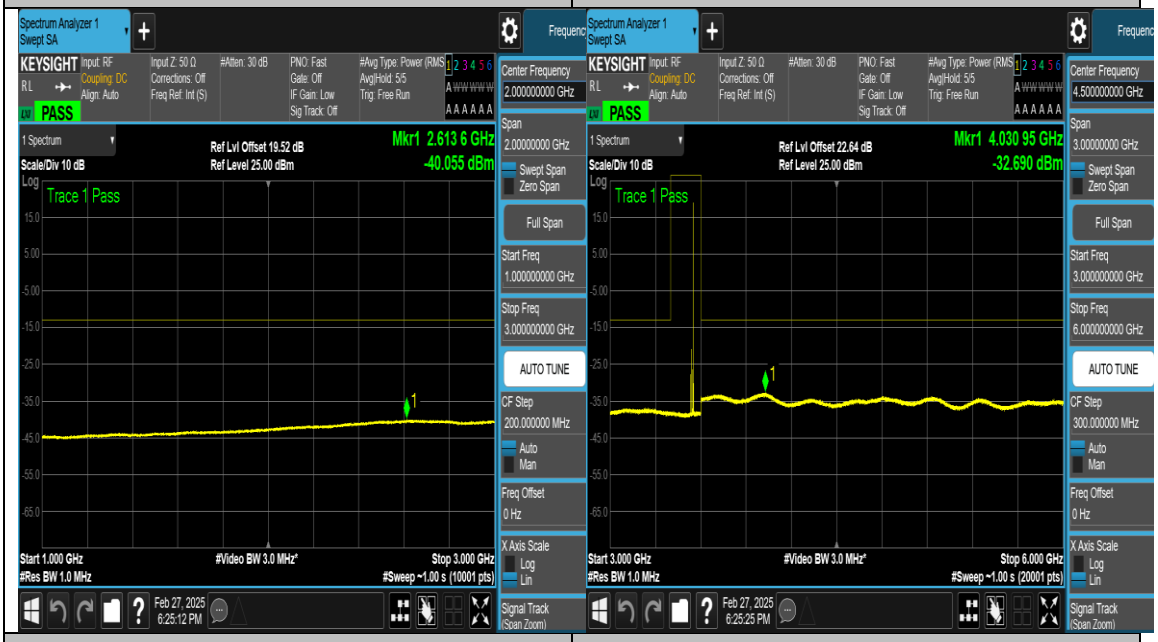


NTNV-DC_41A_n77A-3450-3550-PC2-30-5+15-M+H-N/A-N/A-DFT-QPSK-Edge_1RB_Right-0.009-0.15-PASS



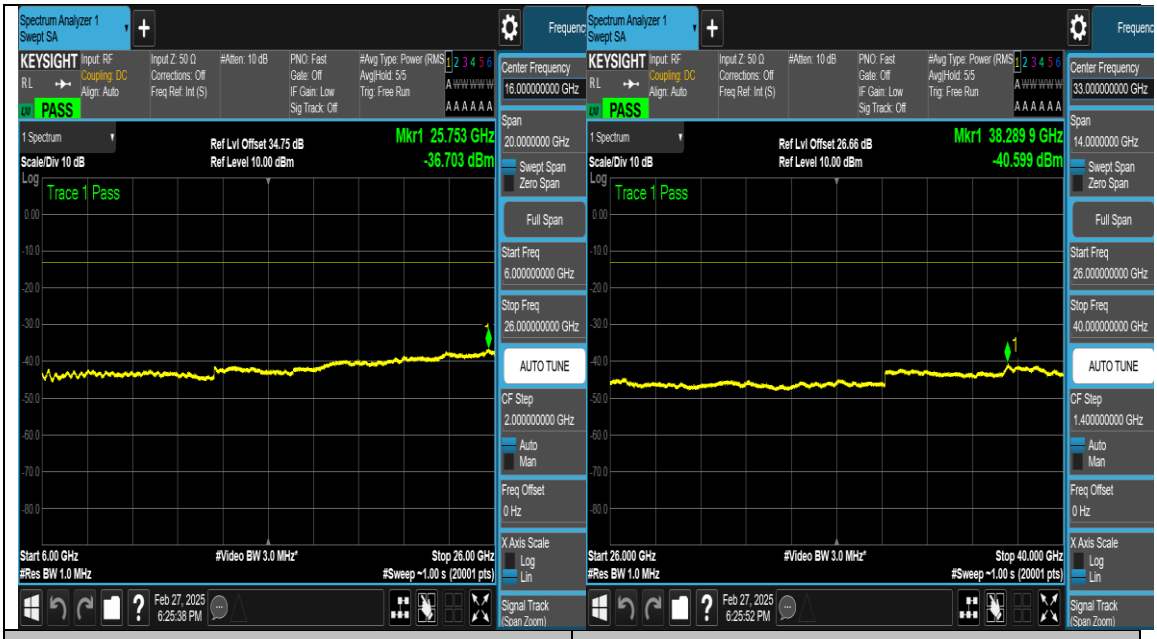
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NTNV-DC_41A_n77A-3450-3550-PC2-30-5+15-M+H-N/A-N/A-DFT-QPSK-Edge_1RB_Right-30-1000-PASS

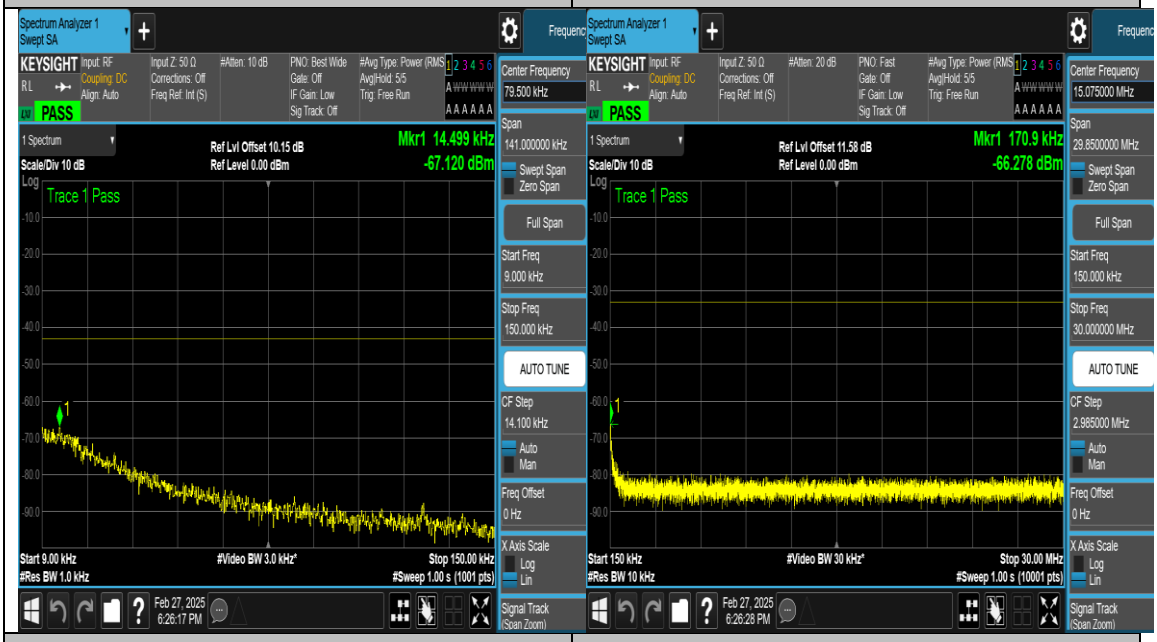


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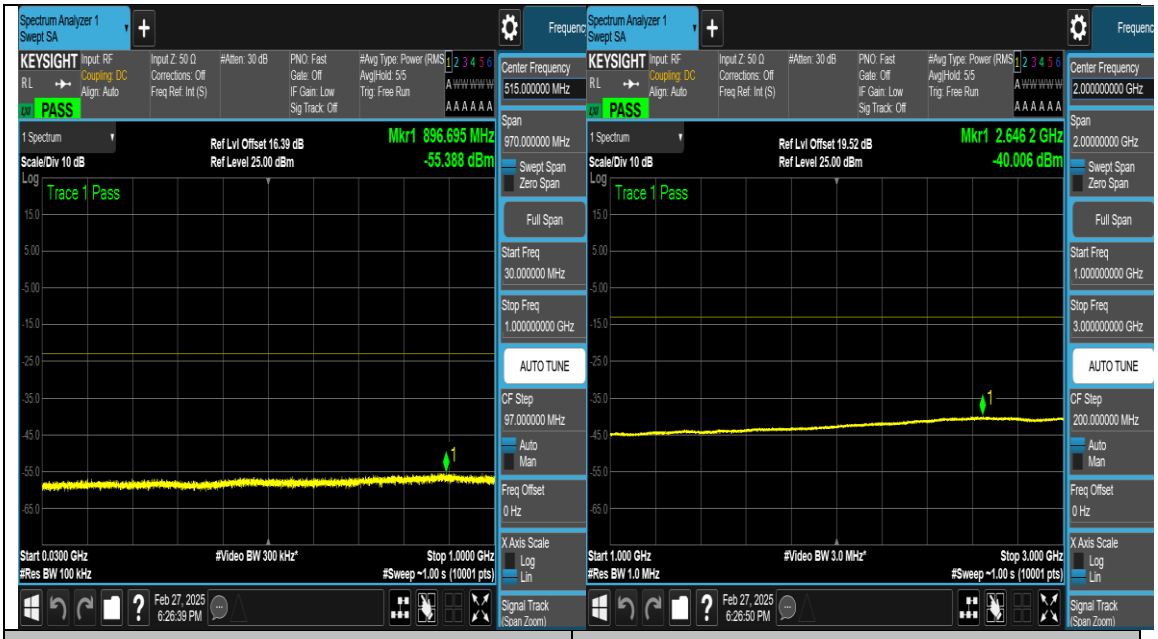
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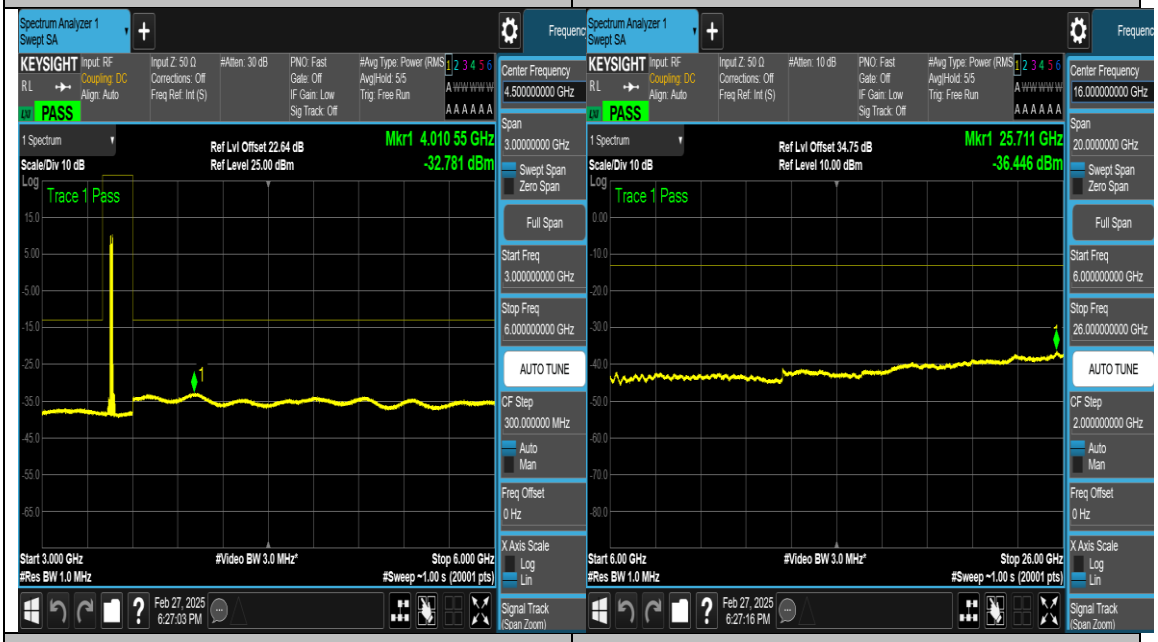
NTNV-DC_41A_n77A-3450-3550-PC2-30-5+15-M+H-N/A-N/A-DFT-QPSK-Edge_1RB_Right-6000-26000-PASS	NTNV-DC_41A_n77A-3450-3550-PC2-30-5+15-M+H-N/A-N/A-DFT-QPSK-Edge_1RB_Right-26000-40000-PASS



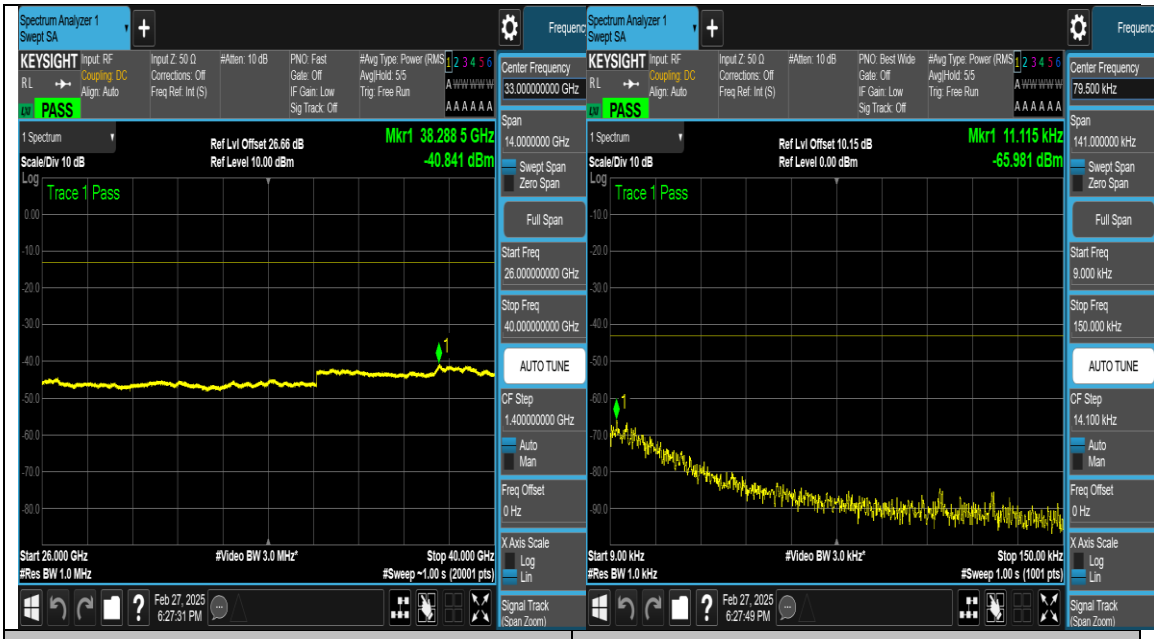
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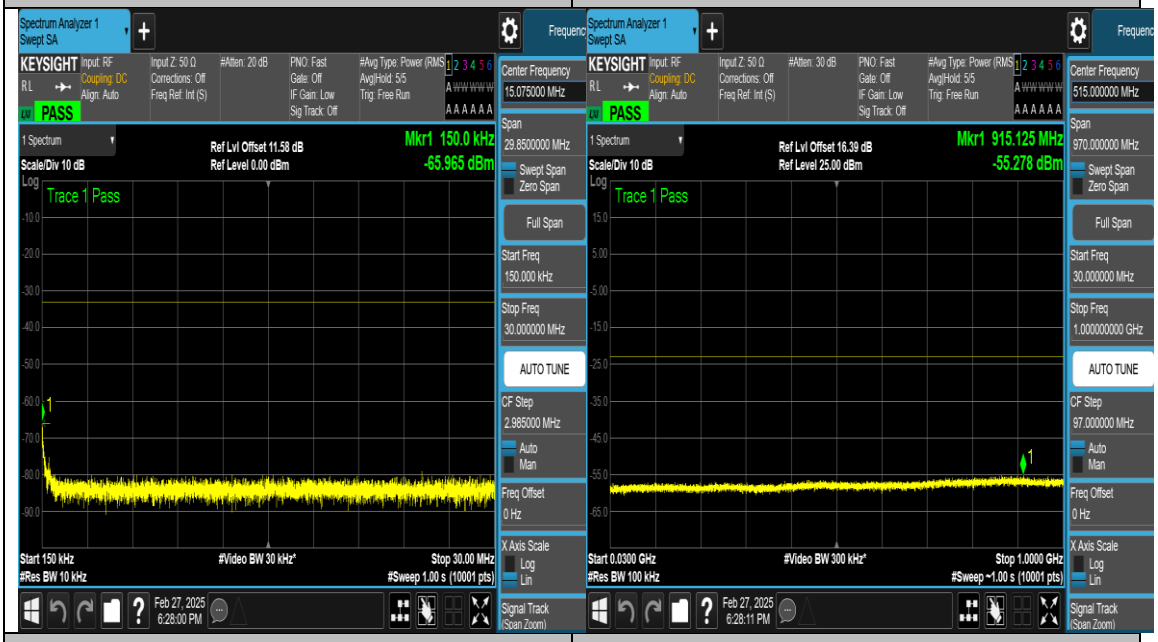
NTNV-DC_41A_n77A-3450-3550-PC2-30-5+20-M+L-N/A-N/A-DFT-QPSK-Outer_Full-30-1000-PASS	NTNV-DC_41A_n77A-3450-3550-PC2-30-5+20-M+L-N/A-N/A-DFT-QPSK-Outer_Full-1000-3000-PASS



NTNV-DC_41A_n77A-3450-3550-PC2-30-5+20-M+L-N/A-N/A-DFT-QPSK-Outer_Full-3000-6000-PASS	NTNV-DC_41A_n77A-3450-3550-PC2-30-5+20-M+L-N/A-N/A-DFT-QPSK-Outer_Full-6000-26000-PASS



NTNV-DC_41A_n77A-3450-3550-PC2-30-5+20-M+L-N/A-N/A-DFT-QPSK-Outer_Full-26000-40000-PASS	NTNV-DC_41A_n77A-3450-3550-PC2-30-5+20-M+L-N/A-N/A-DFT-QPSK-Edge_1RB_Left-0.009-0.15-PASS

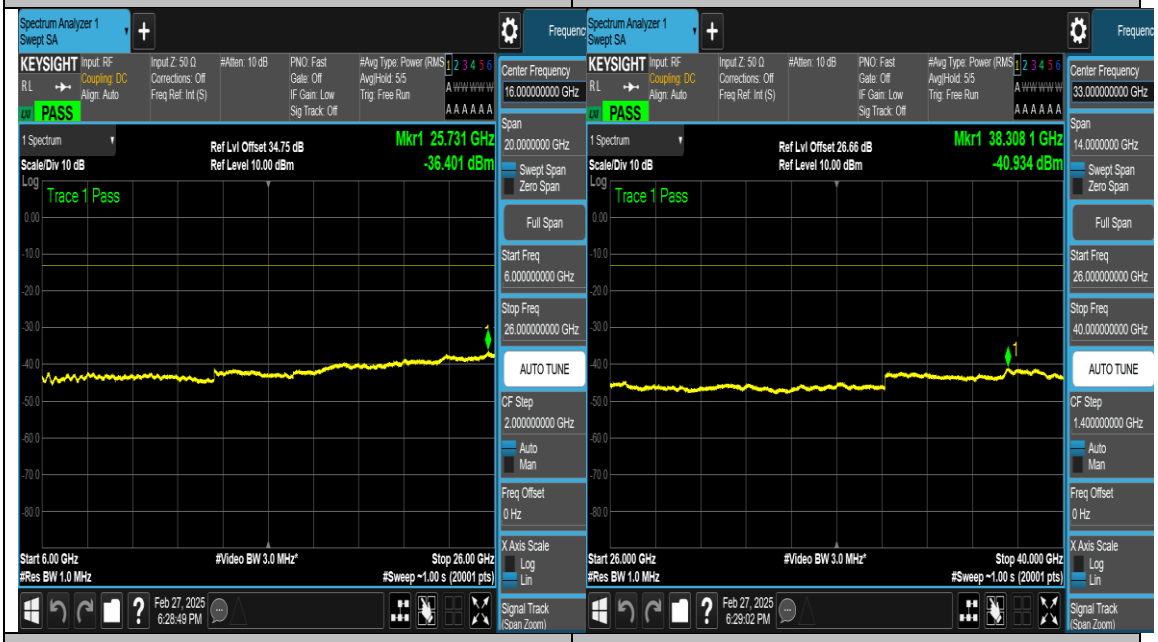


NTNV-DC_41A_n77A-3450-3550-PC2-30-5+20-M+L-N/A-N/A-DFT-QPSK-Edge_1RB_Left-0.15-30-PASS	NTNV-DC_41A_n77A-3450-3550-PC2-30-5+20-M+L-N/A-N/A-DFT-QPSK-Edge_1RB_Left-30-1000-PASS



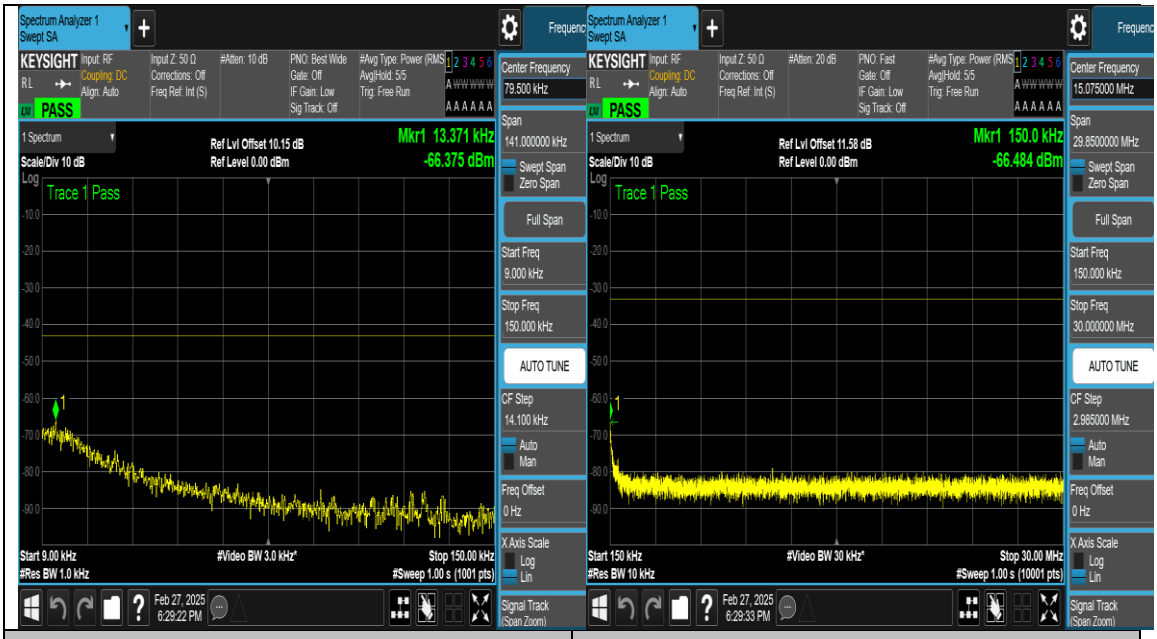
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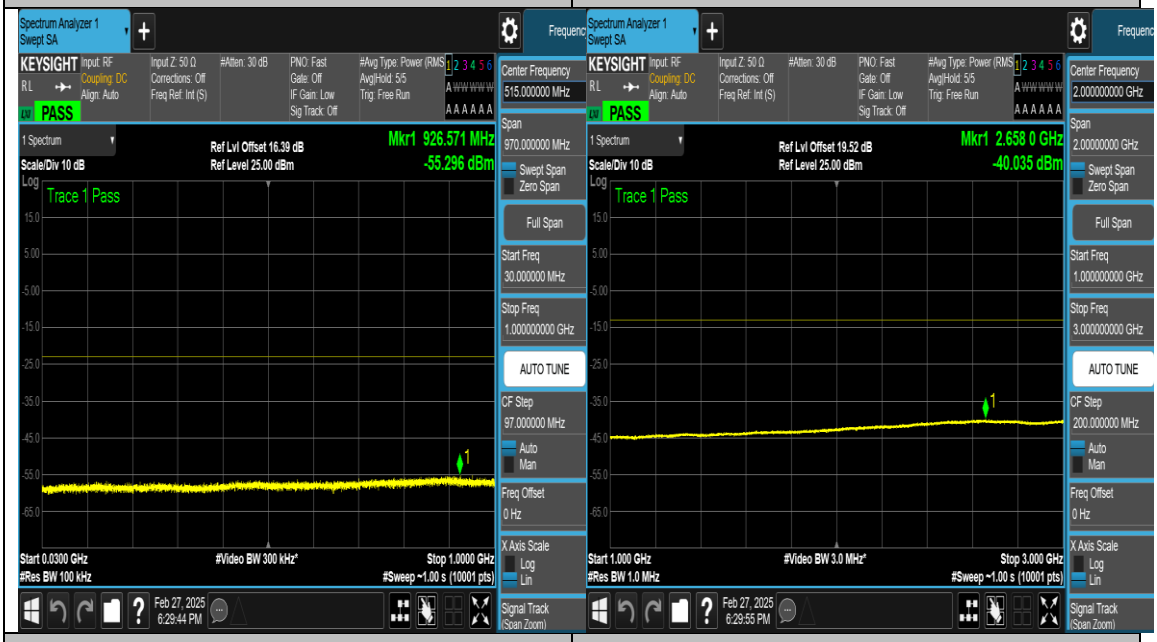
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NTNV-DC_41A_n77A-3450-3550-PC2-30-5+20-M+L-N/A-N/A-DFT-QPSK-Edge_1RB_Left-26000-40000-PASS



NTNV-DC_41A_n77A-3450-3550-PC2-30-5+20-M+L-N/A-N/A-DFT-QPSK-Edge_1RB_Right-0.009-0.15-PASS

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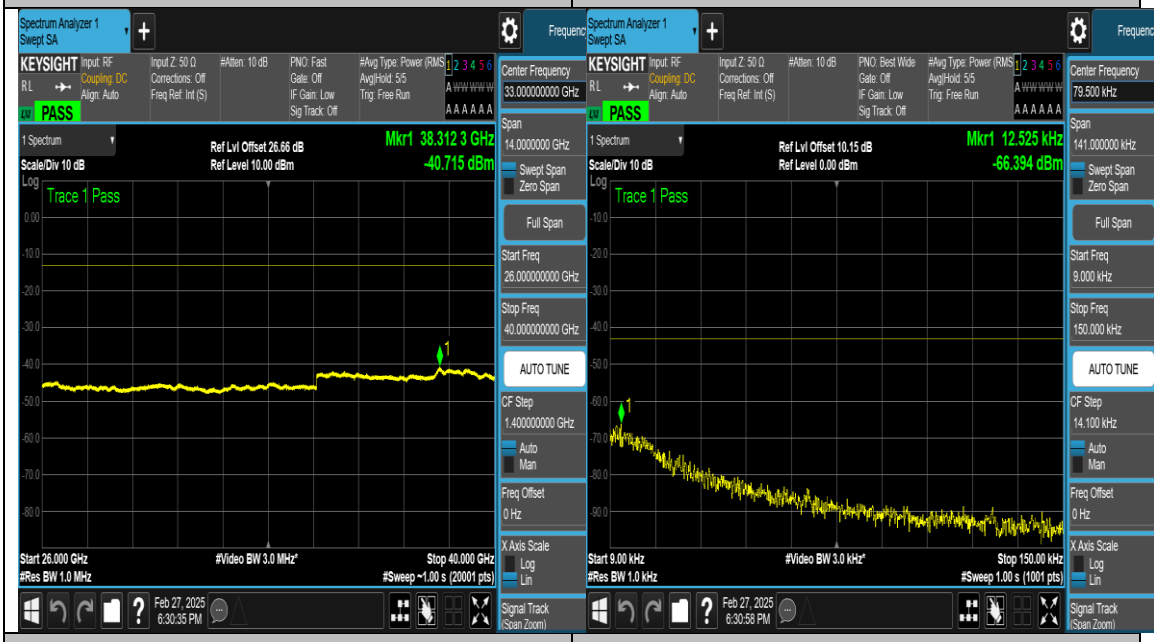


NTNV-DC_41A_n77A-3450-3550-PC2-30-5+20-M+L-N/A-N/A-DFT-QPSK-Edge_1RB_Right-30-1000-PASS

NTNV-DC_41A_n77A-3450-3550-PC2-30-5+20-M+L-N/A-N/A-DFT-QPSK-Edge_1RB_Right-1000-3000-PASS



NTNV-DC_41A_n77A-3450-3550-PC2-30-5+20-M+L-N/A-N/A-DFT-QPSK-Edge_1RB_Right-3000-6000-PASS	NTNV-DC_41A_n77A-3450-3550-PC2-30-5+20-M+L-N/A-N/A-DFT-QPSK-Edge_1RB_Right-6000-26000-PASS



NTNV-DC_41A_n77A-3450-3550-PC2-30-5+20-M+L-N/A-N/A-DFT-QPSK-Edge_1RB_Right-26000-40000-PASS	NTNV-DC_41A_n77A-3450-3550-PC2-30-5+20-M+M-N/A-N/A-DFT-QPSK-Outer_Full-0.009-0.15-PASS