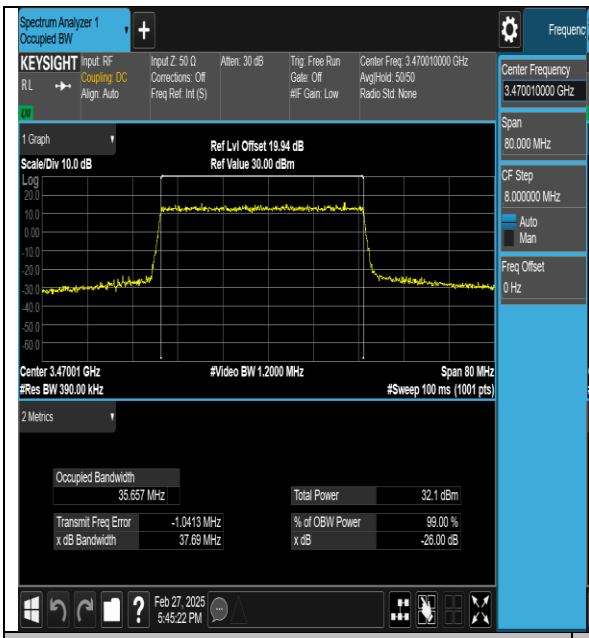

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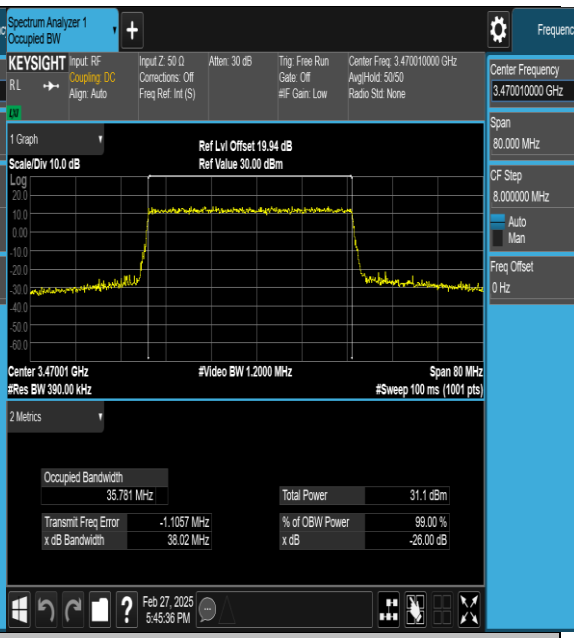

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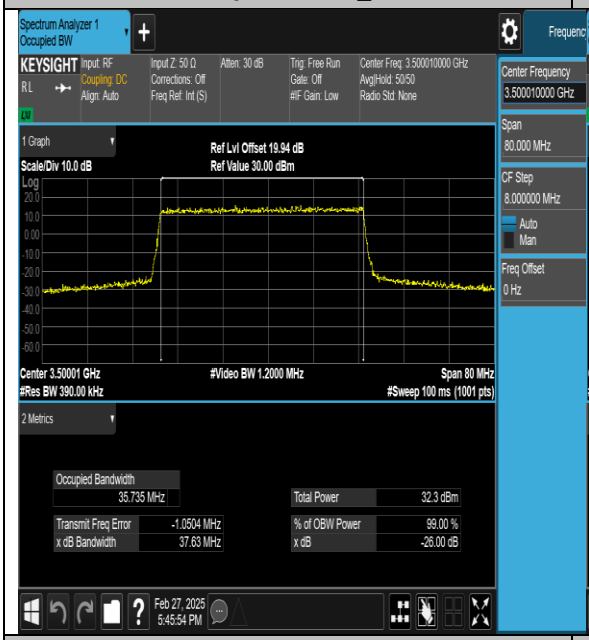

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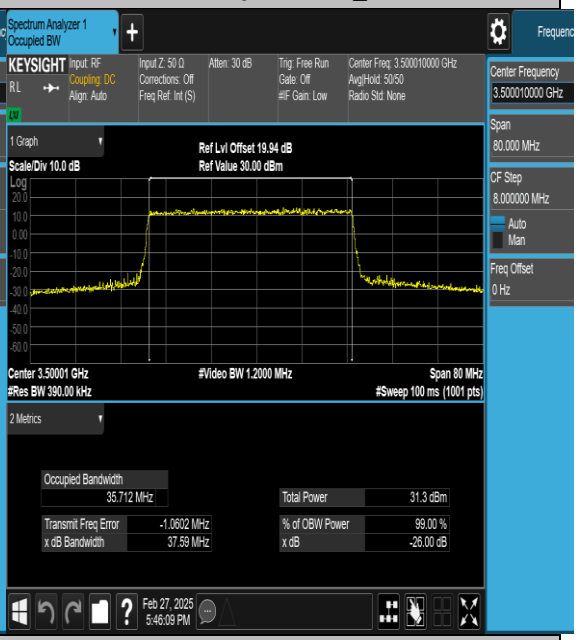

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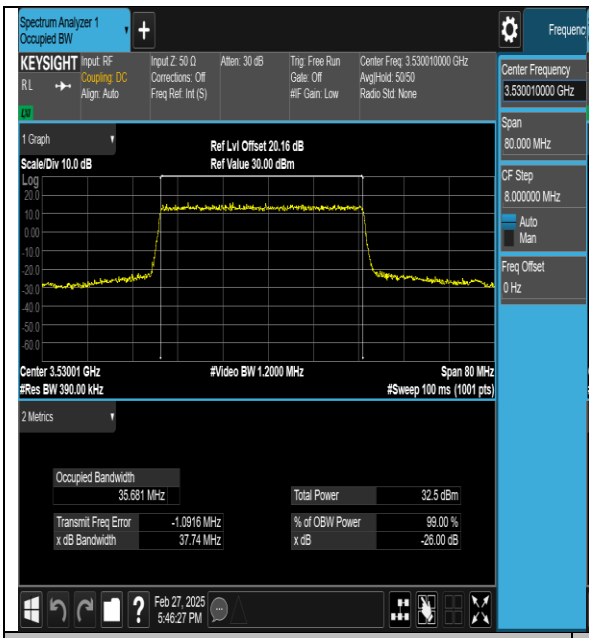

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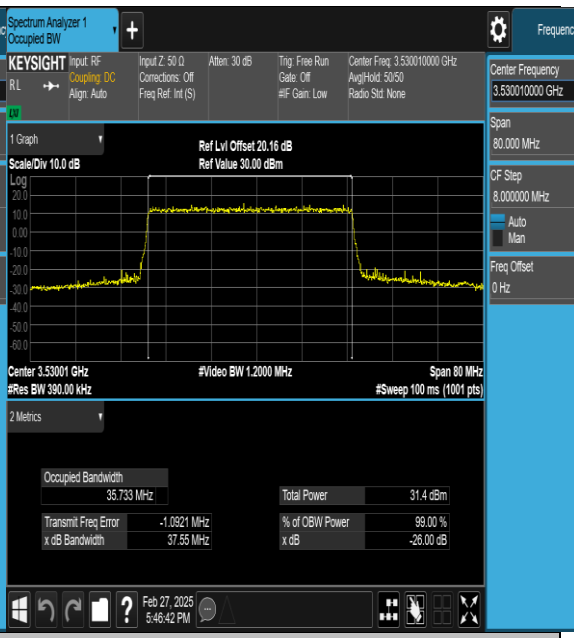

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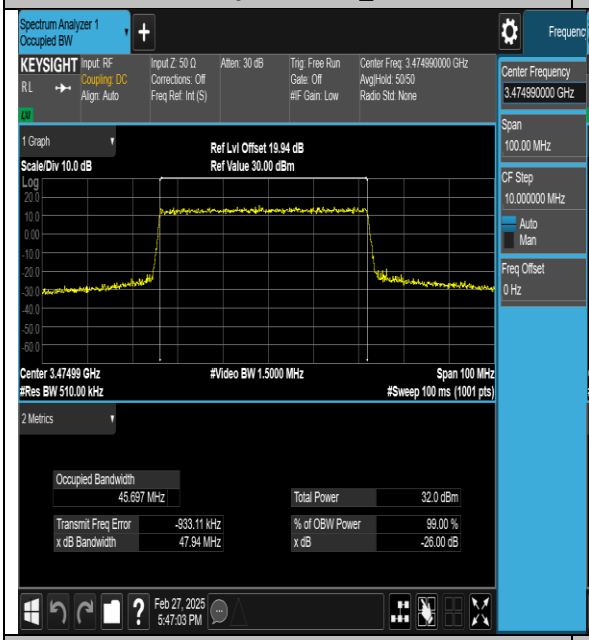

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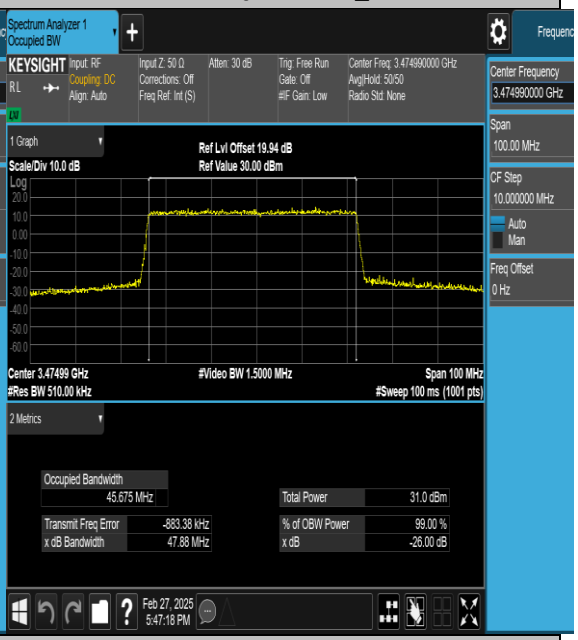

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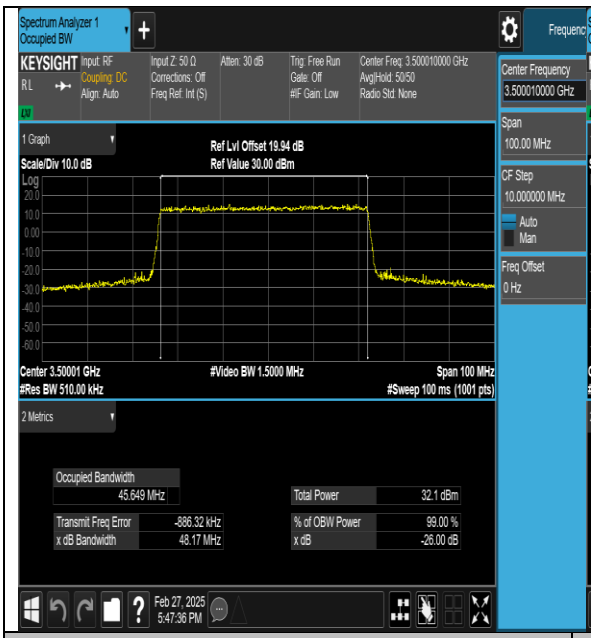

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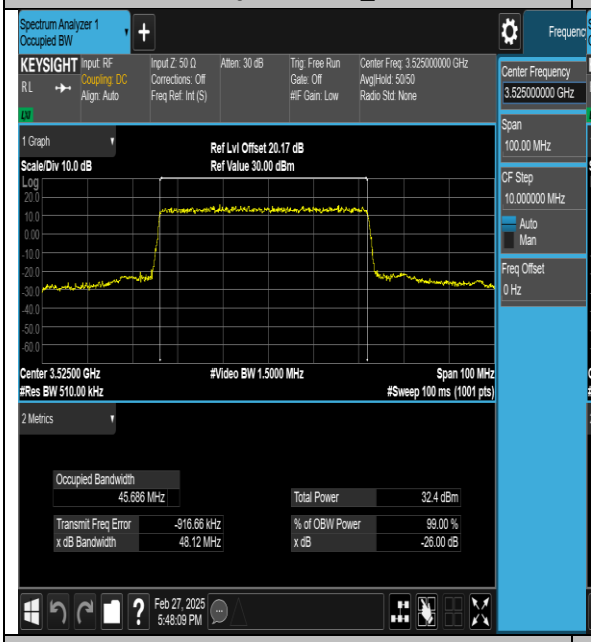

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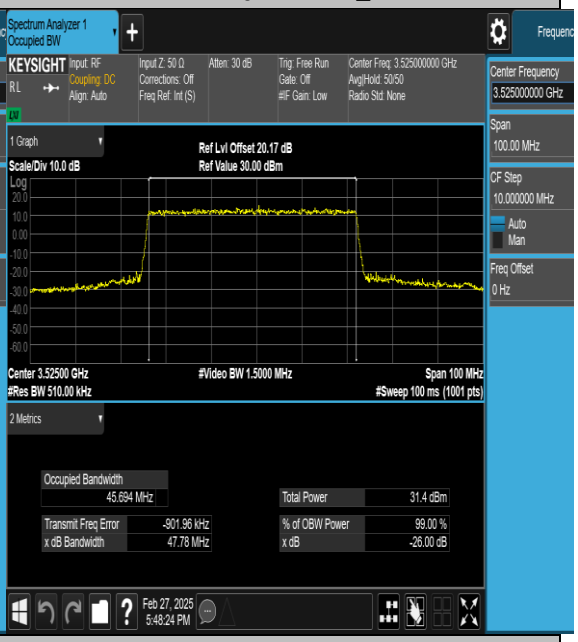

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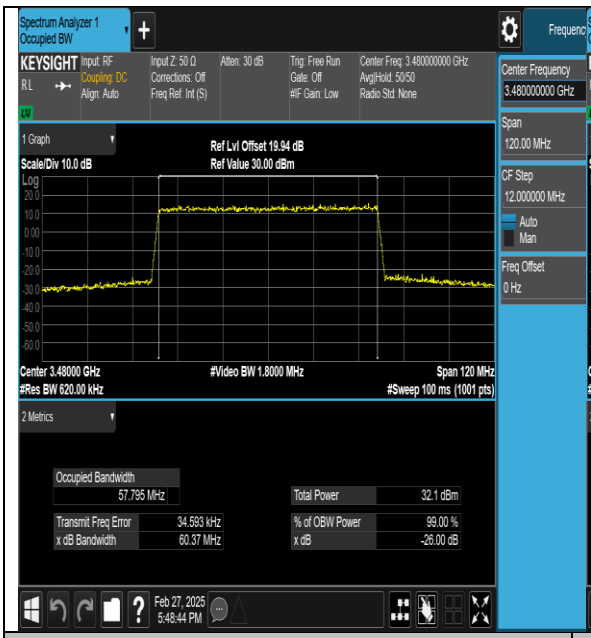

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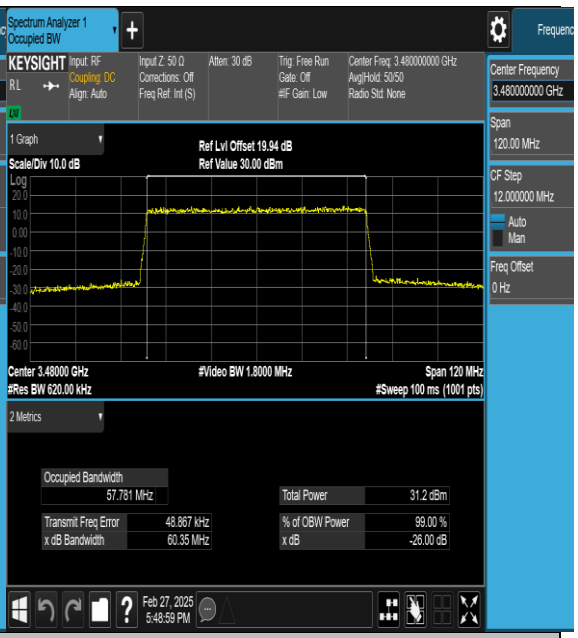

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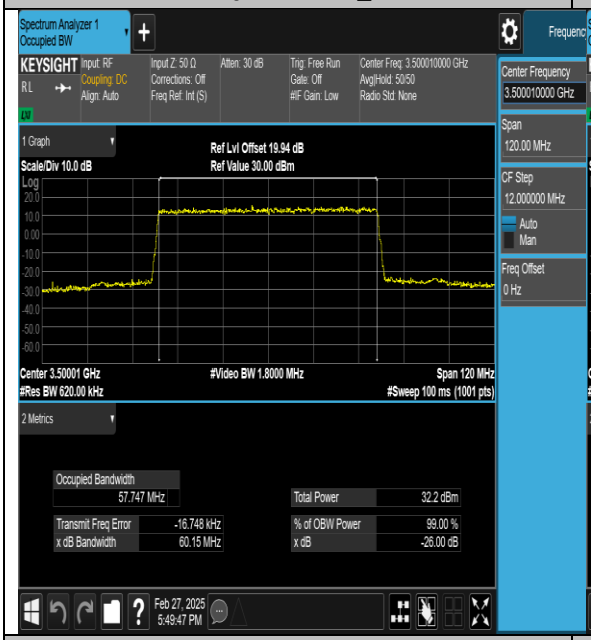

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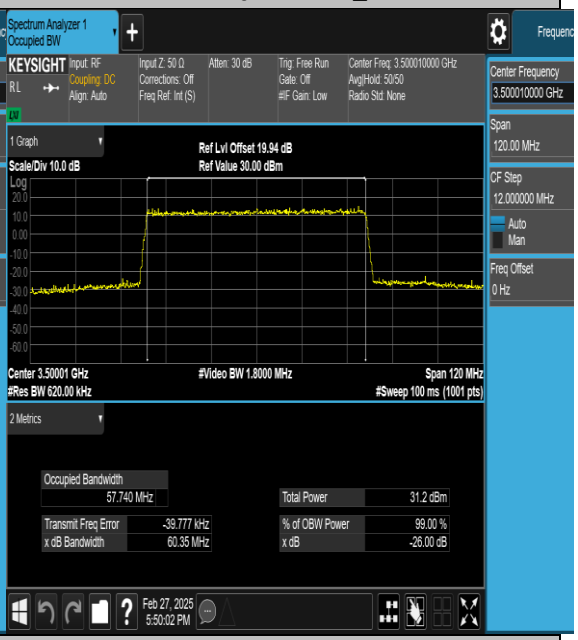

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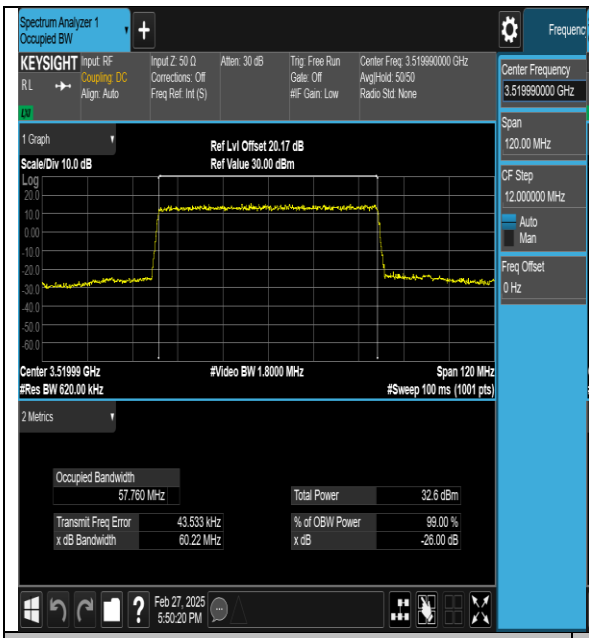

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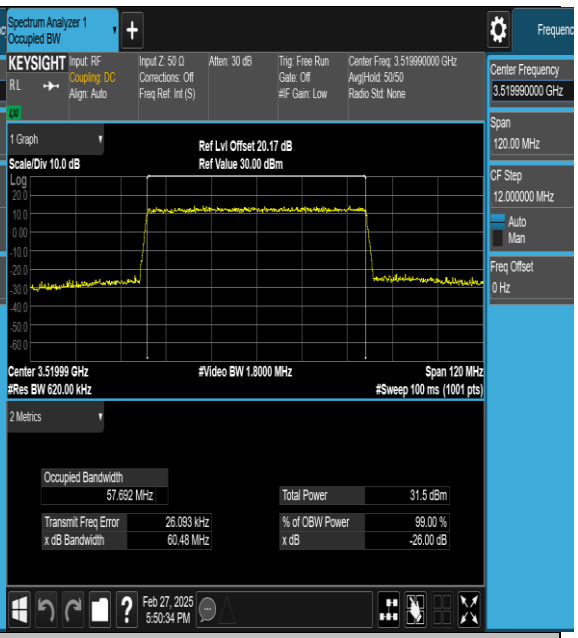

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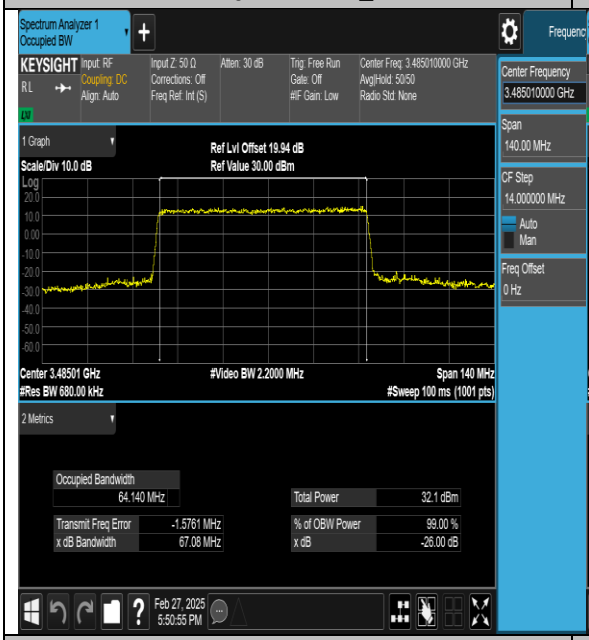

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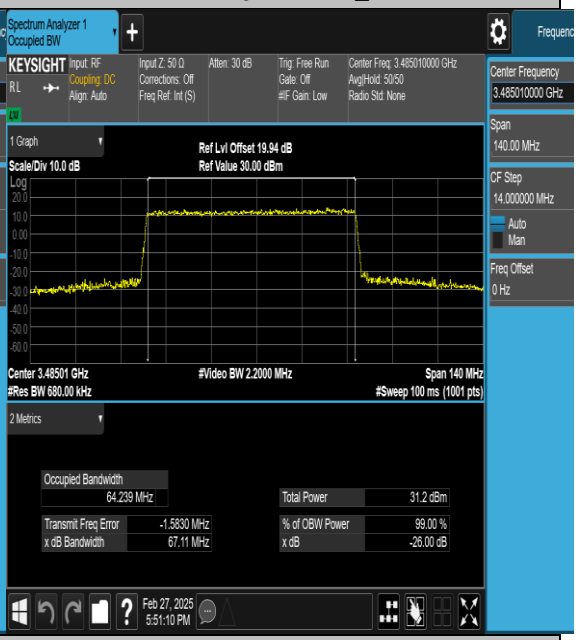

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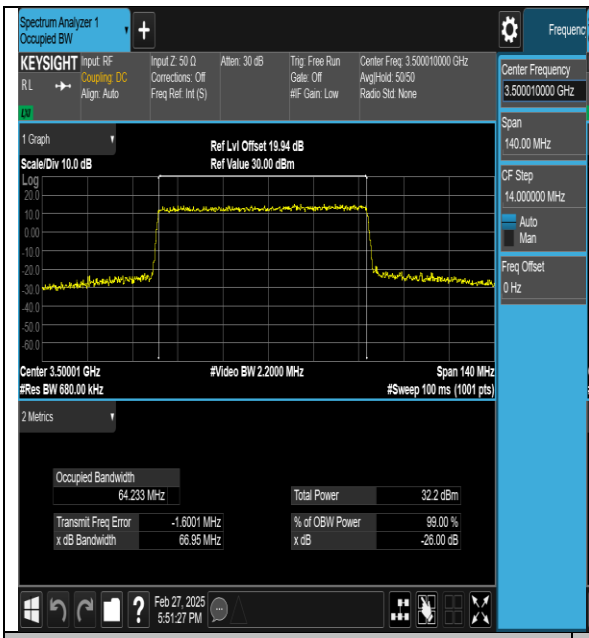

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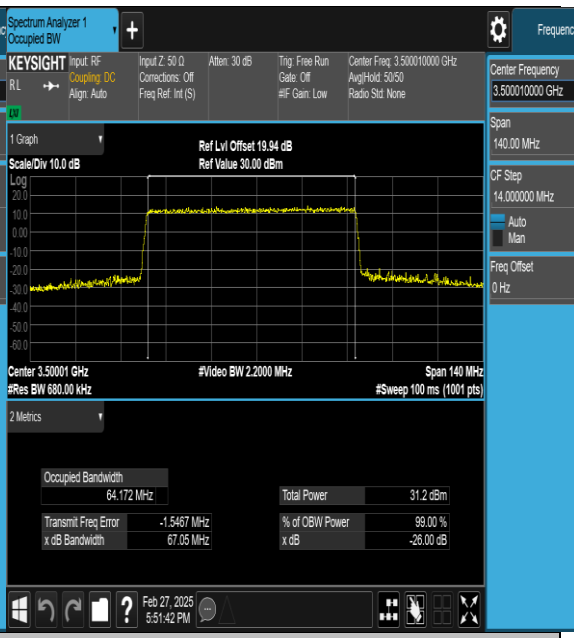

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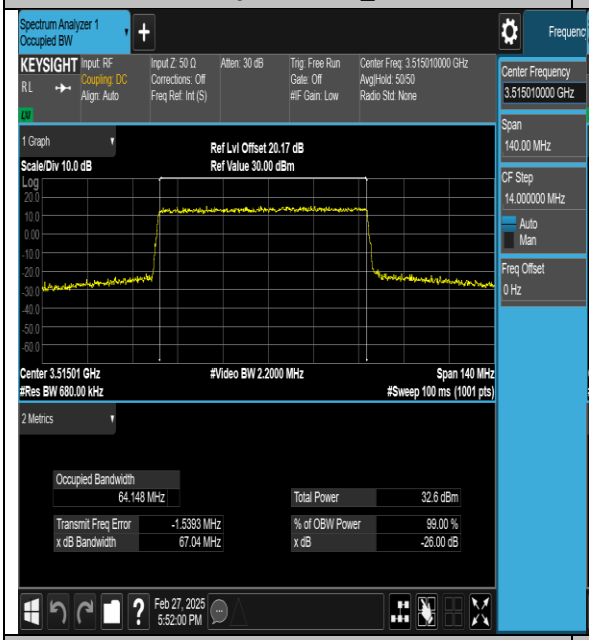

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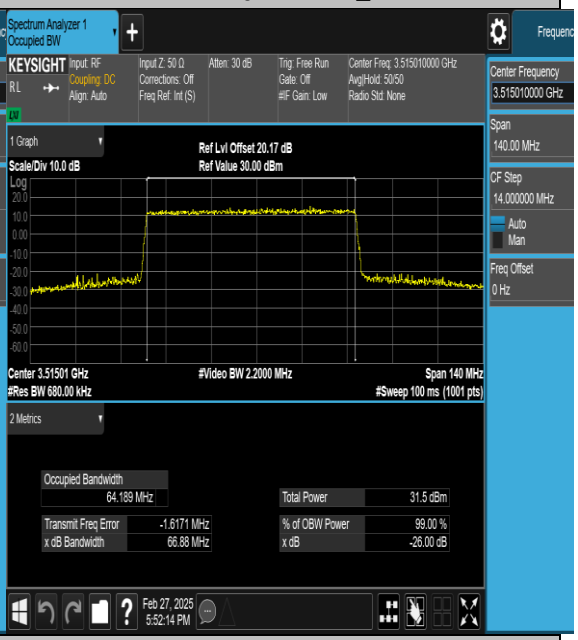

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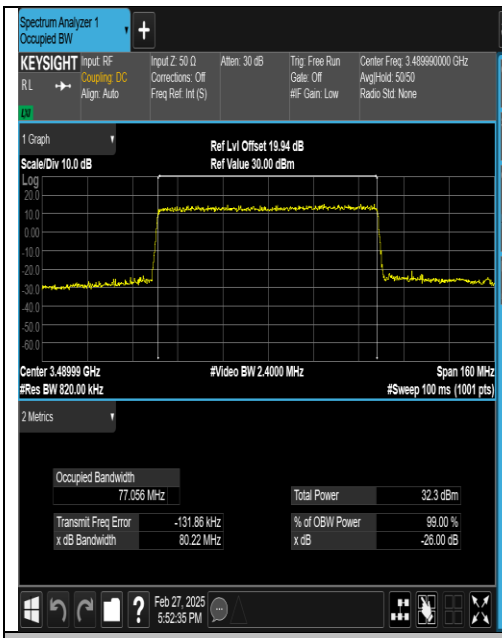

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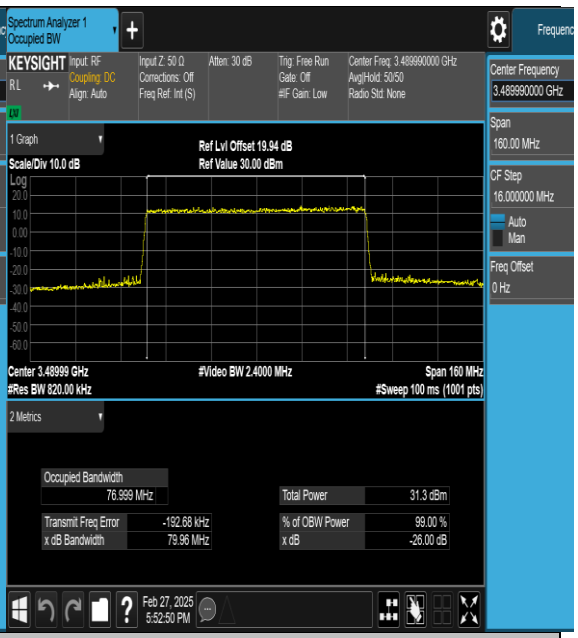

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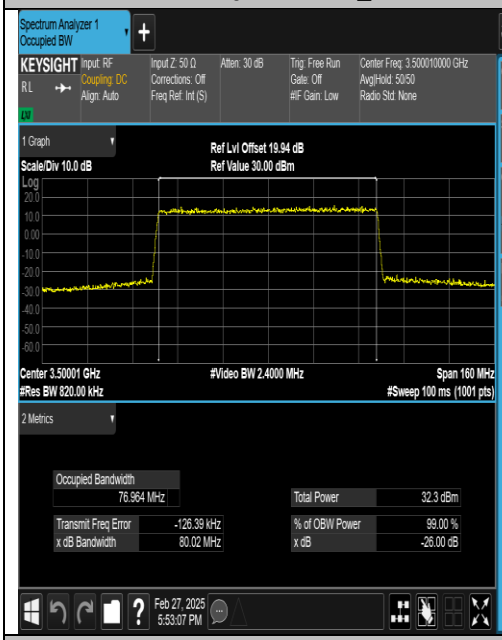

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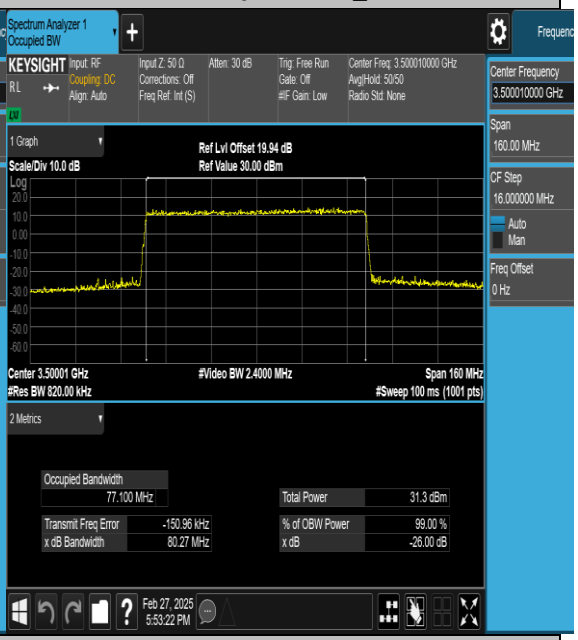

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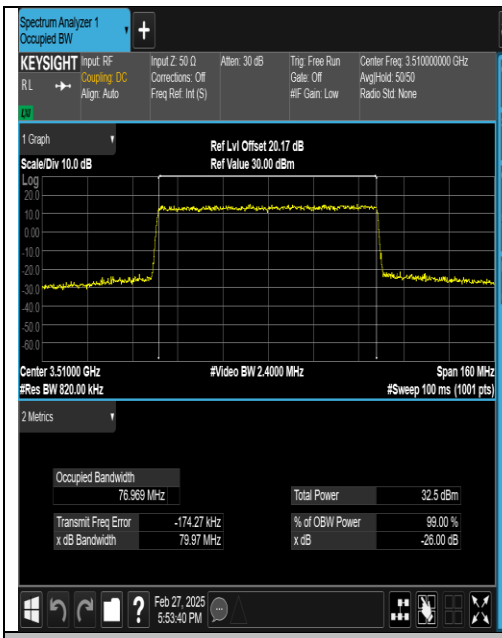

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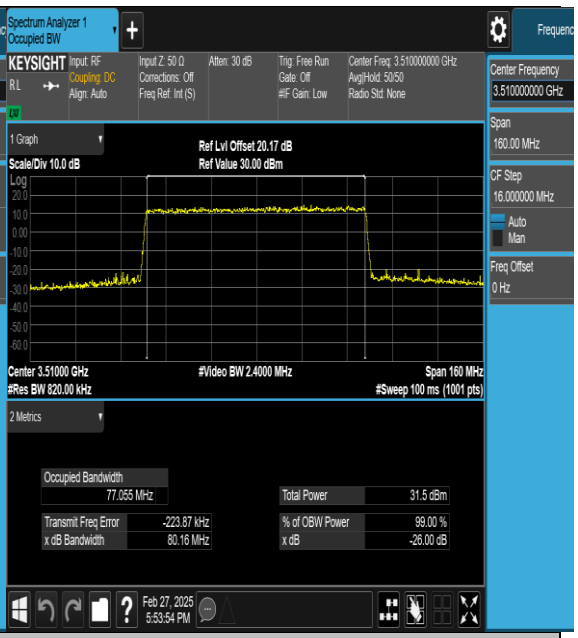

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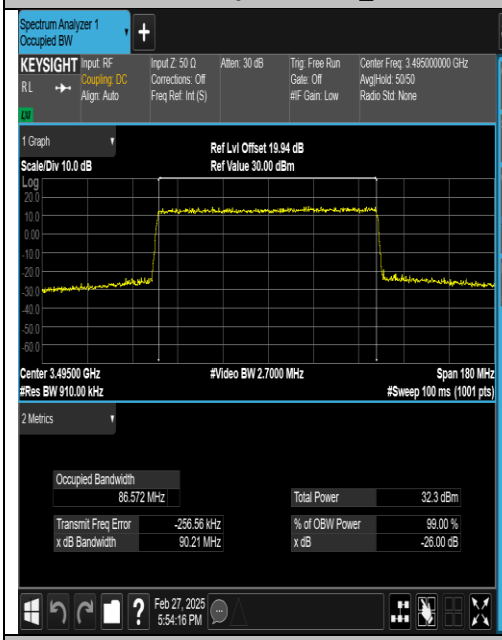

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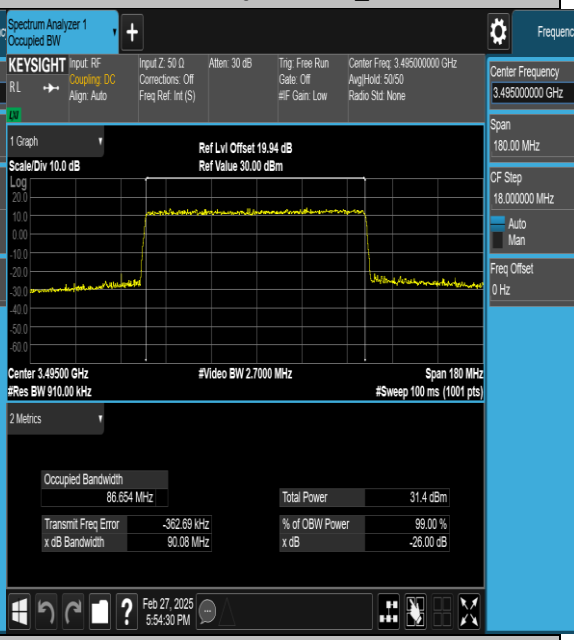

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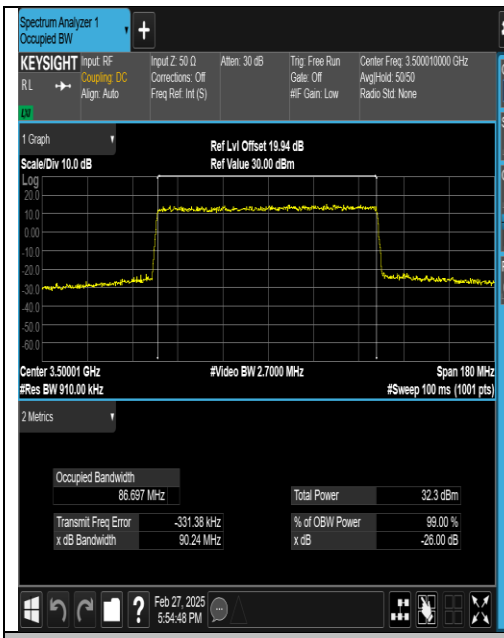

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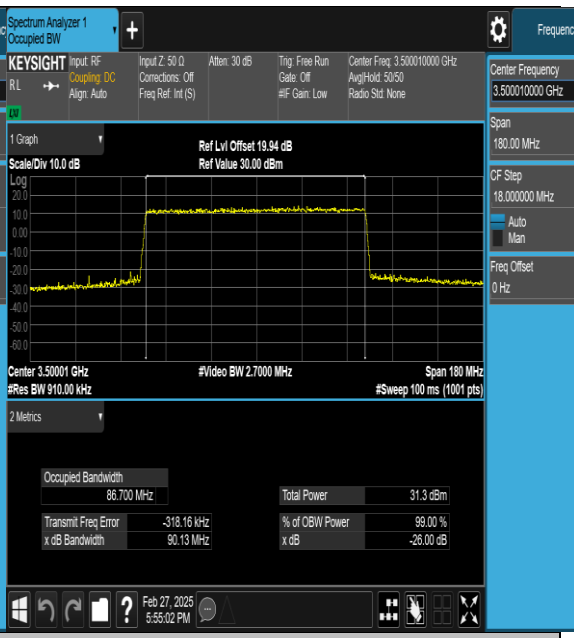

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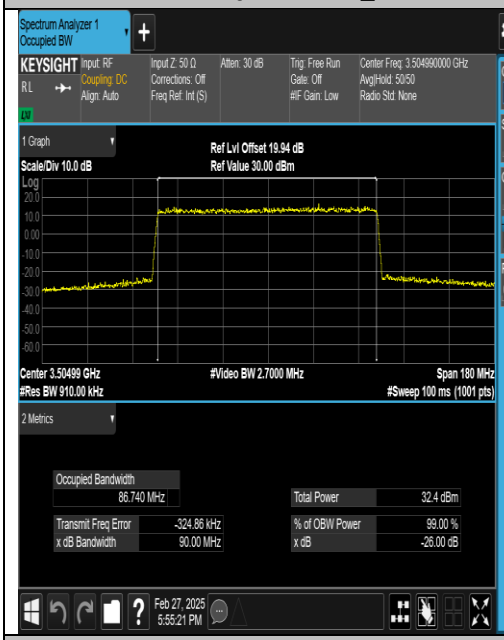

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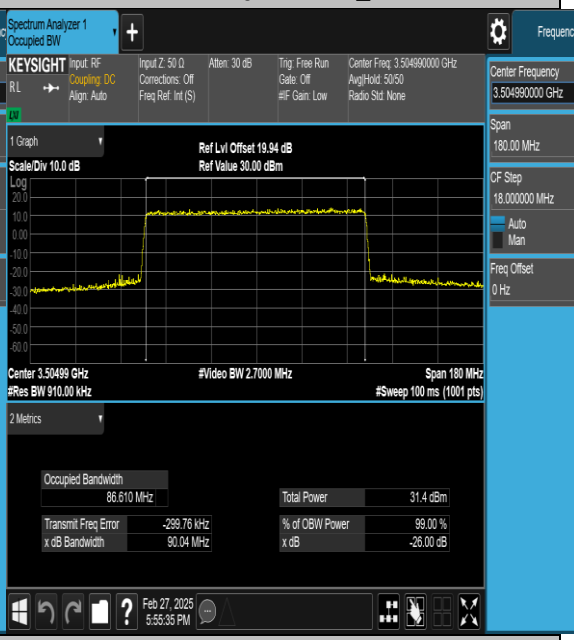

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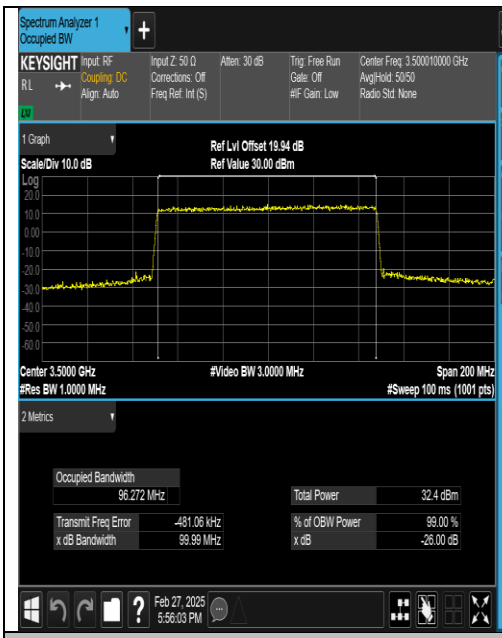

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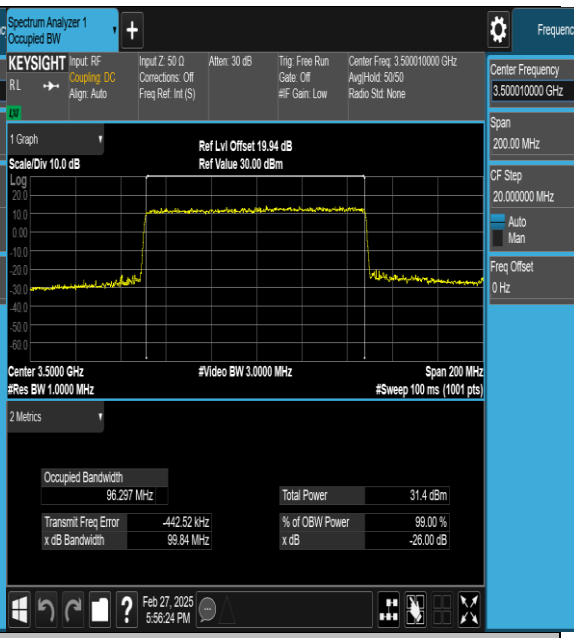

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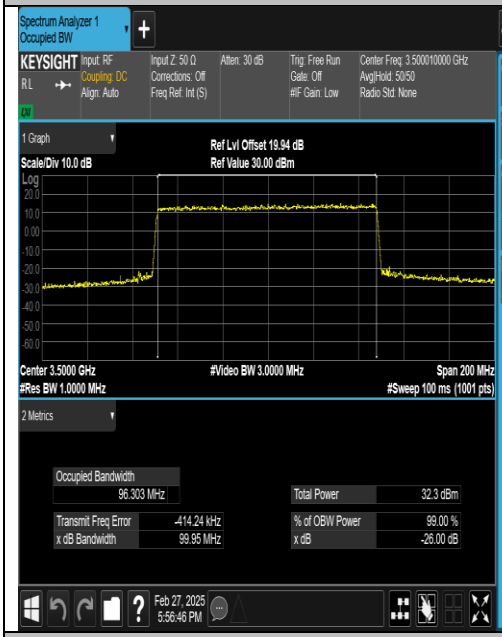

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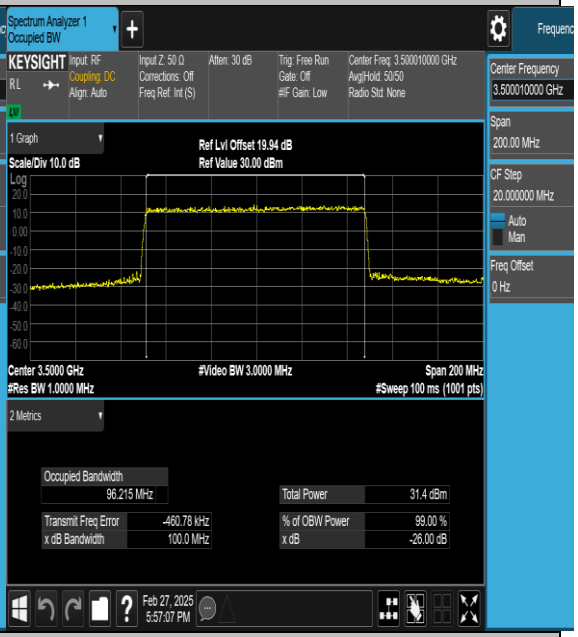
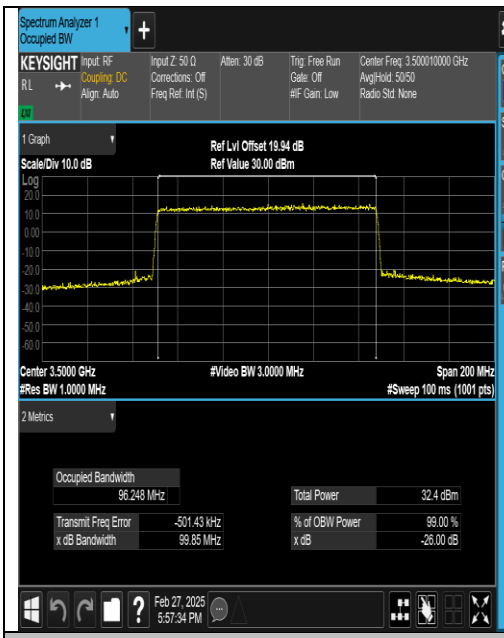
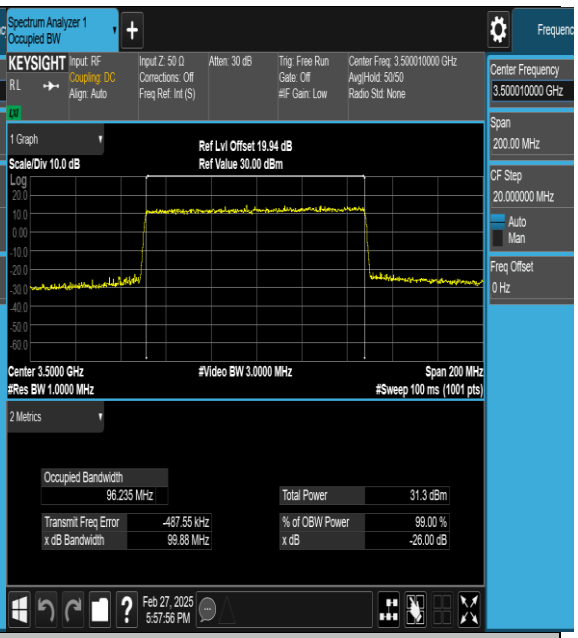


NTNV-DC_41A_n77A-3450-3550-PC2-30-5+100-M+M-N/A-N/A-DFT-16QAM-Outer_Full-Ant1-PASS



NTNV-DC_41A_n77A-3450-3550-PC2-30-5+100-M+H-N/A-N/A-DFT-QPSK-Outer_Full-Ant1-PASS



NTNV-DC_41A_n77A-3450-3550-PC2-30-5+100-M+H-N/A-N/A-DFT-16QAM-Outer_Full-Ant1-PASS

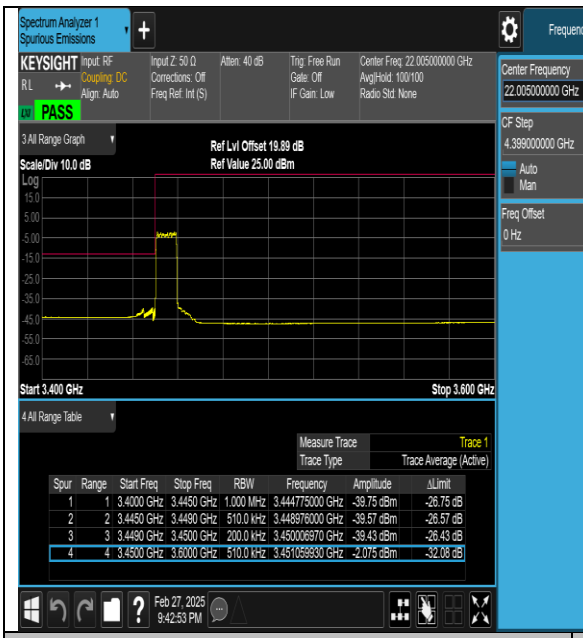
Appendix D: Band Edge for NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Margin	Verdict
DC_41A_n77A-3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	-39.43	26.43	PASS
DC_41A_n77A-3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	-41.81	28.81	PASS
DC_41A_n77A-3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	-37.35	24.35	PASS
DC_41A_n77A-3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	-42.01	29.01	PASS
DC_41A_n77A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	-39.37	26.37	PASS
DC_41A_n77A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	-42.55	29.55	PASS
DC_41A_n77A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	-38.17	25.17	PASS
DC_41A_n77A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	-43.29	30.29	PASS
DC_41A_n77A-3450-3550	30	5+30	DFT-QPSK	M+L	Outer_Full	-41.30	28.30	PASS
DC_41A_n77A-3450-3550	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	-43.71	30.71	PASS
DC_41A_n77A-3450-3550	30	5+30	DFT-QPSK	M+H	Outer_Full	-37.51	24.51	PASS
DC_41A_n77A-3450-3550	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	-43.62	30.62	PASS
DC_41A_n77A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	-40.03	27.03	PASS
DC_41A_n77A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	-43.05	30.05	PASS
DC_41A_n77A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	-38.51	25.51	PASS
DC_41A_n77A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	-42.93	29.93	PASS
DC_41A_n77A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	-41.87	28.87	PASS
DC_41A_n77A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	-43.59	30.59	PASS
DC_41A_n77A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	-38.36	25.36	PASS
DC_41A_n77A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	-43.63	30.63	PASS
DC_41A_n77A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	-41.92	28.92	PASS
DC_41A_n77A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	-42.33	29.33	PASS
DC_41A_n77A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	-39.31	26.31	PASS
DC_41A_n77A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	-42.53	29.53	PASS
DC_41A_n77A-3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	-41.00	28.00	PASS
DC_41A_n77A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	-43.83	30.83	PASS
DC_41A_n77A-3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	-39.71	26.71	PASS
DC_41A_n77A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	-43.71	30.71	PASS
DC_41A_n77A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	-42.17	29.17	PASS
DC_41A_n77A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	-43.71	30.71	PASS
DC_41A_n77A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	-39.99	26.99	PASS
DC_41A_n77A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	-43.75	30.75	PASS
DC_41A_n77A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	-41.92	28.92	PASS
DC_41A_n77A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	-43.20	30.20	PASS
DC_41A_n77A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	-40.93	27.93	PASS
DC_41A_n77A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	-42.90	29.90	PASS

DC_41A_n77A-3450-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	-42.61	29.61	PASS
DC_41A_n77A-3450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	-42.23	29.23	PASS
DC_41A_n77A-3450-3550	30	5+100	DFT-QPSK	M+H	Outer_Full	-41.26	28.26	PASS
DC_41A_n77A-3450-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	-42.02	29.02	PASS
DC_41A_n77A-3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	-39.73	26.73	PASS
DC_41A_n77A-3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	-38.82	25.82	PASS
DC_41A_n77A-3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	-34.22	21.22	PASS
DC_41A_n77A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	-32.23	19.23	PASS

Test Graphs



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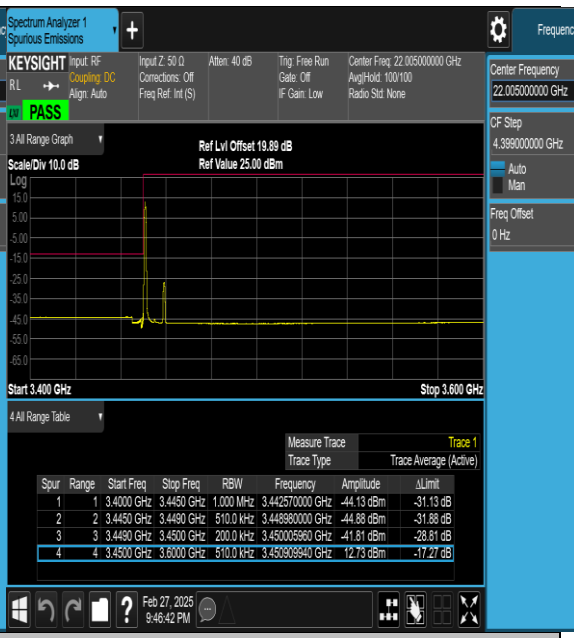

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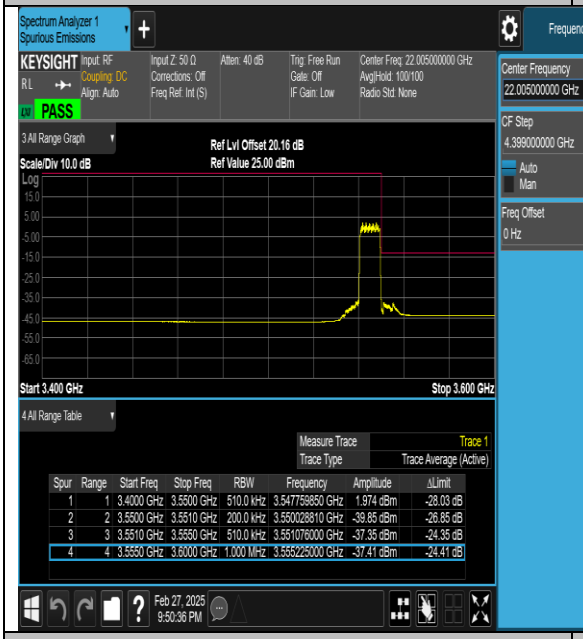

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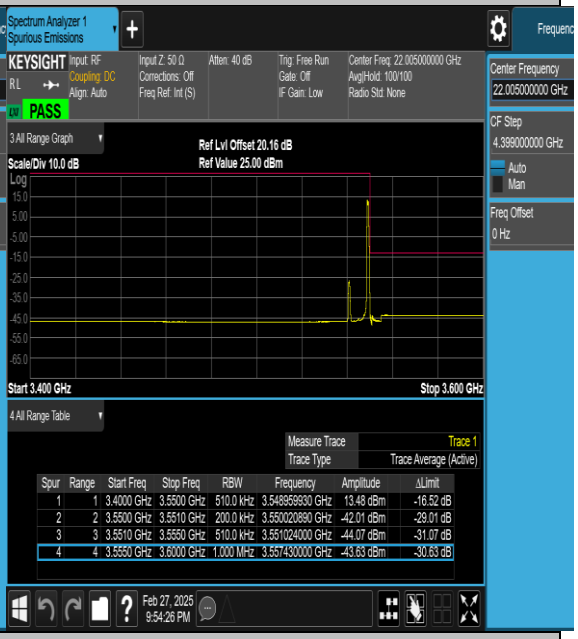

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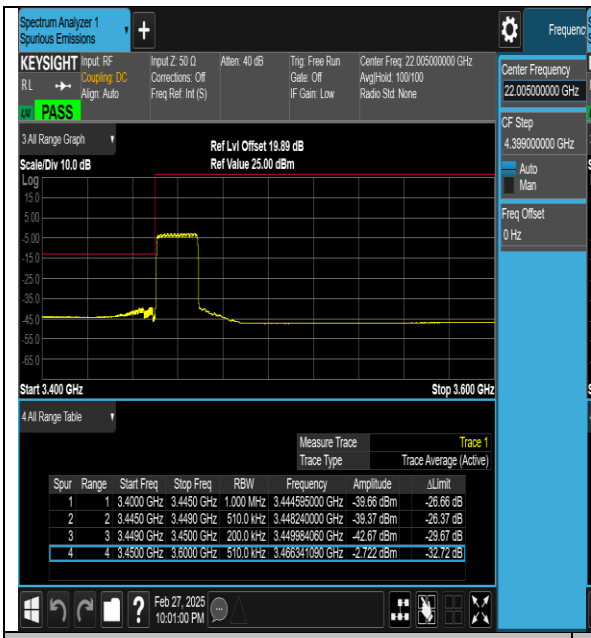

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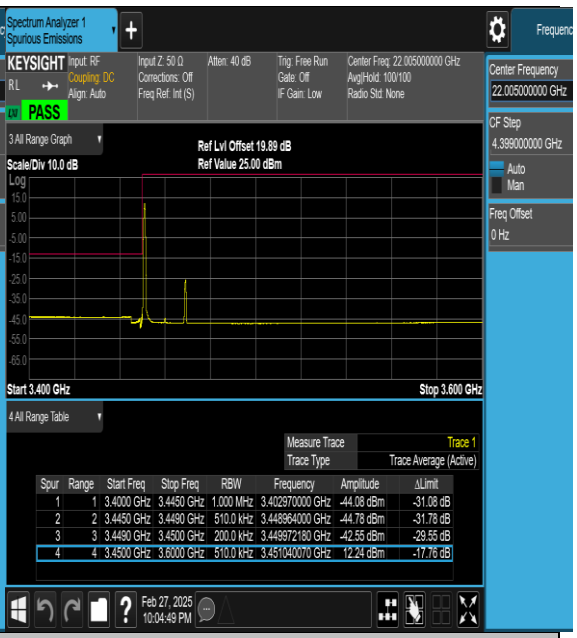

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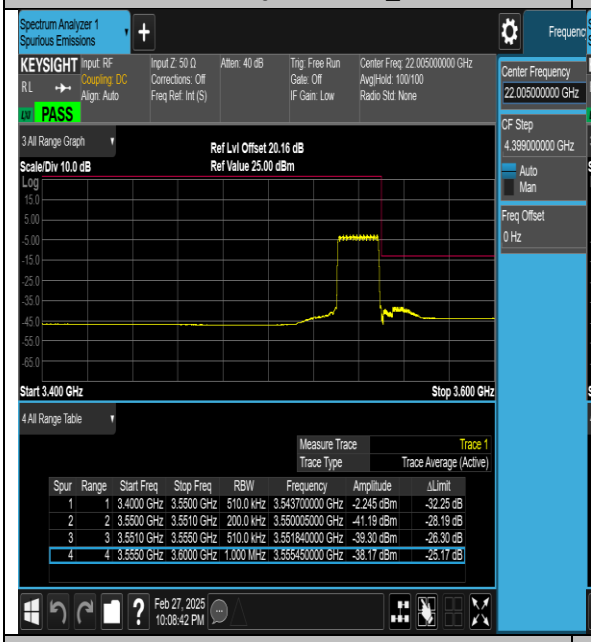

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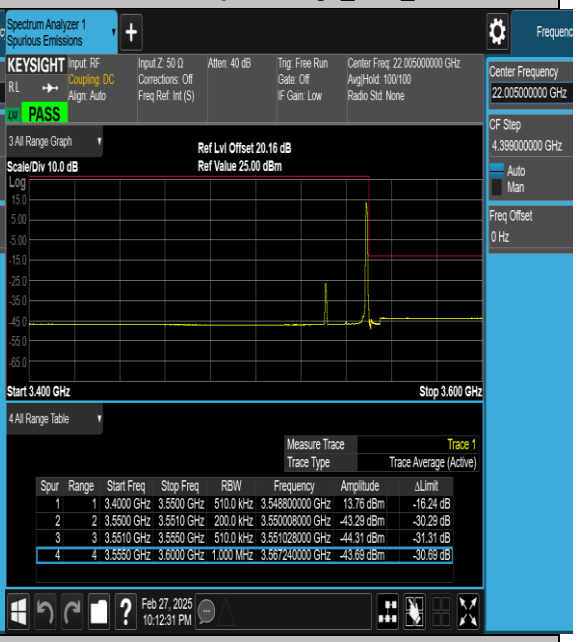

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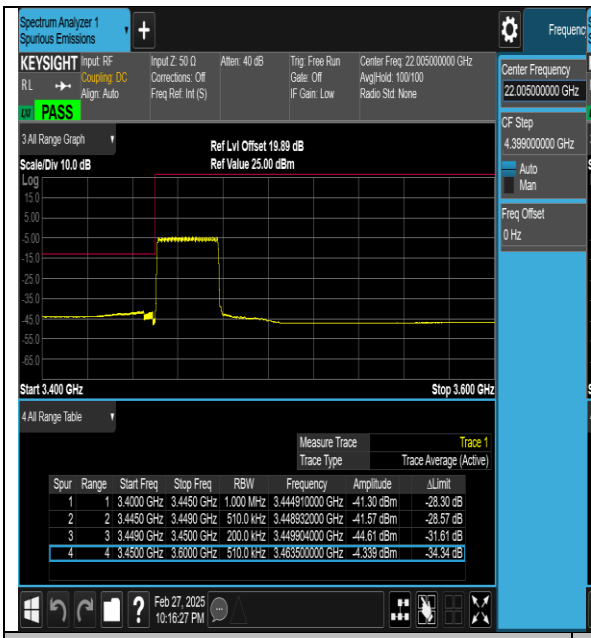

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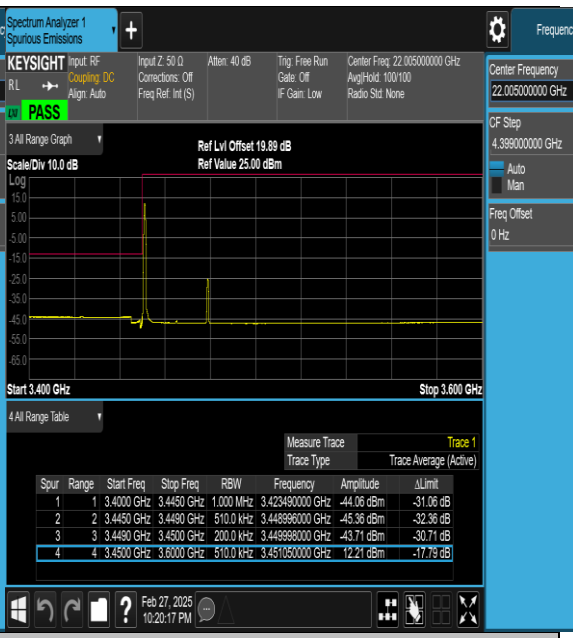

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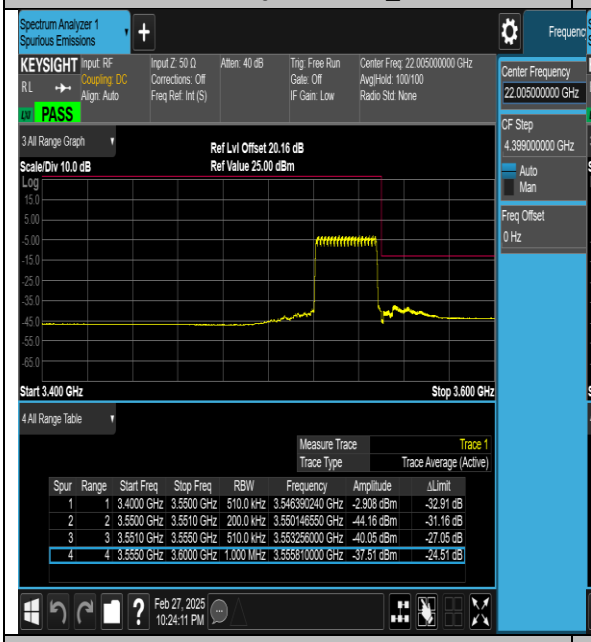

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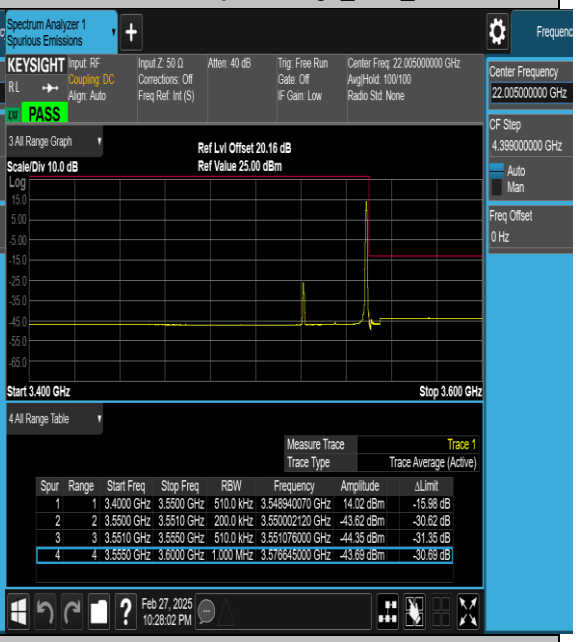

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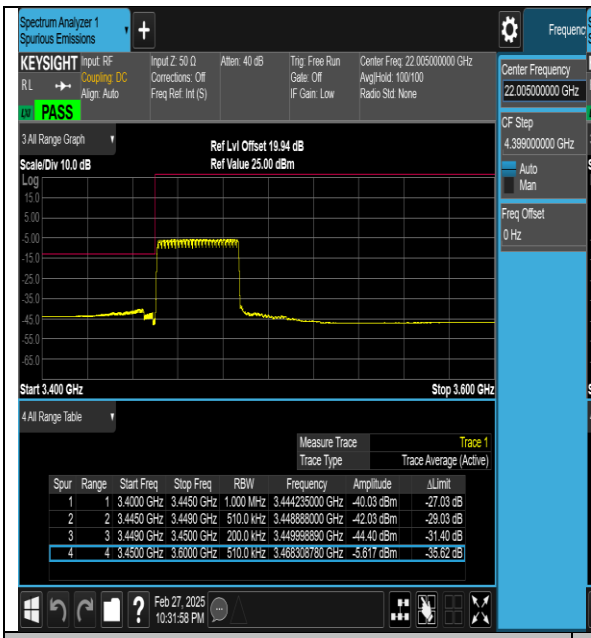

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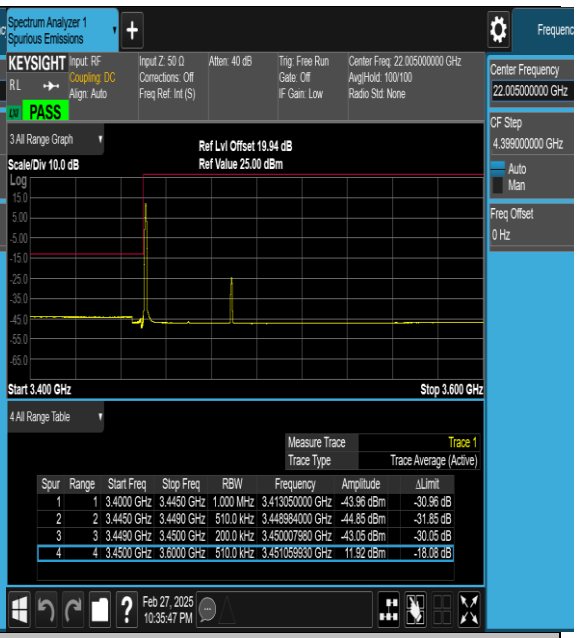

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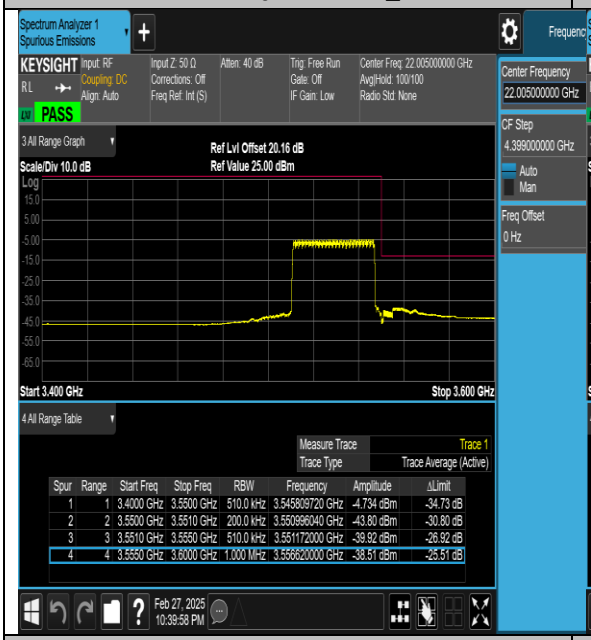

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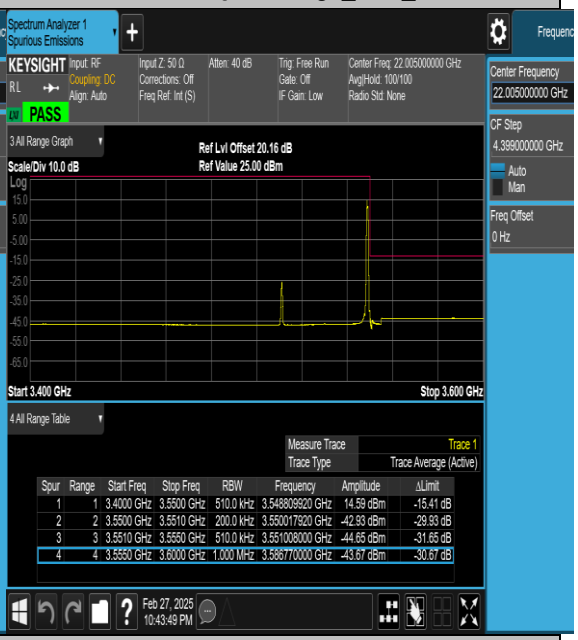

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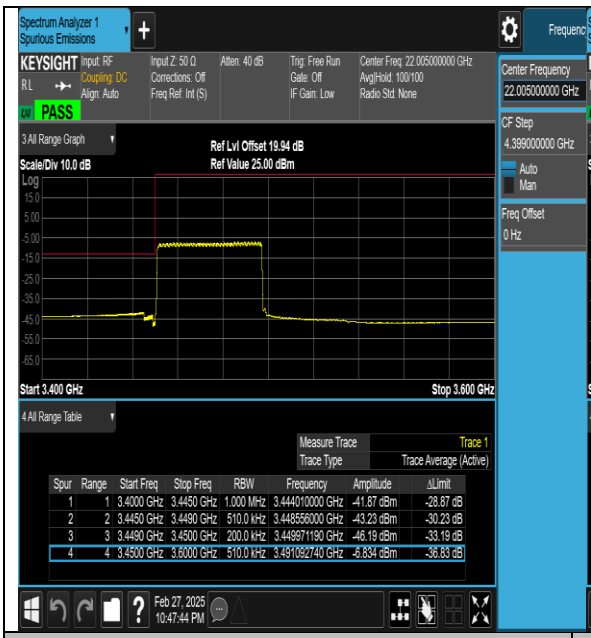

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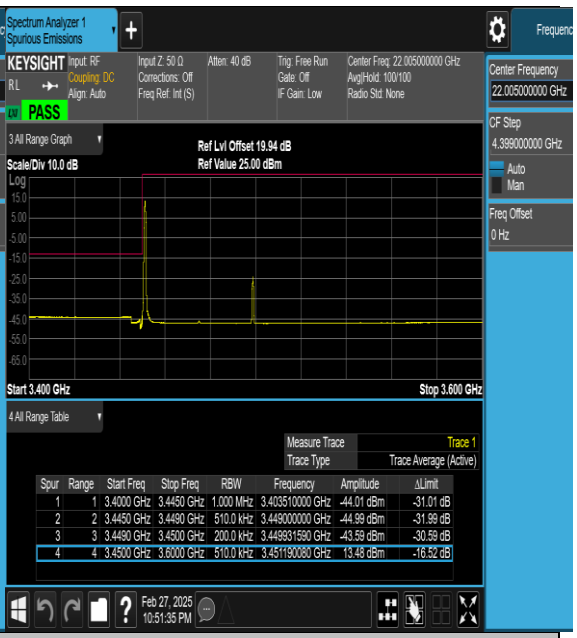

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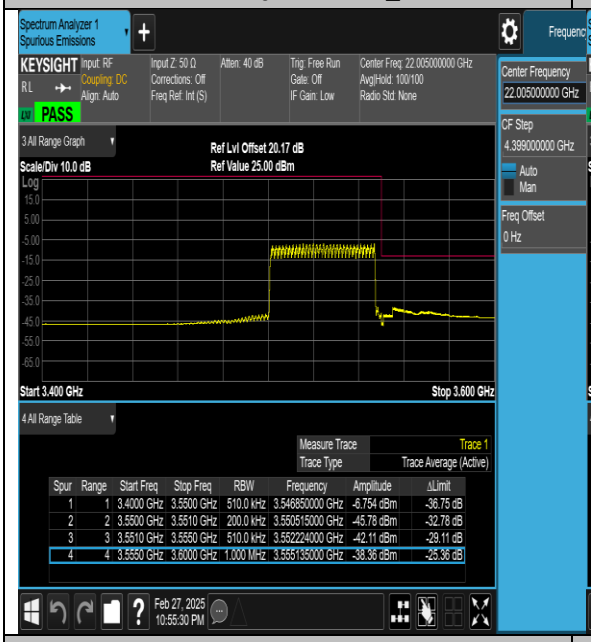

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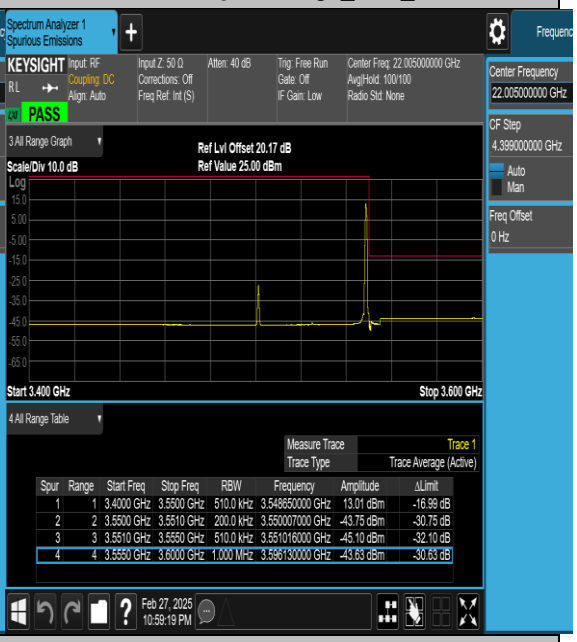

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