Appendix for 41A_n77A(3700-3980MHz)

Product Name: TITAN 5400
Model No: G5G-X62O

Appendix A: Average Power Output Data for NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_41A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Outer_Full	25.02	PC2	PASS
DC_41A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Inner_Full	26.05	PC2	PASS
DC_41A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	24.92	PC2	PASS
DC_41A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	25.00	PC2	PASS
DC_41A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Outer_Full	23.99	PC2	PASS
DC_41A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Inner_Full	24.97	PC2	PASS
DC_41A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	23.85	PC2	PASS
DC_41A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	24.00	PC2	PASS
DC_41A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Outer_Full	25.08	PC2	PASS
DC_41A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Inner_Full	26.03	PC2	PASS
DC_41A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	25.16	PC2	PASS
DC_41A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	24.94	PC2	PASS
DC_41A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Outer_Full	24.08	PC2	PASS
DC_41A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Inner_Full	25.00	PC2	PASS
DC_41A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	24.23	PC2	PASS
DC_41A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	23.89	PC2	PASS
DC_41A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Outer_Full	25.13	PC2	PASS
DC_41A_n77A-3	30	5+10	DFT-QPSK	M+H	Inner_Full	26.10	PC2	PASS

700-3980								
DC_41A_n77A-3 700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	25.09	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	25.09	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+10	DFT-16QAM	M+H	Outer_Full	24.14	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+10	DFT-16QAM	M+H	Inner_Full	25.24	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	23.77	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	24.01	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+15	DFT-QPSK	M+L	Outer_Full	25.16	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+15	DFT-QPSK	M+L	Inner_Full	26.12	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	25.11	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	25.17	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+15	DFT-16QAM	M+L	Outer_Full	24.16	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+15	DFT-16QAM	M+L	Inner_Full	25.08	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	24.39	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	24.14	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+15	DFT-QPSK	M+M	Outer_Full	25.16	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+15	DFT-QPSK	M+M	Inner_Full	26.20	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	25.01	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	25.04	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+15	DFT-16QAM	M+M	Outer_Full	24.23	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+15	DFT-16QAM	M+M	Inner_Full	25.07	PC2	PASS
DC_41A_n77A-3	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	24.00	PC2	PASS

700-3980								
DC_41A_n77A-3 700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	24.24	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+15	DFT-QPSK	M+H	Outer_Full	25.31	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+15	DFT-QPSK	M+H	Inner_Full	26.36	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	25.25	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	25.65	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+15	DFT-16QAM	M+H	Outer_Full	24.34	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+15	DFT-16QAM	M+H	Inner_Full	25.28	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	24.20	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	24.34	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	25.13	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+20	DFT-QPSK	M+L	Inner_Full	26.11	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	25.07	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	24.94	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+20	DFT-16QAM	M+L	Outer_Full	24.08	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+20	DFT-16QAM	M+L	Inner_Full	25.15	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	24.16	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	24.01	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+20	DFT-QPSK	M+M	Outer_Full	25.19	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+20	DFT-QPSK	M+M	Inner_Full	26.12	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	25.03	PC2	PASS
DC_41A_n77A-3	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	24.92	PC2	PASS

700-3980								
DC_41A_n77A-3 700-3980	30	5+20	DFT-16QAM	M+M	Outer_Full	24.24	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+20	DFT-16QAM	M+M	Inner_Full	25.29	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	24.23	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	24.22	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	25.24	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+20	DFT-QPSK	M+H	Inner_Full	26.28	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	25.14	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	25.30	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+20	DFT-16QAM	M+H	Outer_Full	24.28	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+20	DFT-16QAM	M+H	Inner_Full	25.15	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	23.97	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	24.16	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+30	DFT-QPSK	M+L	Outer_Full	25.01	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+30	DFT-QPSK	M+L	Inner_Full	25.95	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	25.20	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	25.06	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+30	DFT-16QAM	M+L	Outer_Full	24.02	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+30	DFT-16QAM	M+L	Inner_Full	25.08	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	24.41	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	23.95	PC2	PASS
DC_41A_n77A-3	30	5+30	DFT-QPSK	M+M	Outer_Full	25.02	PC2	PASS

700-3980								
DC_41A_n77A-3 700-3980	30	5+30	DFT-QPSK	M+M	Inner_Full	26.05	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	24.87	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	24.84	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+30	DFT-16QAM	M+M	Outer_Full	24.01	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+30	DFT-16QAM	M+M	Inner_Full	25.01	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	23.75	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	23.68	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+30	DFT-QPSK	M+H	Outer_Full	25.29	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+30	DFT-QPSK	M+H	Inner_Full	26.17	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	25.19	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	25.28	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+30	DFT-16QAM	M+H	Outer_Full	24.20	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+30	DFT-16QAM	M+H	Inner_Full	25.23	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	24.33	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	24.19	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	25.02	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+40	DFT-QPSK	M+L	Inner_Full	25.96	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	25.15	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	25.09	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+40	DFT-16QAM	M+L	Outer_Full	24.01	PC2	PASS
DC_41A_n77A-3	30	5+40	DFT-16QAM	M+L	Inner_Full	25.02	PC2	PASS

700-3980								
DC_41A_n77A-3 700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	24.34	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	23.92	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	24.95	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+40	DFT-QPSK	M+M	Inner_Full	25.90	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	24.75	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	24.57	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+40	DFT-16QAM	M+M	Outer_Full	23.86	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+40	DFT-16QAM	M+M	Inner_Full	24.89	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	23.87	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	23.57	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	25.28	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+40	DFT-QPSK	M+H	Inner_Full	26.19	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	25.24	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	25.65	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+40	DFT-16QAM	M+H	Outer_Full	24.29	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+40	DFT-16QAM	M+H	Inner_Full	25.26	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	24.24	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	24.52	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+50	DFT-QPSK	M+L	Outer_Full	25.00	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+50	DFT-QPSK	M+L	Inner_Full	25.96	PC2	PASS
DC_41A_n77A-3	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	25.04	PC2	PASS

700-3980								
DC_41A_n77A-3 700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	24.74	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+50	DFT-16QAM	M+L	Outer_Full	23.96	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+50	DFT-16QAM	M+L	Inner_Full	24.97	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	24.03	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	23.73	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+50	DFT-QPSK	M+M	Outer_Full	25.04	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+50	DFT-QPSK	M+M	Inner_Full	26.14	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	25.12	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	24.89	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+50	DFT-16QAM	M+M	Outer_Full	23.91	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+50	DFT-16QAM	M+M	Inner_Full	25.16	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	24.12	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	23.94	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+50	DFT-QPSK	M+H	Outer_Full	25.03	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+50	DFT-QPSK	M+H	Inner_Full	26.03	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	24.95	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	25.39	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+50	DFT-16QAM	M+H	Outer_Full	24.05	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+50	DFT-16QAM	M+H	Inner_Full	25.06	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	24.00	PC2	PASS
DC_41A_n77A-3	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	24.38	PC2	PASS

700-3980								
DC_41A_n77A-3 700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	24.93	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+60	DFT-QPSK	M+L	Inner_Full	25.83	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	25.00	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	24.53	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+60	DFT-16QAM	M+L	Outer_Full	23.90	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+60	DFT-16QAM	M+L	Inner_Full	24.85	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	23.89	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	23.60	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	25.06	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+60	DFT-QPSK	M+M	Inner_Full	26.10	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	24.98	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	24.80	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+60	DFT-16QAM	M+M	Outer_Full	23.94	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+60	DFT-16QAM	M+M	Inner_Full	25.02	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	24.10	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	24.29	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	25.11	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+60	DFT-QPSK	M+H	Inner_Full	26.10	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	24.79	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	25.12	PC2	PASS
DC_41A_n77A-3	30	5+60	DFT-16QAM	M+H	Outer_Full	24.11	PC2	PASS

700-3980								
DC_41A_n77A-3 700-3980	30	5+60	DFT-16QAM	M+H	Inner_Full	25.10	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	23.92	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	24.16	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+70	DFT-QPSK	M+L	Outer_Full	24.75	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+70	DFT-QPSK	M+L	Inner_Full	25.81	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	24.99	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	24.51	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+70	DFT-16QAM	M+L	Outer_Full	23.86	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+70	DFT-16QAM	M+L	Inner_Full	24.80	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	24.05	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	23.75	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+70	DFT-QPSK	M+M	Outer_Full	25.00	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+70	DFT-QPSK	M+M	Inner_Full	25.96	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	24.96	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	24.85	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+70	DFT-16QAM	M+M	Outer_Full	23.88	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+70	DFT-16QAM	M+M	Inner_Full	24.94	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	23.83	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	23.53	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+70	DFT-QPSK	M+H	Outer_Full	25.06	PC2	PASS
DC_41A_n77A-3	30	5+70	DFT-QPSK	M+H	Inner_Full	25.97	PC2	PASS

700-3980								
DC_41A_n77A-3 700-3980	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	24.94	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	25.18	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+70	DFT-16QAM	M+H	Outer_Full	24.03	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+70	DFT-16QAM	M+H	Inner_Full	25.08	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	23.88	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	24.34	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	25.12	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+80	DFT-QPSK	M+L	Inner_Full	26.07	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	25.20	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	24.73	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+80	DFT-16QAM	M+L	Outer_Full	24.00	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+80	DFT-16QAM	M+L	Inner_Full	25.08	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	24.15	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	23.97	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	24.88	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+80	DFT-QPSK	M+M	Inner_Full	25.95	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	24.90	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	24.70	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+80	DFT-16QAM	M+M	Outer_Full	23.88	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+80	DFT-16QAM	M+M	Inner_Full	24.90	PC2	PASS
DC_41A_n77A-3	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	23.89	PC2	PASS

700-3980								
DC_41A_n77A-3 700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	23.81	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	24.96	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+80	DFT-QPSK	M+H	Inner_Full	25.87	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	24.71	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	25.22	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+80	DFT-16QAM	M+H	Outer_Full	23.86	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+80	DFT-16QAM	M+H	Inner_Full	25.07	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	23.71	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	24.13	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+90	DFT-QPSK	M+L	Outer_Full	25.07	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+90	DFT-QPSK	M+L	Inner_Full	26.04	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	25.14	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	24.71	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+90	DFT-16QAM	M+L	Outer_Full	24.07	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+90	DFT-16QAM	M+L	Inner_Full	25.05	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	24.21	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	23.65	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+90	DFT-QPSK	M+M	Outer_Full	24.91	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+90	DFT-QPSK	M+M	Inner_Full	25.91	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	24.78	PC2	PASS
DC_41A_n77A-3	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	24.82	PC2	PASS

700-3980								
DC_41A_n77A-3 700-3980	30	5+90	DFT-16QAM	M+M	Outer_Full	23.98	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+90	DFT-16QAM	M+M	Inner_Full	24.93	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	23.72	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	23.77	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+90	DFT-QPSK	M+H	Outer_Full	24.85	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+90	DFT-QPSK	M+H	Inner_Full	25.79	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	24.59	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	25.29	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+90	DFT-16QAM	M+H	Outer_Full	23.96	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+90	DFT-16QAM	M+H	Inner_Full	24.91	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	23.73	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	24.28	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+100	DFT-QPSK	M+L	Outer_Full	25.05	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+100	DFT-QPSK	M+L	Inner_Full	26.07	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	24.94	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	24.82	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+100	DFT-16QAM	M+L	Outer_Full	24.06	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+100	DFT-16QAM	M+L	Inner_Full	25.07	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	24.01	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	23.91	PC2	PASS
DC_41A_n77A-3	30	5+100	DFT-QPSK	M+M	Outer_Full	24.90	PC2	PASS

700-3980								
DC_41A_n77A-3 700-3980	30	5+100	DFT-QPSK	M+M	Inner_Full	25.83	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	24.40	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	24.56	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+100	DFT-16QAM	M+M	Outer_Full	24.01	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+100	DFT-16QAM	M+M	Inner_Full	24.79	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	23.33	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	23.81	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+100	DFT-QPSK	M+H	Outer_Full	24.93	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+100	DFT-QPSK	M+H	Inner_Full	25.80	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	24.64	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	25.29	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+100	DFT-16QAM	M+H	Outer_Full	23.92	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+100	DFT-16QAM	M+H	Inner_Full	25.03	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	23.52	PC2	PASS
DC_41A_n77A-3 700-3980	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	24.30	PC2	PASS

Appendix B: Peak-to-Average Ratio for NSA

Peak-to-Average Ratio(CCDF)

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
DC_41A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Outer_Full	5.23	≤13	PASS
DC_41A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Outer_Full	6.10	≤13	PASS
DC_41A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Outer_Full	5.39	≤13	PASS
DC_41A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Outer_Full	6.05	≤13	PASS
DC_41A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Outer_Full	4.75	≤13	PASS
DC_41A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Outer_Full	5.64	≤13	PASS
DC_41A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Outer_Full	5.45	≤13	PASS
DC_41A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Outer_Full	6.02	≤13	PASS
DC_41A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Outer_Full	5.34	≤13	PASS
DC_41A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Outer_Full	6.16	≤13	PASS
DC_41A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Outer_Full	4.73	≤13	PASS
DC_41A_n	30	5+15	DFT-16QAM	M+H	Outer_Full	5.69	≤13	PASS

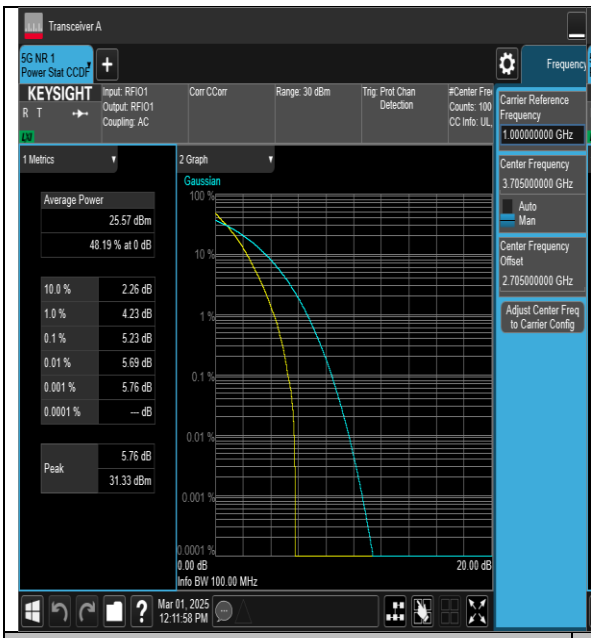
77A-3700-3980								
DC_41A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	5.27	≤13	PASS
DC_41A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Outer_Full	6.08	≤13	PASS
DC_41A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Outer_Full	5.26	≤13	PASS
DC_41A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Outer_Full	6.06	≤13	PASS
DC_41A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	4.78	≤13	PASS
DC_41A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Outer_Full	5.61	≤13	PASS
DC_41A_n77A-3700-3980	30	5+30	DFT-QPSK	M+L	Outer_Full	5.34	≤13	PASS
DC_41A_n77A-3700-3980	30	5+30	DFT-16QAM	M+L	Outer_Full	6.04	≤13	PASS
DC_41A_n77A-3700-3980	30	5+30	DFT-QPSK	M+M	Outer_Full	5.39	≤13	PASS
DC_41A_n77A-3700-3980	30	5+30	DFT-16QAM	M+M	Outer_Full	6.11	≤13	PASS
DC_41A_n77A-3700-3980	30	5+30	DFT-QPSK	M+H	Outer_Full	4.69	≤13	PASS
DC_41A_n77A-3700-3980	30	5+30	DFT-16QAM	M+H	Outer_Full	5.66	≤13	PASS
DC_41A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	5.58	≤13	PASS
DC_41A_n	30	5+40	DFT-16QAM	M+L	Outer_Full	6.14	≤13	PASS

77A-3700-3980								
DC_41A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	5.34	≤13	PASS
DC_41A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Outer_Full	6.18	≤13	PASS
DC_41A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	4.77	≤13	PASS
DC_41A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Outer_Full	5.68	≤13	PASS
DC_41A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Outer_Full	5.23	≤13	PASS
DC_41A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Outer_Full	6.00	≤13	PASS
DC_41A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Outer_Full	5.28	≤13	PASS
DC_41A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Outer_Full	6.07	≤13	PASS
DC_41A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Outer_Full	4.84	≤13	PASS
DC_41A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Outer_Full	5.70	≤13	PASS
DC_41A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	5.31	≤13	PASS
DC_41A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Outer_Full	6.03	≤13	PASS
DC_41A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	5.27	≤13	PASS
DC_41A_n	30	5+60	DFT-16QAM	M+M	Outer_Full	6.15	≤13	PASS

77A-3700-3980								
DC_41A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	4.89	≤13	PASS
DC_41A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Outer_Full	5.80	≤13	PASS
DC_41A_n77A-3700-3980	30	5+70	DFT-QPSK	M+L	Outer_Full	5.40	≤13	PASS
DC_41A_n77A-3700-3980	30	5+70	DFT-16QAM	M+L	Outer_Full	6.23	≤13	PASS
DC_41A_n77A-3700-3980	30	5+70	DFT-QPSK	M+M	Outer_Full	5.54	≤13	PASS
DC_41A_n77A-3700-3980	30	5+70	DFT-16QAM	M+M	Outer_Full	6.26	≤13	PASS
DC_41A_n77A-3700-3980	30	5+70	DFT-QPSK	M+H	Outer_Full	5.42	≤13	PASS
DC_41A_n77A-3700-3980	30	5+70	DFT-16QAM	M+H	Outer_Full	6.22	≤13	PASS
DC_41A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	5.42	≤13	PASS
DC_41A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Outer_Full	6.17	≤13	PASS
DC_41A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	5.49	≤13	PASS
DC_41A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Outer_Full	6.25	≤13	PASS
DC_41A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	5.19	≤13	PASS
DC_41A_n	30	5+80	DFT-16QAM	M+H	Outer_Full	5.96	≤13	PASS

77A-3700-3980								
DC_41A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Outer_Full	5.38	≤13	PASS
DC_41A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Outer_Full	6.07	≤13	PASS
DC_41A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Outer_Full	5.41	≤13	PASS
DC_41A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Outer_Full	6.26	≤13	PASS
DC_41A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Outer_Full	5.28	≤13	PASS
DC_41A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Outer_Full	6.13	≤13	PASS
DC_41A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Outer_Full	5.37	≤13	PASS
DC_41A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Outer_Full	6.15	≤13	PASS
DC_41A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Outer_Full	5.51	≤13	PASS
DC_41A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Outer_Full	6.21	≤13	PASS
DC_41A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Outer_Full	5.35	≤13	PASS
DC_41A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Outer_Full	6.22	≤13	PASS

Test Graphs



NTNV-DC_41A_n77A-3700-3980-PC2-30-5+10-M +L-N/A-N/A-DFT-QPSK-Outer_Full-PASS

NTNV-DC_41A_n77A-3700-3980-PC2-30-5+10-M +L-N/A-N/A-DFT-16QAM-Outer_Full-PASS

NTNV-DC_41A_n77A-3700-3980-PC2-30-5+10-M +M-N/A-N/A-DFT-QPSK-Outer_Full-PASS

NTNV-DC_41A_n77A-3700-3980-PC2-30-5+10-M +M-N/A-N/A-DFT-16QAM-Outer_Full-PASS

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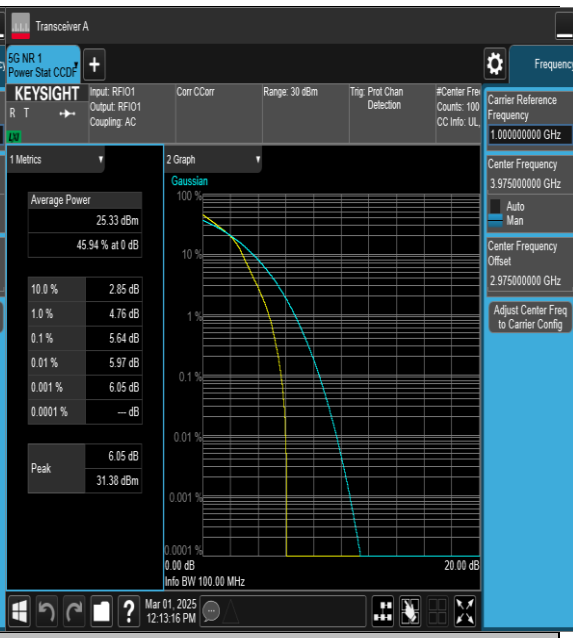

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NTNV-DC_41A_n77A-3700-3980-PC2-30-5+15-M +L-N/A-N/A-DFT-16QAM-Outer_Full-PASS

NTNV-DC_41A_n77A-3700-3980-PC2-30-5+15-M+M-N/A-N/A-DFT-QPSK-Outer_Full-PASS

NTNV-DC_41A_n77A-3700-3980-PC2-30-5+15-M+M-N/A-N/A-DFT-16QAM-Outer_Full-PASS

NTNV-DC_41A_n77A-3700-3980-PC2-30-5+15-M+H-N/A-N/A-DFT-QPSK-Outer_Full-PASS

NTNV-DC_41A_n77A-3700-3980-PC2-30-5+15-M+H-N/A-N/A-DFT-16QAM-Outer_Full-PASS

NTNV-DC_41A_n77A-3700-3980-PC2-30-5+20-M+L-N/A-N/A-DFT-QPSK-Outer_Full-PASS

NTNV-DC_41A_n77A-3700-3980-PC2-30-5+20-M+L-N/A-N/A-DFT-16QAM-Outer_Full-PASS

NTNV-DC_41A_n77A-3700-3980-PC2-30-5+20-M+M-N/A-N/A-DFT-QPSK-Outer_Full-PASS

NTNV-DC_41A_n77A-3700-3980-PC2-30-5+20-M+M-N/A-N/A-DFT-16QAM-Outer_Full-PASS

NTNV-DC_41A_n77A-3700-3980-PC2-30-5+20-M +H-N/A-N/A-DFT-QPSK-Outer_Full-PASS

NTNV-DC_41A_n77A-3700-3980-PC2-30-5+20-M +H-N/A-N/A-DFT-16QAM-Outer_Full-PASS

NTNV-DC_41A_n77A-3700-3980-PC2-30-5+30-M +L-N/A-N/A-DFT-QPSK-Outer_Full-PASS

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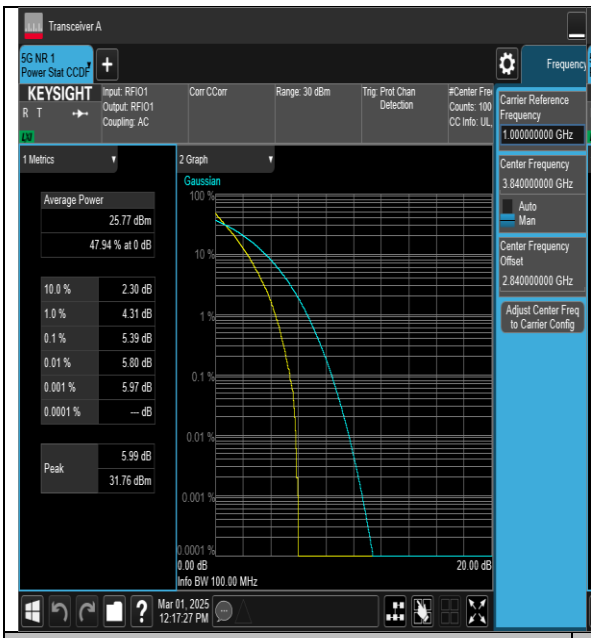

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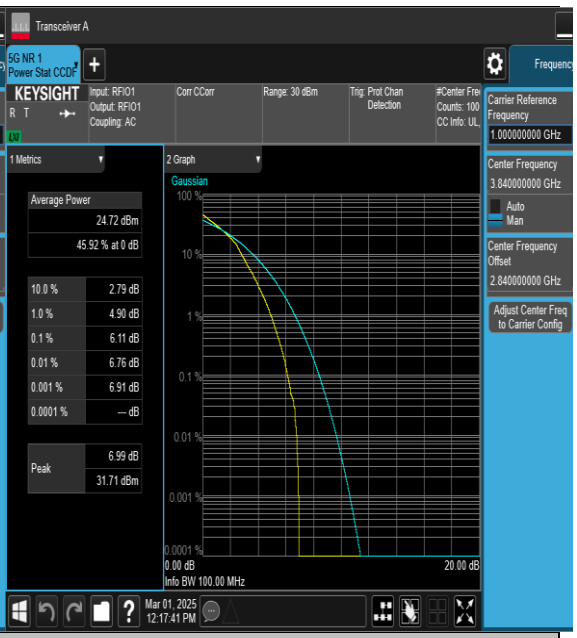

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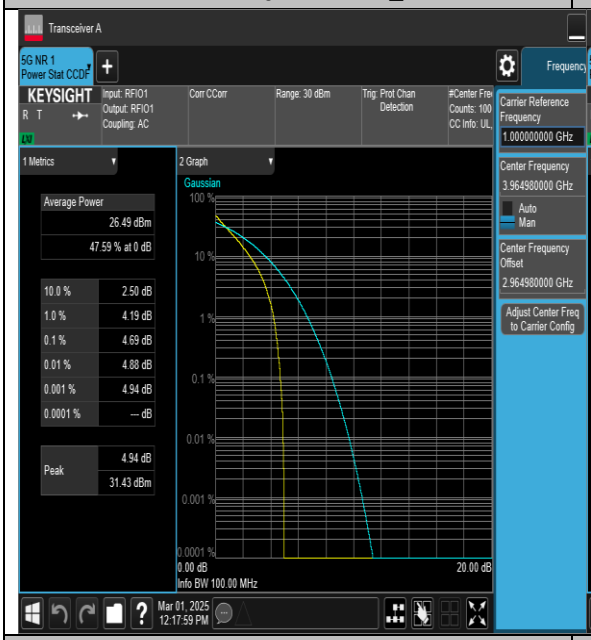

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NTNV-DC_41A_n77A-3700-3980-PC2-30-5+40-M+L-N/A-N/A-DFT-QPSK-Outer_Full-PASS

NTNV-DC_41A_n77A-3700-3980-PC2-30-5+40-M+L-N/A-N/A-DFT-16QAM-Outer_Full-PASS

NTNV-DC_41A_n77A-3700-3980-PC2-30-5+40-M+M-N/A-N/A-DFT-QPSK-Outer_Full-PASS

NTNV-DC_41A_n77A-3700-3980-PC2-30-5+40-M+M-N/A-N/A-DFT-16QAM-Outer_Full-PASS

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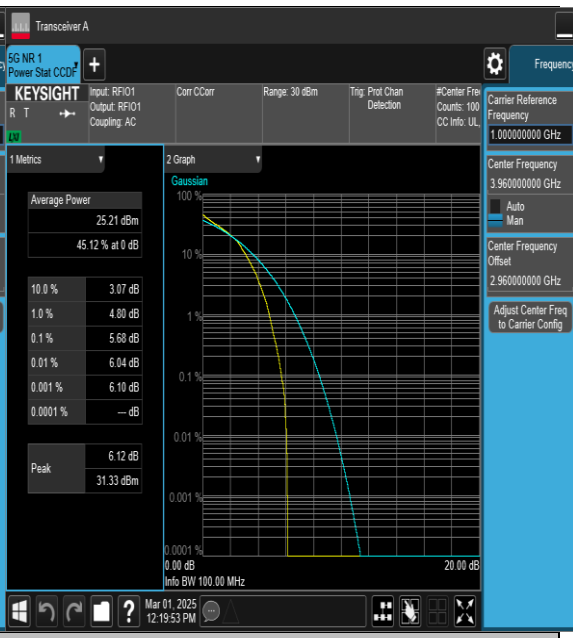

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NTNV-DC_41A_n77A-3700-3980-PC2-30-5+50-M +L-N/A-N/A-DFT-16QAM-Outer_Full-PASS

NTNV-DC_41A_n77A-3700-3980-PC2-30-5+50-M+M-N/A-N/A-DFT-QPSK-Outer_Full-PASS

NTNV-DC_41A_n77A-3700-3980-PC2-30-5+50-M+M-N/A-N/A-DFT-16QAM-Outer_Full-PASS

NTNV-DC_41A_n77A-3700-3980-PC2-30-5+50-M+H-N/A-N/A-DFT-QPSK-Outer_Full-PASS

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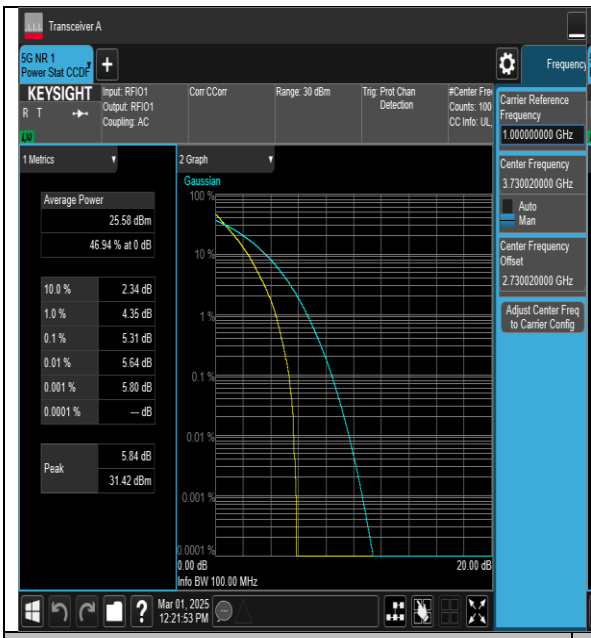

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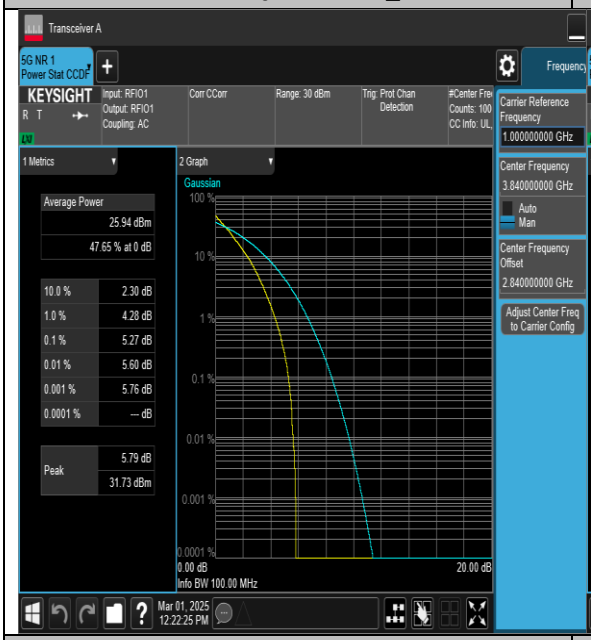

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NTNV-DC_41A_n77A-3700-3980-PC2-30-5+60-M+M-N/A-N/A-DFT-16QAM-Outer_Full-PASS

NTNV-DC_41A_n77A-3700-3980-PC2-30-5+60-M+H-N/A-N/A-DFT-QPSK-Outer_Full-PASS

NTNV-DC_41A_n77A-3700-3980-PC2-30-5+60-M+H-N/A-N/A-DFT-16QAM-Outer_Full-PASS

NTNV-DC_41A_n77A-3700-3980-PC2-30-5+70-M+L-N/A-N/A-DFT-QPSK-Outer_Full-PASS

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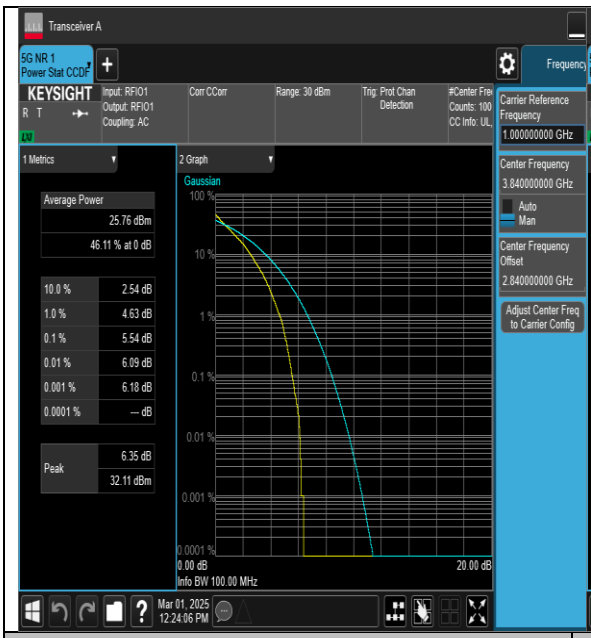

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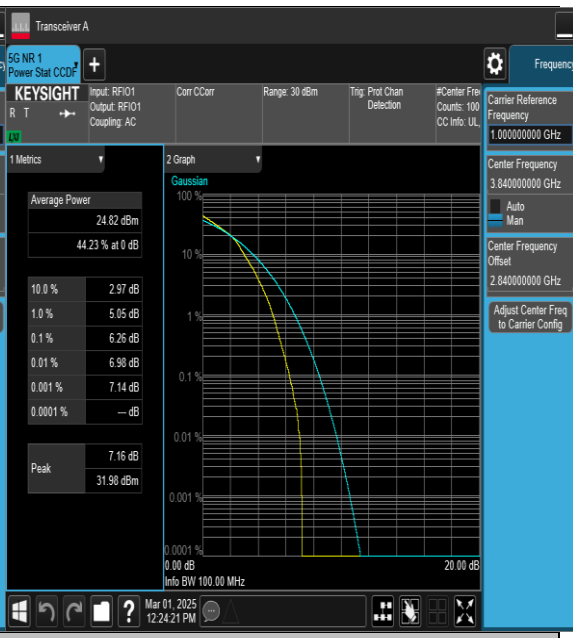

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NTNV-DC_41A_n77A-3700-3980-PC2-30-5+70-M+H-N/A-N/A-DFT-QPSK-Outer_Full-PASS

NTNV-DC_41A_n77A-3700-3980-PC2-30-5+70-M+H-N/A-N/A-DFT-16QAM-Outer_Full-PASS

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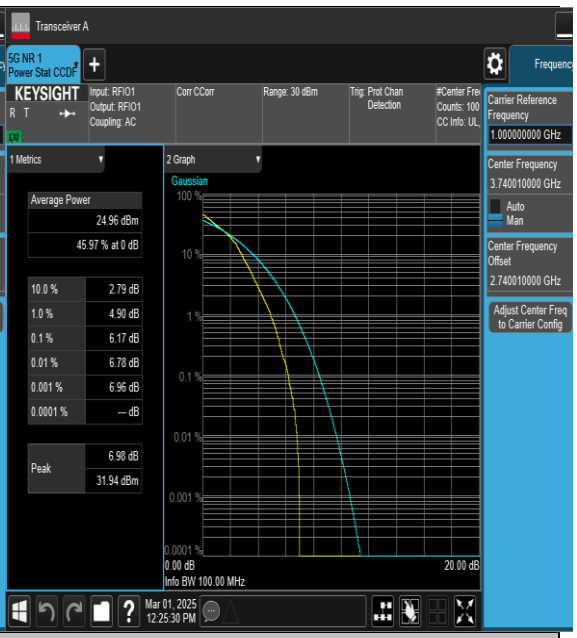

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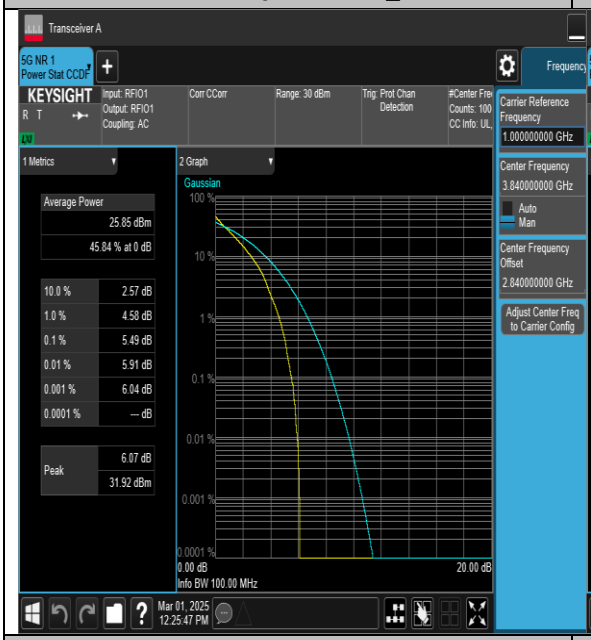

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NTNV-DC_41A_n77A-3700-3980-PC2-30-5+80-M+H-N/A-N/A-DFT-QPSK-Outer_Full-PASS

NTNV-DC_41A_n77A-3700-3980-PC2-30-5+80-M+H-N/A-N/A-DFT-16QAM-Outer_Full-PASS

NTNV-DC_41A_n77A-3700-3980-PC2-30-5+90-M+L-N/A-N/A-DFT-QPSK-Outer_Full-PASS

NTNV-DC_41A_n77A-3700-3980-PC2-30-5+90-M+L-N/A-N/A-DFT-16QAM-Outer_Full-PASS

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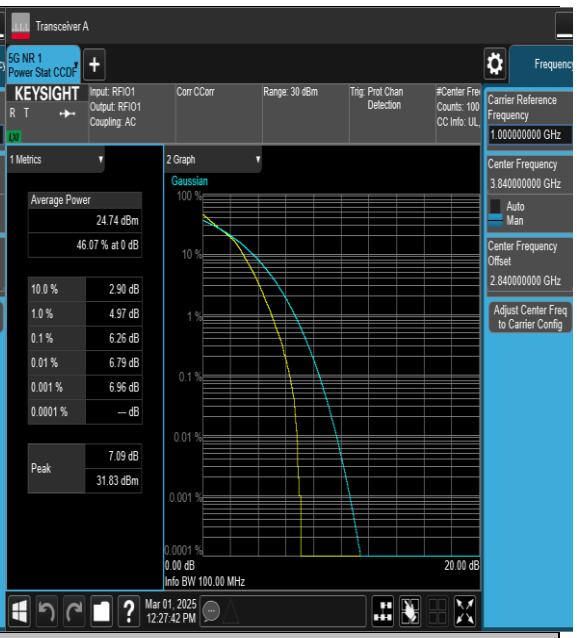

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NTNV-DC_41A_n77A-3700-3980-PC2-30-5+90-M+H-N/A-N/A-DFT-16QAM-Outer_Full-PASS

NTNV-DC_41A_n77A-3700-3980-PC2-30-5+100-M+L-N/A-N/A-DFT-QPSK-Outer_Full-PASS

NTNV-DC_41A_n77A-3700-3980-PC2-30-5+100-M+L-N/A-N/A-DFT-16QAM-Outer_Full-PASS

NTNV-DC_41A_n77A-3700-3980-PC2-30-5+100-M+M-N/A-N/A-DFT-QPSK-Outer_Full-PASS

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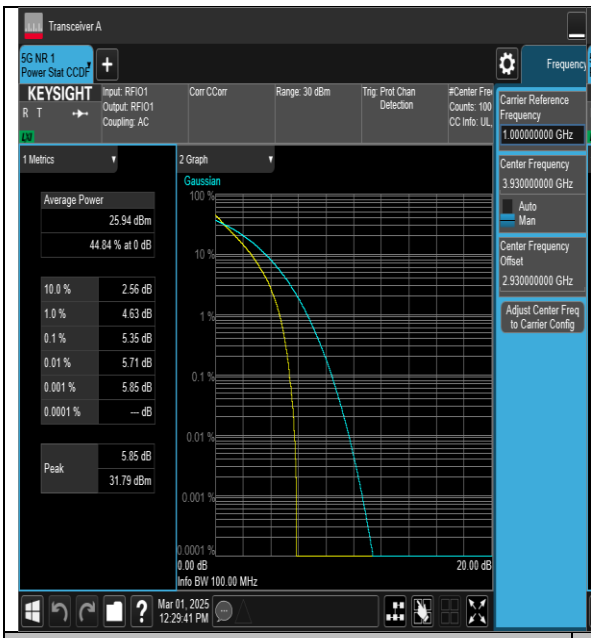

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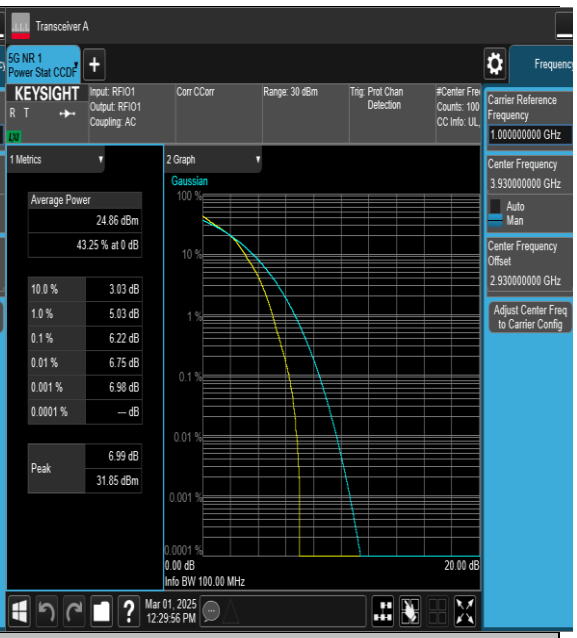


NTNV-DC_41A_n77A-3700-3980-PC2-30-5+100-M+H-N/A-N/A-DFT-16QAM-Outer_Full-PASS

Appendix C: 26dB Bandwidth and Occupied Bandwidth for NSA

Test Result

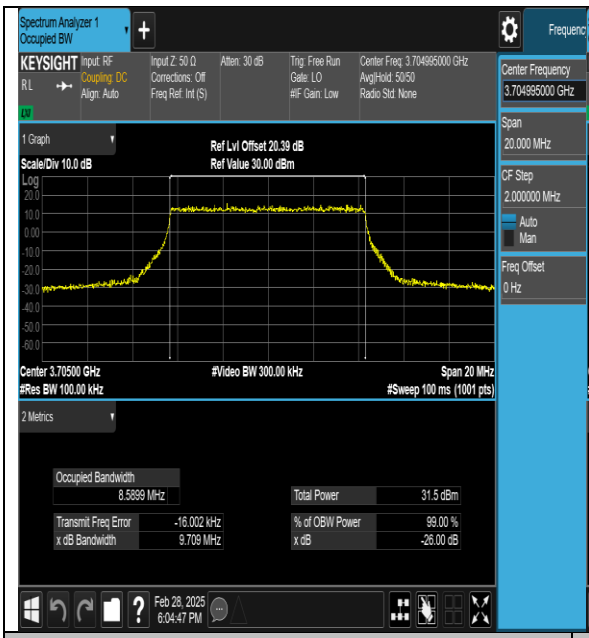
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_41A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Outer_Full	8.5899	9.709	PASS
DC_41A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Outer_Full	8.5815	9.633	PASS
DC_41A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Outer_Full	8.6091	9.854	PASS
DC_41A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Outer_Full	8.5994	9.756	PASS
DC_41A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Outer_Full	8.6127	9.769	PASS
DC_41A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Outer_Full	8.6056	9.789	PASS
DC_41A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Outer_Full	12.895	14.32	PASS
DC_41A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Outer_Full	12.879	14.24	PASS
DC_41A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Outer_Full	12.858	14.27	PASS
DC_41A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Outer_Full	12.869	14.16	PASS
DC_41A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Outer_Full	12.911	14.15	PASS
DC_41A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Outer_Full	12.881	14.24	PASS
DC_41A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	17.903	19.38	PASS
DC_41A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Outer_Full	17.868	19.48	PASS
DC_41A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Outer_Full	17.882	19.43	PASS
DC_41A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Outer_Full	17.892	19.51	PASS
DC_41A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	17.865	19.38	PASS

DC_41A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Outer_Full	17.888	19.36	PASS
DC_41A_n77A-3700-3980	30	5+30	DFT-QPSK	M+L	Outer_Full	26.794	28.71	PASS
DC_41A_n77A-3700-3980	30	5+30	DFT-16QAM	M+L	Outer_Full	26.812	28.74	PASS
DC_41A_n77A-3700-3980	30	5+30	DFT-QPSK	M+M	Outer_Full	26.816	28.76	PASS
DC_41A_n77A-3700-3980	30	5+30	DFT-16QAM	M+M	Outer_Full	26.797	28.69	PASS
DC_41A_n77A-3700-3980	30	5+30	DFT-QPSK	M+H	Outer_Full	26.784	28.48	PASS
DC_41A_n77A-3700-3980	30	5+30	DFT-16QAM	M+H	Outer_Full	26.789	28.52	PASS
DC_41A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	35.705	37.75	PASS
DC_41A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Outer_Full	35.699	37.81	PASS
DC_41A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	35.721	37.79	PASS
DC_41A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Outer_Full	35.706	37.86	PASS
DC_41A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	35.675	37.80	PASS
DC_41A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Outer_Full	35.716	37.69	PASS
DC_41A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Outer_Full	45.671	48.19	PASS
DC_41A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Outer_Full	45.669	48.10	PASS
DC_41A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Outer_Full	45.619	48.09	PASS
DC_41A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Outer_Full	45.617	48.00	PASS
DC_41A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Outer_Full	45.691	47.95	PASS
DC_41A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Outer_Full	45.654	48.11	PASS
DC_41A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	57.852	60.63	PASS
DC_41A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Outer_Full	57.785	60.49	PASS

DC_41A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	57.776	60.53	PASS
DC_41A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Outer_Full	57.852	60.52	PASS
DC_41A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	57.742	60.47	PASS
DC_41A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Outer_Full	57.747	60.63	PASS
DC_41A_n77A-3700-3980	30	5+70	DFT-QPSK	M+L	Outer_Full	64.254	66.91	PASS
DC_41A_n77A-3700-3980	30	5+70	DFT-16QAM	M+L	Outer_Full	64.296	67.01	PASS
DC_41A_n77A-3700-3980	30	5+70	DFT-QPSK	M+M	Outer_Full	64.334	67.10	PASS
DC_41A_n77A-3700-3980	30	5+70	DFT-16QAM	M+M	Outer_Full	64.332	67.19	PASS
DC_41A_n77A-3700-3980	30	5+70	DFT-QPSK	M+H	Outer_Full	64.254	67.02	PASS
DC_41A_n77A-3700-3980	30	5+70	DFT-16QAM	M+H	Outer_Full	64.222	67.03	PASS
DC_41A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	77.079	80.16	PASS
DC_41A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Outer_Full	77.147	80.24	PASS
DC_41A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	77.125	80.29	PASS
DC_41A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Outer_Full	77.098	80.14	PASS
DC_41A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	77.086	80.06	PASS
DC_41A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Outer_Full	77.090	80.20	PASS
DC_41A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Outer_Full	86.700	90.07	PASS
DC_41A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Outer_Full	86.684	89.98	PASS
DC_41A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Outer_Full	86.718	90.16	PASS
DC_41A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Outer_Full	86.668	90.06	PASS
DC_41A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Outer_Full	86.746	90.01	PASS

DC_41A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Outer_Full	86.661	90.03	PASS
DC_41A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Outer_Full	96.396	100.1	PASS
DC_41A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Outer_Full	96.378	100.1	PASS
DC_41A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Outer_Full	96.380	100.2	PASS
DC_41A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Outer_Full	96.404	99.99	PASS
DC_41A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Outer_Full	96.394	99.79	PASS
DC_41A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Outer_Full	96.256	100.0	PASS

Test Graphs



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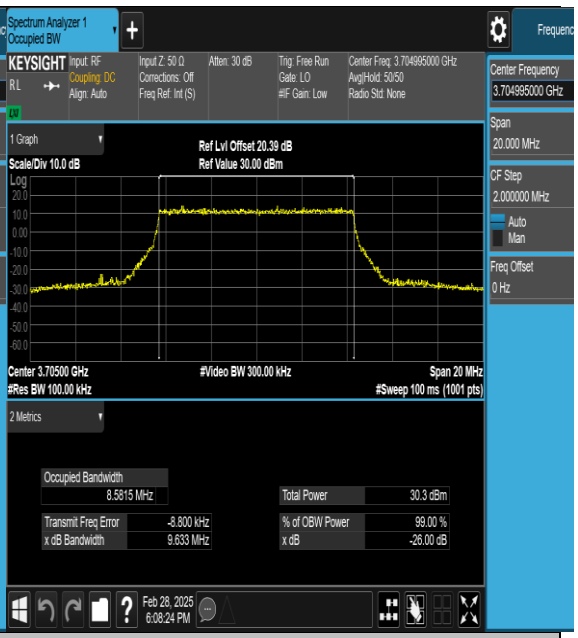

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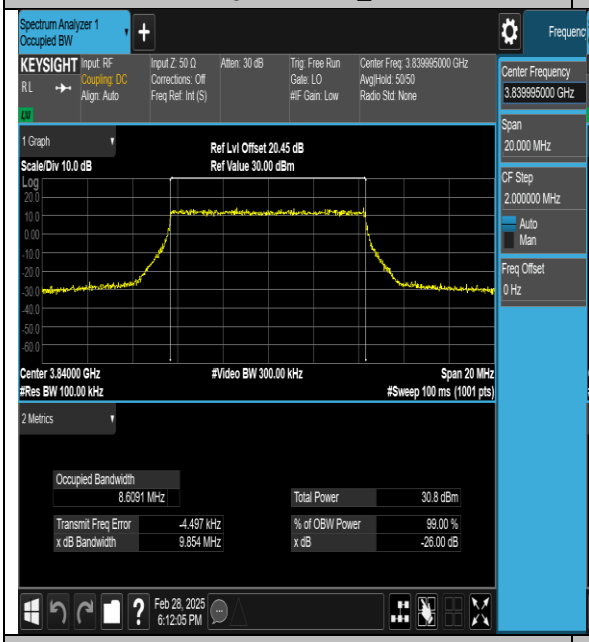

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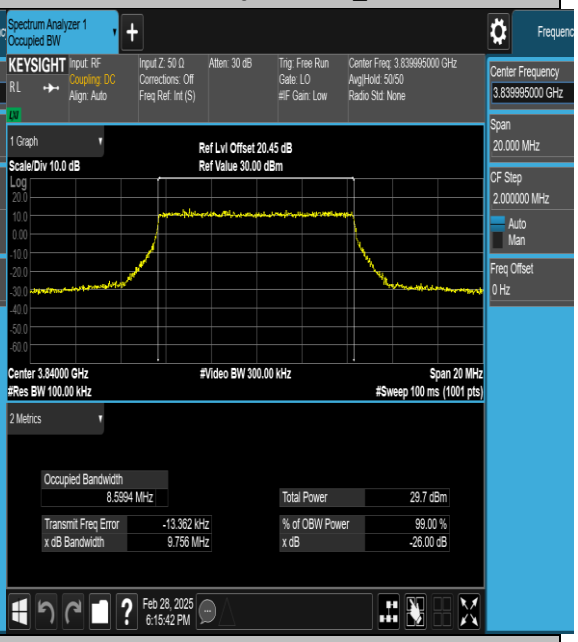

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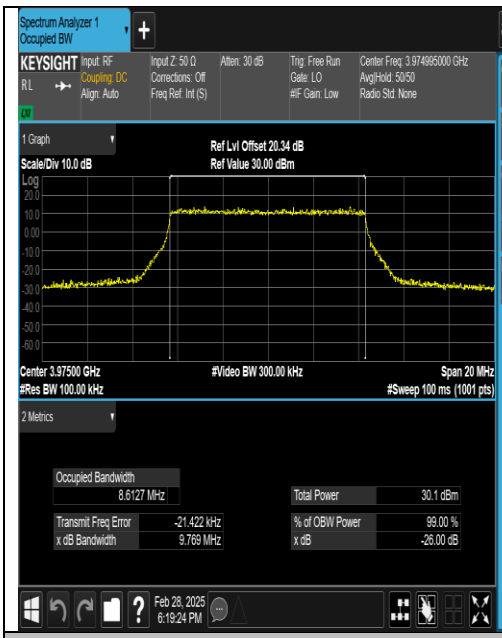

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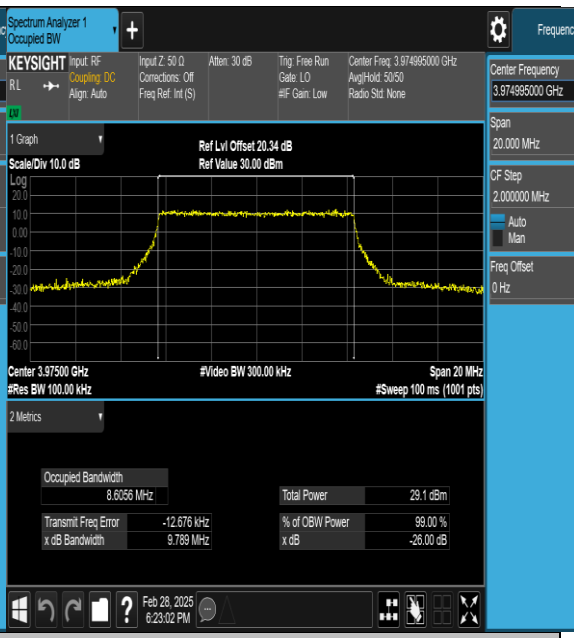

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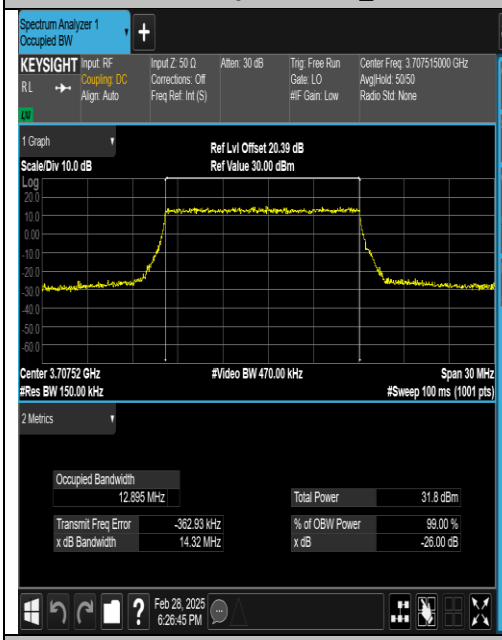

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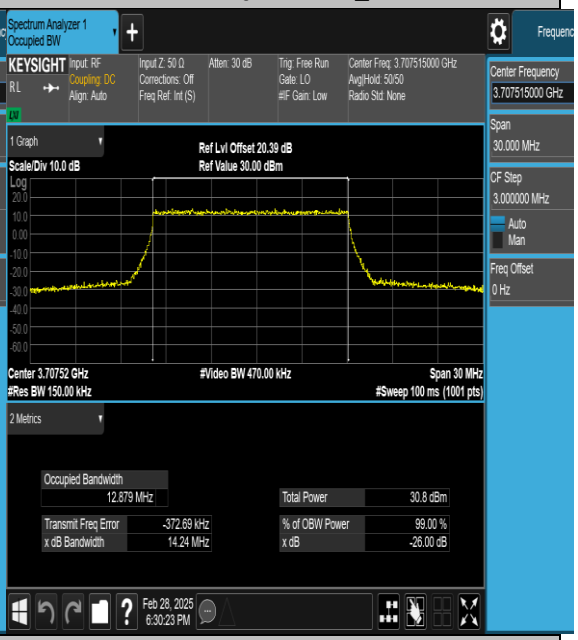

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