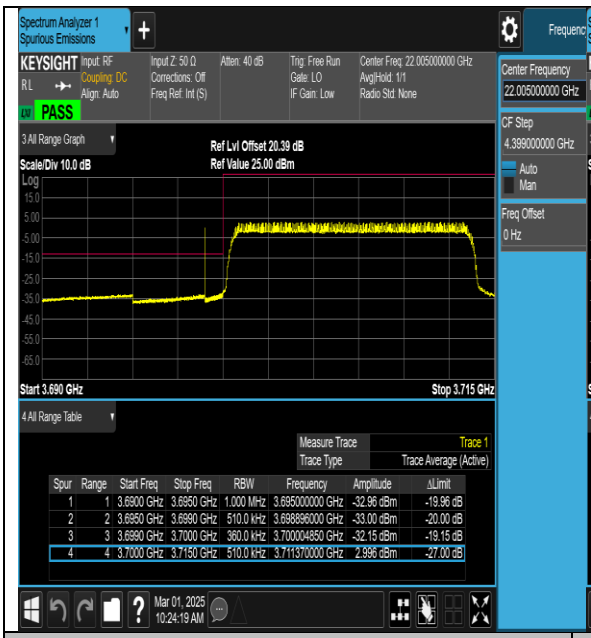

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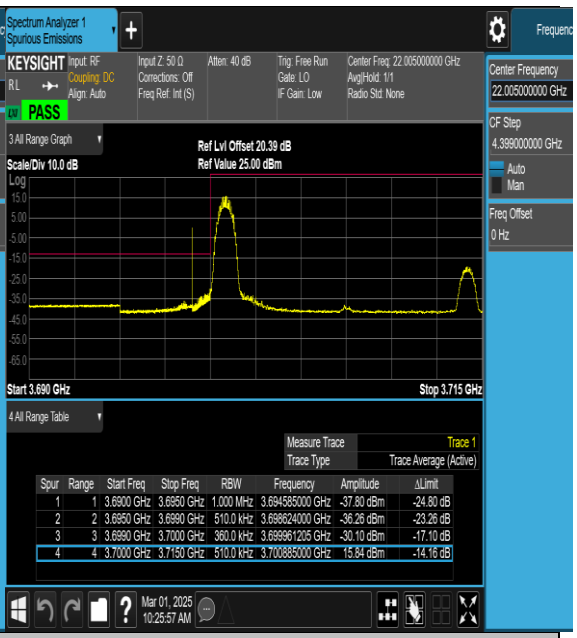

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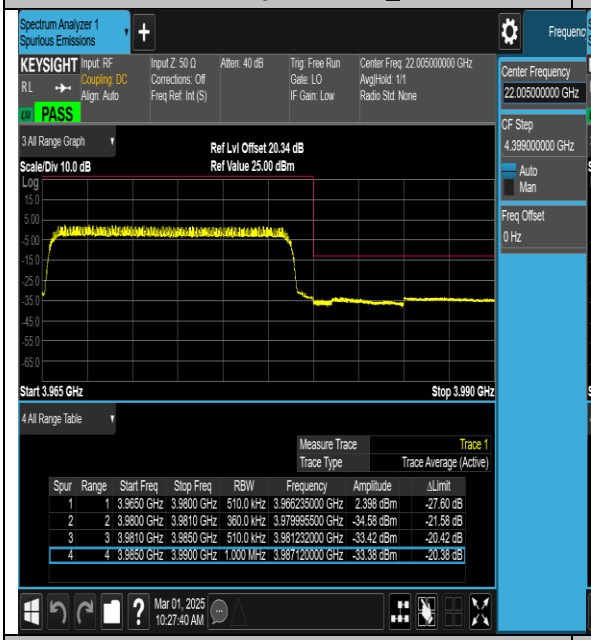

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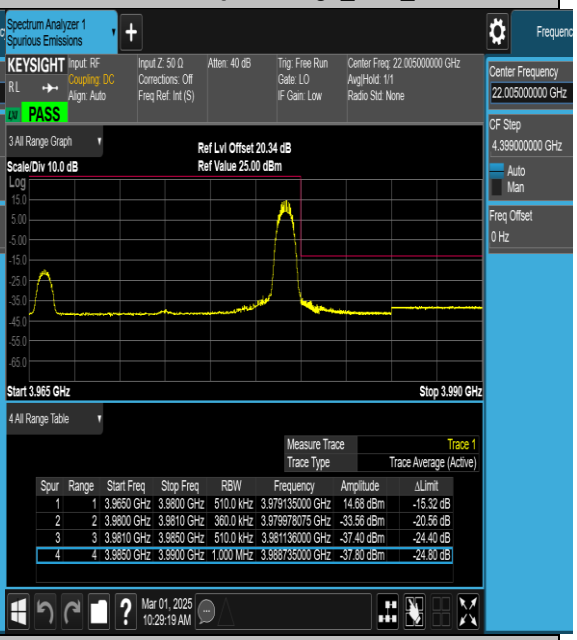

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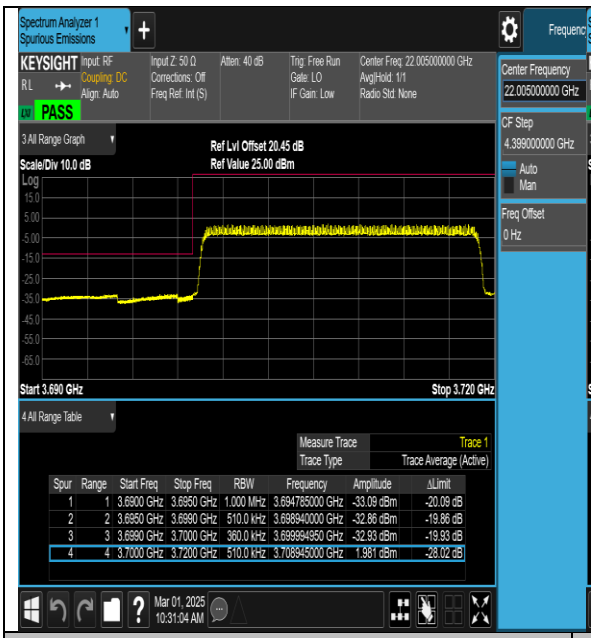

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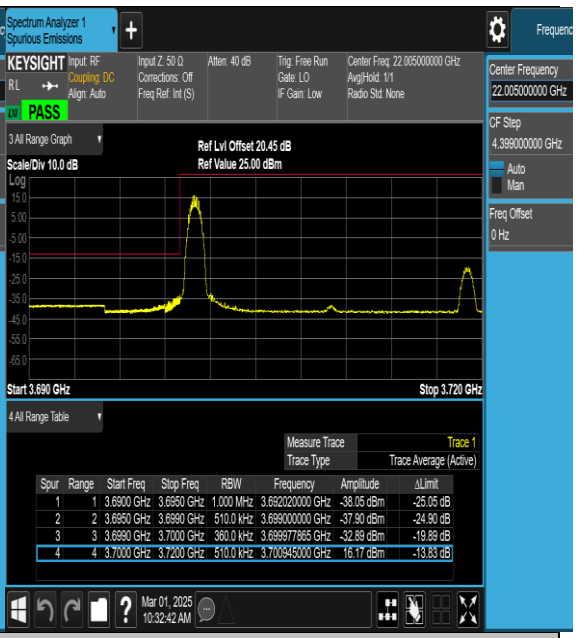

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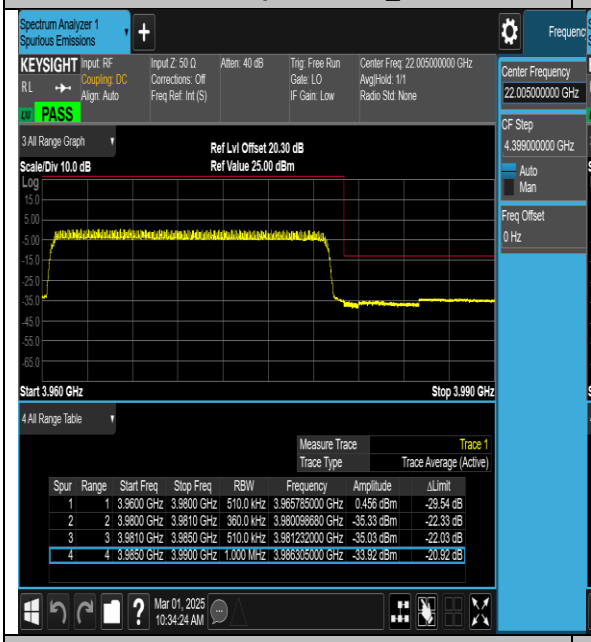

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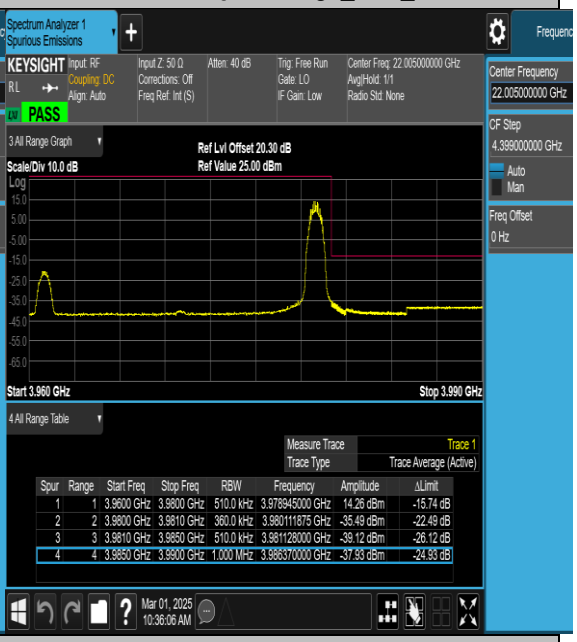

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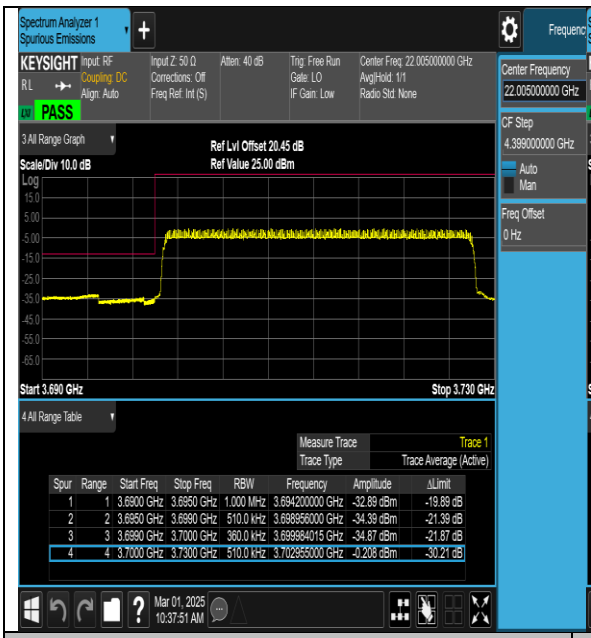

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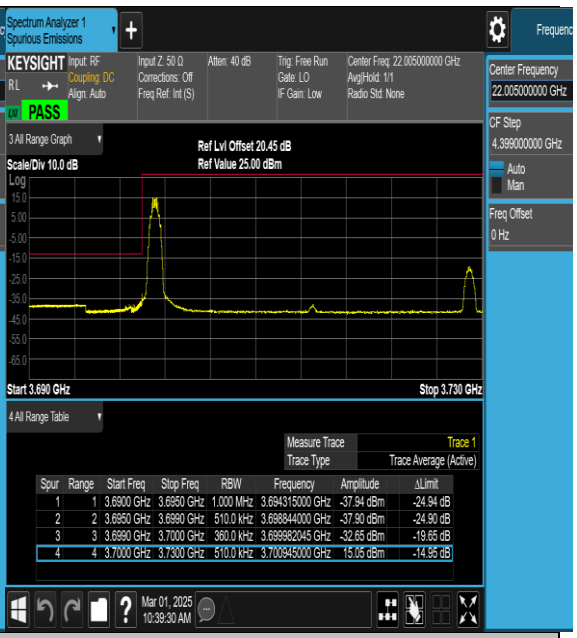

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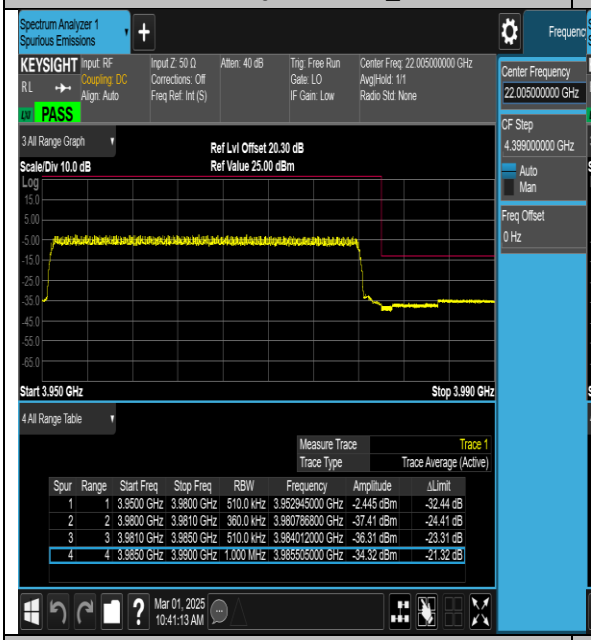

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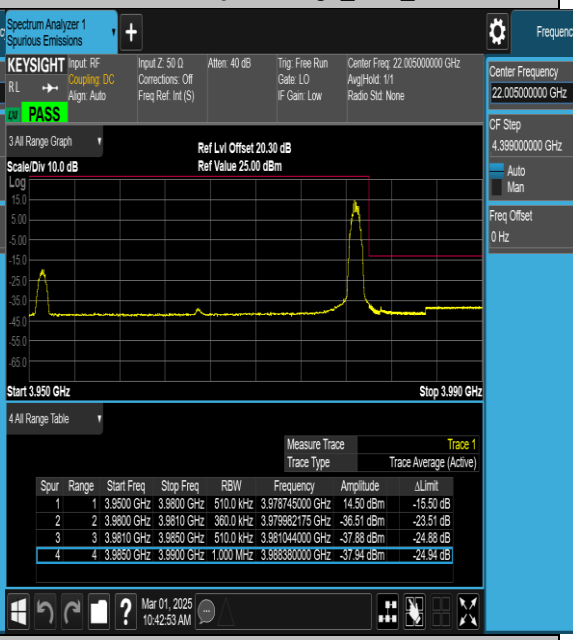

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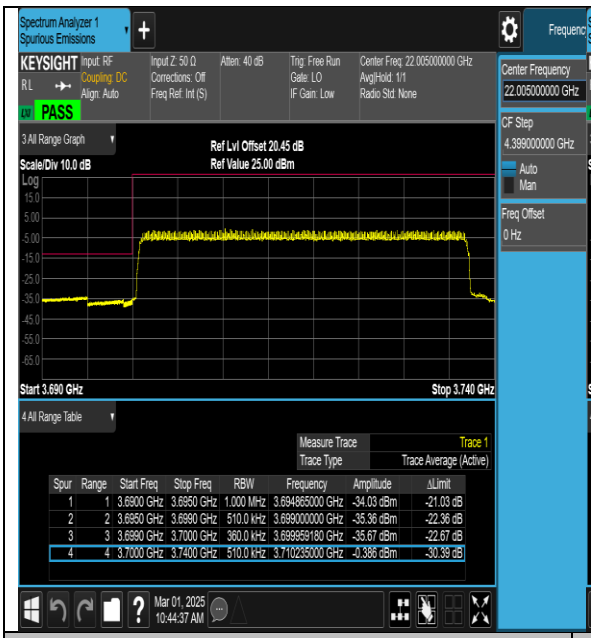

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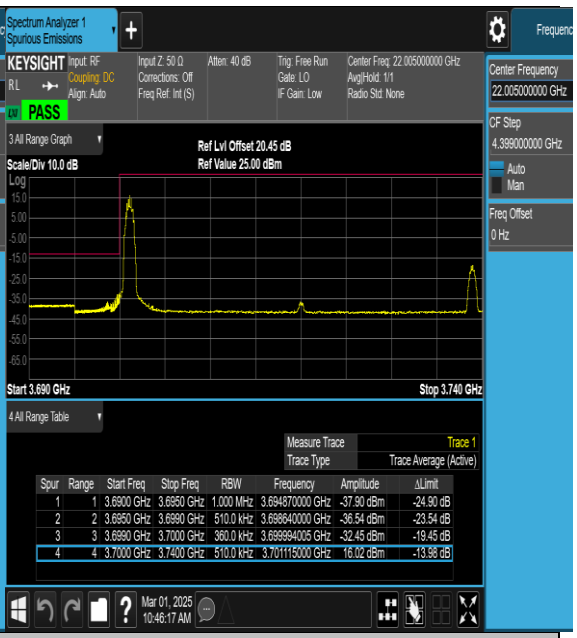

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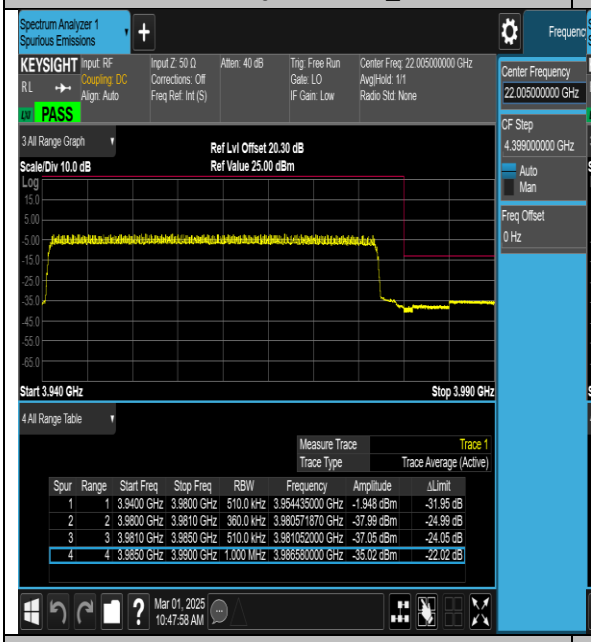

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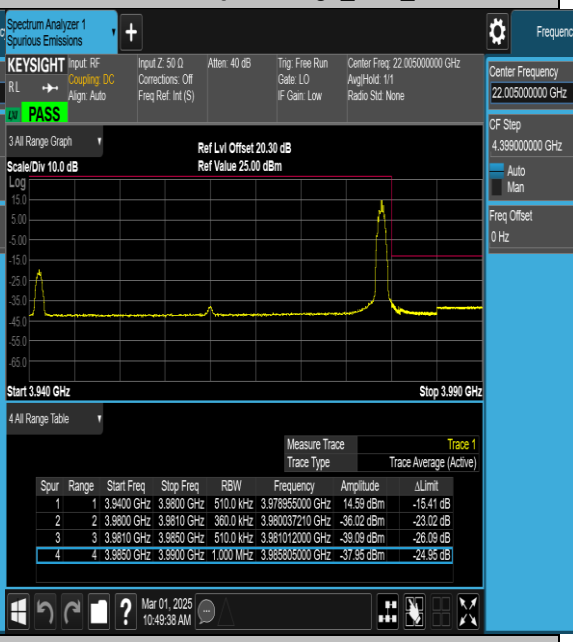

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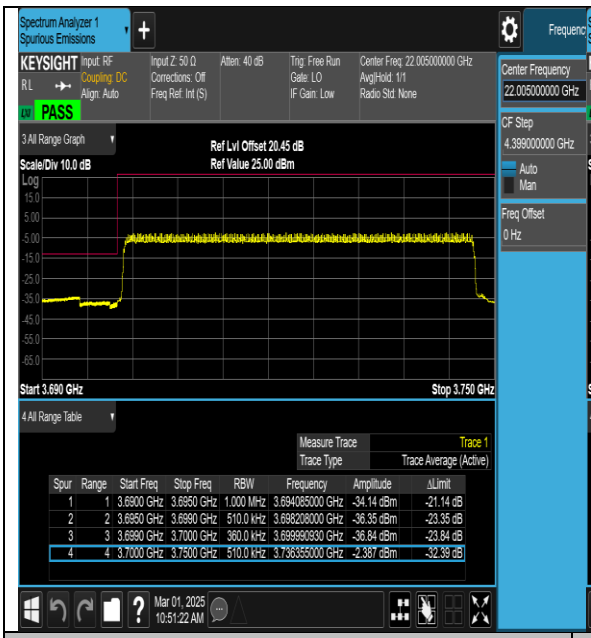

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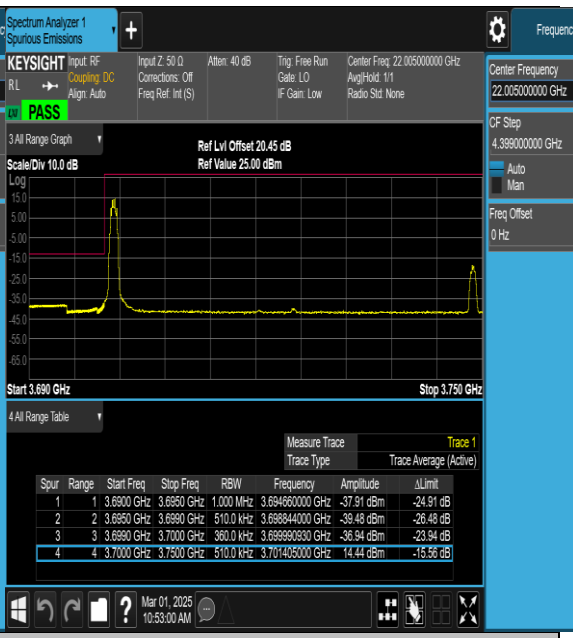

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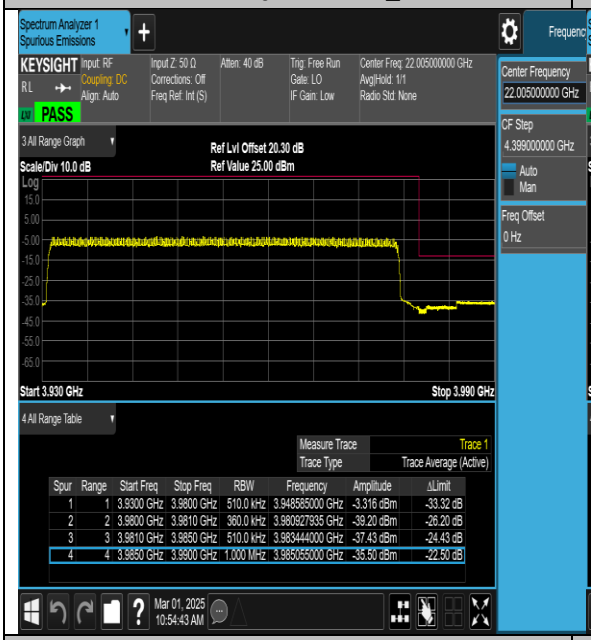

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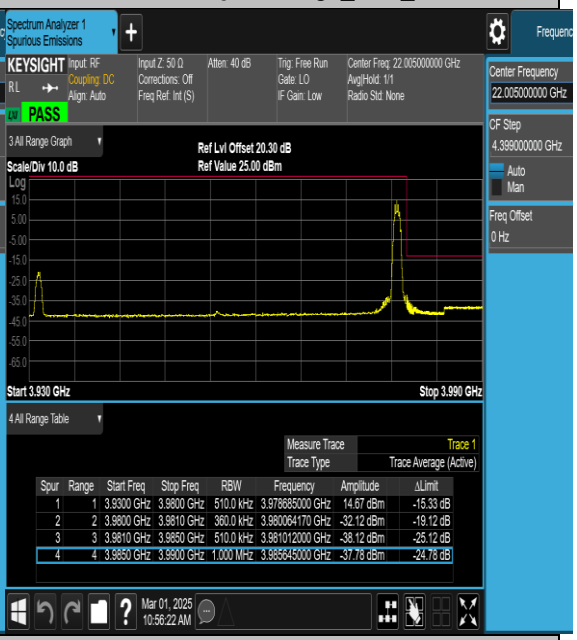

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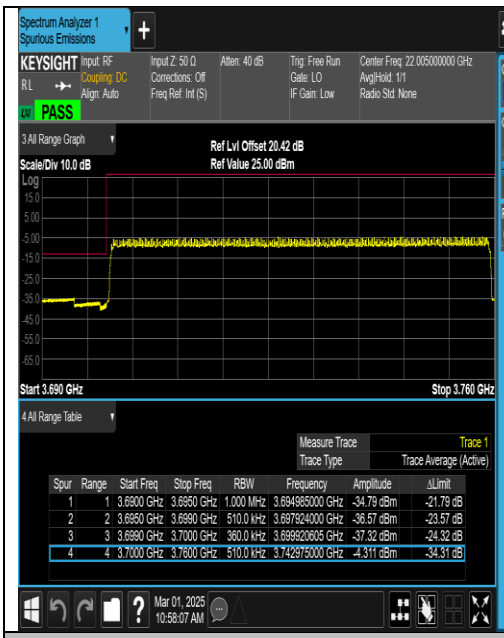

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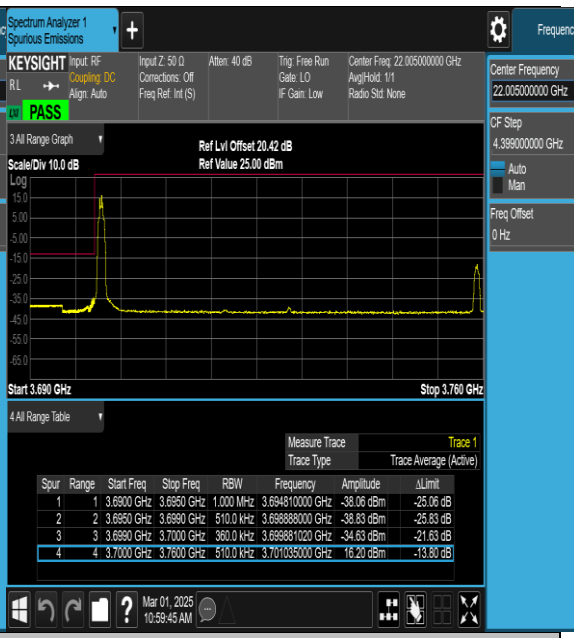

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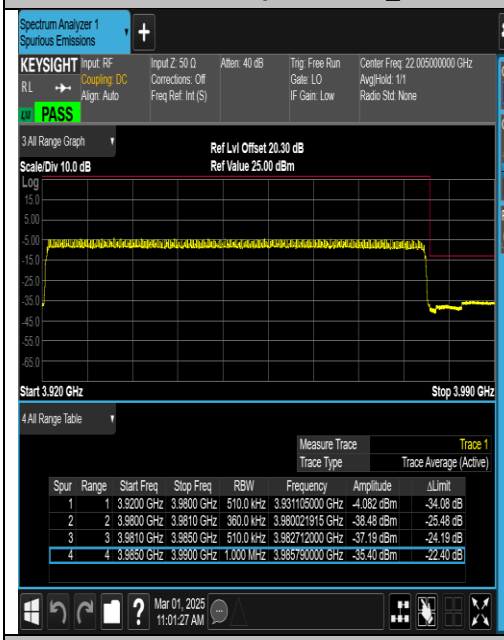

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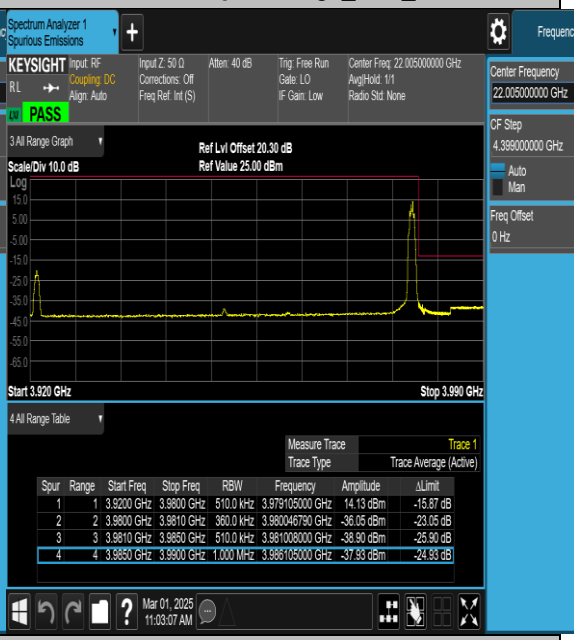

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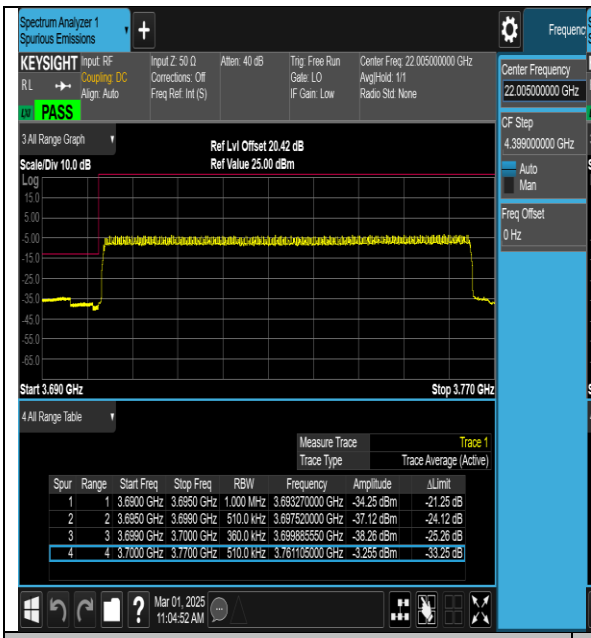

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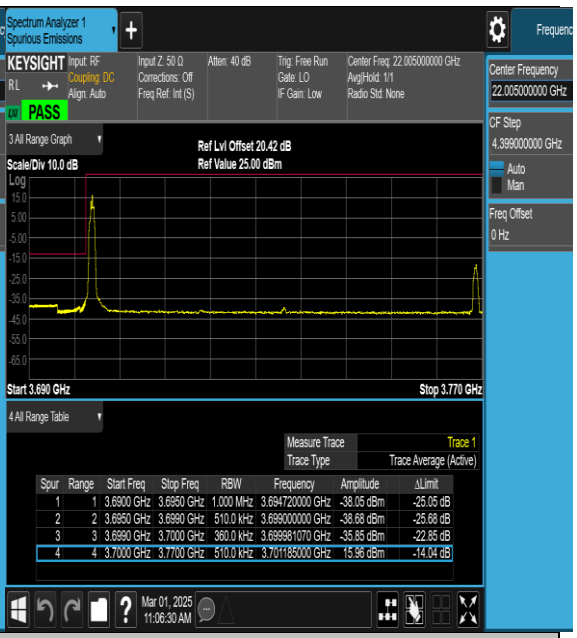

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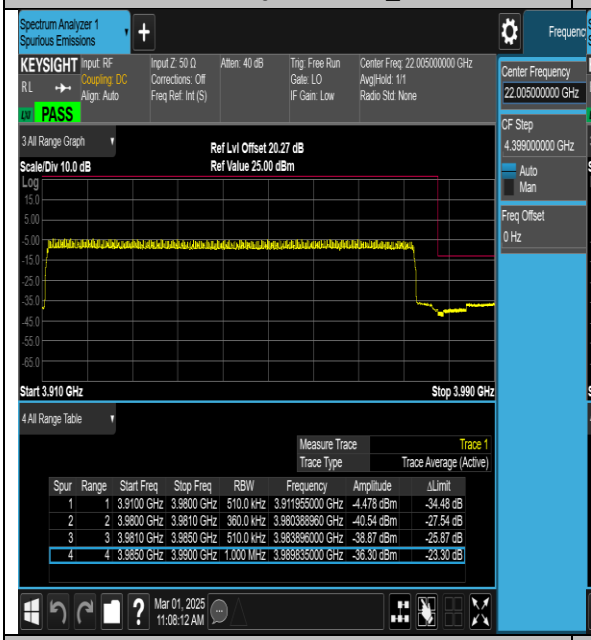

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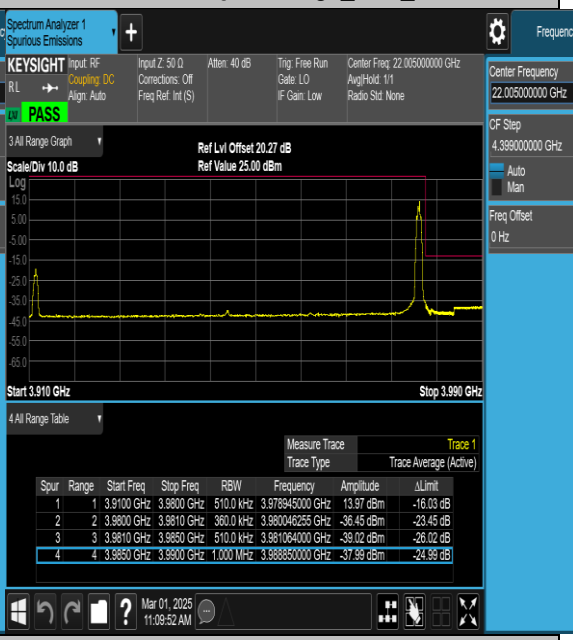

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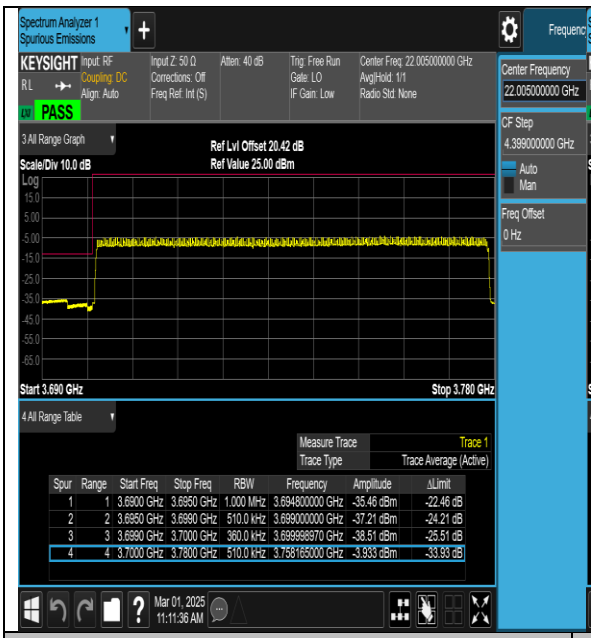

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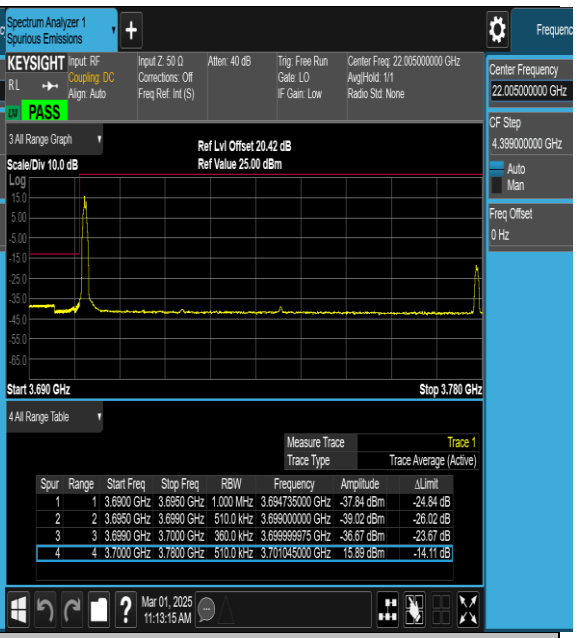

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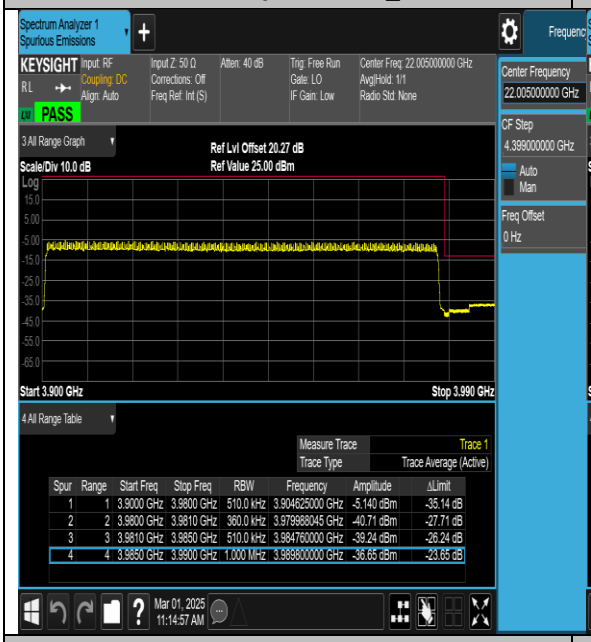

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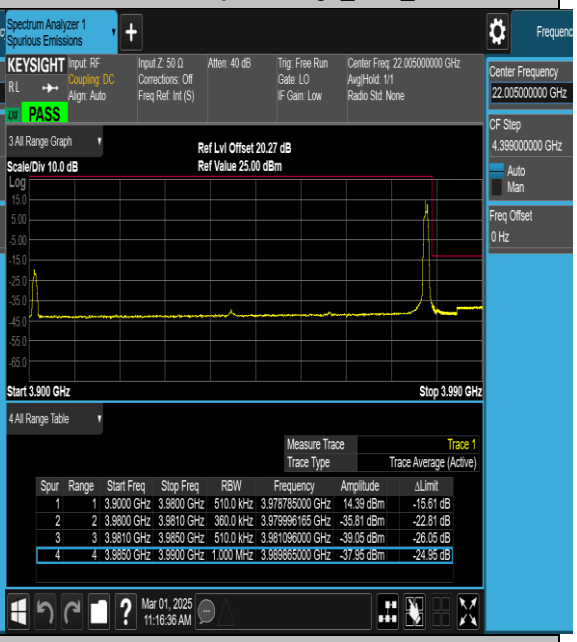

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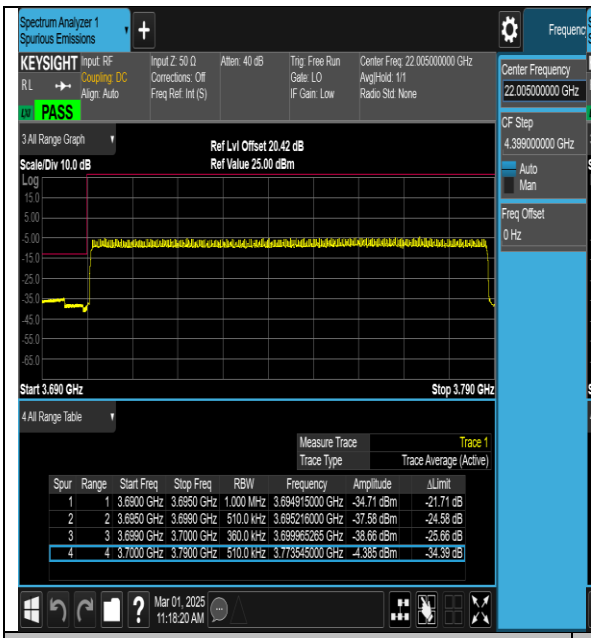

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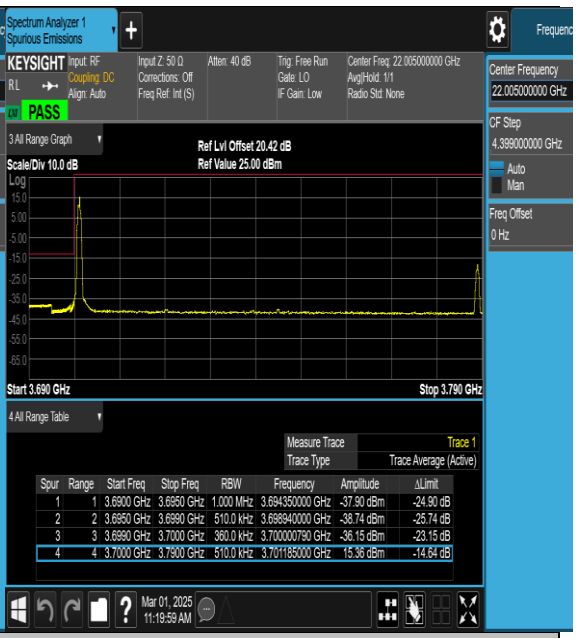

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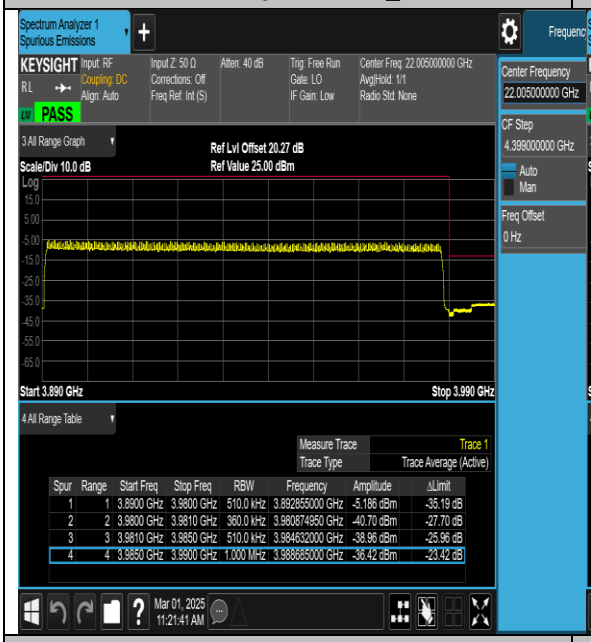

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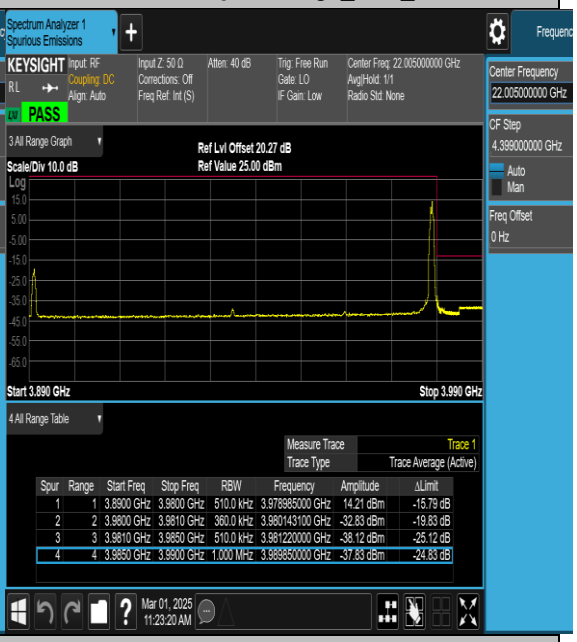

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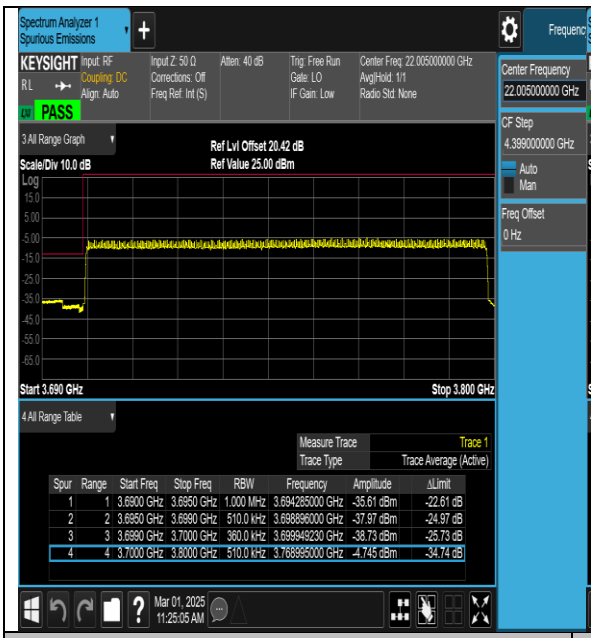

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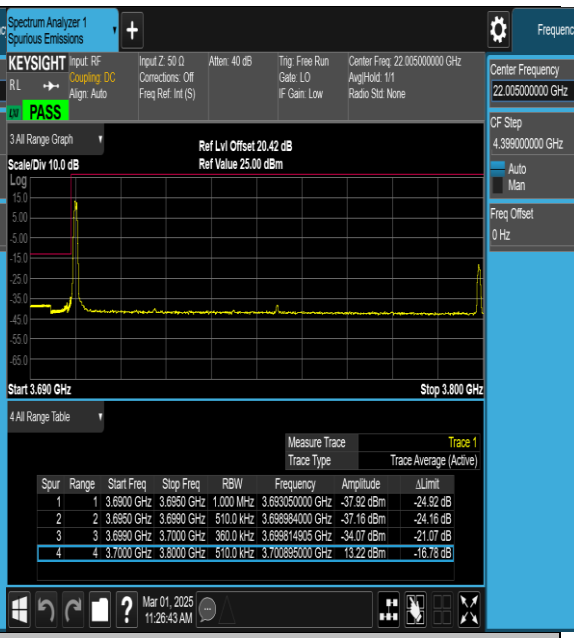

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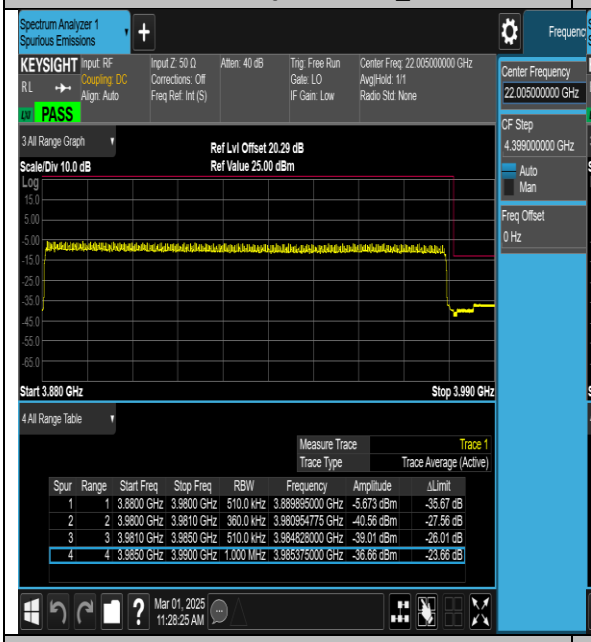

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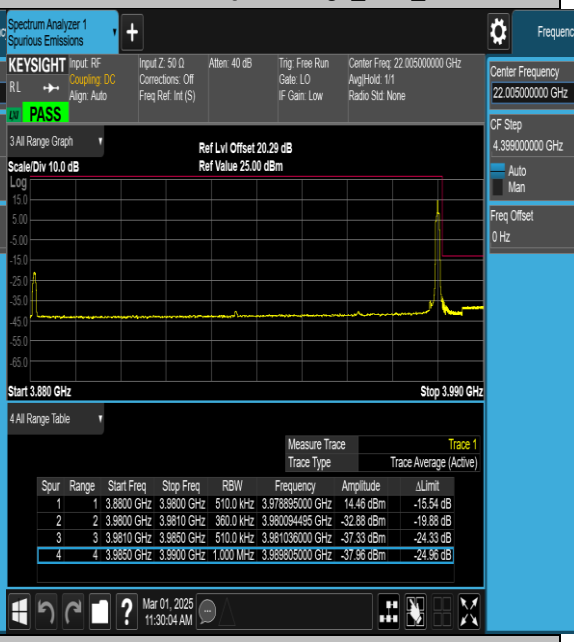


NTNV-DC_41A_n77A-3700-3980-PC2-30-5+100-M+H-N/A-N/A-DFT-QPSK-Edge_1RB_Right-PASS

Appendix E: Conducted Spurious Emission for NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	StartFreq	StopFreq	Result	Limit	Verdict
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-64.69	-43	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+L	Outer_Full	0.15	30	-72.52	-33	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+L	Outer_Full	30	1000	-53.34	-23	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+L	Outer_Full	1000	6000	-32.55	-13	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+L	Outer_Full	6000	26000	-36.21	-13	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+L	Outer_Full	26000	40000	-40.10	-13	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-63.43	-43	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-74.41	-33	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-54.01	-23	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-32.71	-13	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-36.24	-13	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-40.35	-13	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-63.42	-43	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-75.00	-33	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-53.77	-23	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-32.58	-13	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-35.82	-13	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-40.43	-13	PASS

DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-63.58	-43	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+M	Outer_Full	0.15	30	-74.99	-33	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+M	Outer_Full	30	1000	-53.47	-23	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+M	Outer_Full	1000	6000	-32.51	-13	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+M	Outer_Full	6000	26000	-36.29	-13	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+M	Outer_Full	26000	40000	-40.28	-13	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-64.39	-43	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-73.21	-33	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-53.49	-23	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-32.66	-13	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-36.07	-13	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-40.47	-13	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-63.70	-43	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-75.95	-33	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-53.76	-23	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-32.72	-13	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-35.71	-13	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-40.50	-13	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-63.46	-43	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+H	Outer_Full	0.15	30	-74.98	-33	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+H	Outer_Full	30	1000	-53.52	-23	PASS

DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+H	Outer_Full	1000	6000	-32.27	-13	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+H	Outer_Full	6000	26000	-35.91	-13	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+H	Outer_Full	26000	40000	-40.24	-13	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-64.40	-43	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-74.17	-33	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-53.56	-23	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-32.61	-13	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-36.19	-13	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-40.32	-13	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-64.54	-43	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-74.40	-33	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-52.90	-23	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-32.62	-13	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-36.08	-13	PASS
DC_41A_n77A -3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-40.34	-13	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-64.20	-43	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+L	Outer_Full	0.15	30	-74.39	-33	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+L	Outer_Full	30	1000	-53.88	-23	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+L	Outer_Full	1000	6000	-31.29	-13	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+L	Outer_Full	6000	26000	-36.03	-13	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+L	Outer_Full	26000	40000	-39.91	-13	PASS

DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-64.03	-43	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-76.49	-33	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-53.48	-23	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-32.46	-13	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-35.85	-13	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-40.22	-13	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-64.66	-43	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-75.05	-33	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-53.94	-23	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-32.17	-13	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-35.89	-13	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-40.14	-13	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-64.00	-43	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+M	Outer_Full	0.15	30	-74.99	-33	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+M	Outer_Full	30	1000	-53.94	-23	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+M	Outer_Full	1000	6000	-32.78	-13	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+M	Outer_Full	6000	26000	-36.11	-13	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+M	Outer_Full	26000	40000	-40.57	-13	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-64.07	-43	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-75.13	-33	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-53.76	-23	PASS

DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-32.55	-13	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-35.94	-13	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-40.51	-13	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-63.47	-43	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-75.83	-33	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-53.60	-23	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-32.48	-13	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-35.92	-13	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-40.22	-13	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-64.45	-43	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+H	Outer_Full	0.15	30	-74.67	-33	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+H	Outer_Full	30	1000	-53.57	-23	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+H	Outer_Full	1000	6000	-31.36	-13	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+H	Outer_Full	6000	26000	-36.03	-13	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+H	Outer_Full	26000	40000	-40.17	-13	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-64.55	-43	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-73.13	-33	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-54.26	-23	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-32.67	-13	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-35.75	-13	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-40.26	-13	PASS

DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-63.45	-43	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-74.63	-33	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-54.07	-23	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-32.58	-13	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-36.24	-13	PASS
DC_41A_n77A -3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-40.48	-13	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-63.59	-43	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	0.15	30	-73.52	-33	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	30	1000	-53.96	-23	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	1000	6000	-29.55	-13	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	6000	26000	-36.07	-13	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	26000	40000	-40.42	-13	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-64.08	-43	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-72.71	-33	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-53.64	-23	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-32.82	-13	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-36.00	-13	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-40.07	-13	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-64.67	-43	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-76.42	-33	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-53.94	-23	PASS

DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-32.51	-13	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-35.48	-13	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-39.83	-13	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-64.11	-43	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+M	Outer_Full	0.15	30	-73.45	-33	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+M	Outer_Full	30	1000	-54.24	-23	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+M	Outer_Full	1000	6000	-32.54	-13	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+M	Outer_Full	6000	26000	-35.70	-13	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+M	Outer_Full	26000	40000	-40.34	-13	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-64.13	-43	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-74.68	-33	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-52.74	-23	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-32.65	-13	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-36.04	-13	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-40.12	-13	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-63.74	-43	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-76.76	-33	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-53.86	-23	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-32.27	-13	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-36.24	-13	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-40.19	-13	PASS

DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-64.69	-43	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	0.15	30	-71.15	-33	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	30	1000	-53.85	-23	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	1000	6000	-28.82	-13	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	6000	26000	-36.16	-13	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	26000	40000	-40.38	-13	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-63.46	-43	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-74.47	-33	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-53.95	-23	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-32.73	-13	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-35.68	-13	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-40.23	-13	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-63.07	-43	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-74.06	-33	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-53.61	-23	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-31.91	-13	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-36.05	-13	PASS
DC_41A_n77A -3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-40.23	-13	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-63.96	-43	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+L	Outer_Full	0.15	30	-71.91	-33	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+L	Outer_Full	30	1000	-54.01	-23	PASS

DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+L	Outer_Full	1000	6000	-29.08	-13	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+L	Outer_Full	6000	26000	-36.20	-13	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+L	Outer_Full	26000	40000	-40.21	-13	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-63.87	-43	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-73.48	-33	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-53.49	-23	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-32.61	-13	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-36.09	-13	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-40.44	-13	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-63.64	-43	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-72.55	-33	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-53.89	-23	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-32.47	-13	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-35.91	-13	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-40.31	-13	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-63.28	-43	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+M	Outer_Full	0.15	30	-77.36	-33	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+M	Outer_Full	30	1000	-53.50	-23	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+M	Outer_Full	1000	6000	-32.82	-13	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+M	Outer_Full	6000	26000	-35.87	-13	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+M	Outer_Full	26000	40000	-40.01	-13	PASS

DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-64.48	-43	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-76.83	-33	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-53.83	-23	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-32.70	-13	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-36.17	-13	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-40.09	-13	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-63.33	-43	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-73.89	-33	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-53.94	-23	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-32.43	-13	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-35.94	-13	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-40.39	-13	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-63.58	-43	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+H	Outer_Full	0.15	30	-77.07	-33	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+H	Outer_Full	30	1000	-53.46	-23	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+H	Outer_Full	1000	6000	-28.74	-13	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+H	Outer_Full	6000	26000	-35.97	-13	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+H	Outer_Full	26000	40000	-40.41	-13	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-63.18	-43	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-75.29	-33	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-53.98	-23	PASS

DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-32.70	-13	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-36.14	-13	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-40.48	-13	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-63.64	-43	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-73.58	-33	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-54.01	-23	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-32.37	-13	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-35.89	-13	PASS
DC_41A_n77A -3700-3980	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-40.11	-13	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-63.47	-43	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	0.15	30	-73.92	-33	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	30	1000	-53.26	-23	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	1000	6000	-29.40	-13	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	6000	26000	-36.07	-13	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	26000	40000	-40.53	-13	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-64.60	-43	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-72.04	-33	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-53.81	-23	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-32.70	-13	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-36.19	-13	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-40.11	-13	PASS

DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-64.32	-43	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-77.26	-33	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-53.66	-23	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-32.76	-13	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-35.99	-13	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-40.43	-13	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-63.57	-43	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	0.15	30	-77.37	-33	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	30	1000	-53.57	-23	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	1000	6000	-32.76	-13	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	6000	26000	-35.83	-13	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	26000	40000	-40.41	-13	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-64.72	-43	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-74.03	-33	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-53.40	-23	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-32.57	-13	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-36.01	-13	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-39.30	-13	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-64.44	-43	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-74.84	-33	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-54.11	-23	PASS

DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-32.56	-13	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-35.77	-13	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-40.44	-13	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-63.97	-43	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	0.15	30	-74.45	-33	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	30	1000	-53.67	-23	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	1000	6000	-29.90	-13	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	6000	26000	-36.02	-13	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	26000	40000	-40.38	-13	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-63.81	-43	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-73.61	-33	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-54.02	-23	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-32.66	-13	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-35.82	-13	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-40.28	-13	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-63.01	-43	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-73.22	-33	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-52.99	-23	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-32.32	-13	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-36.08	-13	PASS
DC_41A_n77A -3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-40.51	-13	PASS

DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-63.69	-43	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+L	Outer_Full	0.15	30	-74.46	-33	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+L	Outer_Full	30	1000	-53.70	-23	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+L	Outer_Full	1000	6000	-30.11	-13	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+L	Outer_Full	6000	26000	-36.25	-13	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+L	Outer_Full	26000	40000	-40.55	-13	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-64.29	-43	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-72.54	-33	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-52.91	-23	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-32.67	-13	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-36.04	-13	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-40.34	-13	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-64.26	-43	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-75.15	-33	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-53.35	-23	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-32.92	-13	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-36.09	-13	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-40.37	-13	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-64.55	-43	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+M	Outer_Full	0.15	30	-73.36	-33	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+M	Outer_Full	30	1000	-53.96	-23	PASS

DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+M	Outer_Full	1000	6000	-32.60	-13	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+M	Outer_Full	6000	26000	-35.95	-13	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+M	Outer_Full	26000	40000	-40.39	-13	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-63.01	-43	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-71.12	-33	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-53.76	-23	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-32.68	-13	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-35.93	-13	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-40.47	-13	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-64.49	-43	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-74.71	-33	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-53.78	-23	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-32.60	-13	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-35.80	-13	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-40.31	-13	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-63.83	-43	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+H	Outer_Full	0.15	30	-71.21	-33	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+H	Outer_Full	30	1000	-53.61	-23	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+H	Outer_Full	1000	6000	-29.44	-13	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+H	Outer_Full	6000	26000	-36.27	-13	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+H	Outer_Full	26000	40000	-40.16	-13	PASS

DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-63.92	-43	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-73.42	-33	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-53.88	-23	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-32.62	-13	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-36.08	-13	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-40.13	-13	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-62.98	-43	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-73.22	-33	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-54.01	-23	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-32.61	-13	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-36.03	-13	PASS
DC_41A_n77A -3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-40.27	-13	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-64.91	-43	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	0.15	30	-74.84	-33	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	30	1000	-53.12	-23	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	1000	6000	-29.85	-13	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	6000	26000	-36.06	-13	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	26000	40000	-40.39	-13	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-64.00	-43	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-76.04	-33	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-53.25	-23	PASS

DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-32.58	-13	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-36.02	-13	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-40.03	-13	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-63.02	-43	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-74.81	-33	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-53.58	-23	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-32.28	-13	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-35.99	-13	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-40.21	-13	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-63.82	-43	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	0.15	30	-76.89	-33	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	30	1000	-53.47	-23	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	1000	6000	-32.55	-13	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	6000	26000	-36.15	-13	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	26000	40000	-40.48	-13	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-64.20	-43	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-73.01	-33	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-53.58	-23	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-32.62	-13	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-36.04	-13	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-39.85	-13	PASS

DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-64.08	-43	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-75.57	-33	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-53.69	-23	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-32.88	-13	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-36.13	-13	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-40.39	-13	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-63.04	-43	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	0.15	30	-73.21	-33	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	30	1000	-53.50	-23	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	1000	6000	-29.99	-13	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	6000	26000	-36.24	-13	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	26000	40000	-40.41	-13	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-64.16	-43	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-76.09	-33	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-54.19	-23	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-32.86	-13	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-35.96	-13	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-40.32	-13	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-64.26	-43	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-75.84	-33	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-53.96	-23	PASS

DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-32.46	-13	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-36.30	-13	PASS
DC_41A_n77A -3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-40.24	-13	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-64.50	-43	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+L	Outer_Full	0.15	30	-76.95	-33	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+L	Outer_Full	30	1000	-53.84	-23	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+L	Outer_Full	1000	6000	-29.68	-13	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+L	Outer_Full	6000	26000	-35.73	-13	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+L	Outer_Full	26000	40000	-40.23	-13	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-63.62	-43	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-75.53	-33	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-54.03	-23	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-32.50	-13	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-35.90	-13	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-40.10	-13	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-65.11	-43	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-70.54	-33	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-53.88	-23	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-32.77	-13	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-36.08	-13	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-40.33	-13	PASS

DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-64.64	-43	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+M	Outer_Full	0.15	30	-71.80	-33	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+M	Outer_Full	30	1000	-53.45	-23	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+M	Outer_Full	1000	6000	-32.69	-13	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+M	Outer_Full	6000	26000	-36.12	-13	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+M	Outer_Full	26000	40000	-40.31	-13	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-64.02	-43	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-73.73	-33	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-53.80	-23	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-32.67	-13	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-36.05	-13	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-39.69	-13	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-63.95	-43	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-71.84	-33	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-53.07	-23	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-32.55	-13	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-36.08	-13	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-39.99	-13	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-64.56	-43	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+H	Outer_Full	0.15	30	-74.23	-33	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+H	Outer_Full	30	1000	-53.27	-23	PASS

DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+H	Outer_Full	1000	6000	-30.57	-13	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+H	Outer_Full	6000	26000	-36.11	-13	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+H	Outer_Full	26000	40000	-40.43	-13	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-63.34	-43	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-77.59	-33	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-53.91	-23	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-32.54	-13	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-36.26	-13	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-39.67	-13	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-63.03	-43	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-75.42	-33	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-53.62	-23	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-32.57	-13	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-36.32	-13	PASS
DC_41A_n77A -3700-3980	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-40.10	-13	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-63.86	-43	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	0.15	30	-76.35	-33	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	30	1000	-53.74	-23	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	1000	6000	-30.63	-13	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	6000	26000	-36.27	-13	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	26000	40000	-40.03	-13	PASS

DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-64.18	-43	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-74.12	-33	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-53.24	-23	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-32.74	-13	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-36.27	-13	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-40.54	-13	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-64.39	-43	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-72.35	-33	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-53.66	-23	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-32.50	-13	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-36.20	-13	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-40.36	-13	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-64.70	-43	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	0.15	30	-75.43	-33	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	30	1000	-53.88	-23	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	1000	6000	-32.69	-13	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	6000	26000	-36.00	-13	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	26000	40000	-40.18	-13	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-64.41	-43	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-75.21	-33	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-53.56	-23	PASS

DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-32.45	-13	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-36.14	-13	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-40.15	-13	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-64.18	-43	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-72.62	-33	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-53.75	-23	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-32.51	-13	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-36.24	-13	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-40.20	-13	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-63.25	-43	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	0.15	30	-70.82	-33	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	30	1000	-53.65	-23	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	1000	6000	-30.08	-13	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	6000	26000	-35.97	-13	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	26000	40000	-40.61	-13	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-62.84	-43	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-72.68	-33	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-53.37	-23	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-32.74	-13	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-36.06	-13	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-40.50	-13	PASS

DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-63.45	-43	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-76.96	-33	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-53.51	-23	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-32.68	-13	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-35.79	-13	PASS
DC_41A_n77A -3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-40.01	-13	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-64.20	-43	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+L	Outer_Full	0.15	30	-73.52	-33	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+L	Outer_Full	30	1000	-53.49	-23	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+L	Outer_Full	1000	6000	-30.26	-13	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+L	Outer_Full	6000	26000	-36.16	-13	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+L	Outer_Full	26000	40000	-40.31	-13	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-64.67	-43	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-75.76	-33	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-53.85	-23	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-32.56	-13	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-36.10	-13	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-40.47	-13	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-64.25	-43	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-75.07	-33	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-53.56	-23	PASS

DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-32.36	-13	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-36.05	-13	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-40.30	-13	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-63.21	-43	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+M	Outer_Full	0.15	30	-73.89	-33	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+M	Outer_Full	30	1000	-54.03	-23	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+M	Outer_Full	1000	6000	-32.72	-13	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+M	Outer_Full	6000	26000	-36.17	-13	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+M	Outer_Full	26000	40000	-40.29	-13	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-63.68	-43	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-74.61	-33	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-54.05	-23	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-32.52	-13	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-36.39	-13	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-40.21	-13	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-66.30	-43	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-69.63	-33	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-55.36	-23	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-32.99	-13	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-36.39	-13	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-40.67	-13	PASS

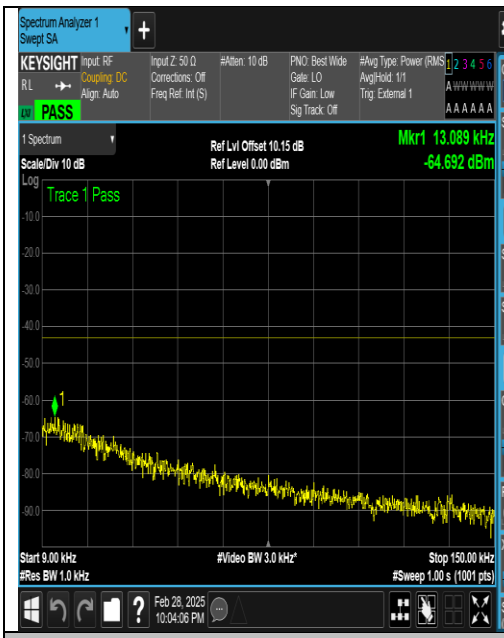
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-64.12	-43	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+H	Outer_Full	0.15	30	-73.03	-33	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+H	Outer_Full	30	1000	-53.89	-23	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+H	Outer_Full	1000	6000	-31.25	-13	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+H	Outer_Full	6000	26000	-36.51	-13	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+H	Outer_Full	26000	40000	-40.47	-13	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-64.93	-43	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-75.35	-33	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-53.62	-23	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-32.50	-13	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-35.97	-13	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-40.23	-13	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-64.61	-43	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-73.40	-33	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-53.40	-23	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-32.40	-13	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-36.49	-13	PASS
DC_41A_n77A -3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-40.30	-13	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-64.89	-43	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+L	Outer_Full	0.15	30	-73.30	-33	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+L	Outer_Full	30	1000	-53.51	-23	PASS

DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+L	Outer_Full	1000	6000	-30.95	-13	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+L	Outer_Full	6000	26000	-36.15	-13	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+L	Outer_Full	26000	40000	-40.27	-13	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-63.79	-43	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-75.84	-33	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-53.37	-23	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-32.78	-13	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-35.99	-13	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-40.15	-13	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-64.53	-43	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-73.03	-33	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-53.57	-23	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-32.57	-13	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-35.49	-13	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-40.31	-13	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-64.18	-43	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+M	Outer_Full	0.15	30	-72.84	-33	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+M	Outer_Full	30	1000	-53.62	-23	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+M	Outer_Full	1000	6000	-32.65	-13	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+M	Outer_Full	6000	26000	-36.00	-13	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+M	Outer_Full	26000	40000	-40.20	-13	PASS

DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-64.83	-43	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-67.85	-33	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-53.43	-23	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-32.74	-13	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-35.97	-13	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-40.27	-13	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-63.44	-43	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-71.15	-33	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-53.45	-23	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-32.71	-13	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-35.69	-13	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-40.08	-13	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-62.14	-43	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+H	Outer_Full	0.15	30	-71.78	-33	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+H	Outer_Full	30	1000	-53.85	-23	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+H	Outer_Full	1000	6000	-31.72	-13	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+H	Outer_Full	6000	26000	-36.06	-13	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+H	Outer_Full	26000	40000	-40.13	-13	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-63.93	-43	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-66.37	-33	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-53.59	-23	PASS

DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-32.75	-13	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-35.98	-13	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-40.35	-13	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-63.41	-43	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-68.15	-33	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-53.73	-23	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-32.51	-13	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-35.77	-13	PASS
DC_41A_n77A -3700-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-39.99	-13	PASS

Test Graphs



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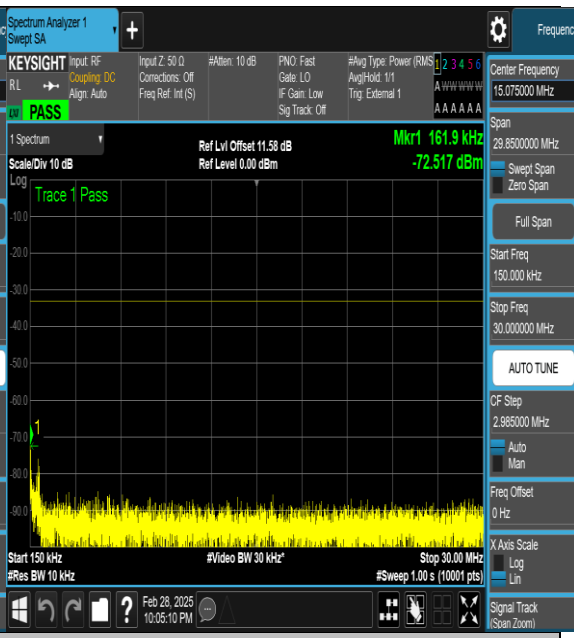

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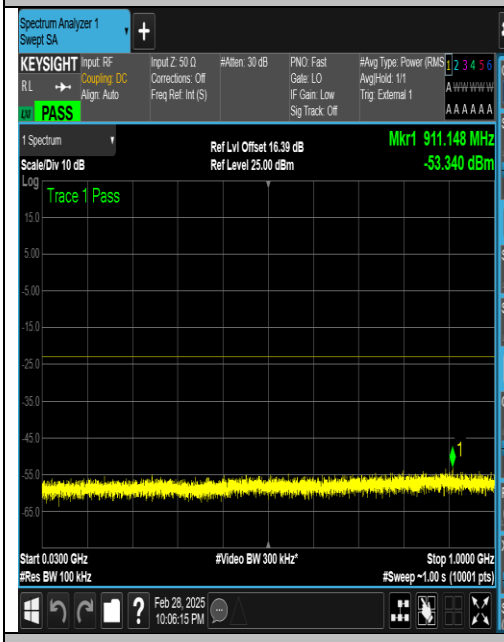

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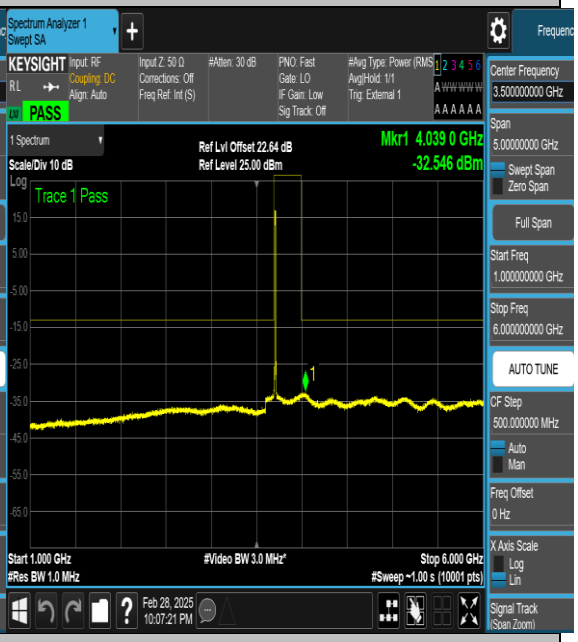

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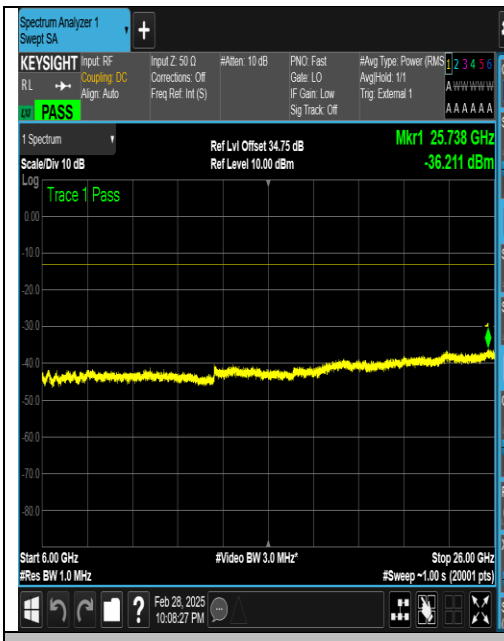

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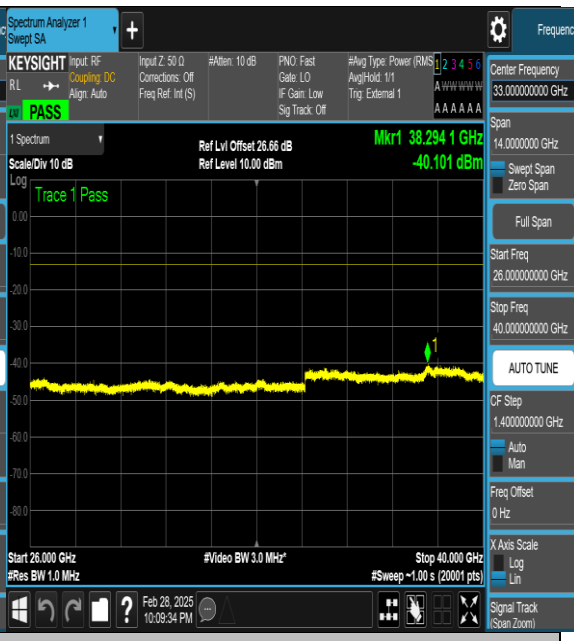

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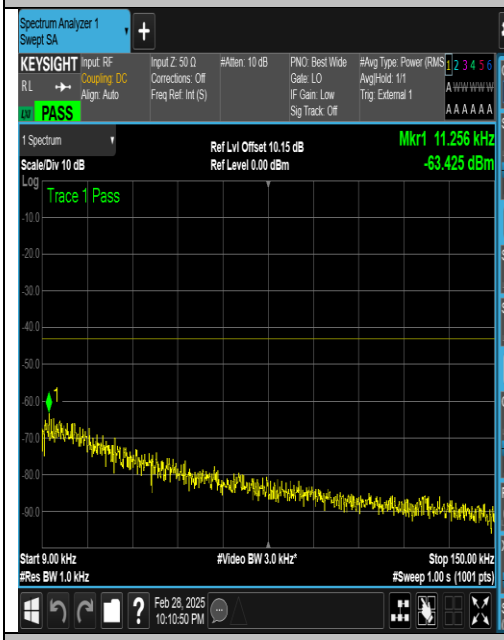

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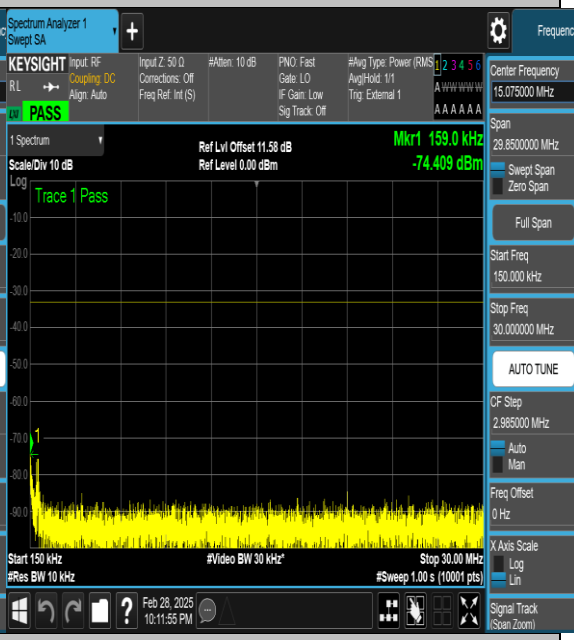

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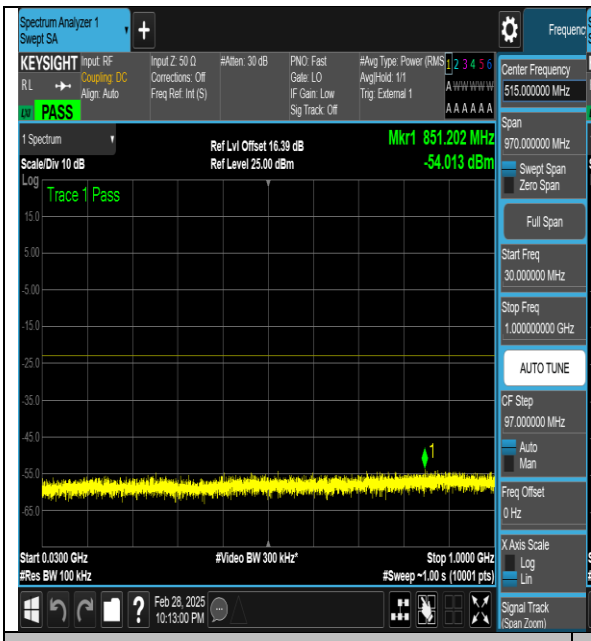

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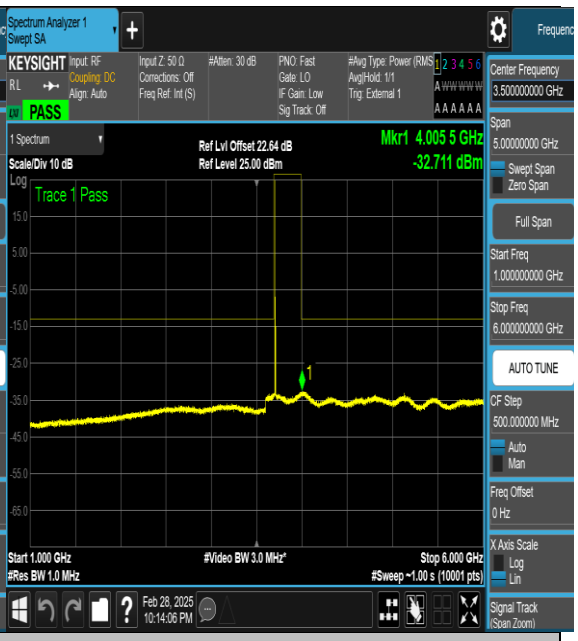

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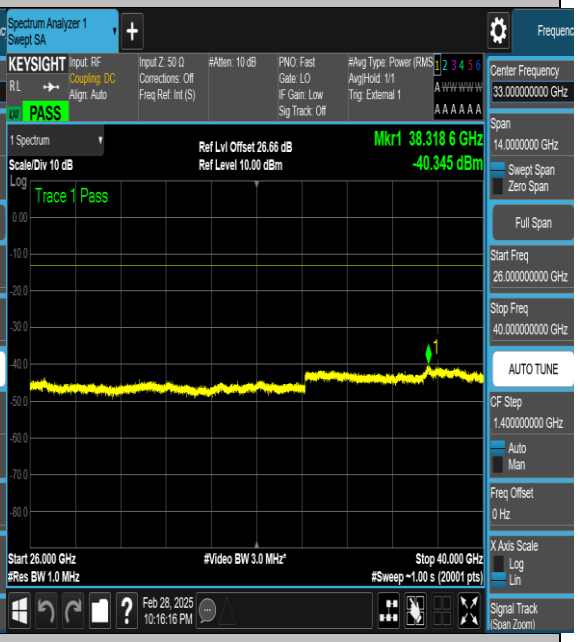

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