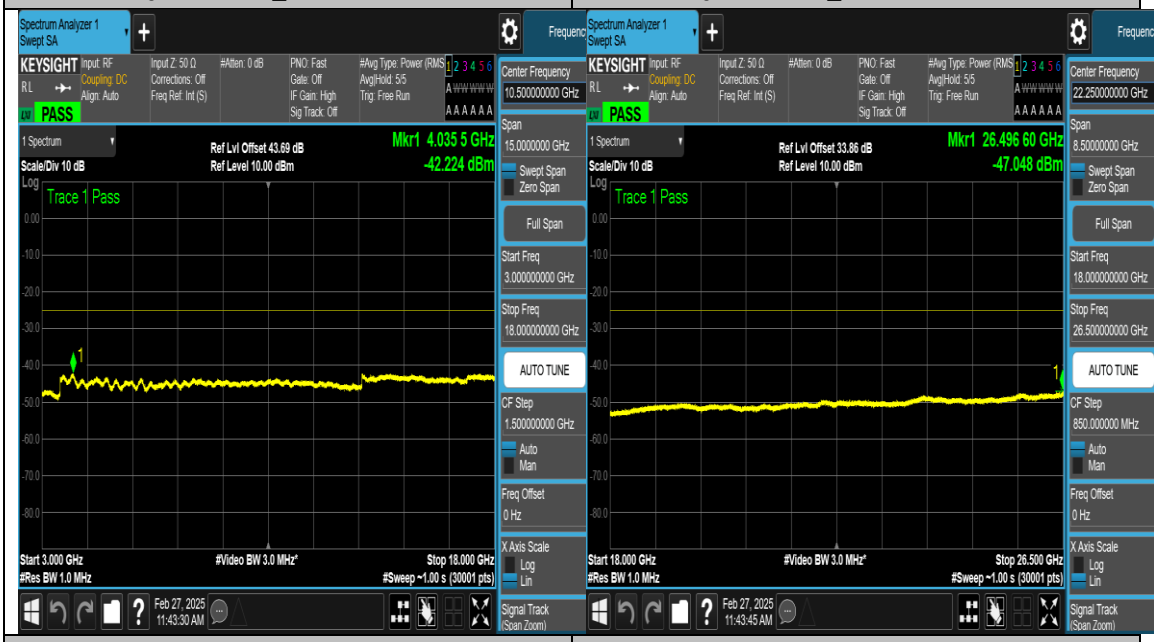
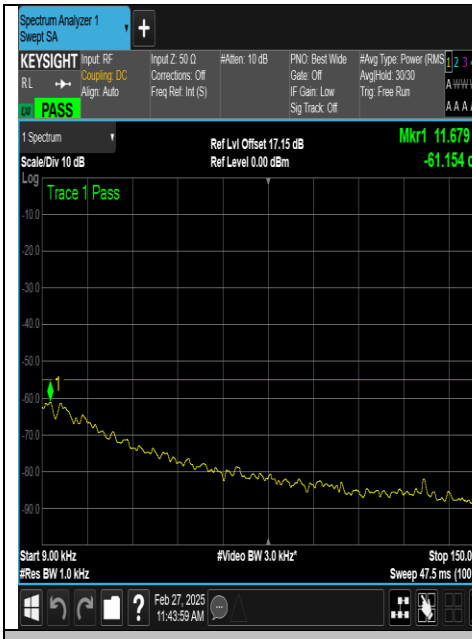


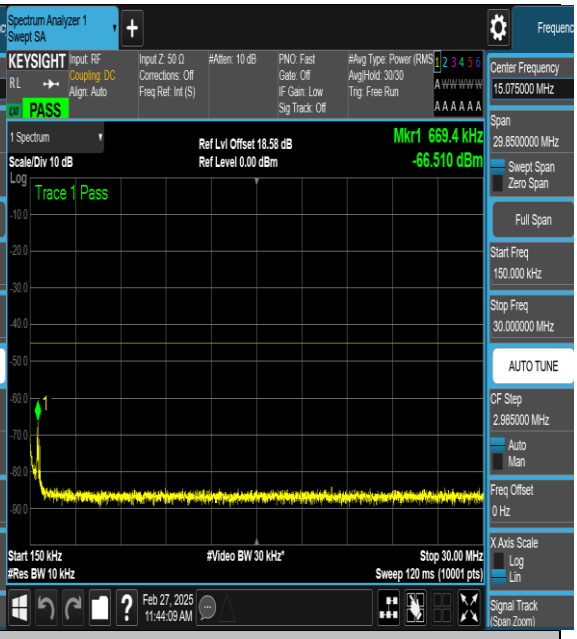
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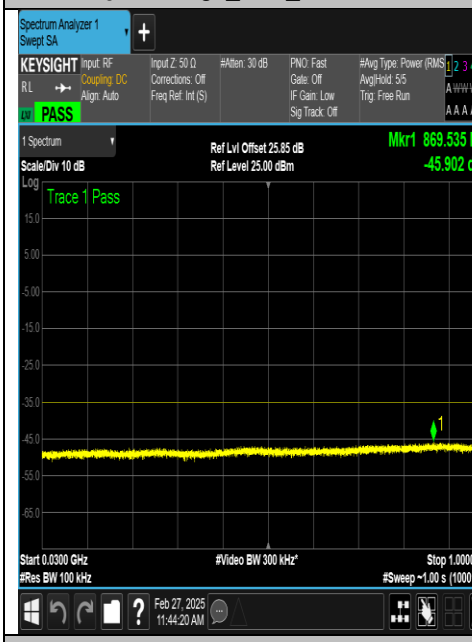
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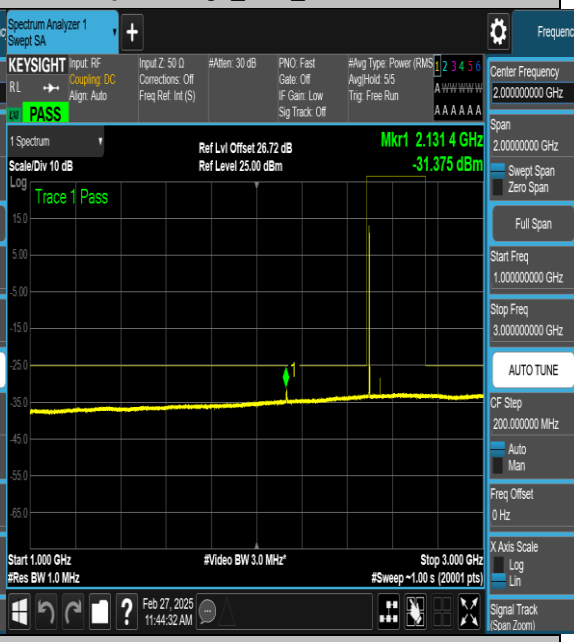
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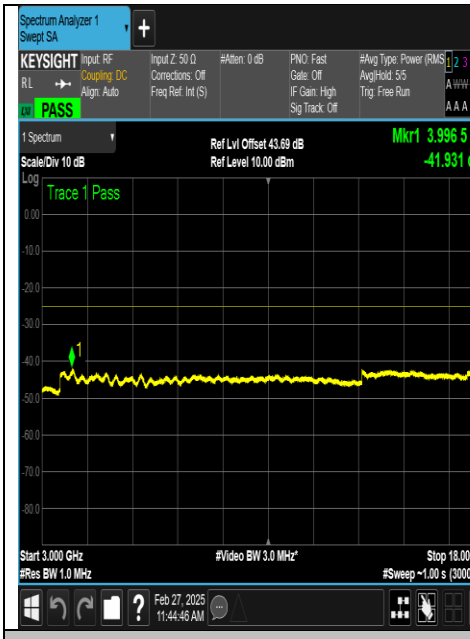
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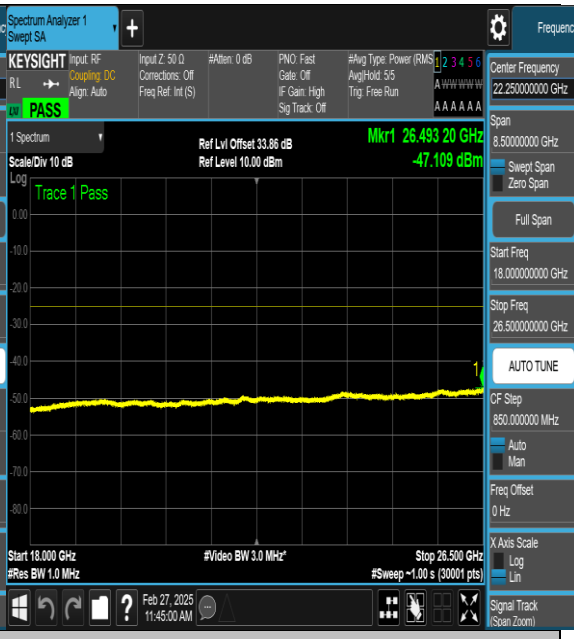
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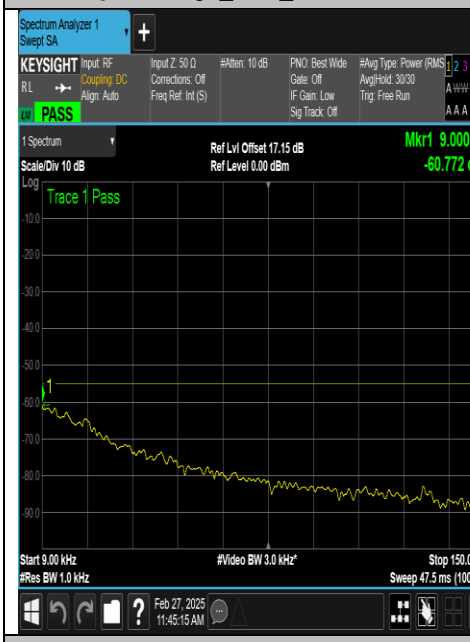
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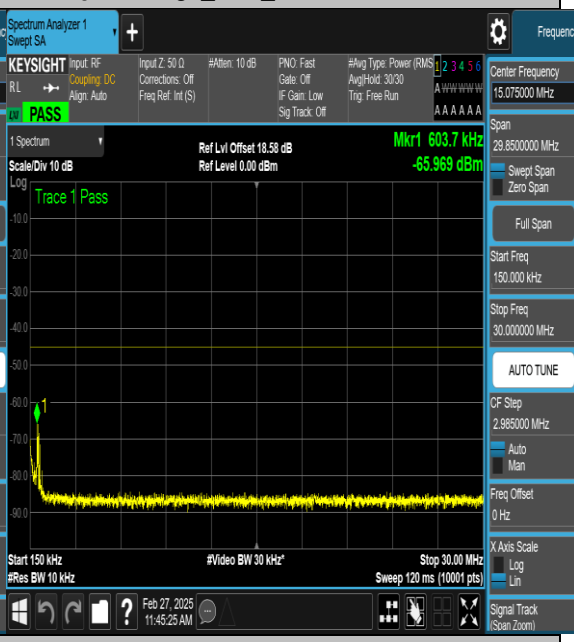
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NTNV-DC_4A_n41A-PC2-30-5+50-M+L-N/A-N/A-DFT-QPSK-Edge_1RB_Right-0.009-0.15-PASS

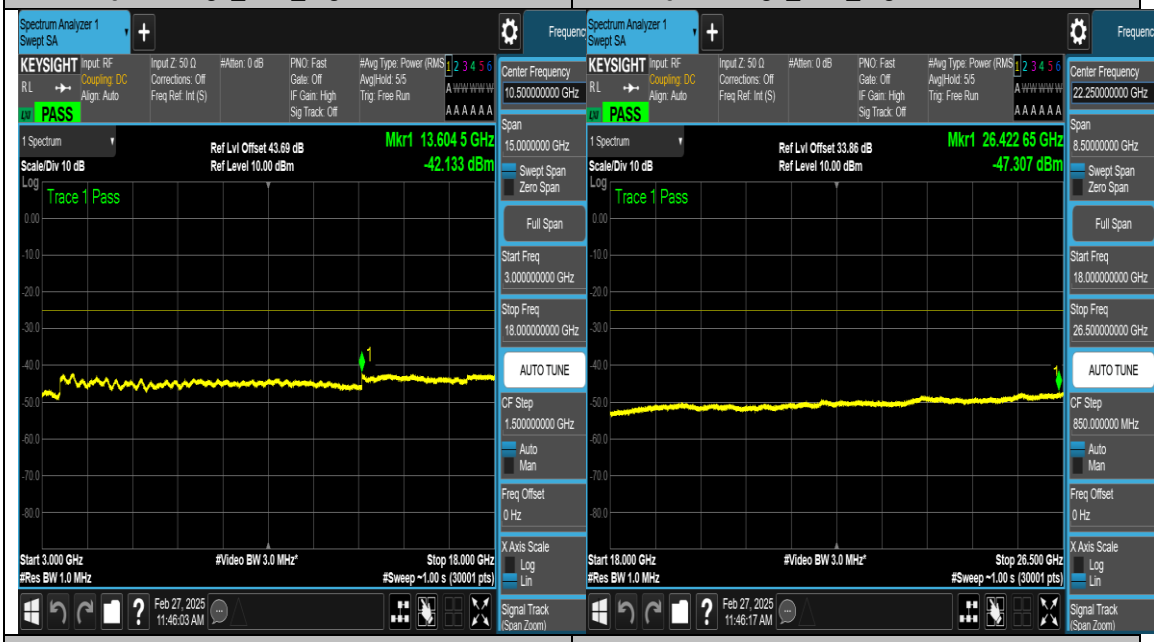


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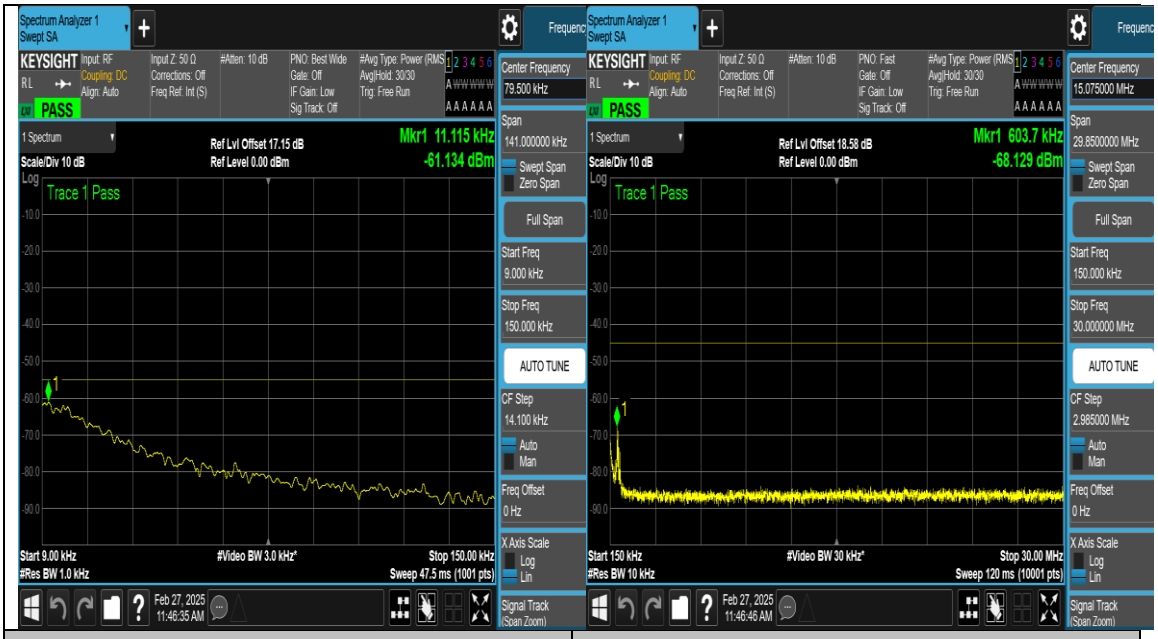
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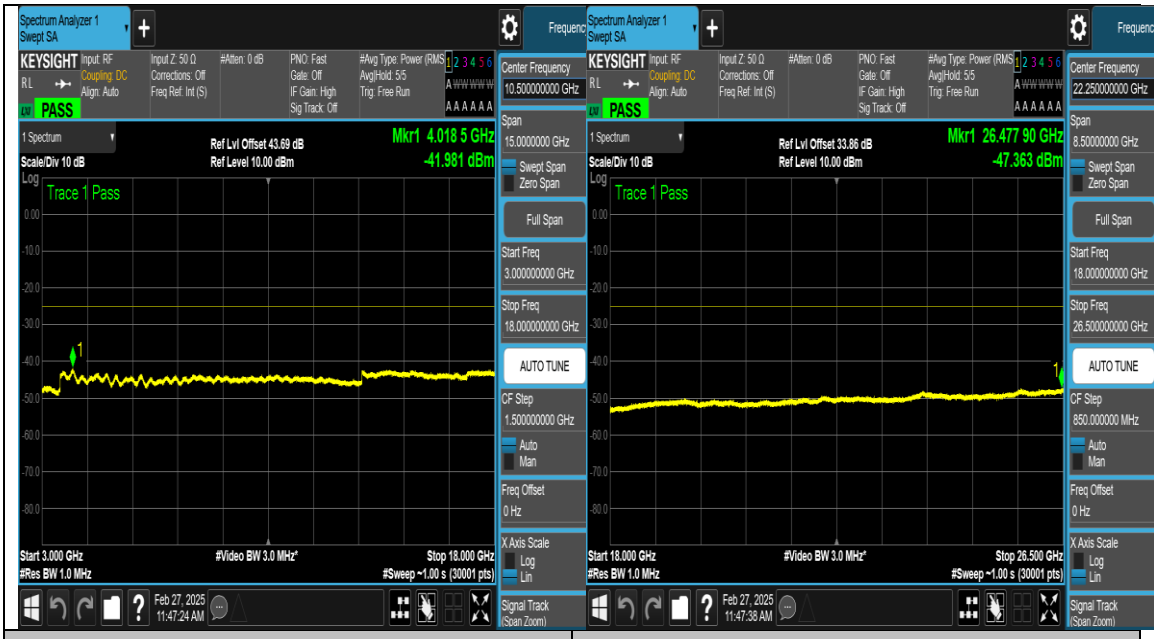
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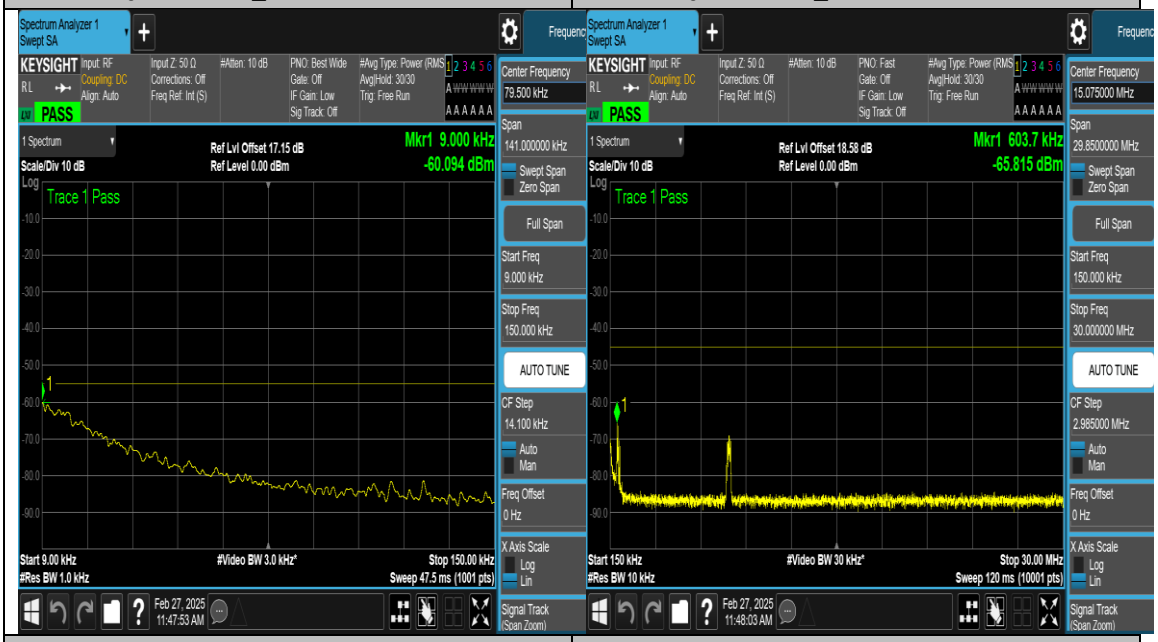
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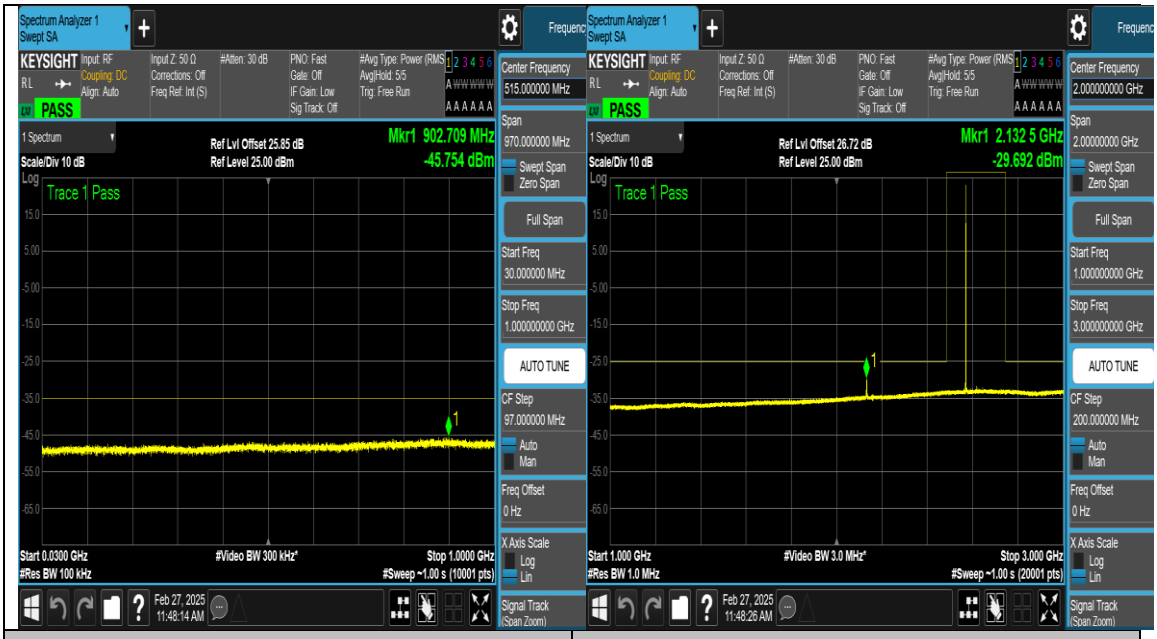
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NTNV-DC_4A_n41A-PC2-30-5+50-M+M-N/A-N/A-DFT-QPSK-Outer_Full-3000-18000-PASS	NTNV-DC_4A_n41A-PC2-30-5+50-M+M-N/A-N/A-DFT-QPSK-Outer_Full-18000-26500-PASS

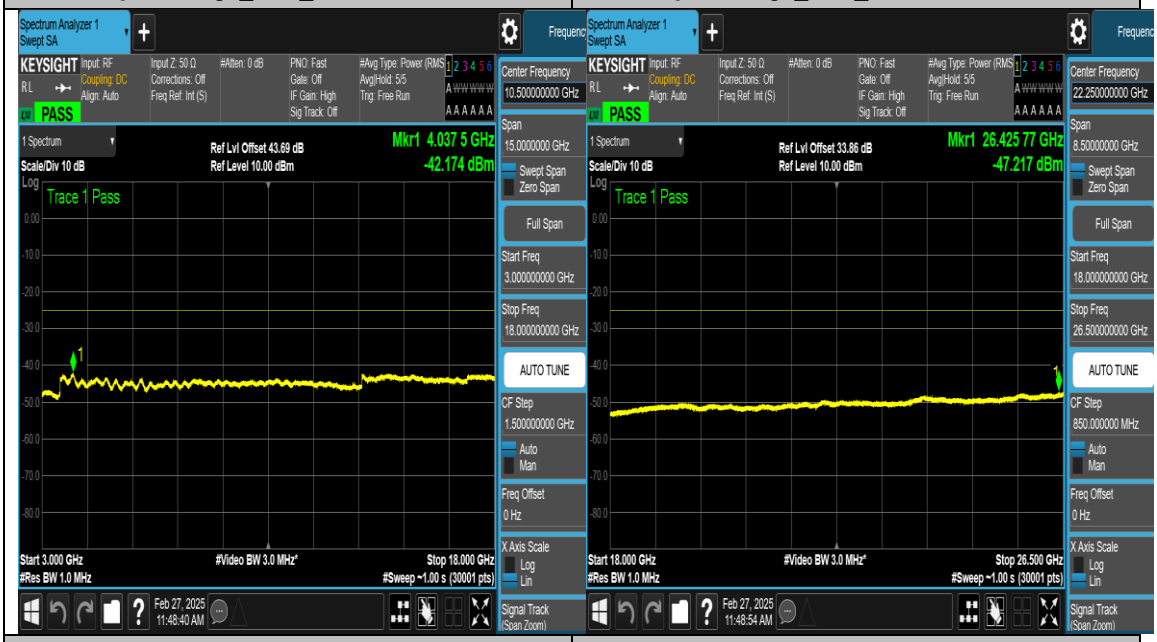


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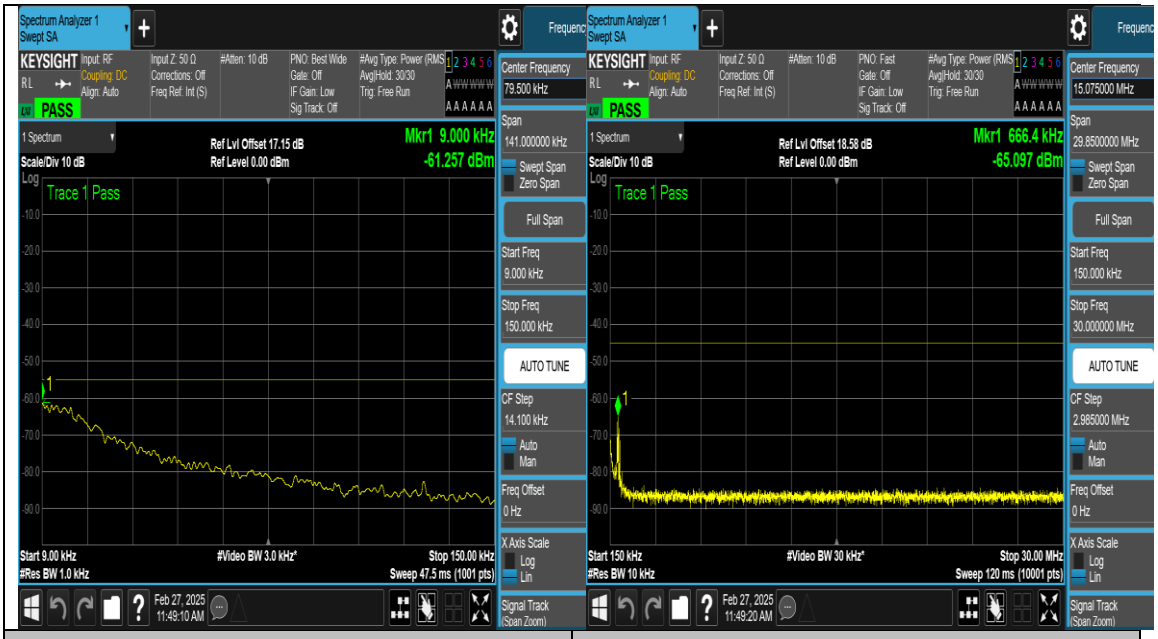
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NTNV-DC_4A_n41A-PC2-30-5+50-M+M-N/A-N/A-DFT-QPSK-Edge_1RB_Left-3000-18000-PASS

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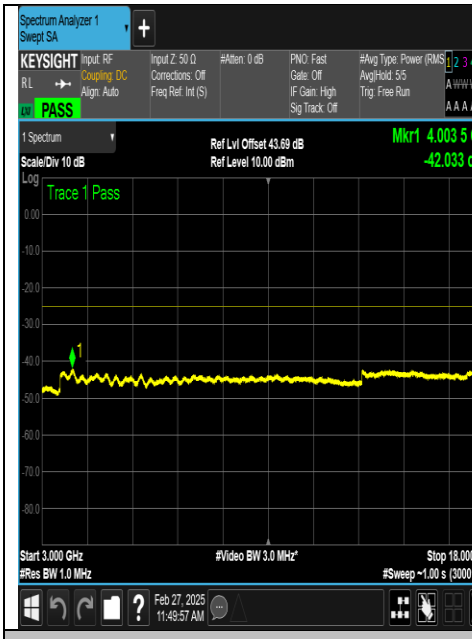
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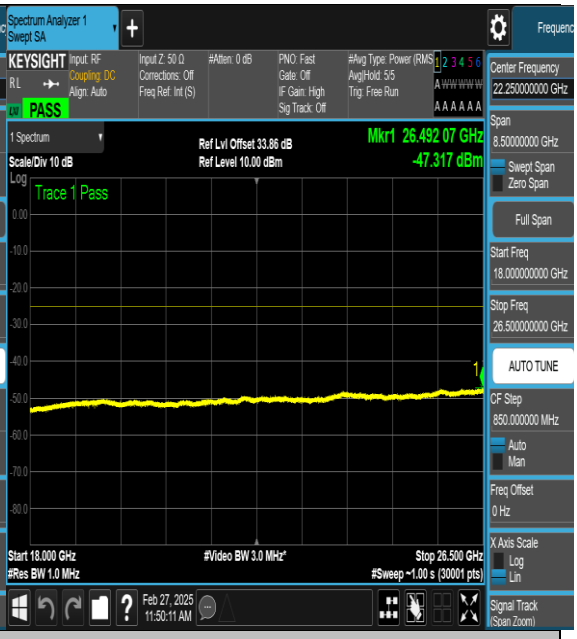


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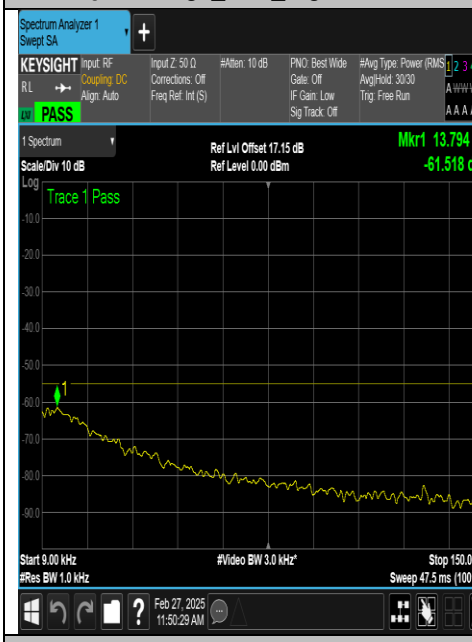
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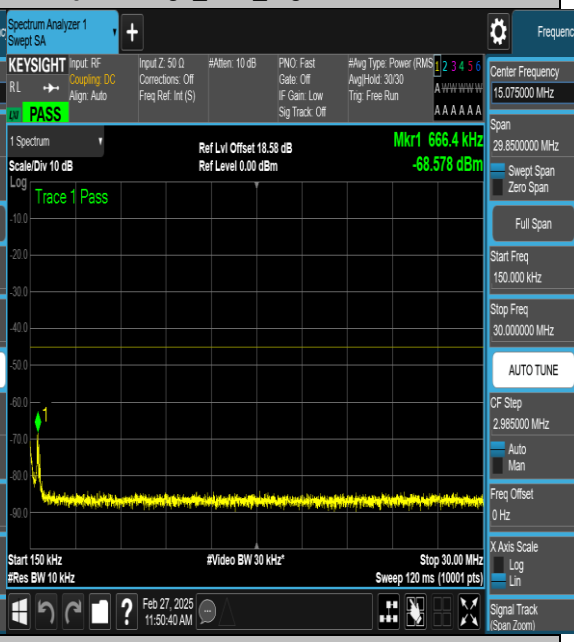
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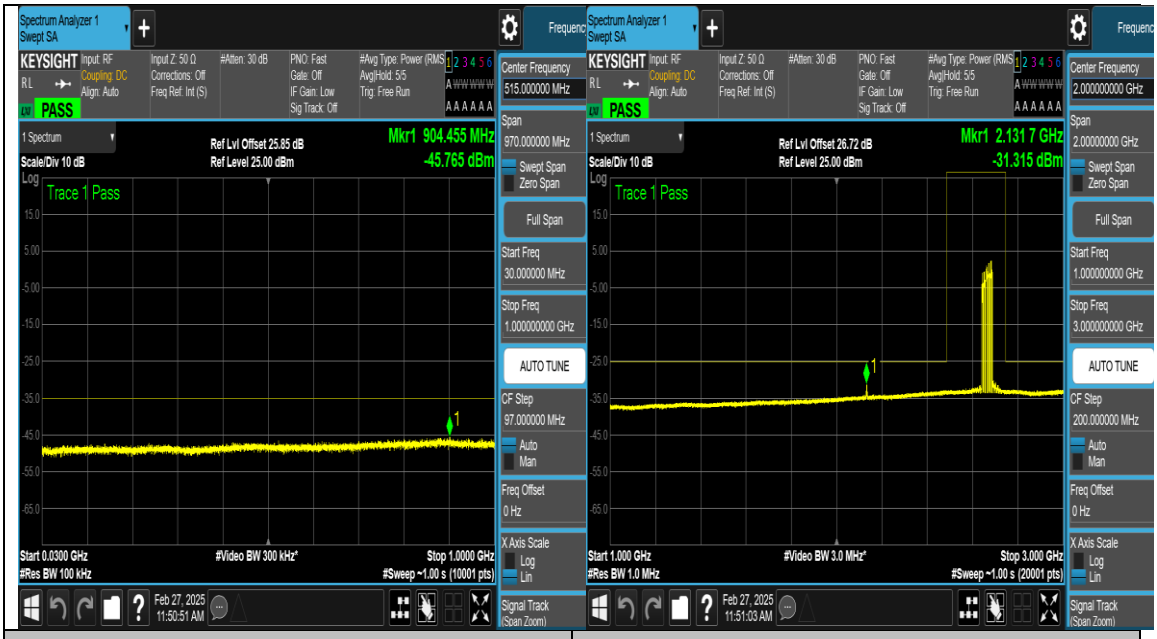
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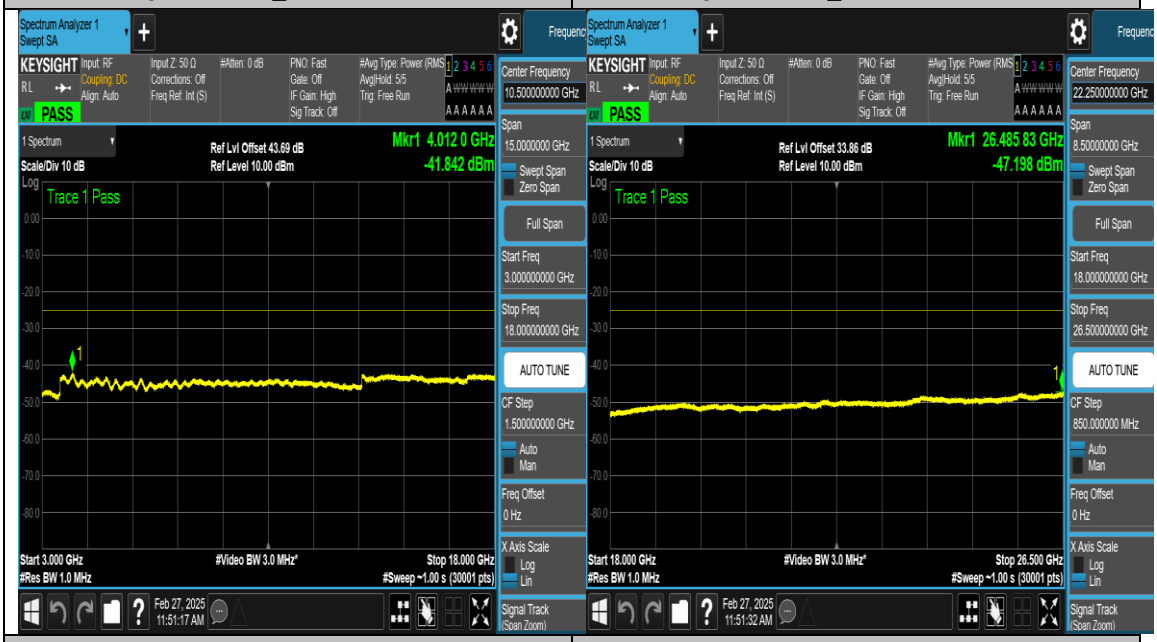


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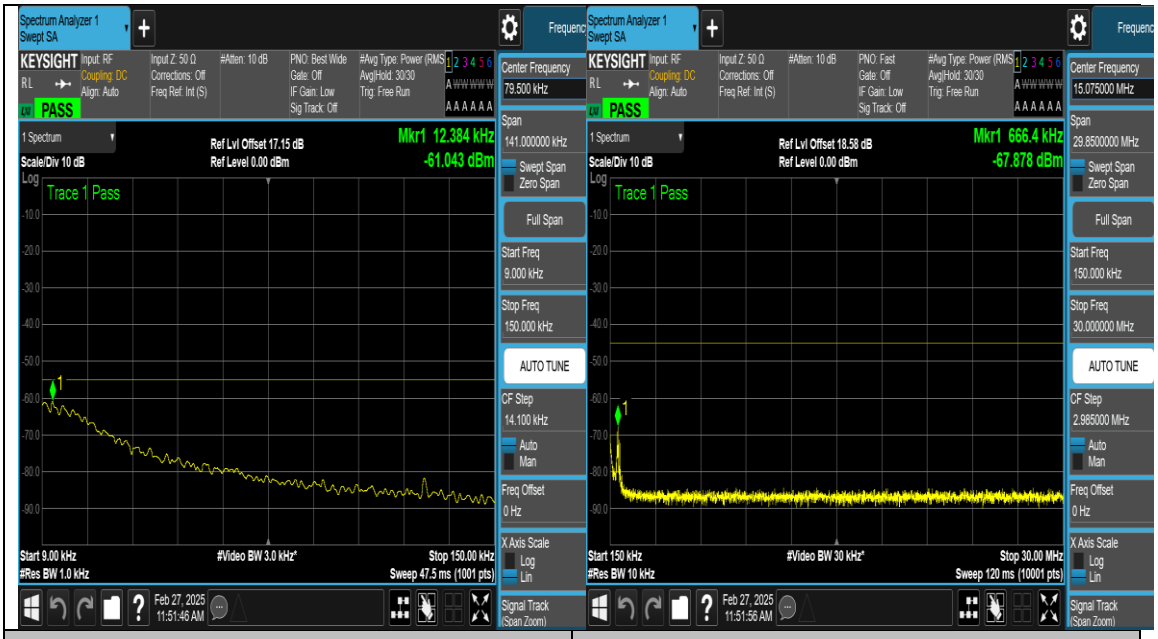
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NTNV-DC_4A_n41A-PC2-30-5+50-M+H-N/A-N/A-DFT-QPSK-Outer_Full-3000-18000-PASS

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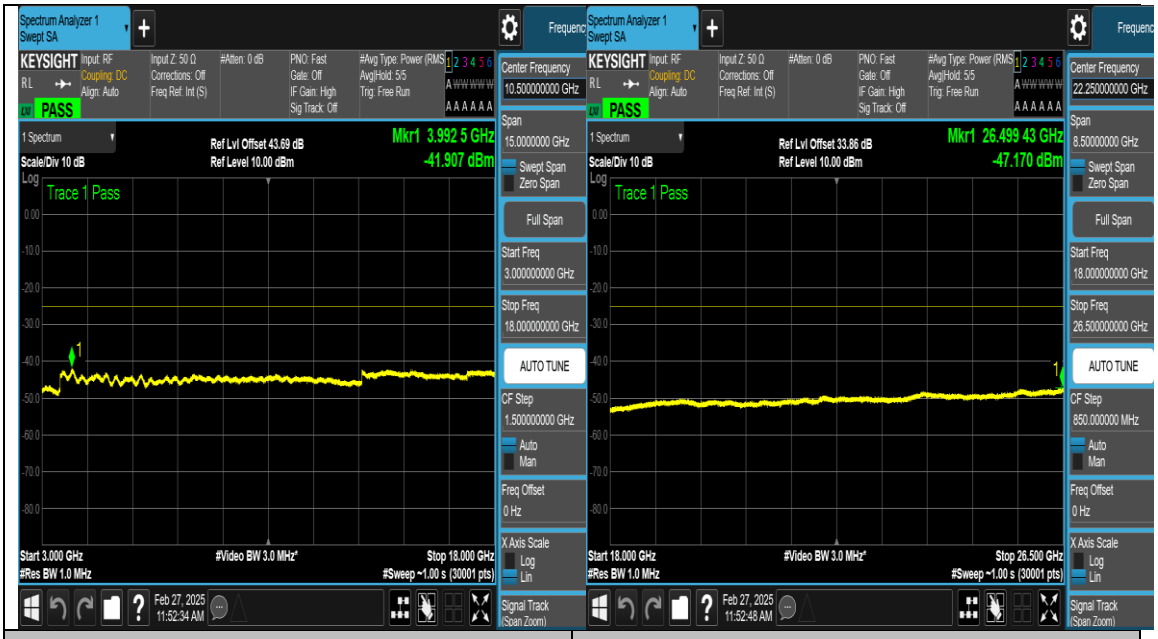
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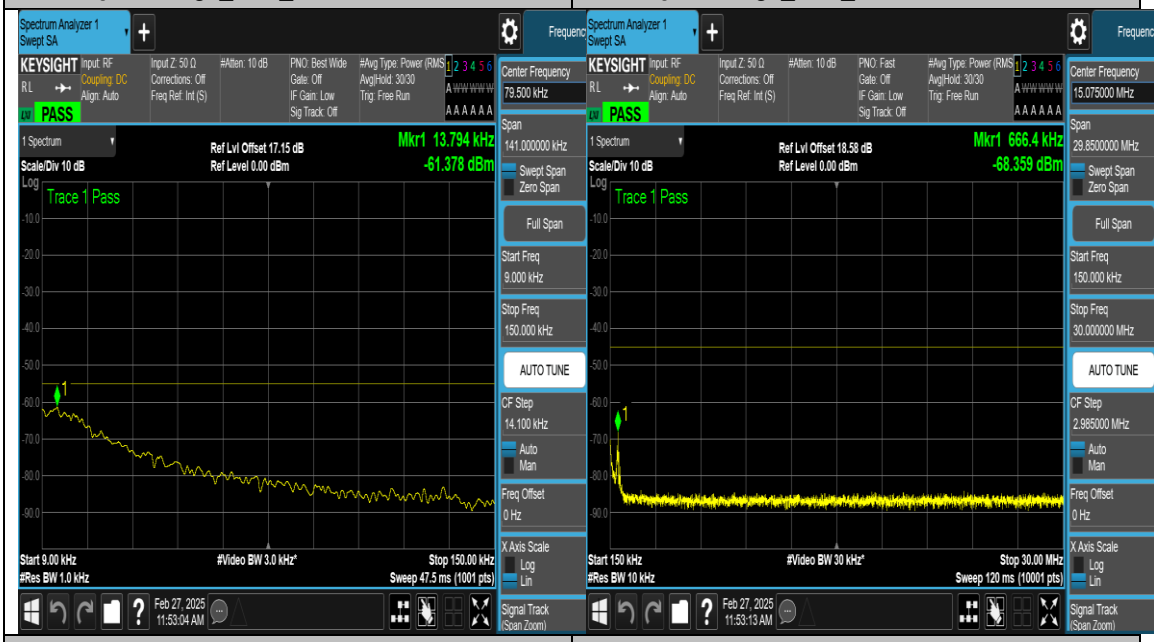


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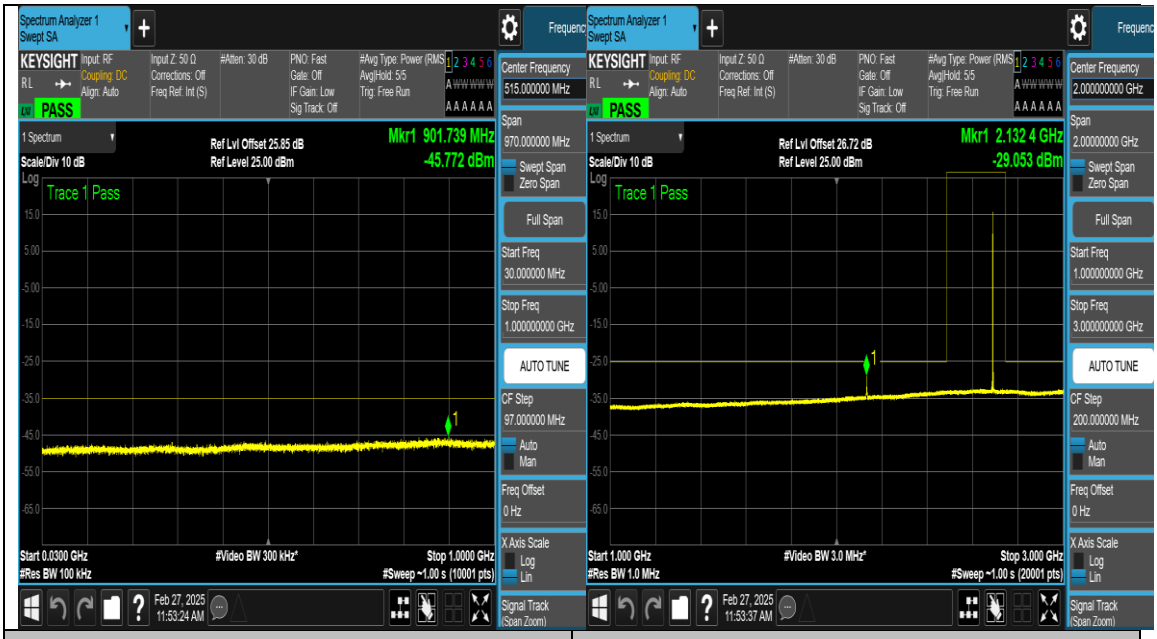
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NTNV-DC_4A_n41A-PC2-30-5+50-M+H-N/A-N/A-DFT-QPSK-Edge_1RB_Left-3000-18000-PASS	NTNV-DC_4A_n41A-PC2-30-5+50-M+H-N/A-N/A-DFT-QPSK-Edge_1RB_Left-18000-26500-PASS

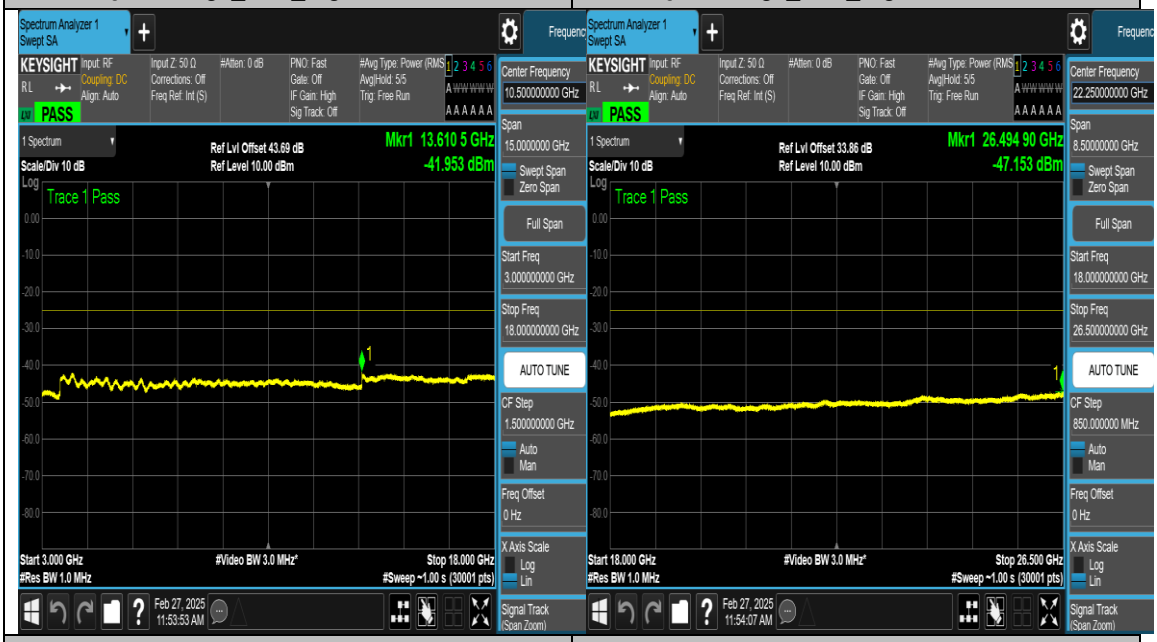


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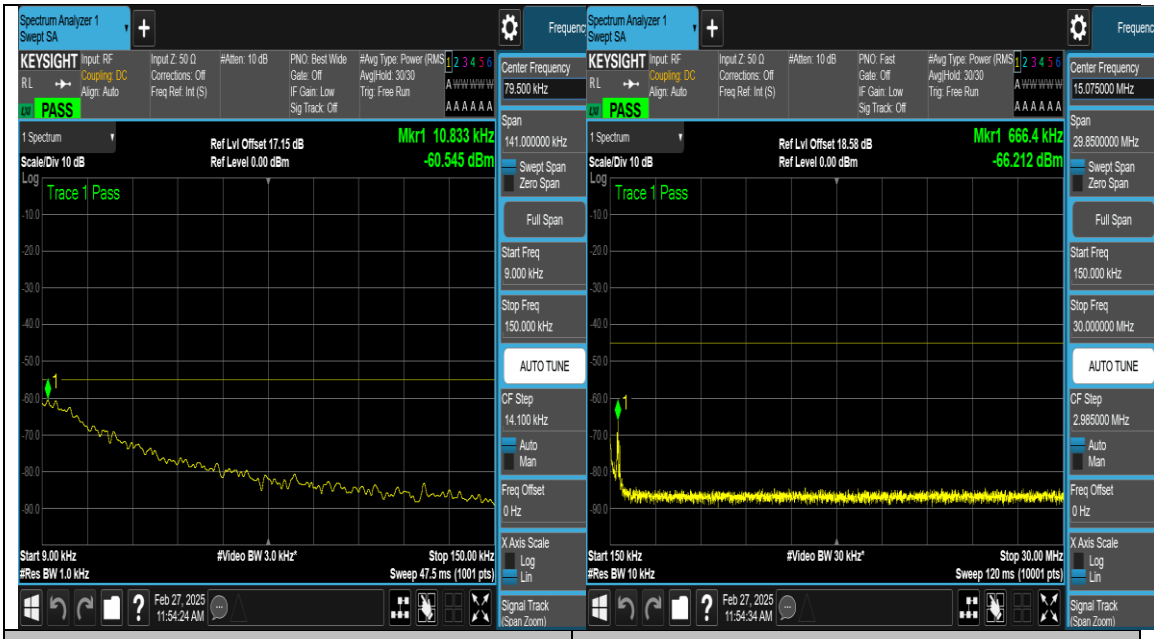
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NTNV-DC_4A_n41A-PC2-30-5+50-M+H-N/A-N/A-DFT-QPSK-Edge_1RB_Right-1000-3000-PASS



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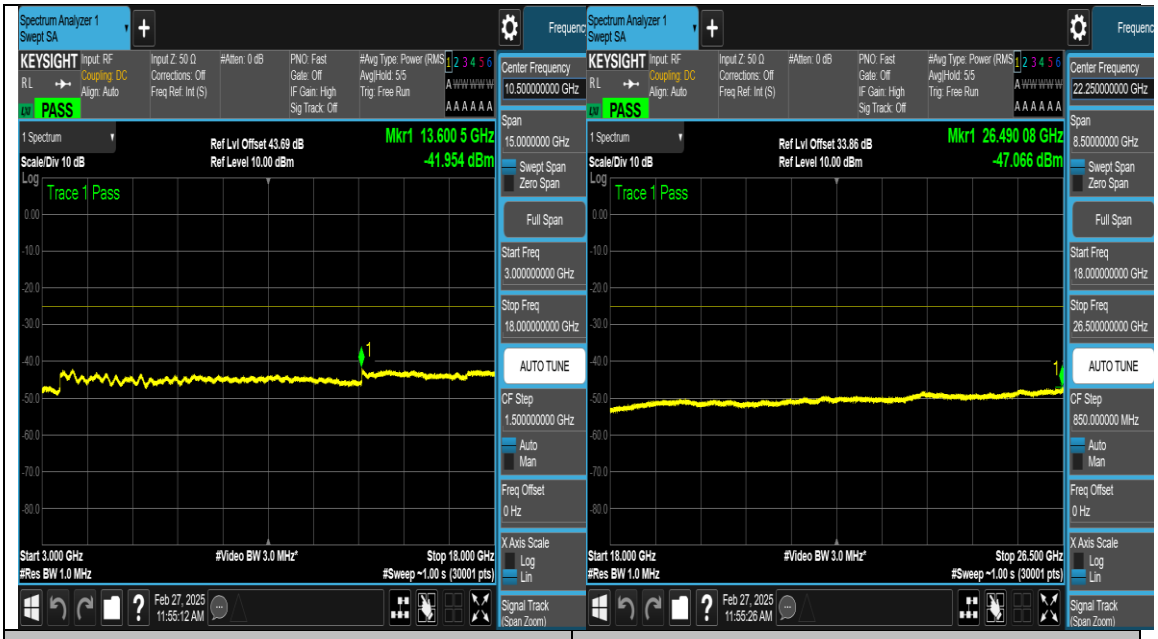
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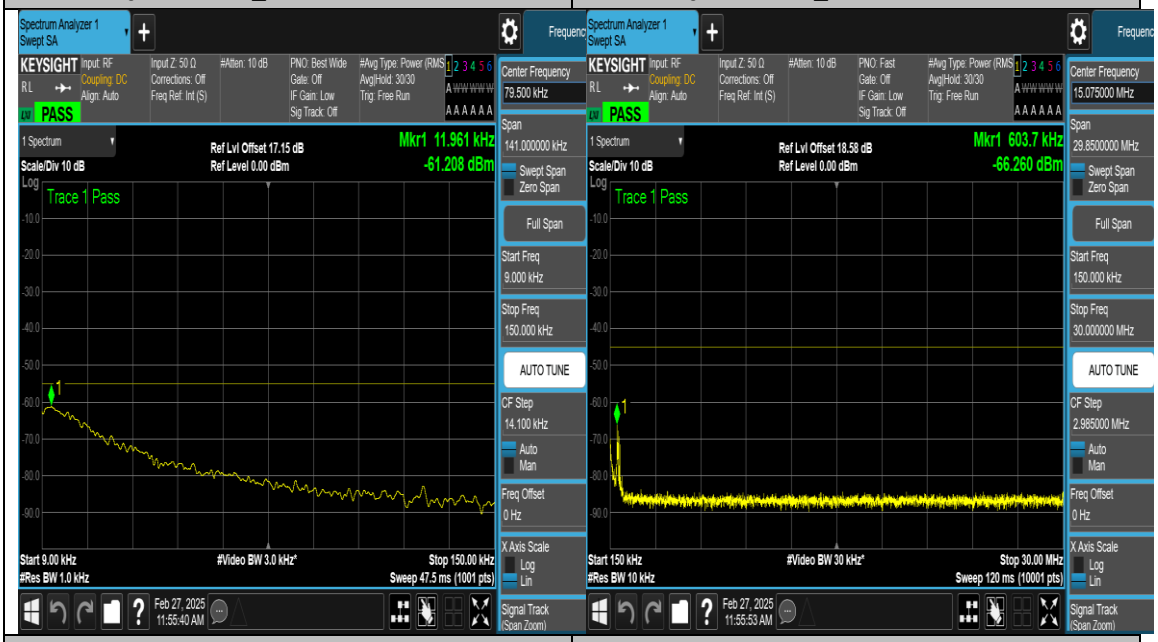
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NTNV-DC_4A_n41A-PC2-30-5+60-M+L-N/A-N/A-DFT-QPSK-Outer_Full-30-1000-PASS	NTNV-DC_4A_n41A-PC2-30-5+60-M+L-N/A-N/A-DFT-QPSK-Outer_Full-1000-3000-PASS



NTNV-DC_4A_n41A-PC2-30-5+60-M+L-N/A-N/A-DFT-QPSK-Outer_Full-3000-18000-PASS	NTNV-DC_4A_n41A-PC2-30-5+60-M+L-N/A-N/A-DFT-QPSK-Outer_Full-18000-26500-PASS



NTNV-DC_4A_n41A-PC2-30-5+60-M+L-N/A-N/A-DFT-QPSK-Edge_1RB_Left-0.009-0.15-PASS	NTNV-DC_4A_n41A-PC2-30-5+60-M+L-N/A-N/A-DFT-QPSK-Edge_1RB_Left-0.15-30-PASS