

PePLink Pole Point Block Diagram and Technical Description

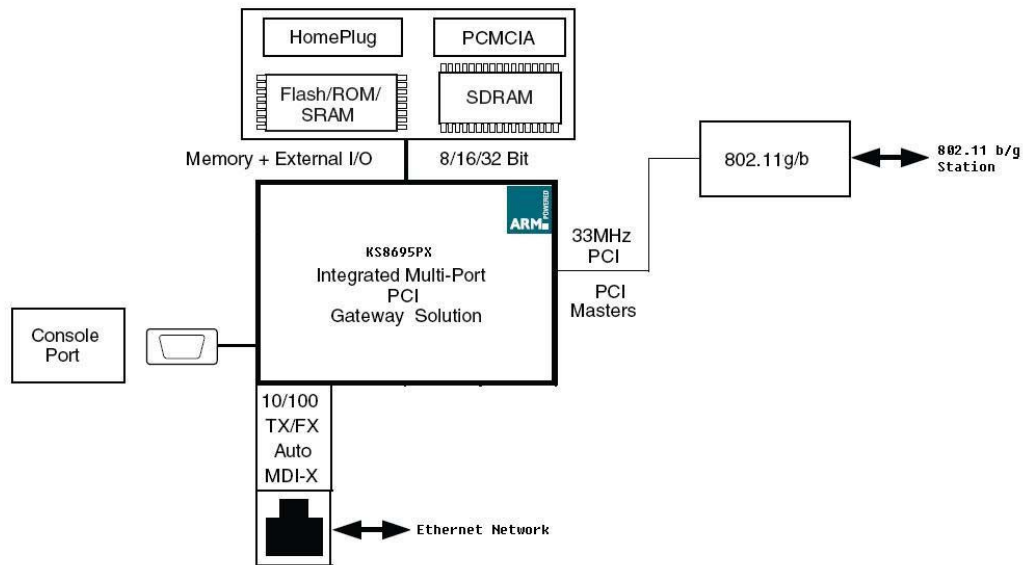


Figure 1. KS8695P PCI Gateway System Options

Description of System

The KS8695PX Integrated Multi-Port PCI Gateway solution provide a 10/100 TX/FX Auto MDI-X port connect to a Ethernet Network. On the other hand, the CPU also provide a 33 MHz PCI interface for a 802.11 b/g RF module, which can be used to connected a 802.11 b/g station.

The IEEE802.11b/g WLAN MAC protocol is implemented in the Wi-Fi adapter's firmware. It supports basic service set (BSS) and independent basic service set (IBSS) operation under distributed coordination function (DCF). Low level protocol functions such as request to send (RTS)/clear to send (CTS) generation and acknowledgment, fragmentation and de-fragmentation, and automatic beacon monitoring are handled without host intervention. Active scanning is performed autonomously once initiated by a host command. Host interface command and status handshakes allow concurrent operations from multi-threaded I/O drivers.

Access point features are implemented in the host system's firmware. It can act as an access point with multiple BSSID and ESSID. It bridges client traffic on the WLAN to the Ethernet port with configured VLAN tagged. It supports IEEE802.11i. A web based administration interface is provided for configuring the device.