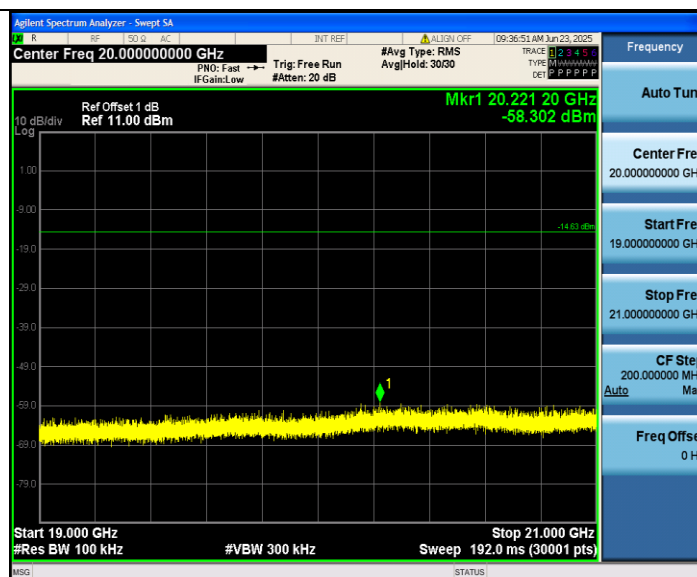
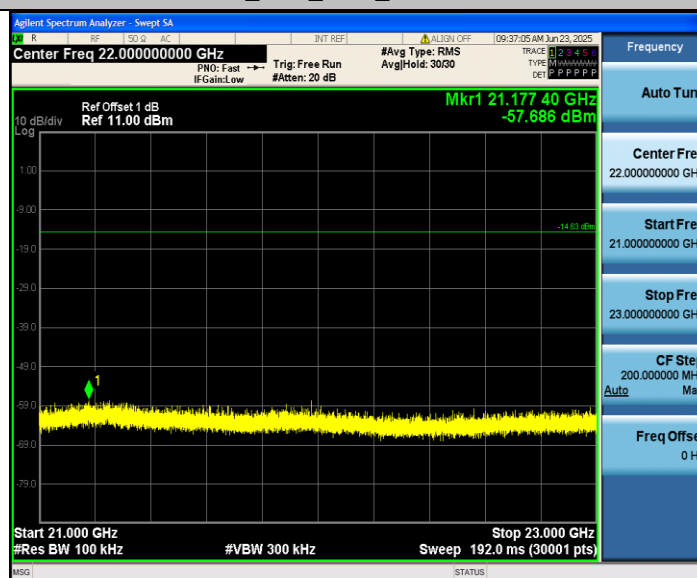
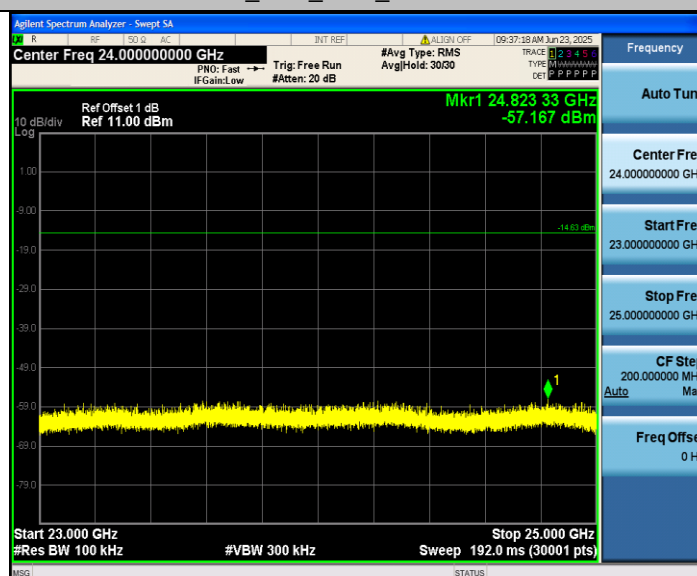




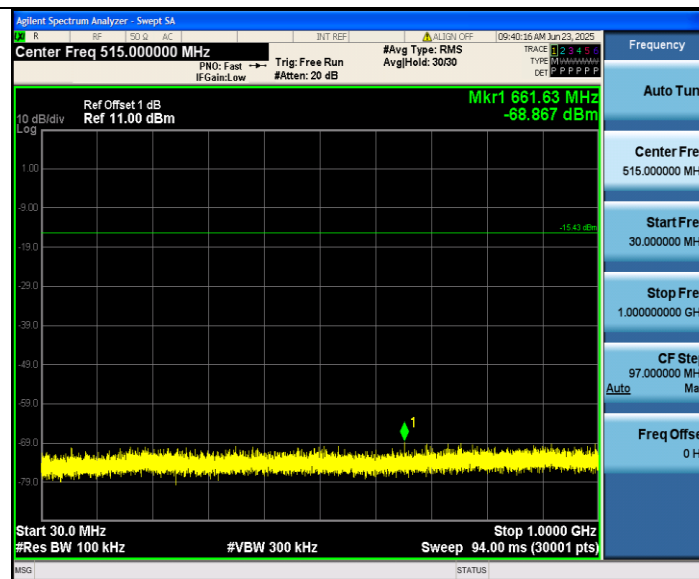
2DH5_Ant1_2441_21000~23000



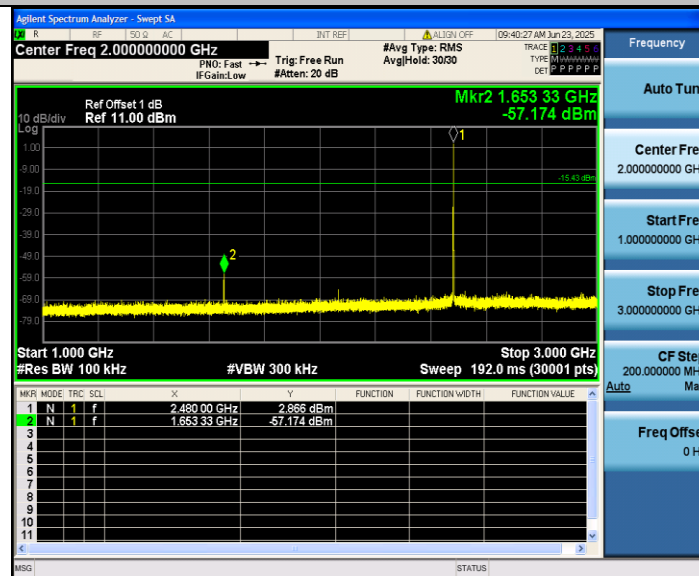
2DH5_Ant1_2441_23000~25000



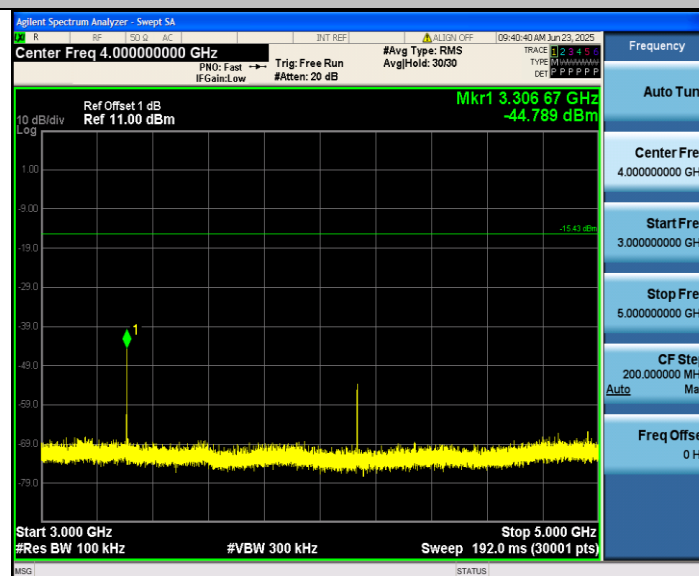
2DH5_Ant1_2480_30~1000



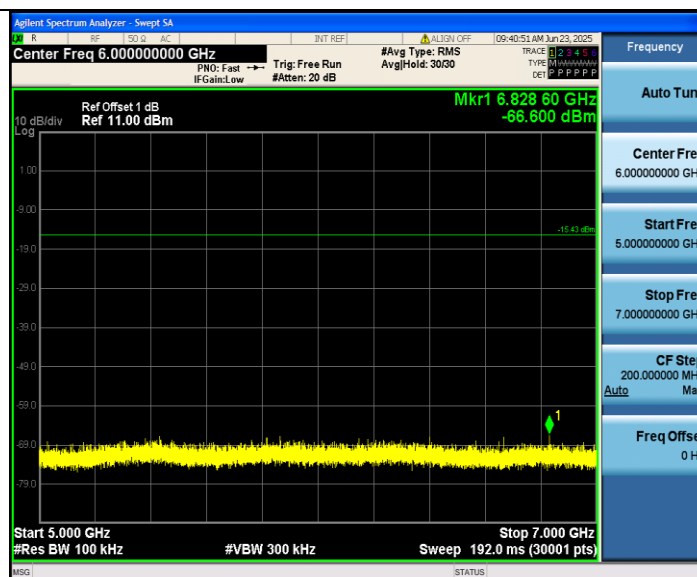
2DH5_Ant1_2480_1000~3000



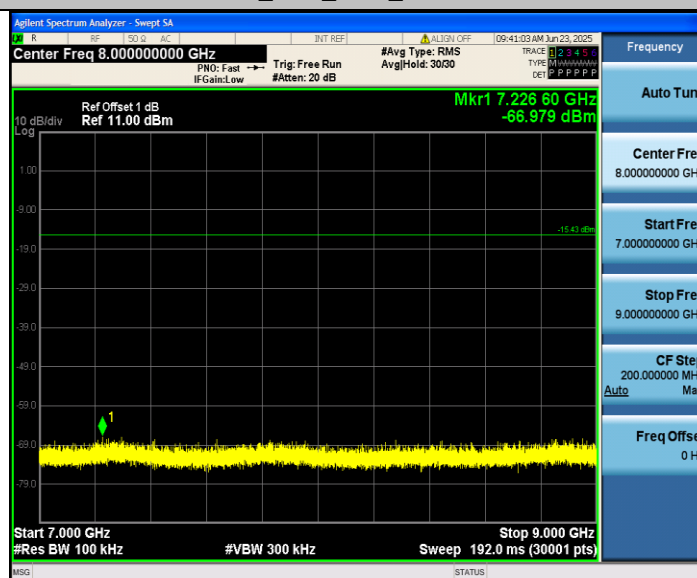
2DH5_Ant1_2480_3000~5000



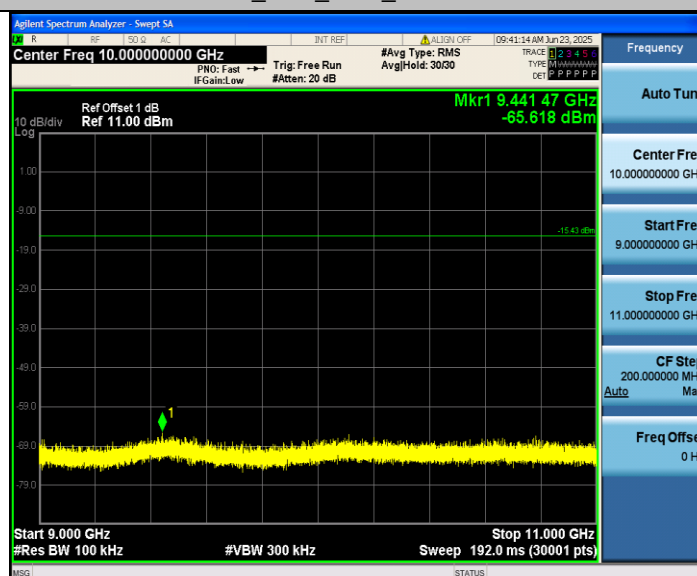
2DH5_Ant1_2480_5000~7000



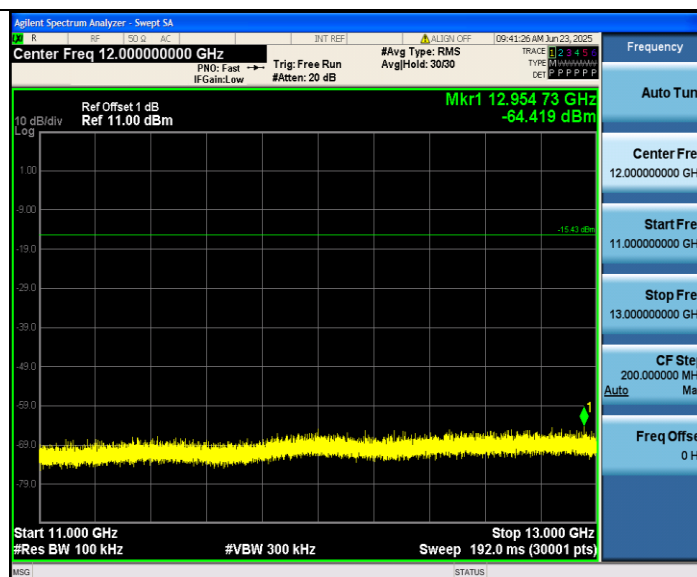
2DH5_Ant1_2480_7000~9000



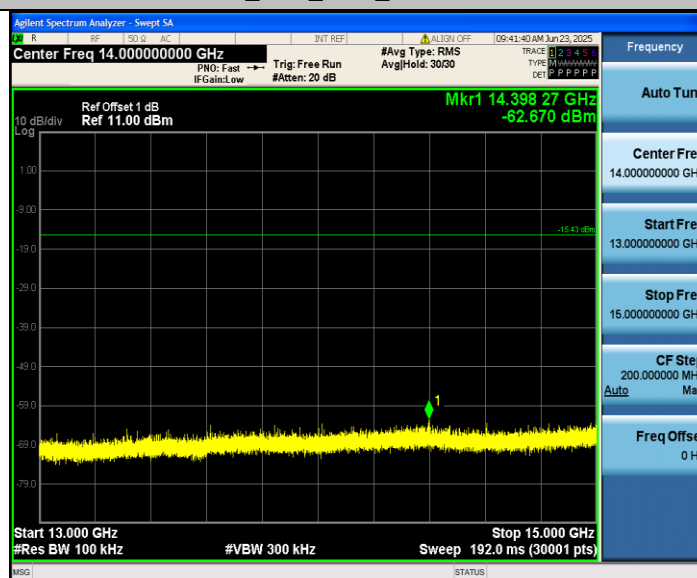
2DH5_Ant1_2480_9000~11000



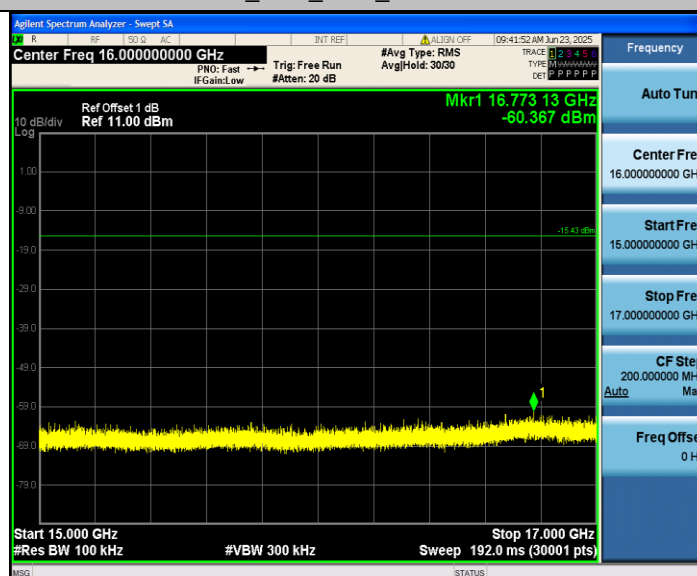
2DH5_Ant1_2480_11000~13000



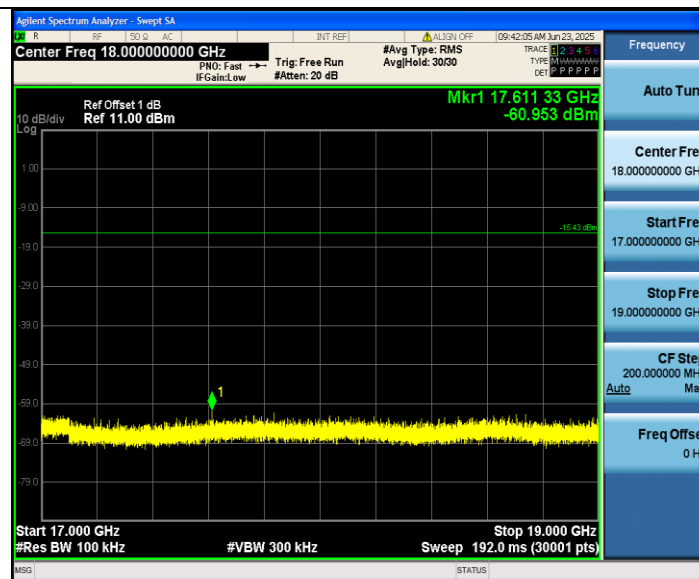
2DH5_Ant1_2480_13000~15000



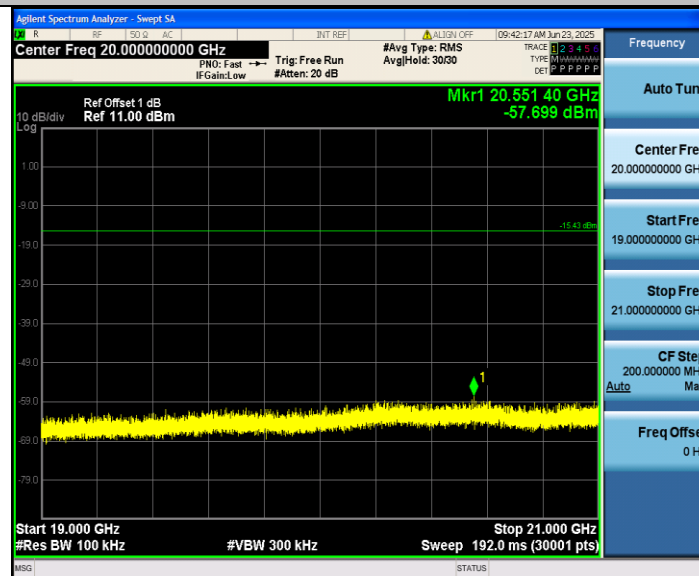
2DH5_Ant1_2480_15000~17000



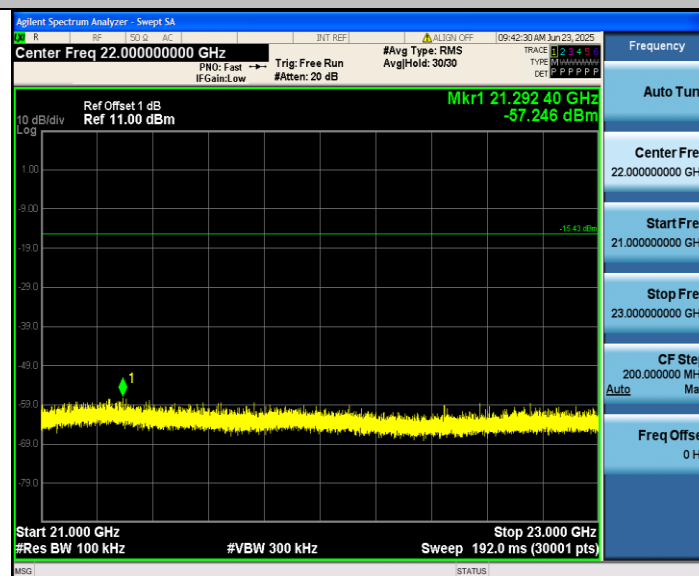
2DH5_Ant1_2480_17000~19000



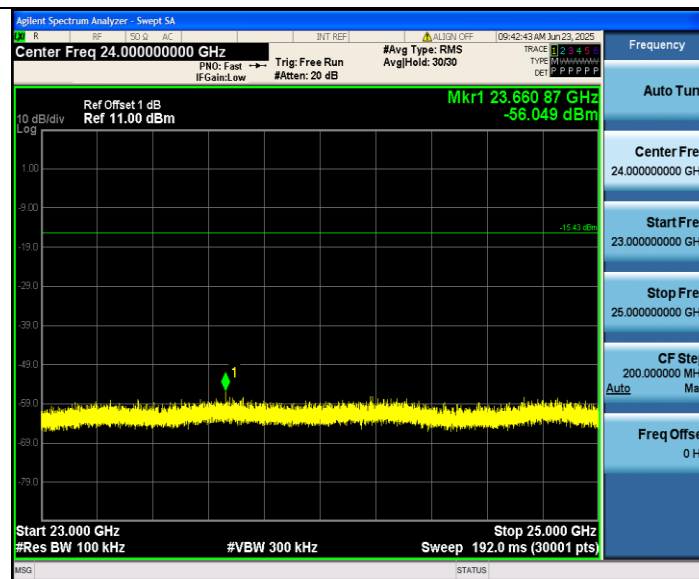
2DH5_Ant1_2480_19000~21000



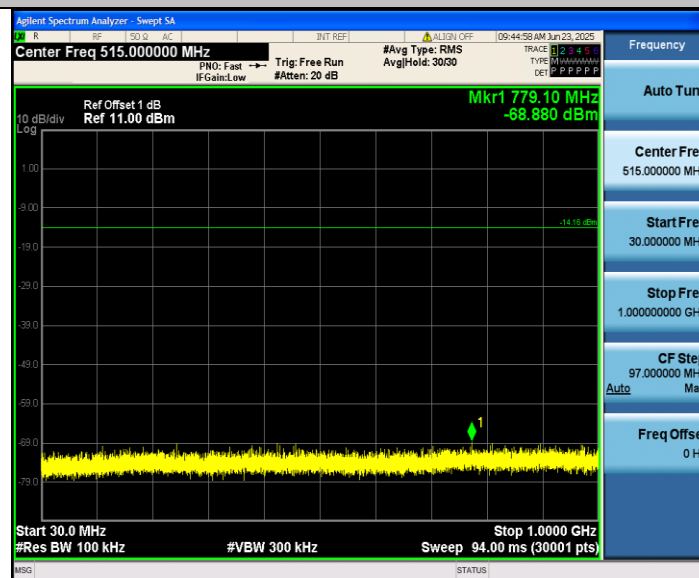
2DH5_Ant1_2480_21000~23000



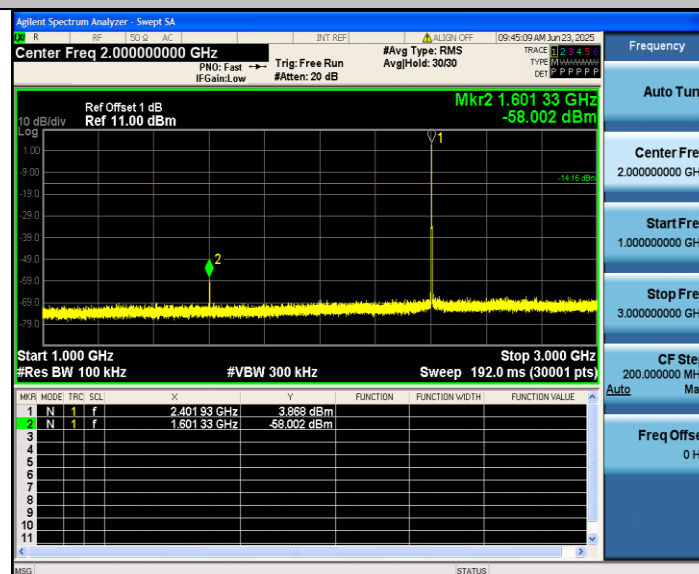
2DH5_Ant1_2480_23000~25000



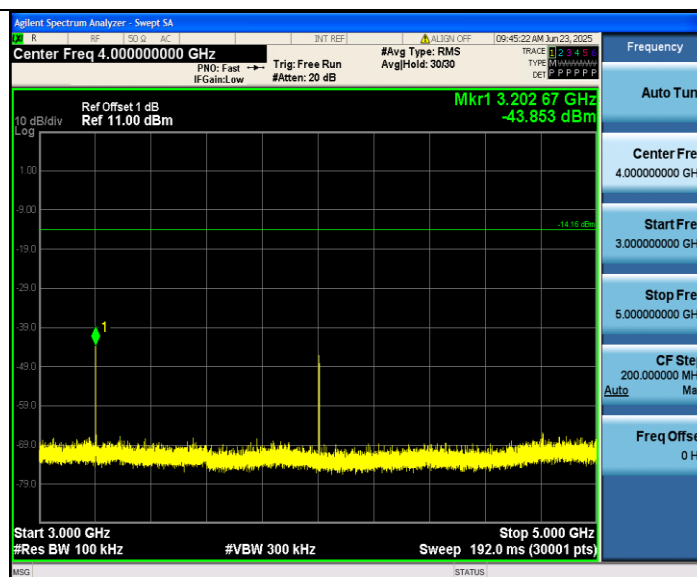
3DH5_Ant1_2402_30~1000



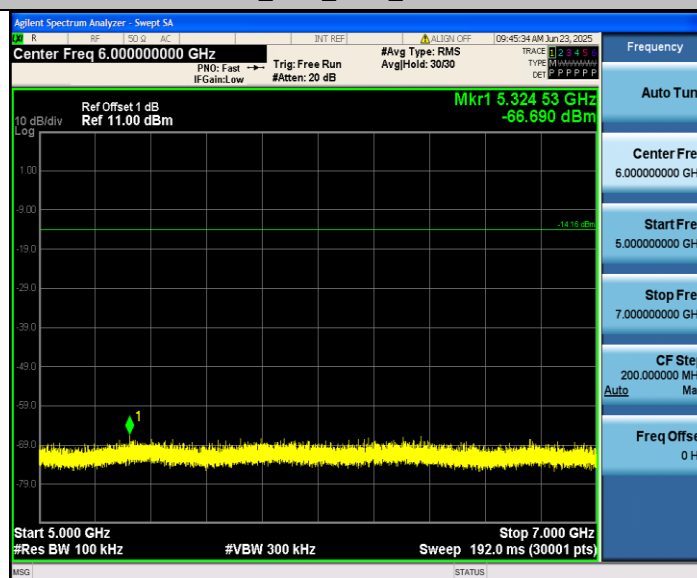
3DH5_Ant1_2402_1000~3000



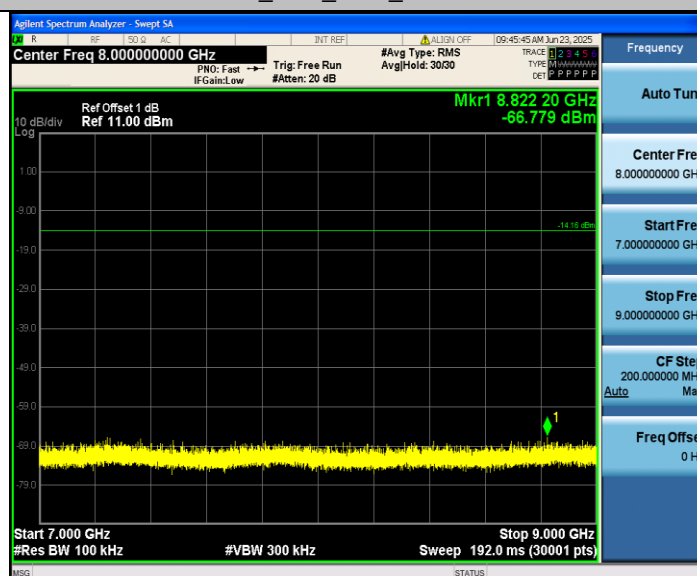
3DH5_Ant1_2402_3000~5000



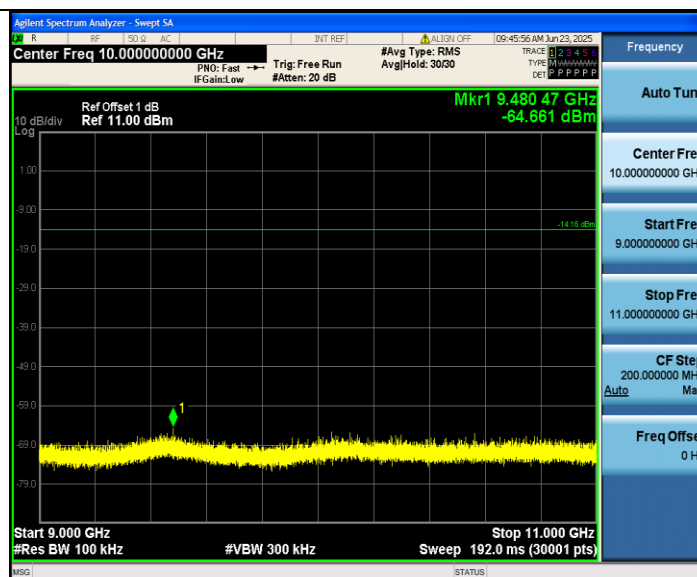
3DH5_Ant1_2402_5000~7000



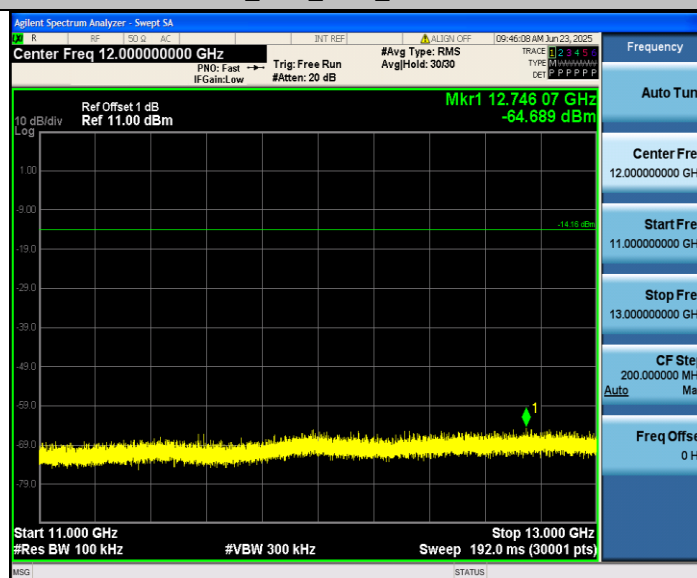
3DH5_Ant1_2402_7000~9000



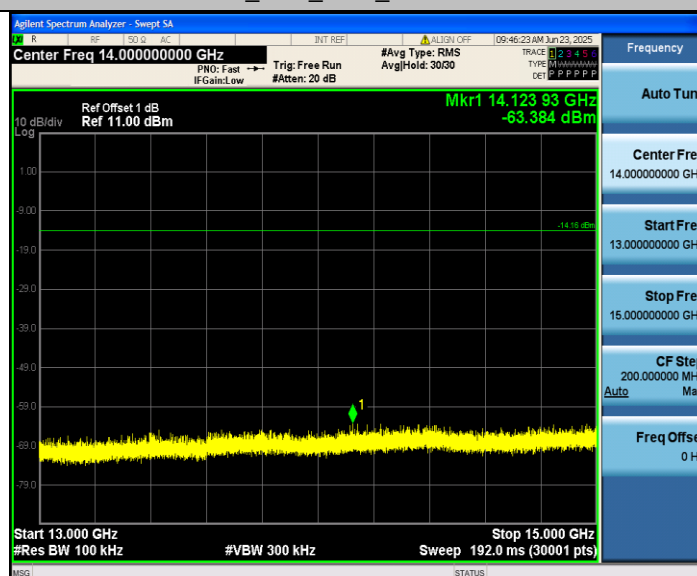
3DH5_Ant1_2402_9000~11000



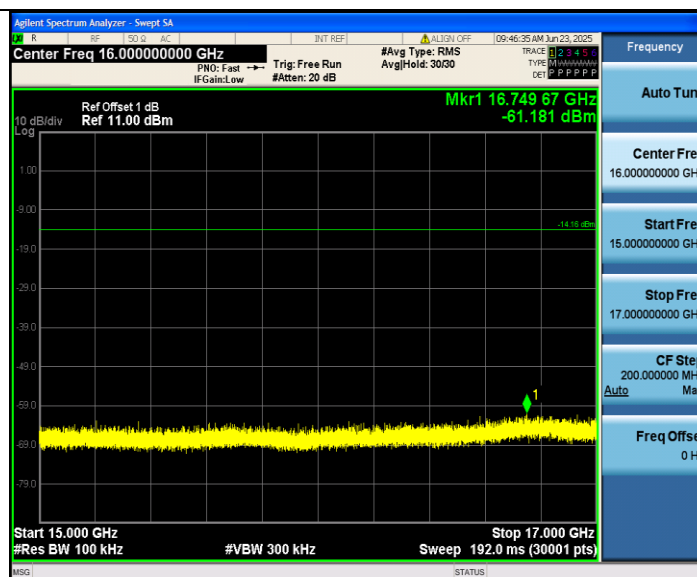
3DH5_Ant1_2402_11000~13000



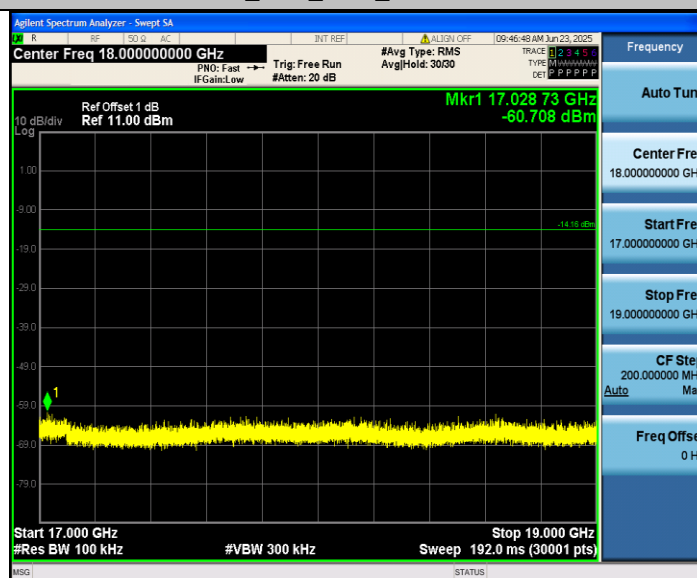
3DH5_Ant1_2402_13000~15000



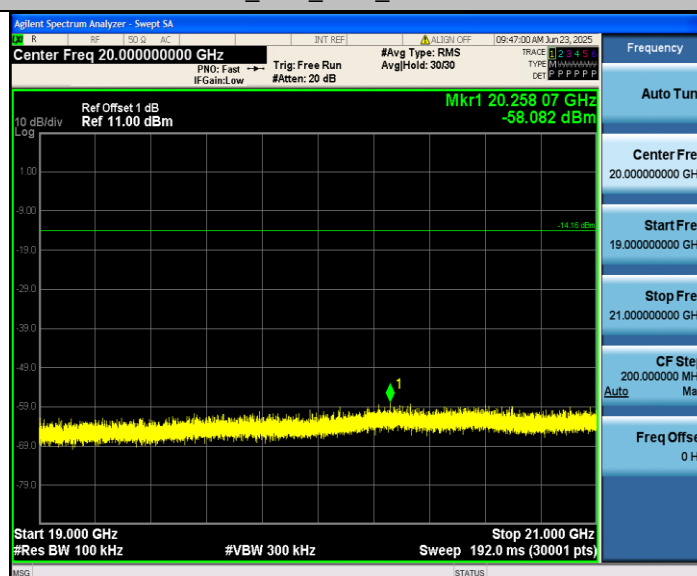
3DH5_Ant1_2402_15000~17000



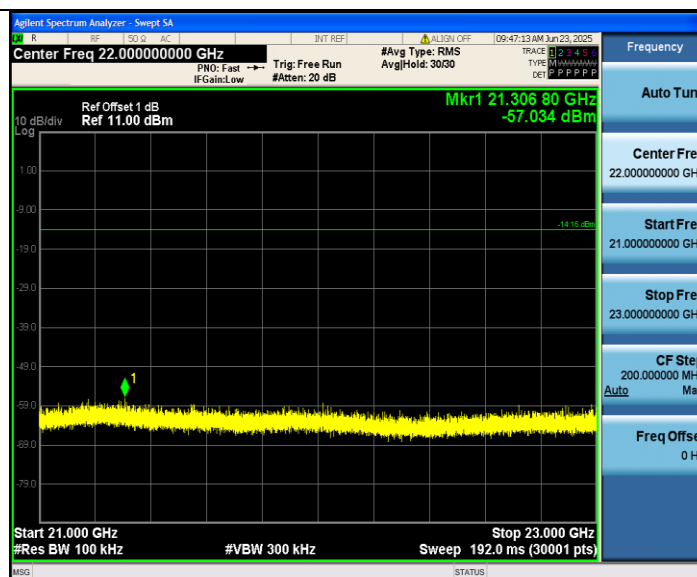
3DH5_Ant1_2402_17000~19000



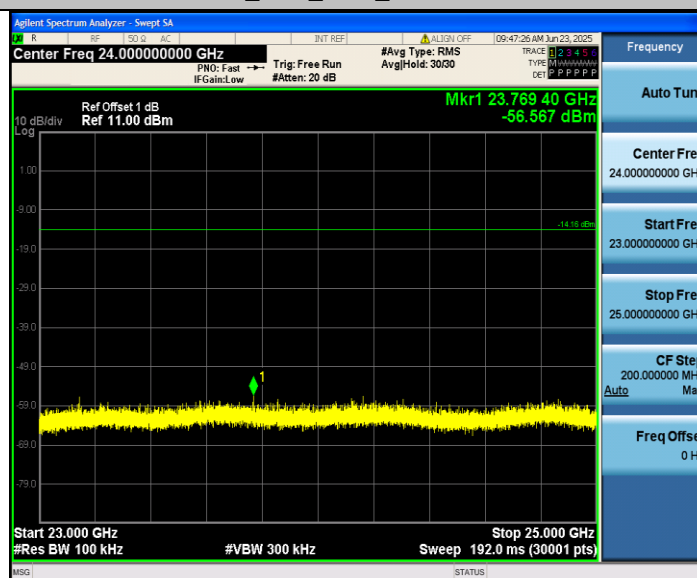
3DH5_Ant1_2402_19000~21000



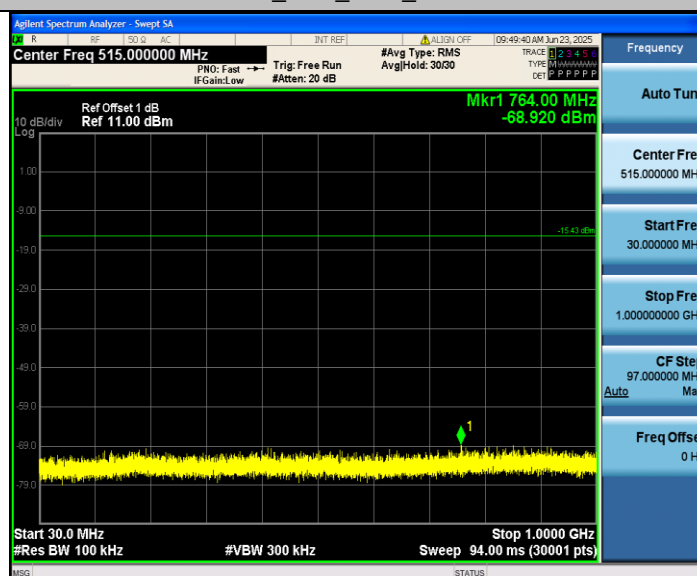
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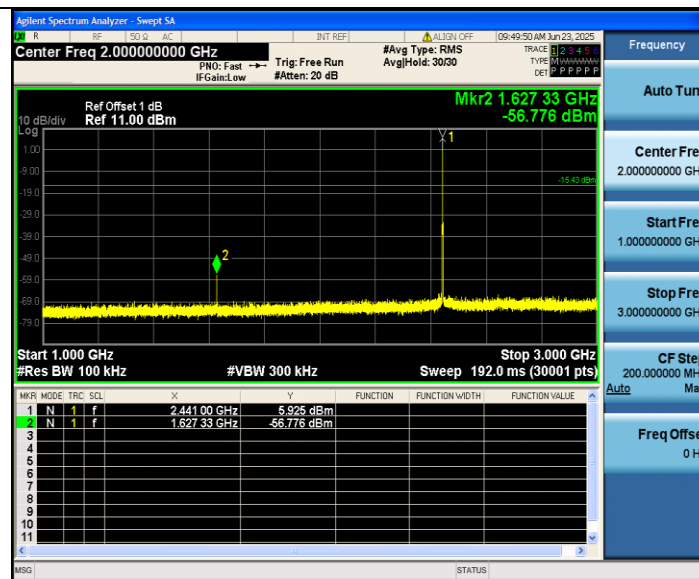
3DH5_Ant1_2402_23000~25000



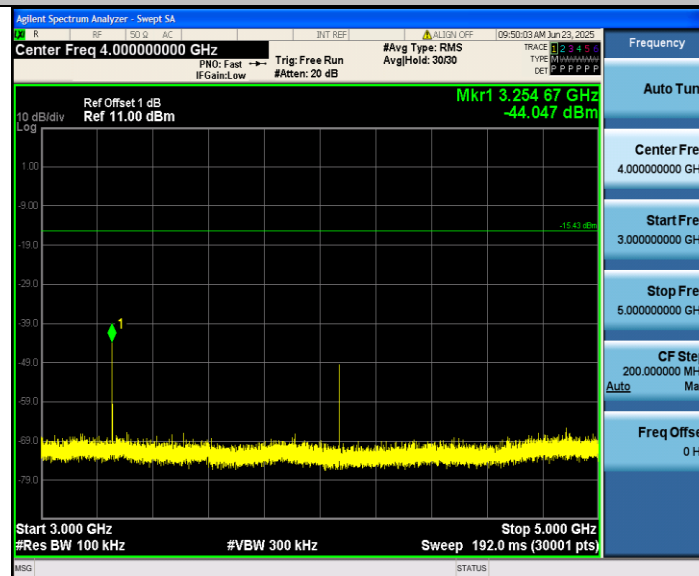
3DH5_Ant1_2441_30~1000



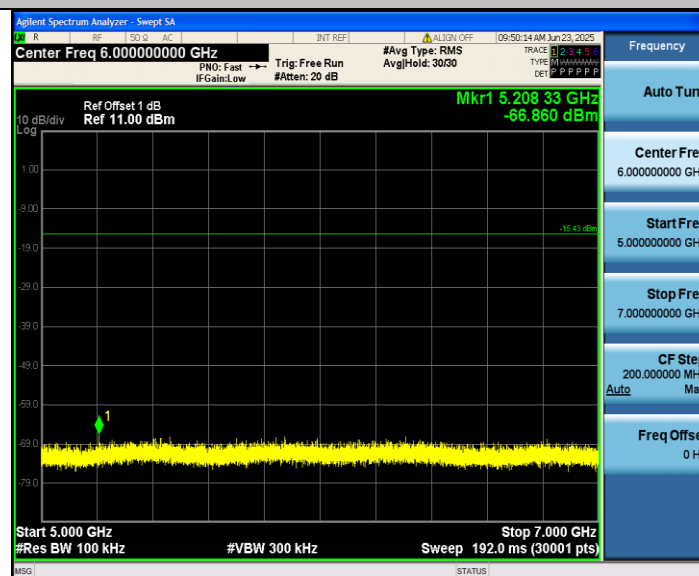
3DH5_Ant1_2441_1000~3000



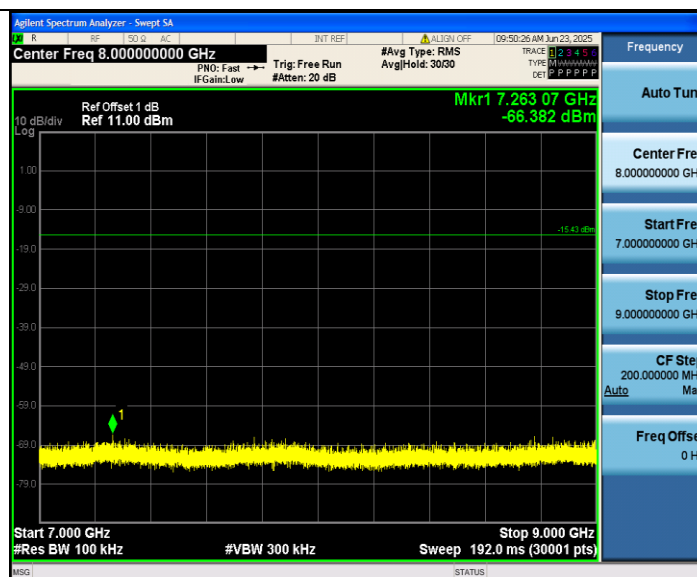
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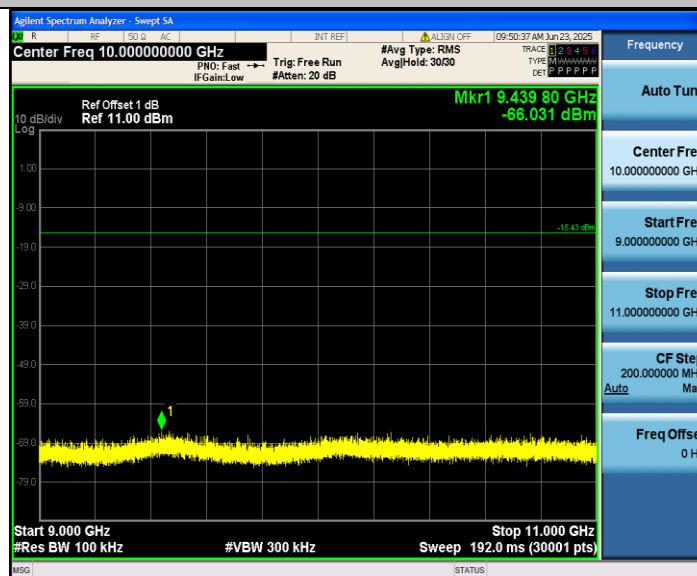
3DH5_Ant1_2441_5000~7000



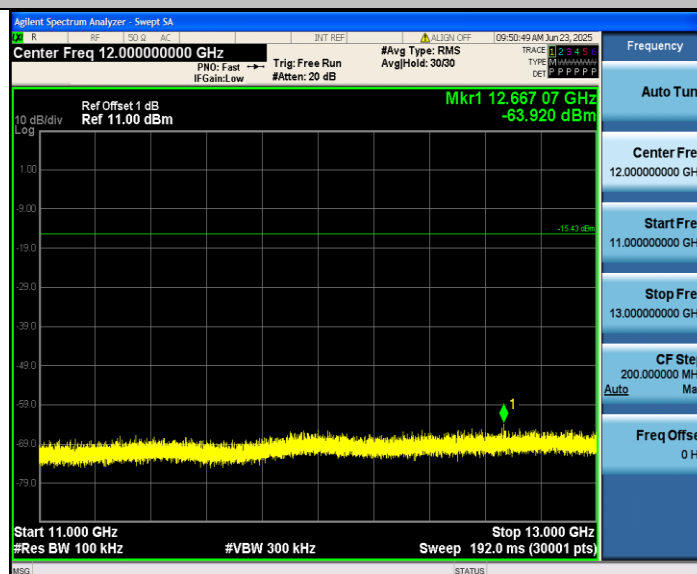
3DH5_Ant1_2441_7000~9000



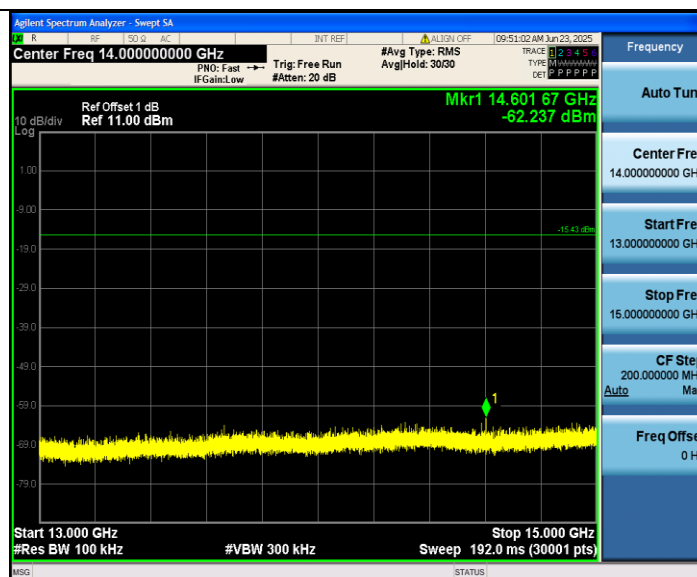
3DH5_Ant1_2441_9000~11000



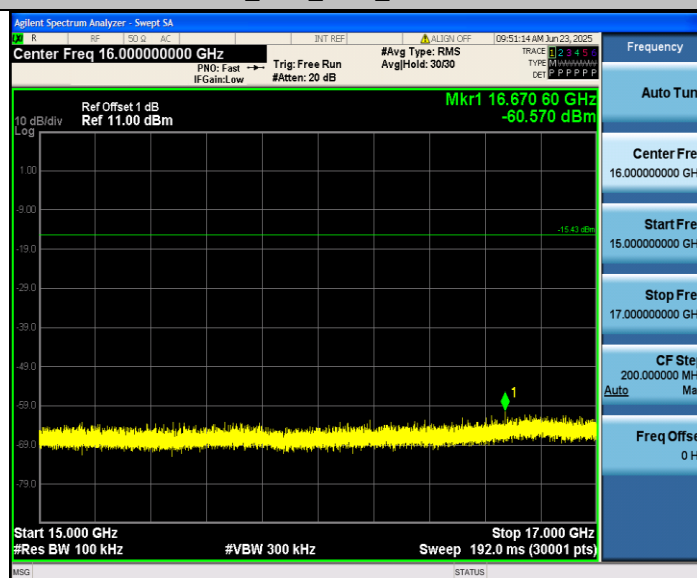
3DH5_Ant1_2441_11000~13000



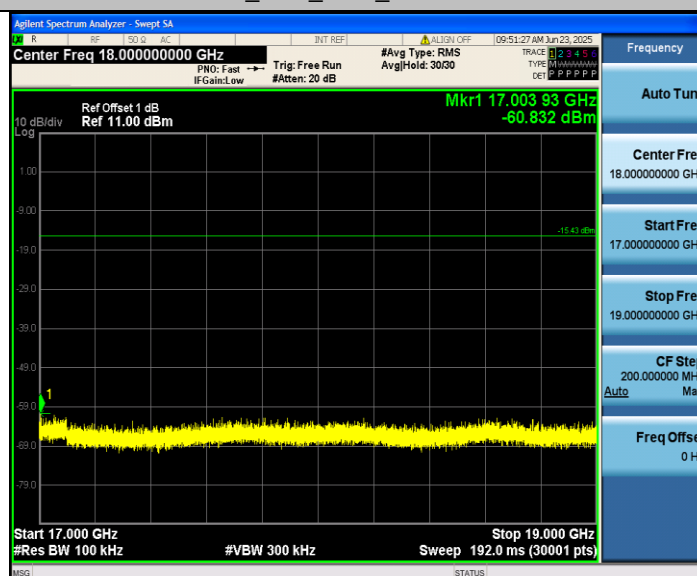
3DH5_Ant1_2441_13000~15000



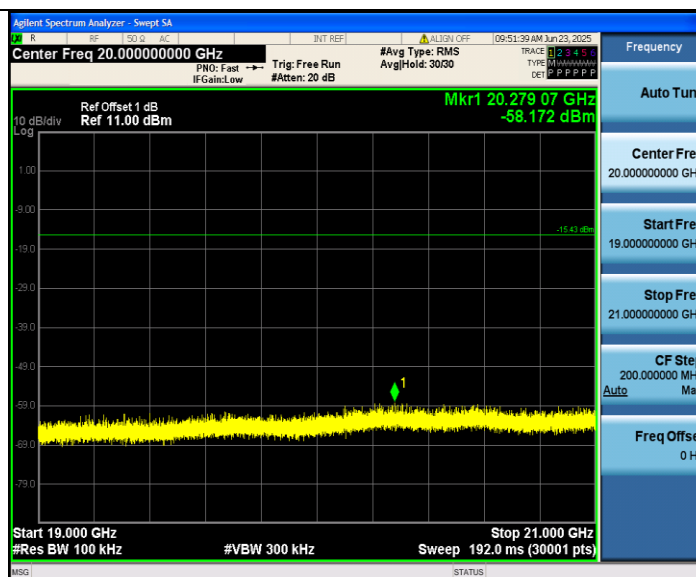
3DH5_Ant1_2441_15000~17000



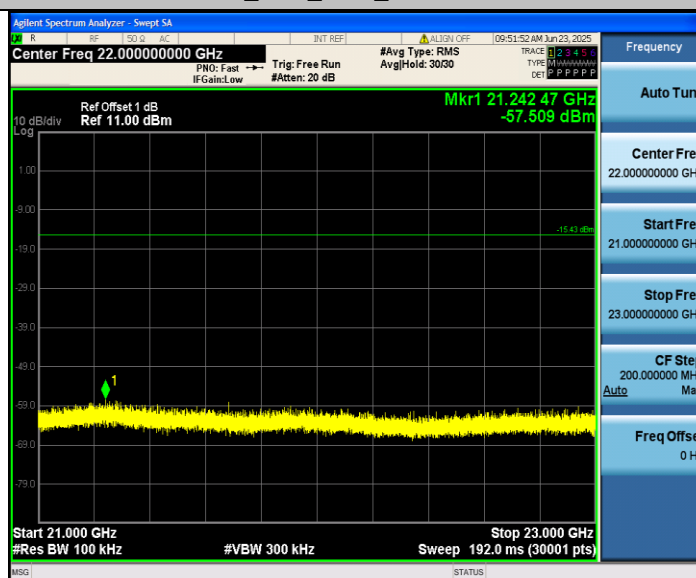
3DH5_Ant1_2441_17000~19000



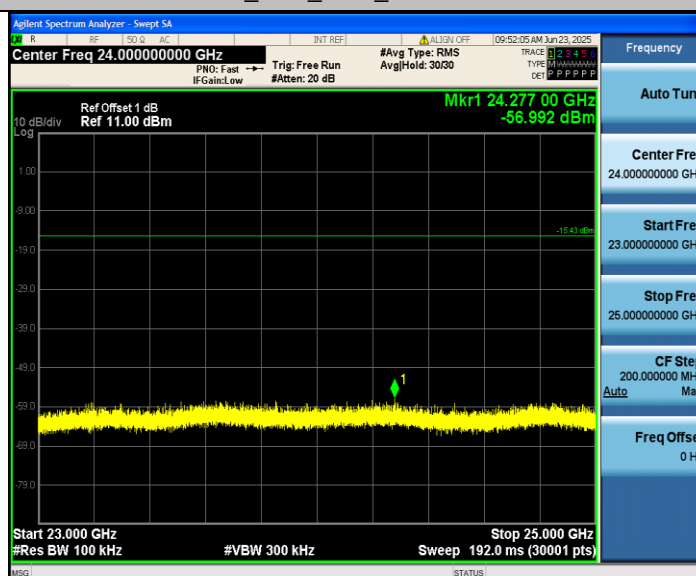
3DH5_Ant1_2441_19000~21000



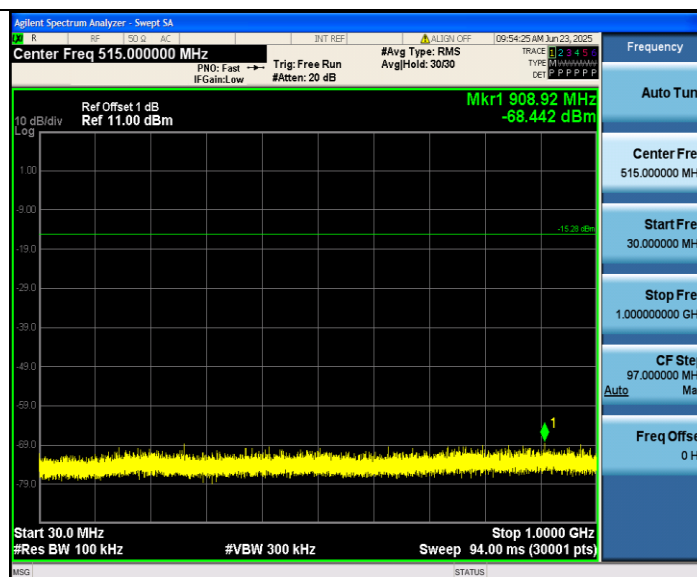
3DH5_Ant1_2441_21000~23000



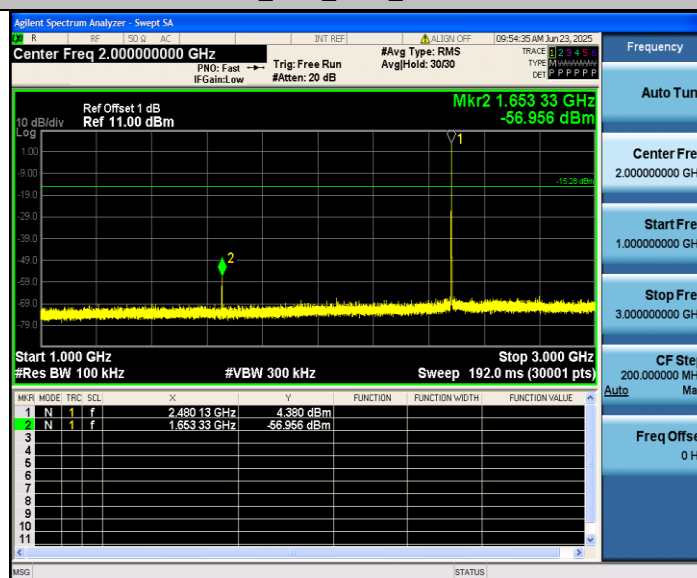
3DH5_Ant1_2441_23000~25000



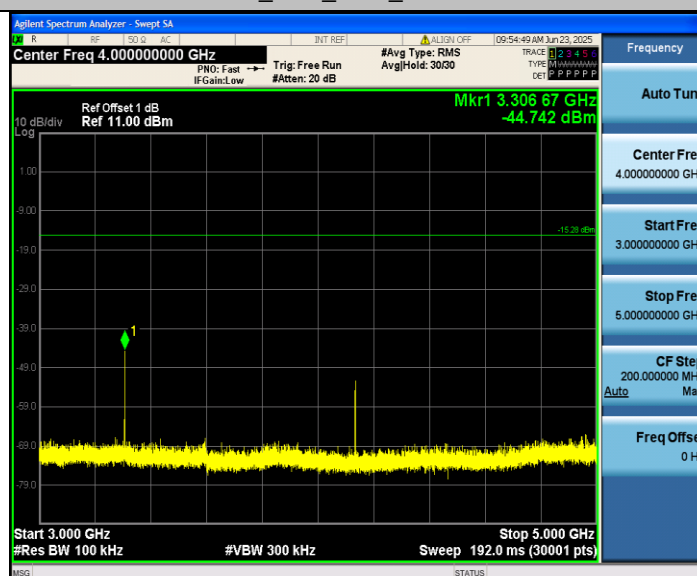
3DH5_Ant1_2480_30~1000



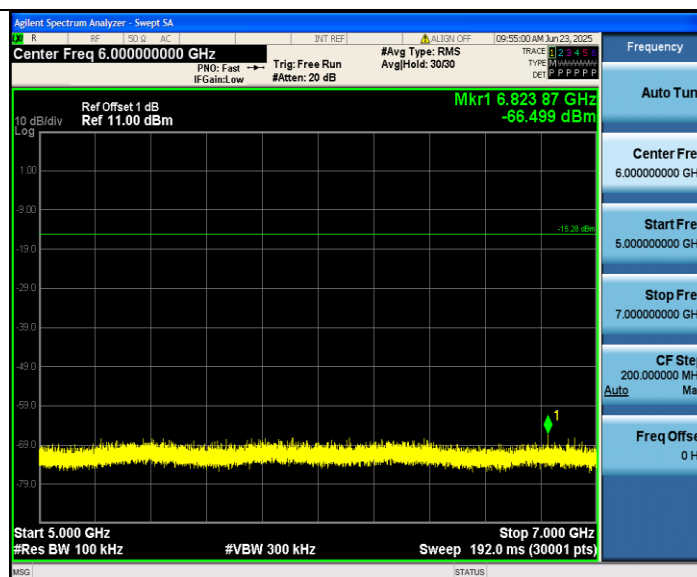
3DH5_Ant1_2480_1000~3000



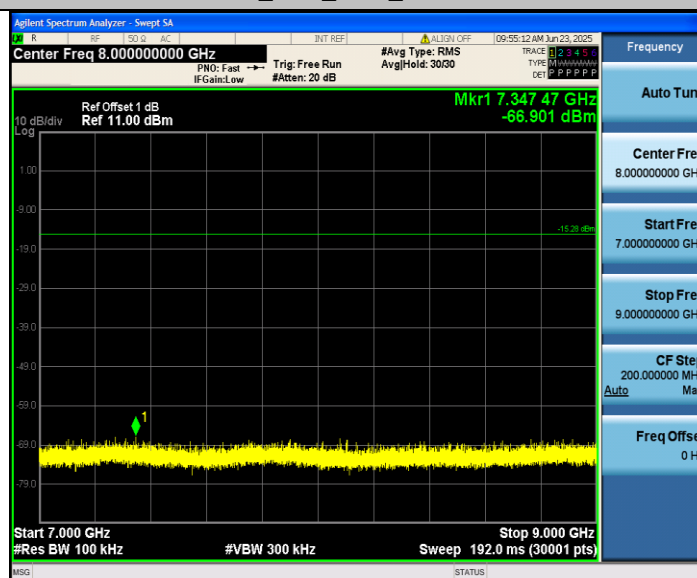
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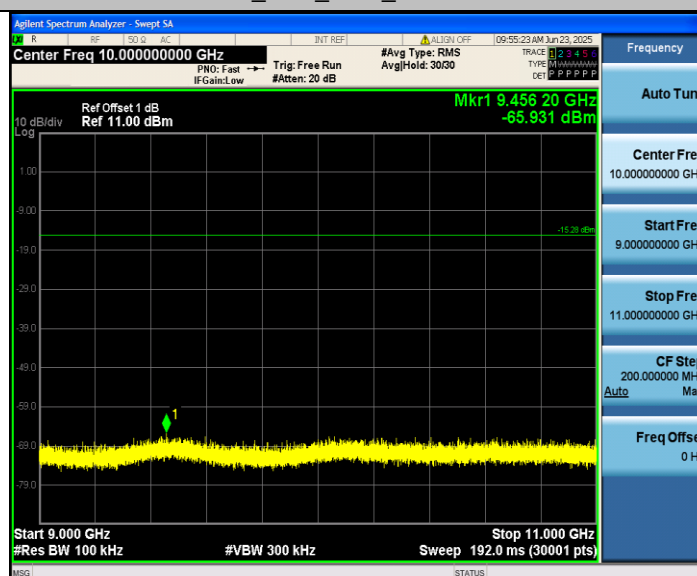
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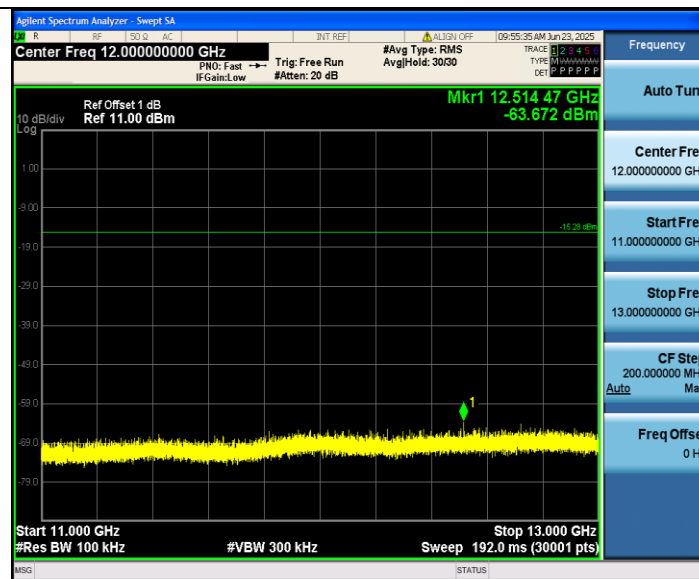
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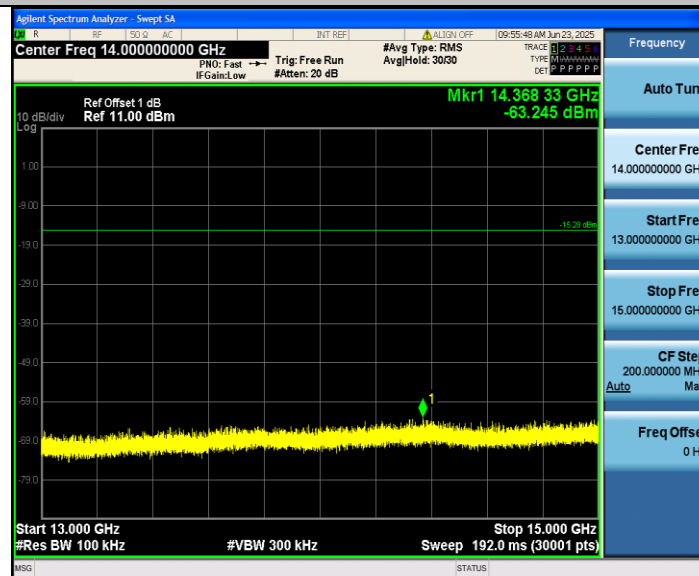
3DH5_Ant1_2480_9000~11000



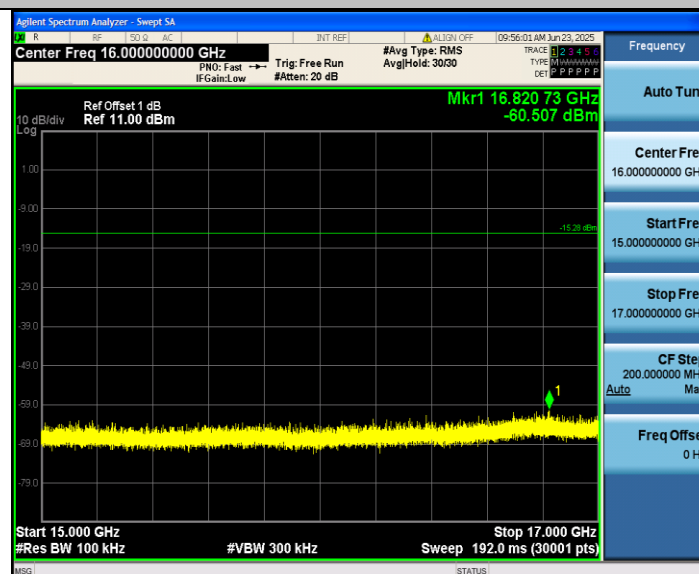
3DH5_Ant1_2480_11000~13000



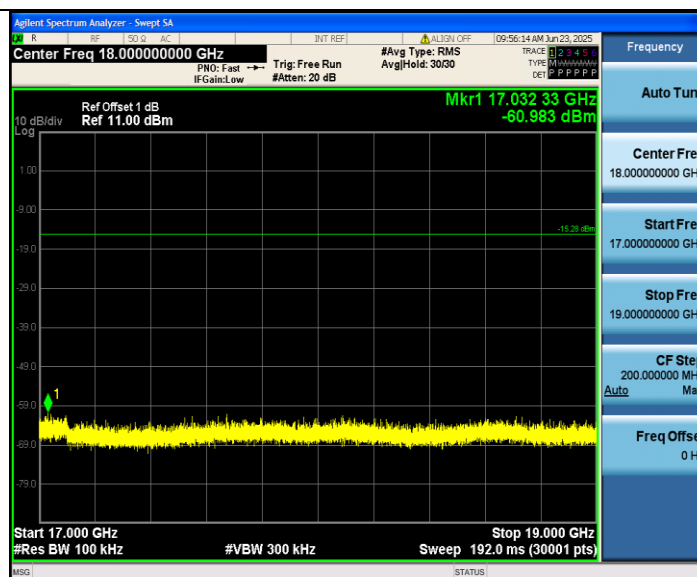
3DH5_Ant1_2480_13000~15000



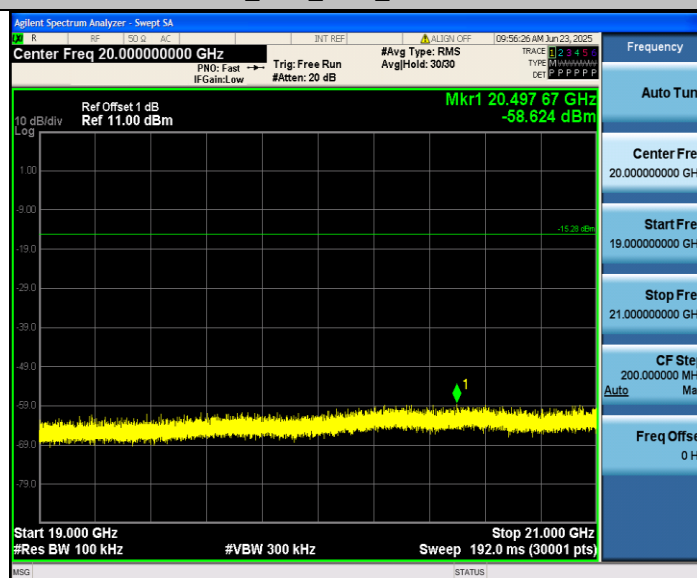
3DH5_Ant1_2480_15000~17000



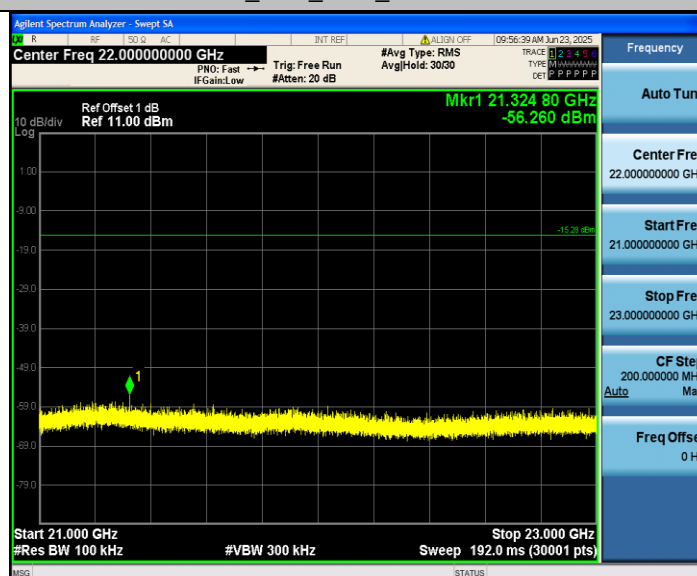
3DH5_Ant1_2480_17000~19000



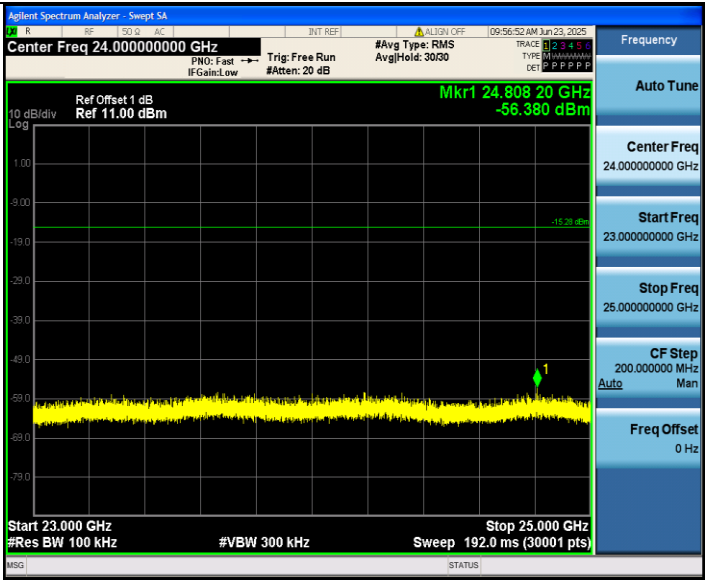
3DH5_Ant1_2480_19000~21000



3DH5_Ant1_2480_21000~23000



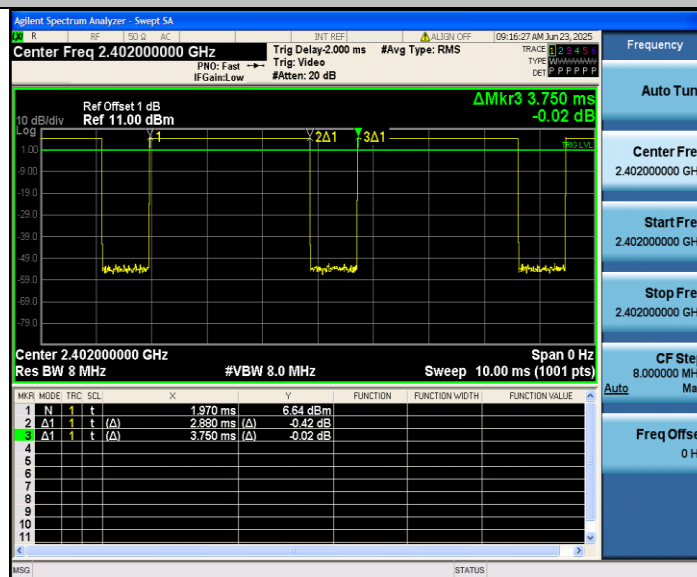
3DH5_Ant1_2480_23000~25000



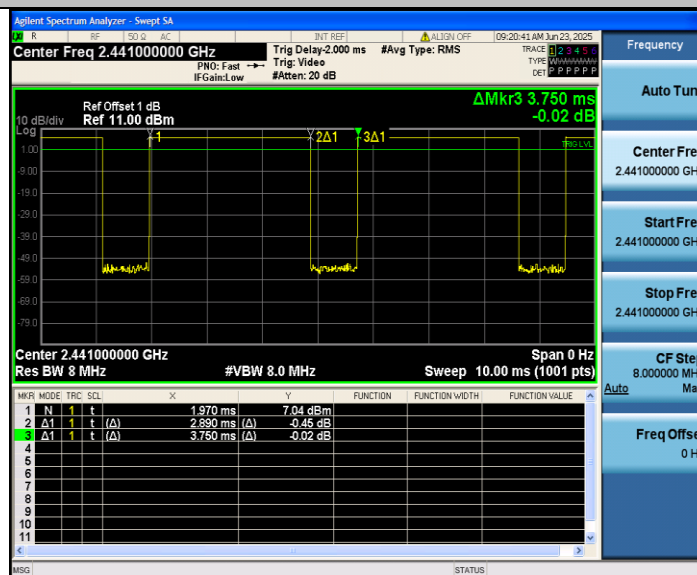
Appendix I: Duty Cycle

TestMode	Frequency [MHz]	ON Time [ms]	Period [ms]	Duty Cycle [%]	Duty Cycle Factor[dB]
DH5	2402	2.88	3.75	76.80	1.15
	2441	2.89	3.75	77.07	1.13
	2480	2.89	3.75	77.07	1.13
2DH5	2402	2.89	3.75	77.07	1.13
	2441	2.89	3.75	77.07	1.13
	2480	2.89	3.75	77.07	1.13
3DH5	2402	2.89	3.75	77.07	1.13
	2441	2.89	3.75	77.07	1.13
	2480	2.90	3.75	77.33	1.12

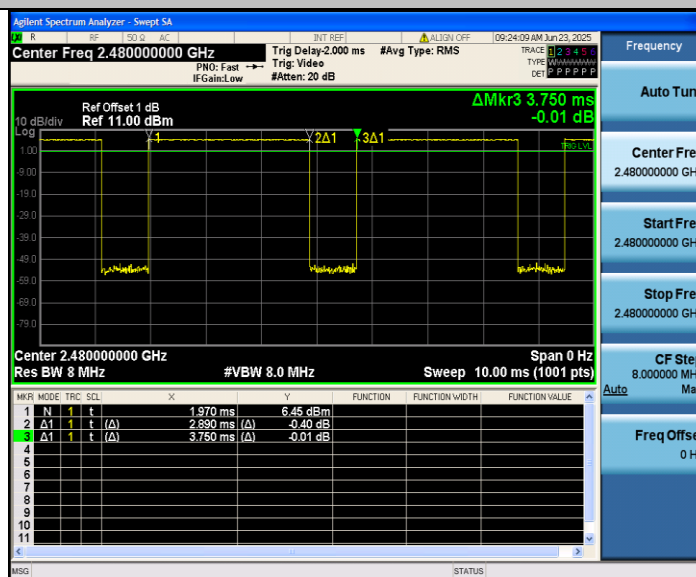
DH5_Ant1_2402



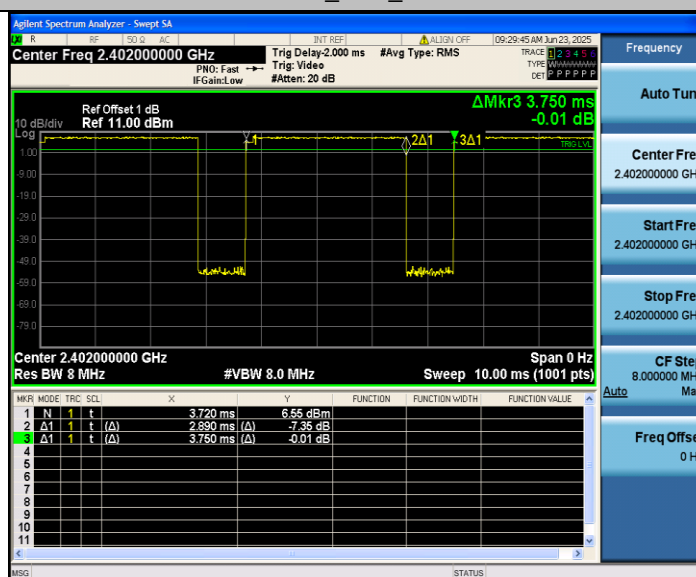
DH5_Ant1_2441



DH5_Ant1_2480



2DH5 Ant1 2402



2DH5 Ant1 2441

