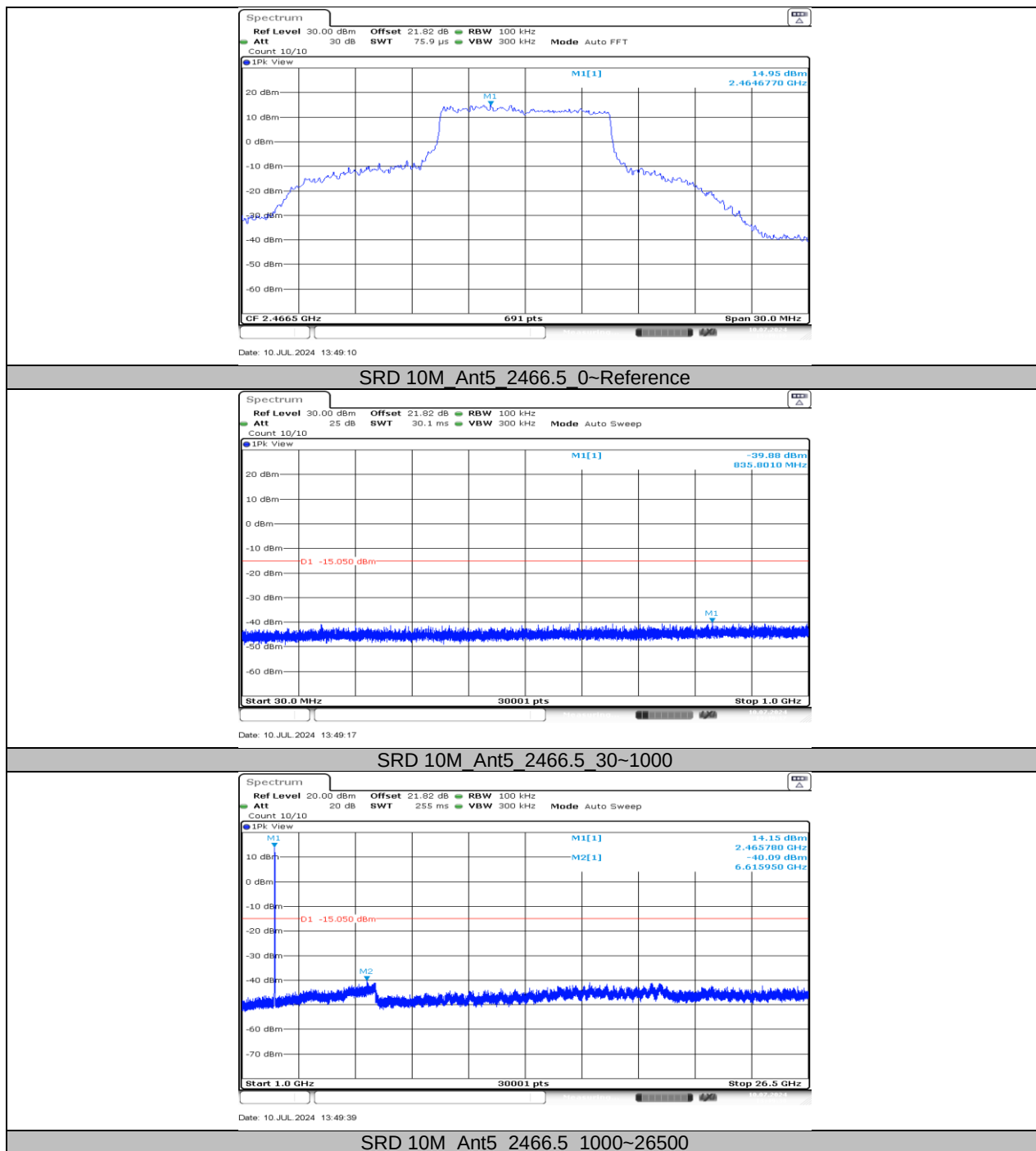
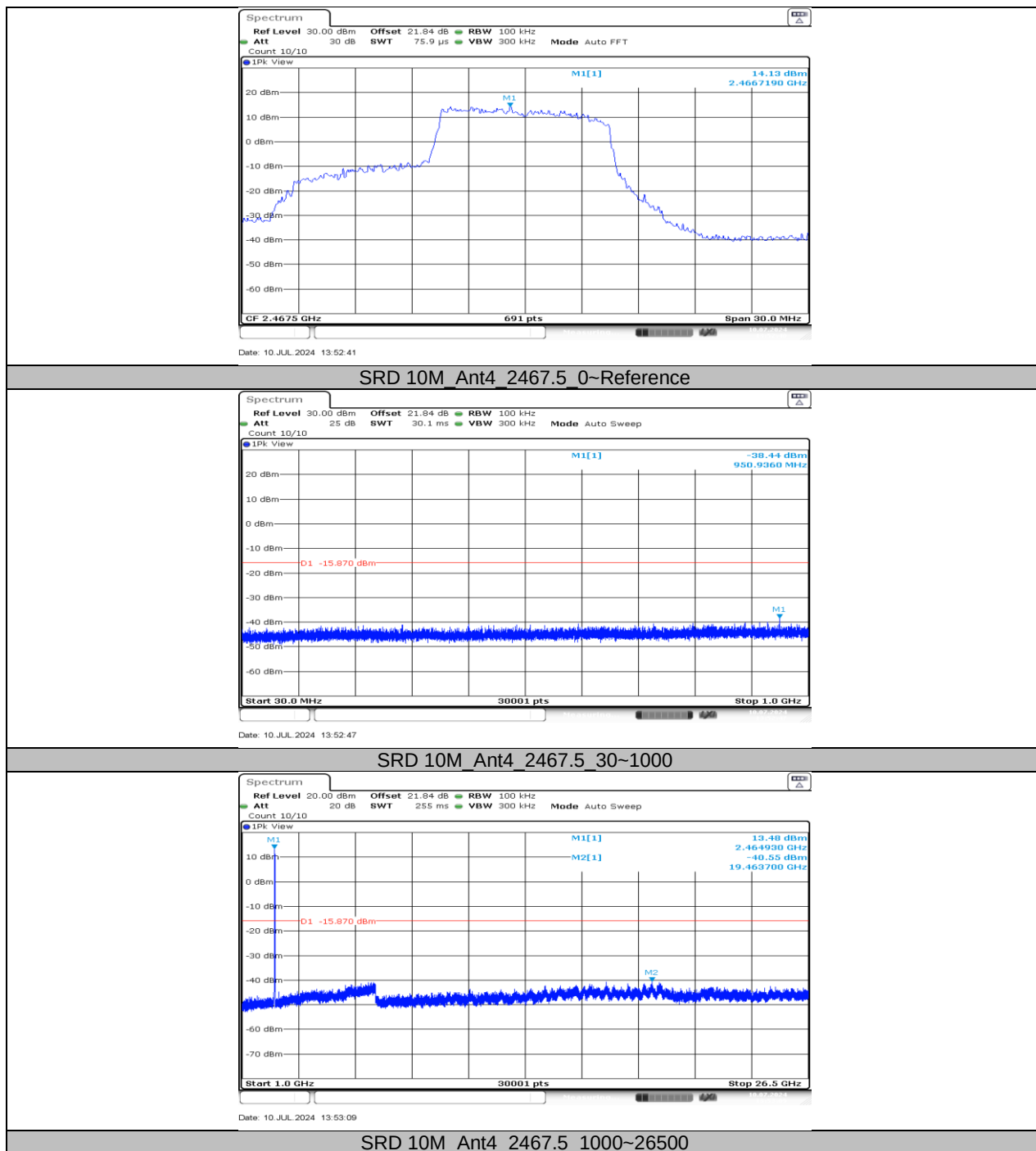
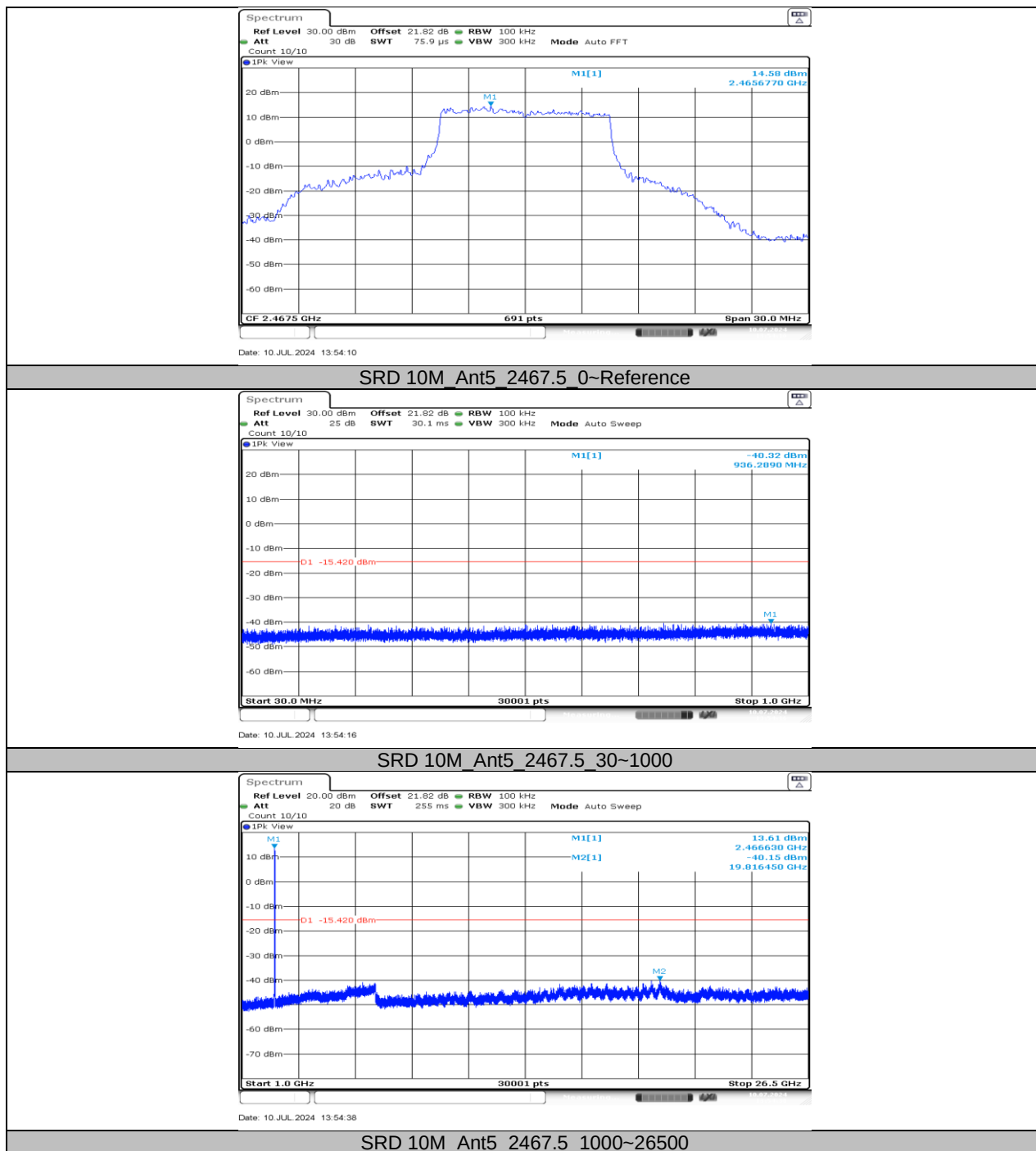
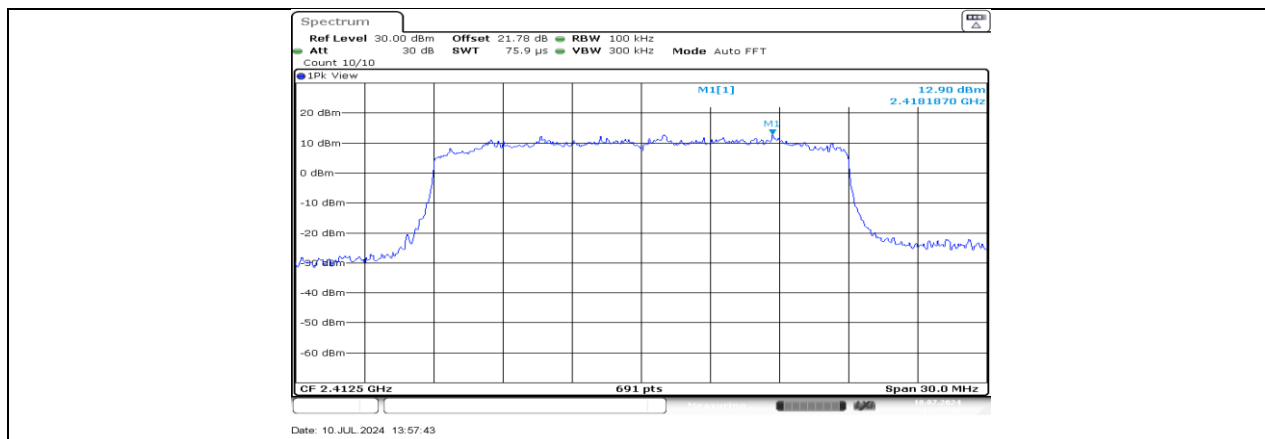


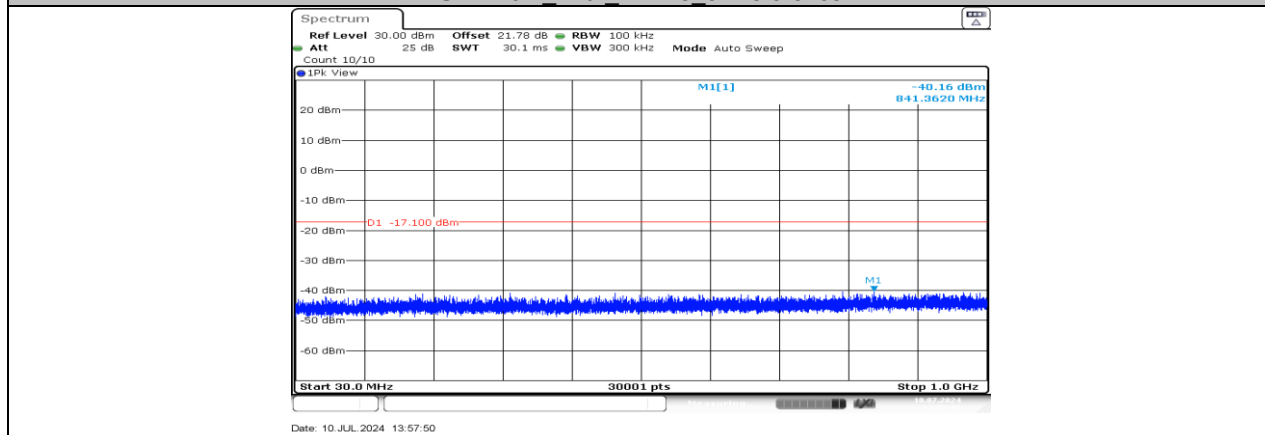




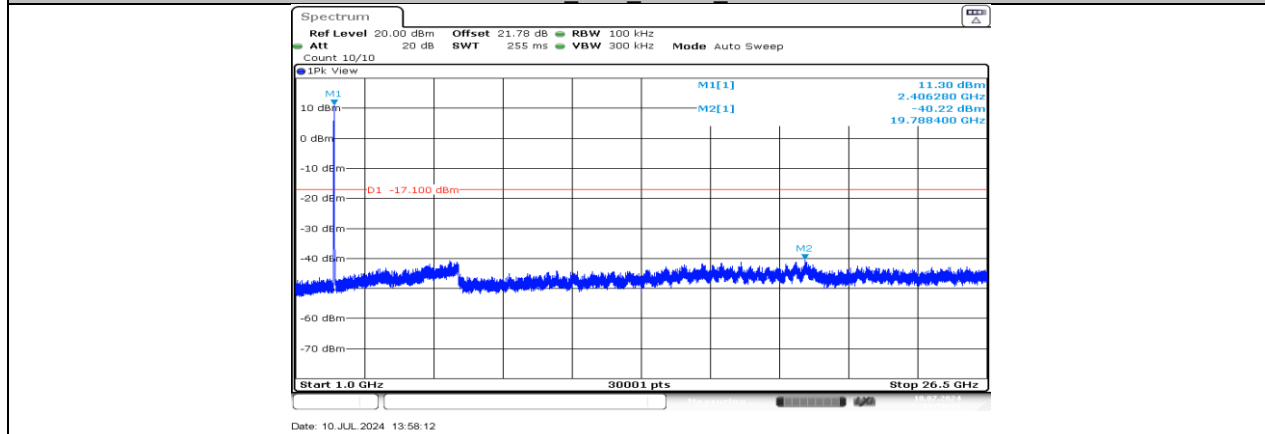




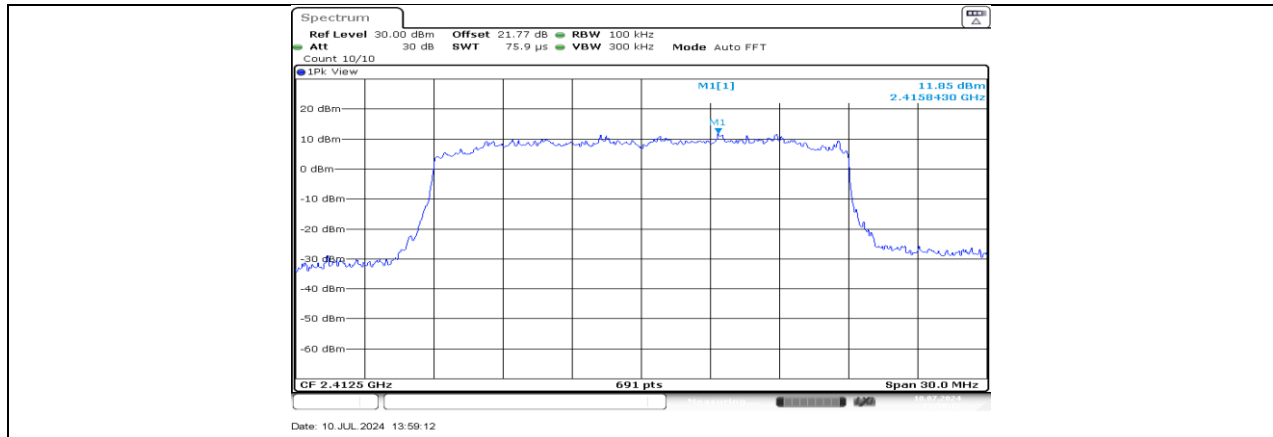
SRD 20M_Ant4_2412.5_0~Reference



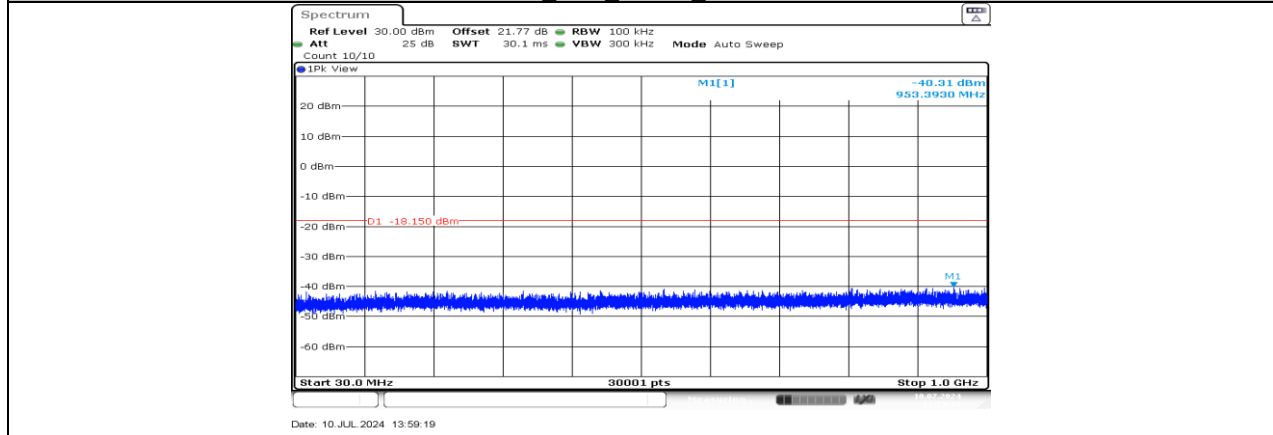
SRD 20M_Ant4_2412.5_30~1000



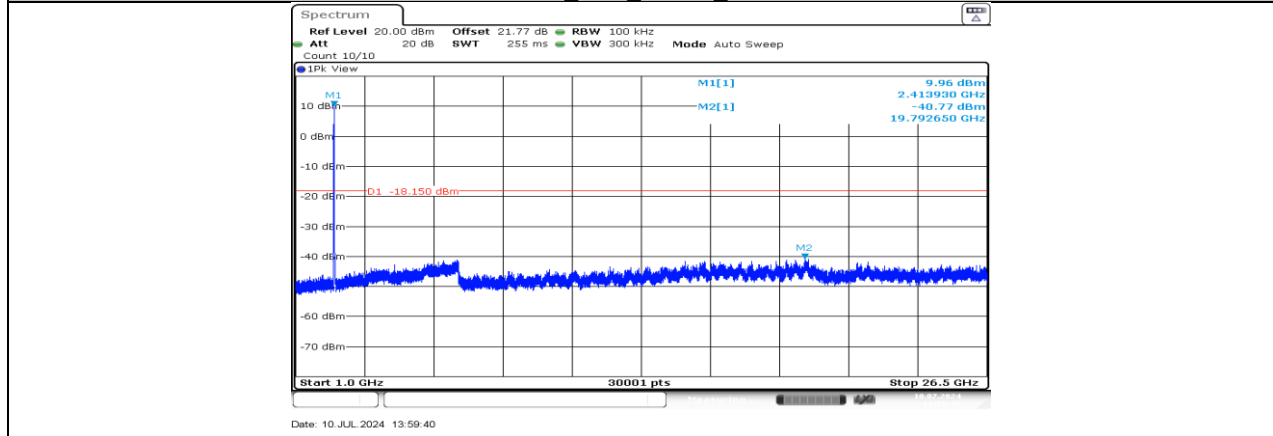
SRD 20M_Ant4_2412.5_1000~26500



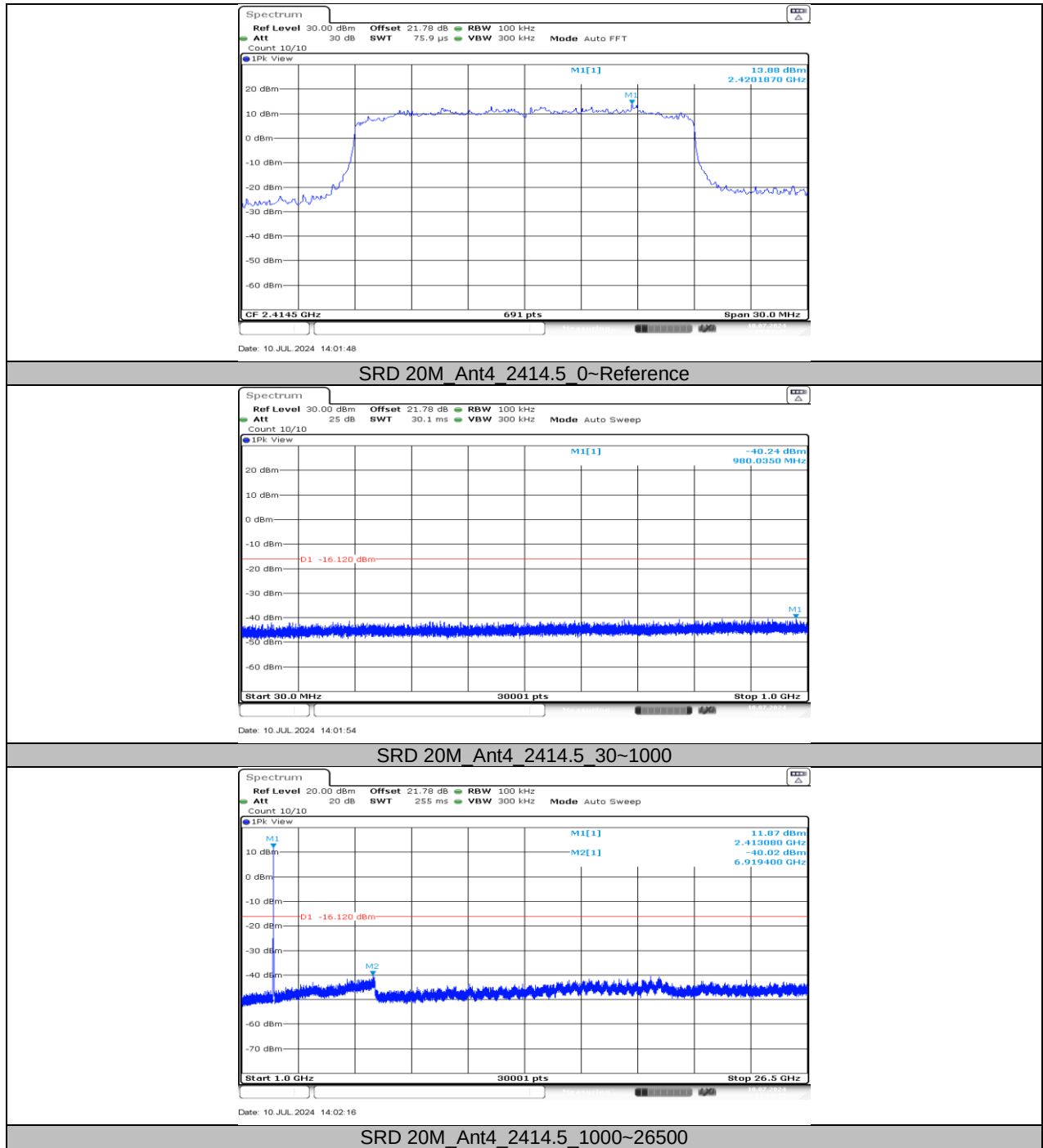
SRD 20M_Ant5_2412.5_0~Reference

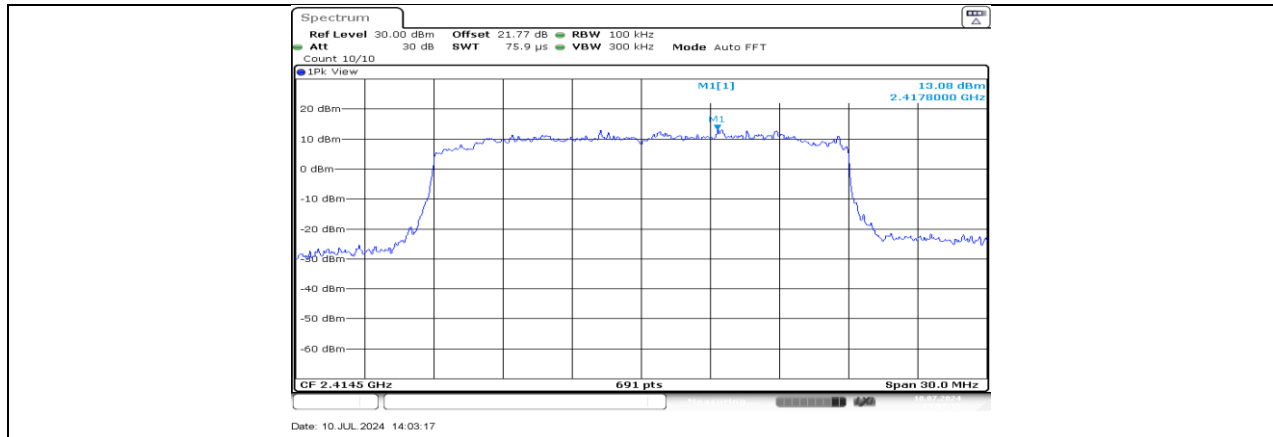


SRD 20M_Ant5_2412.5_30~1000

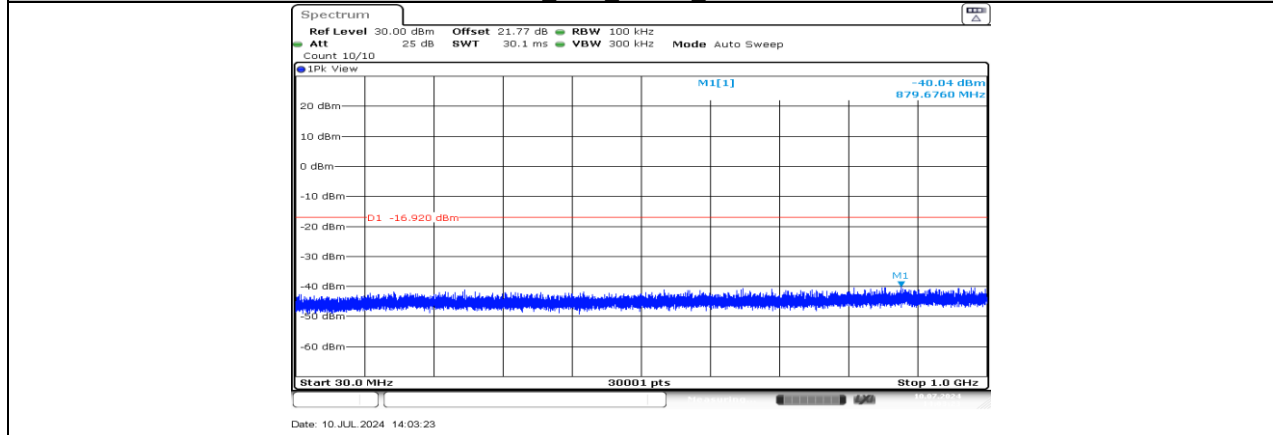


SRD 20M_Ant5_2412.5_1000~26500

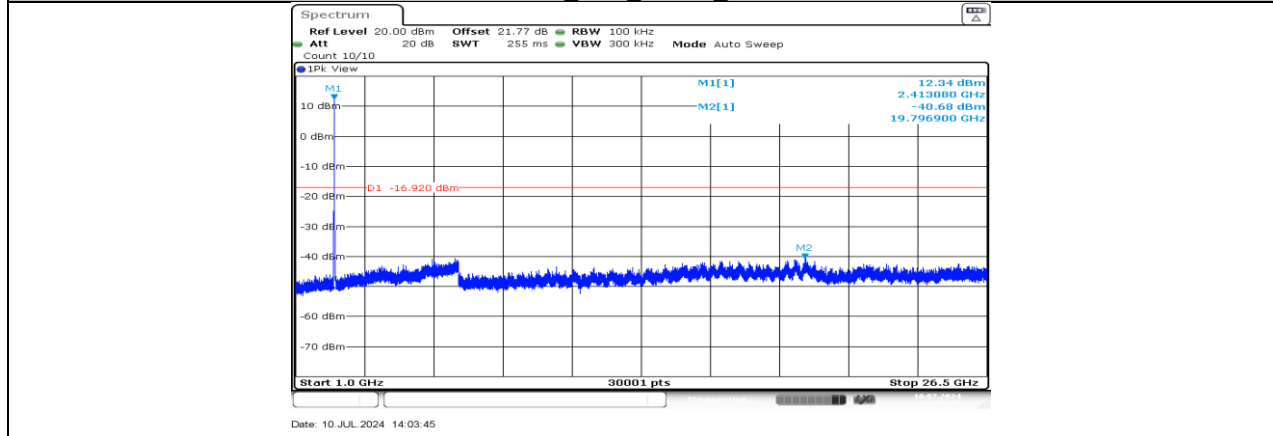




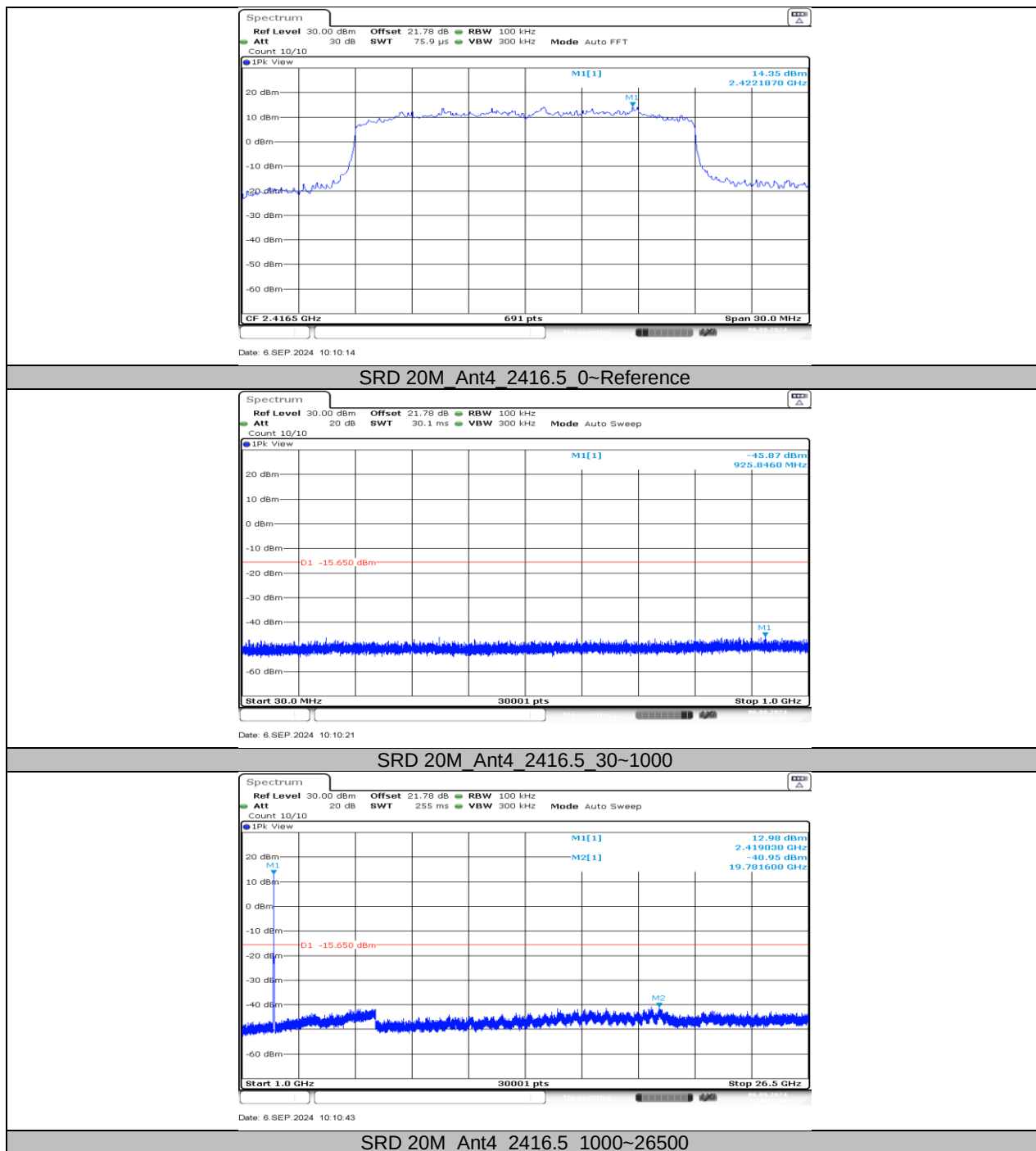
SRD 20M_Ant5_2414.5_0~Reference

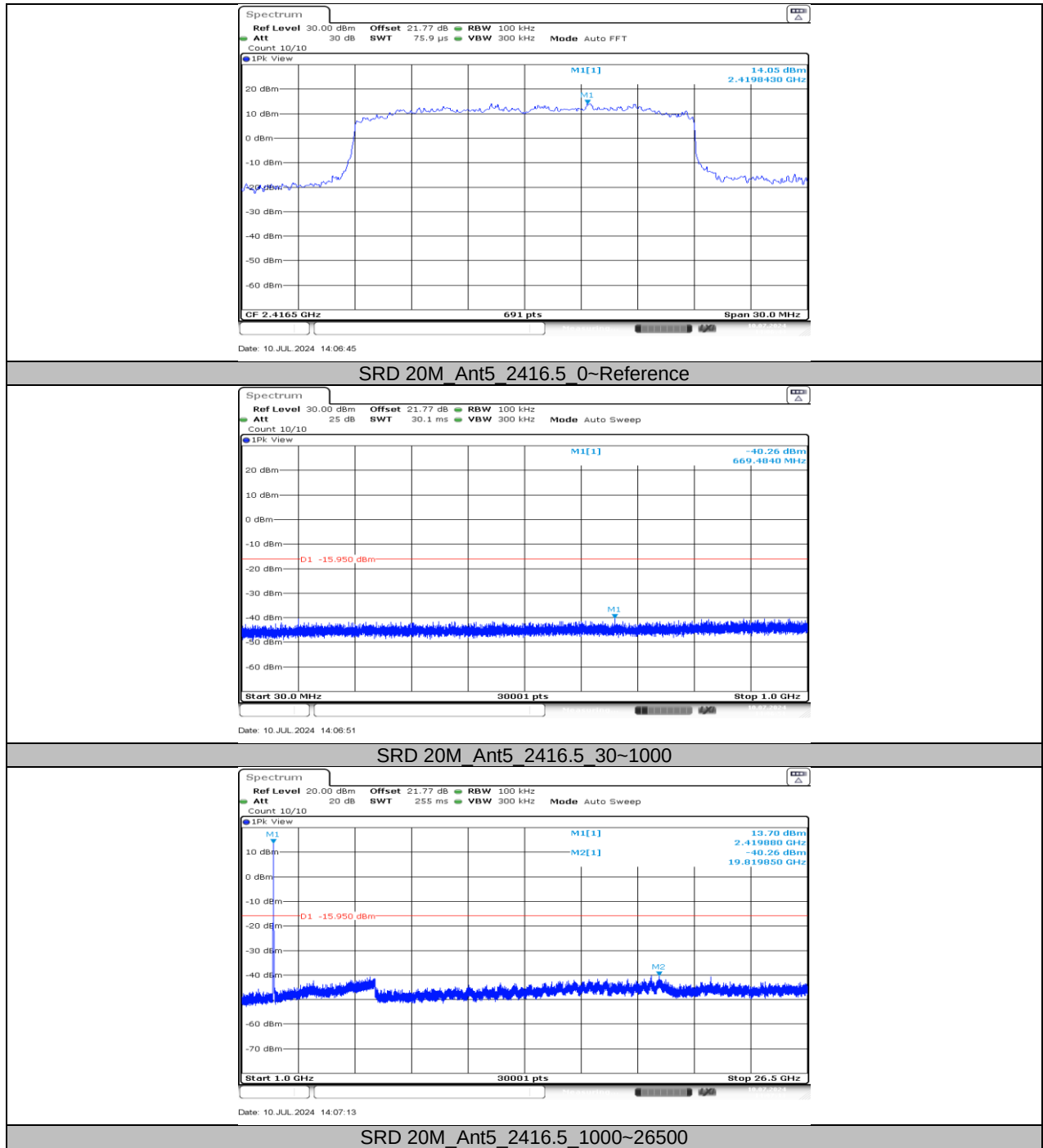


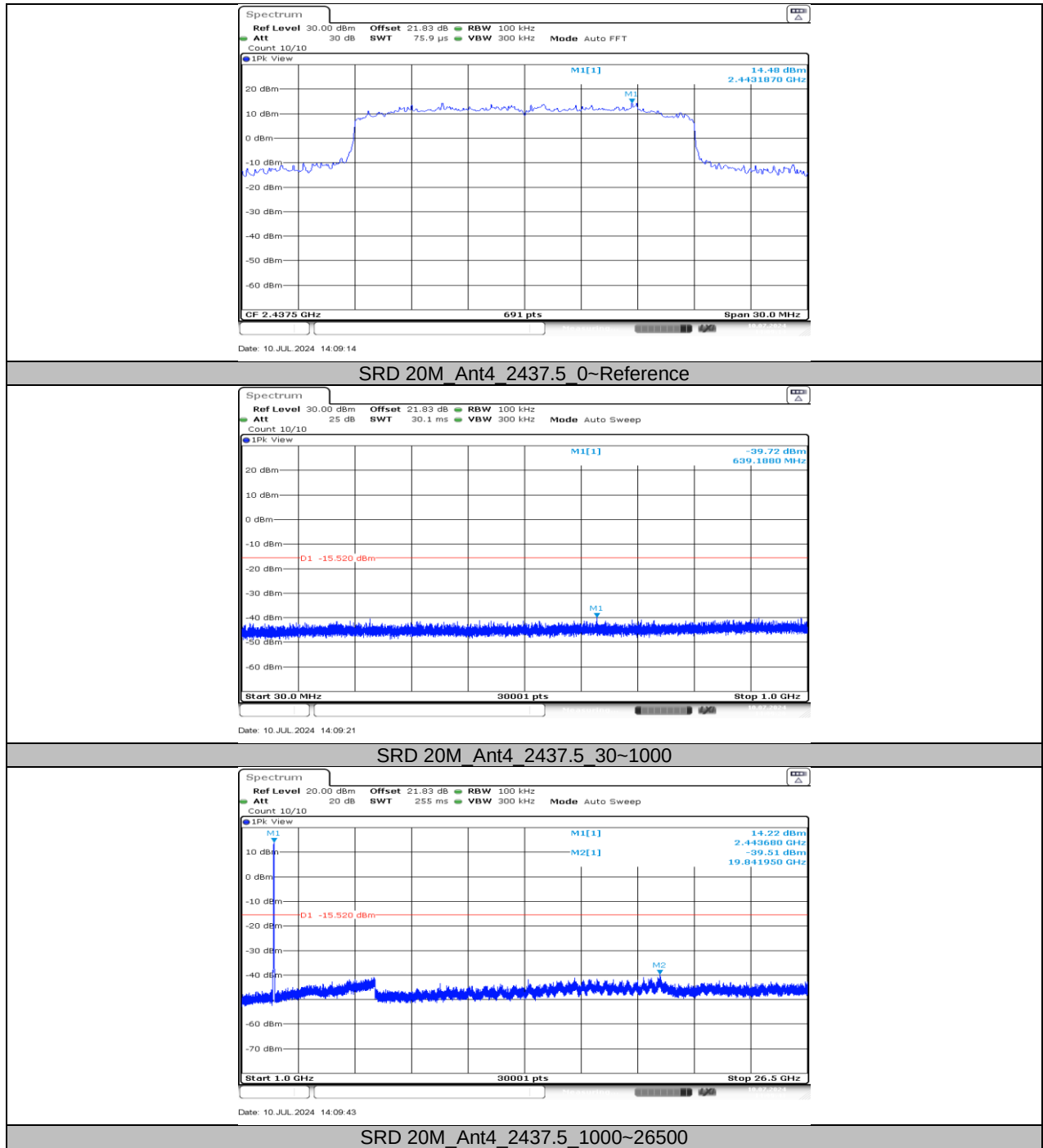
SRD 20M_Ant5_2414.5_1000~26500

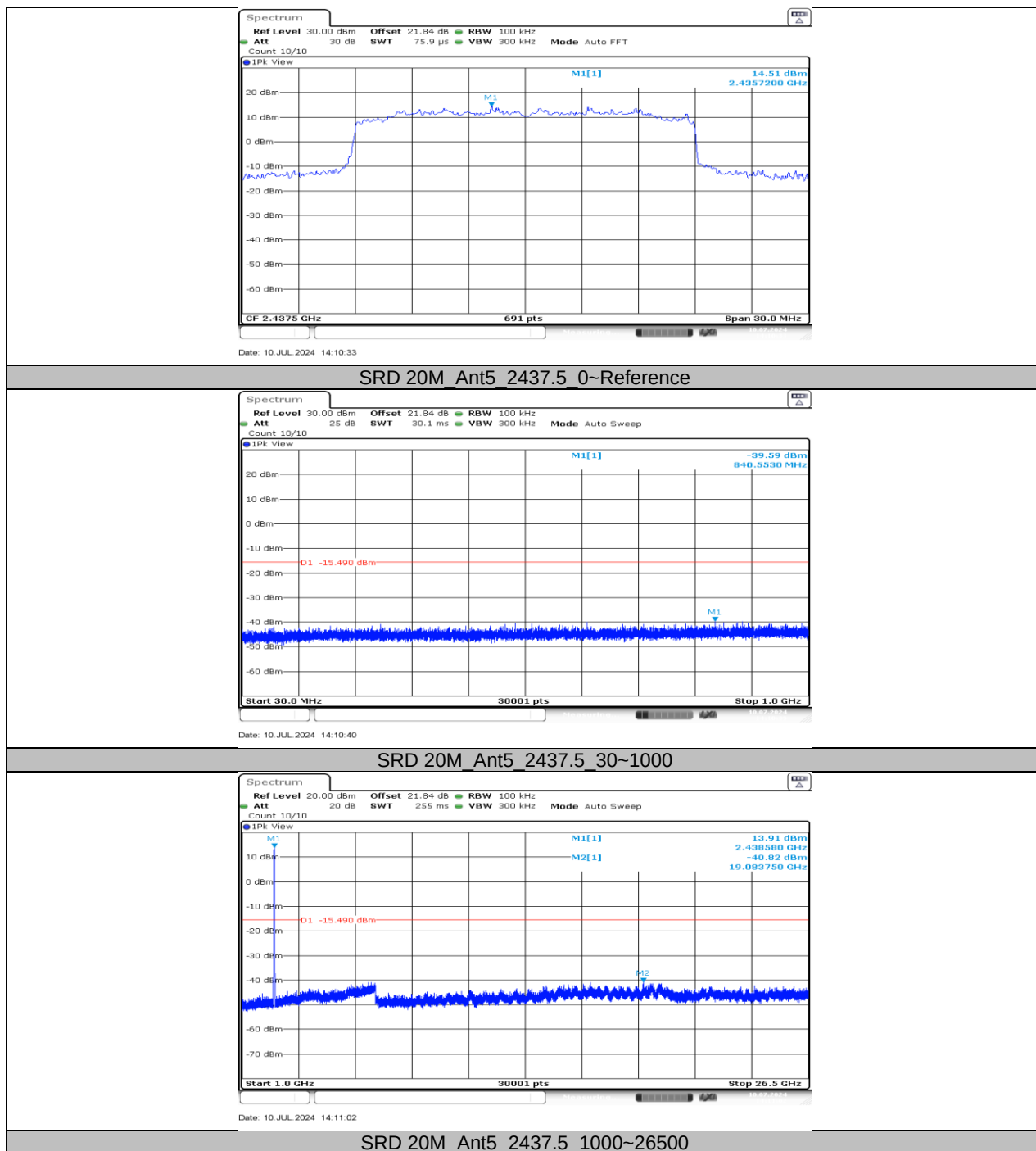


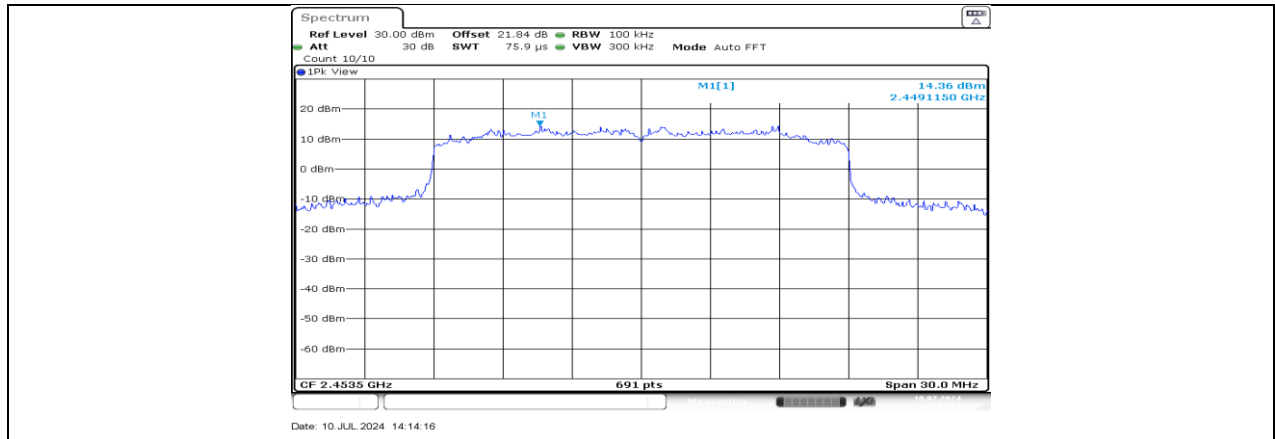
SRD 20M_Ant5_2414.5_1000~26500



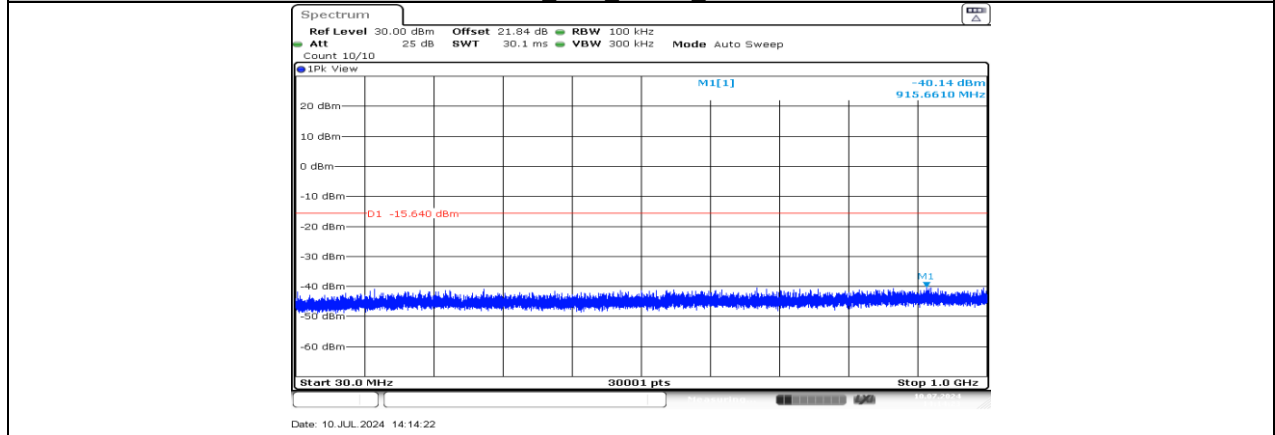




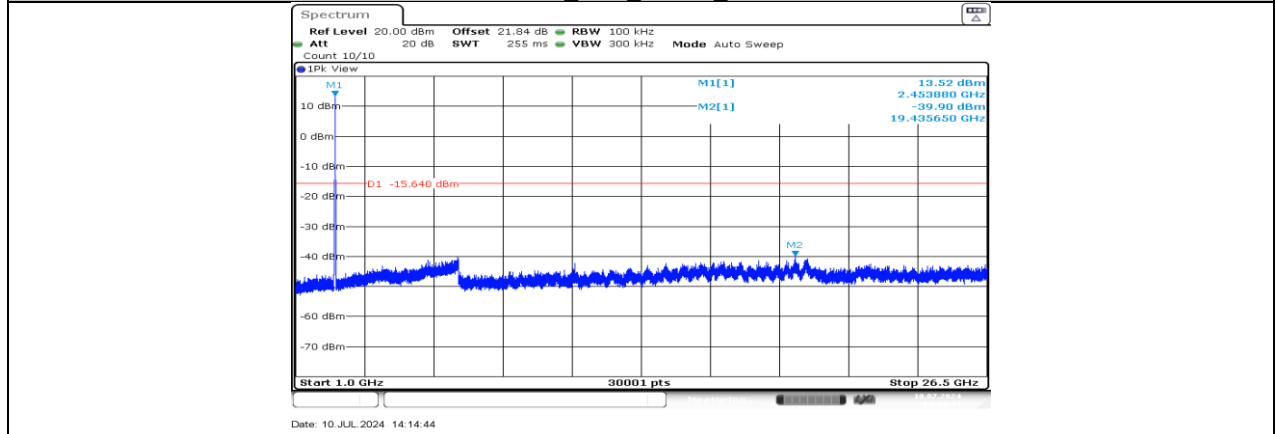




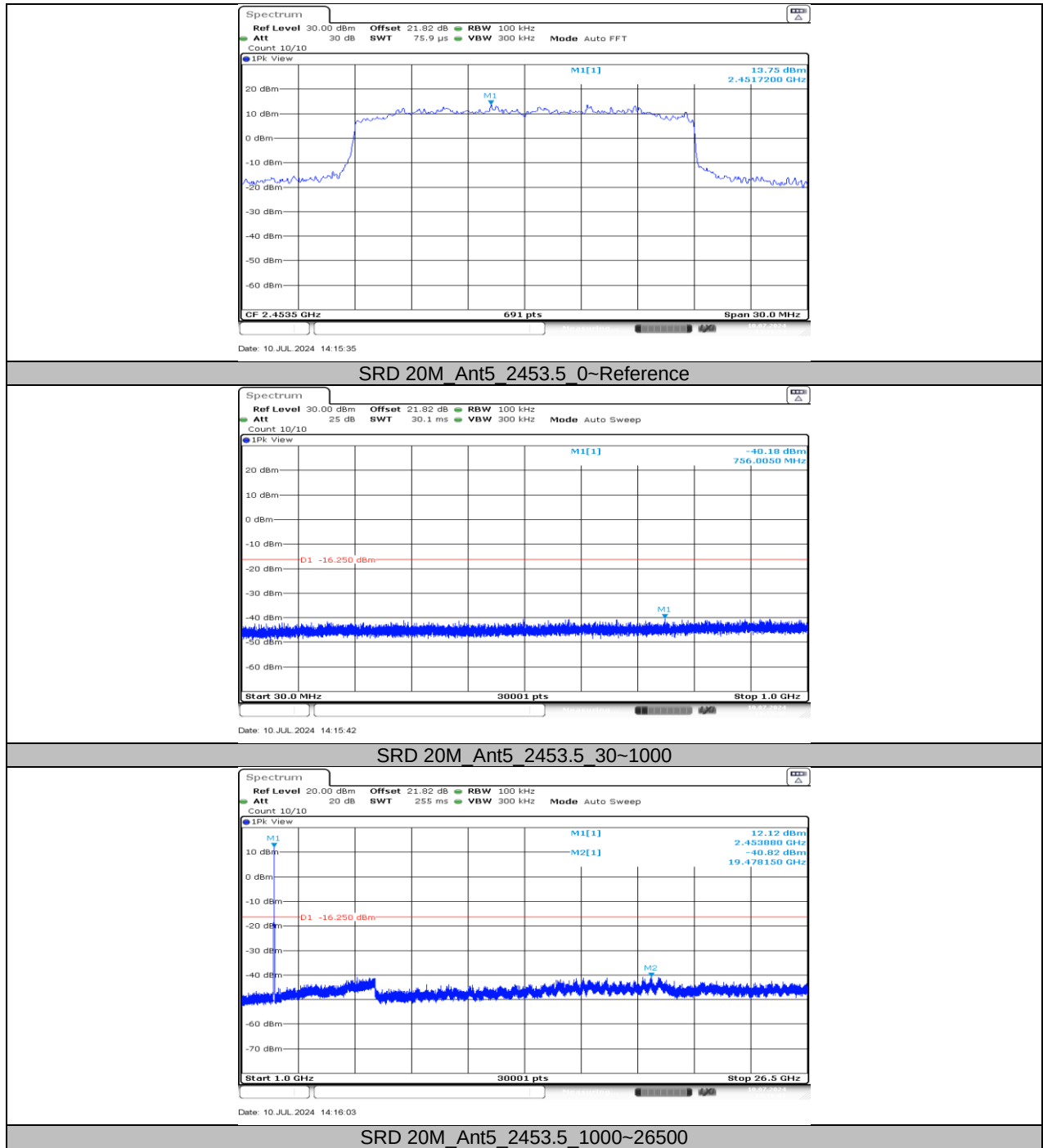
SRD 20M_Ant4_2453.5_0~Reference

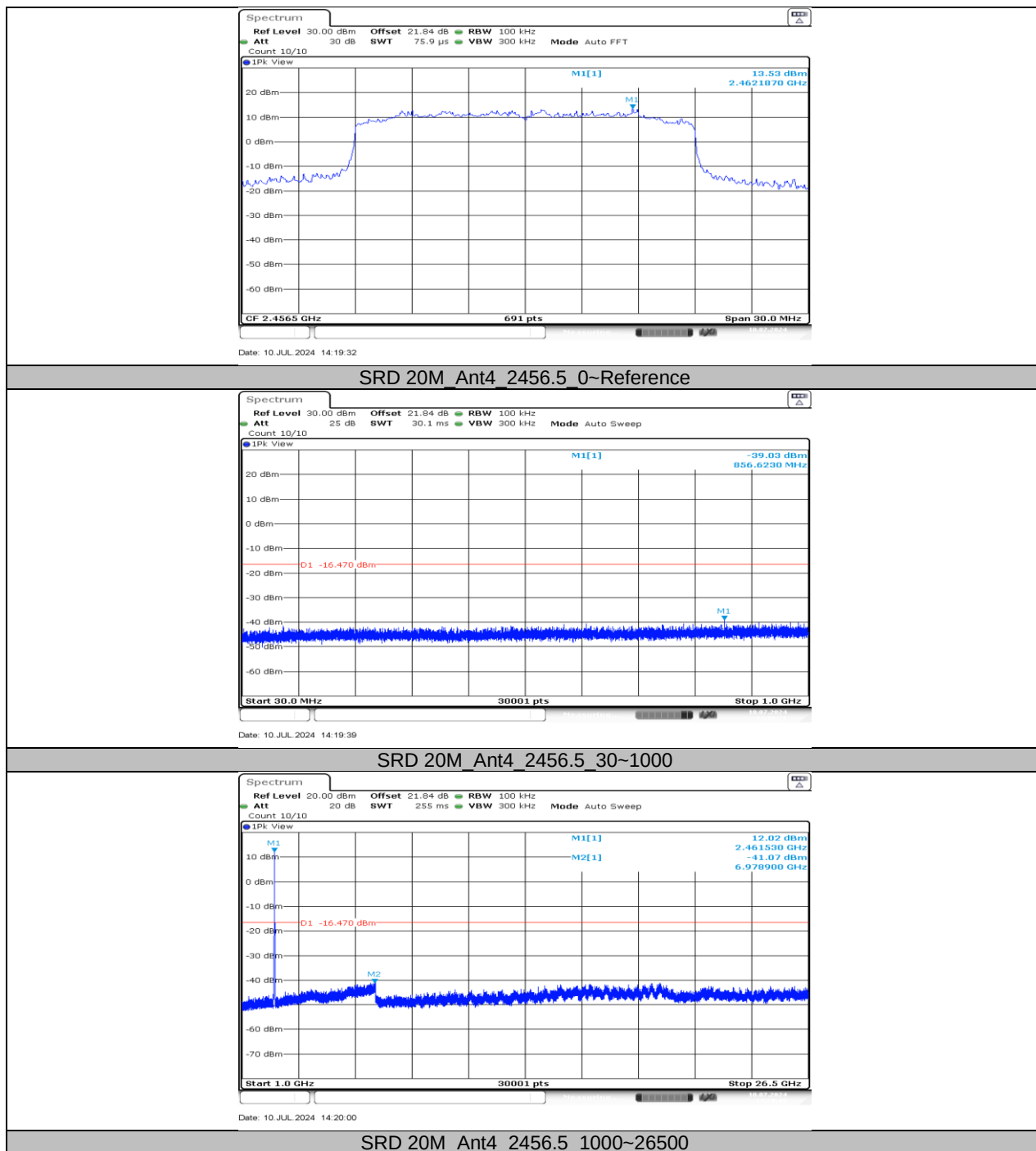


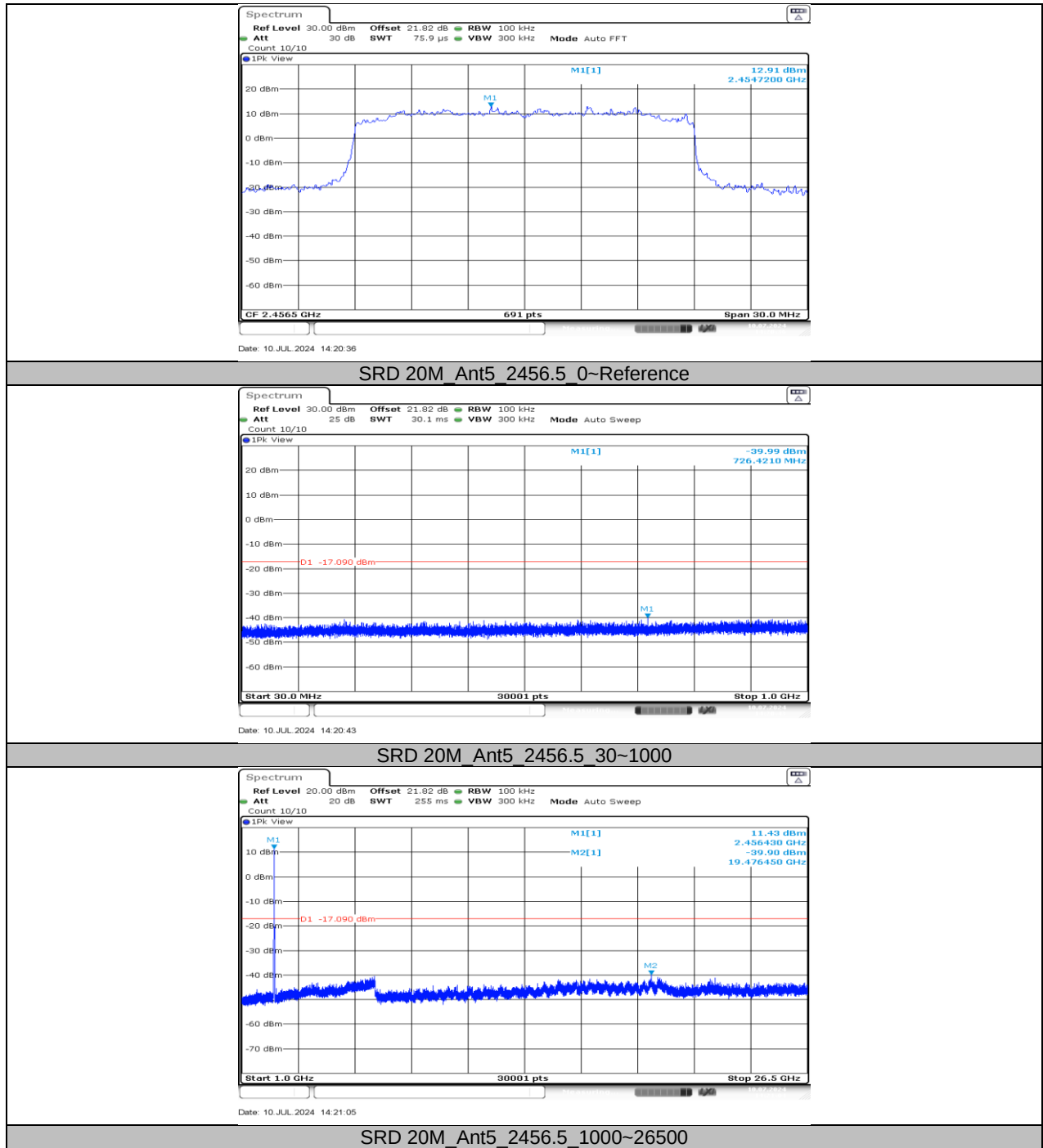
SRD 20M_Ant4_2453.5_30~1000

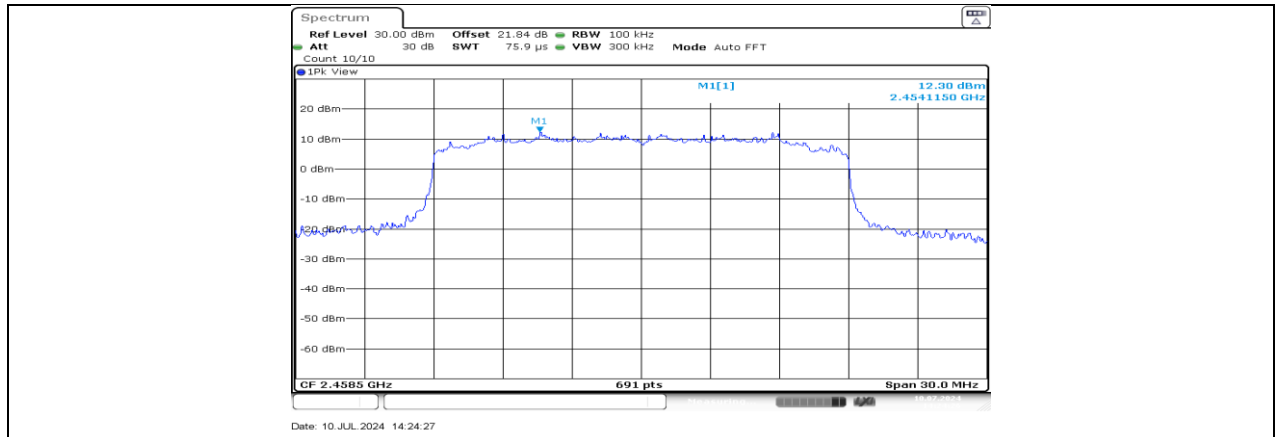


SRD 20M_Ant4_2453.5_1000~26500

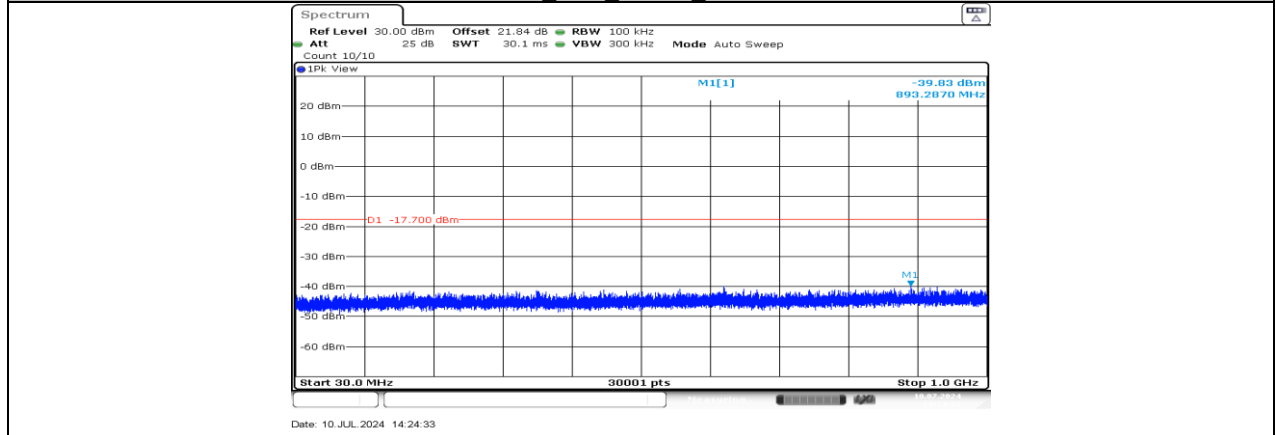




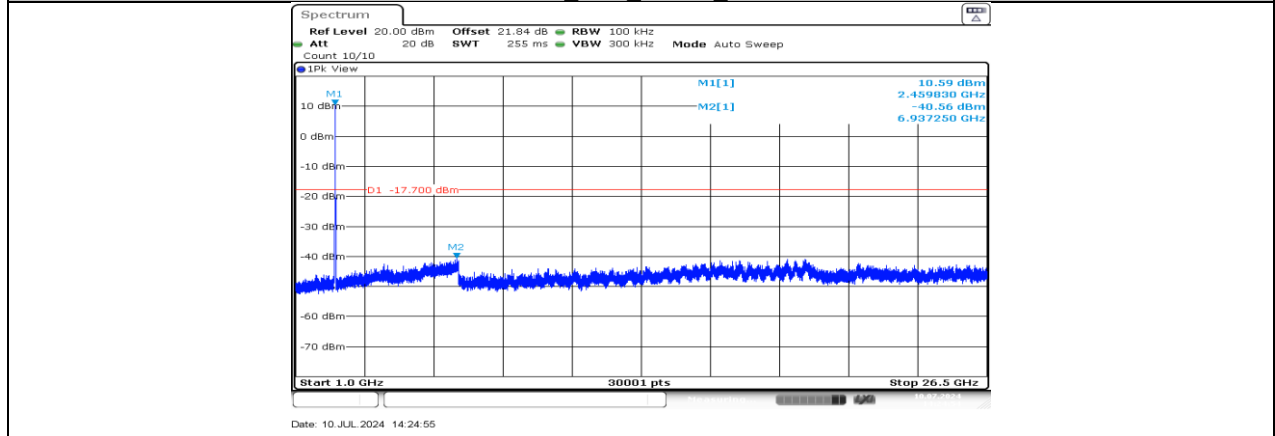




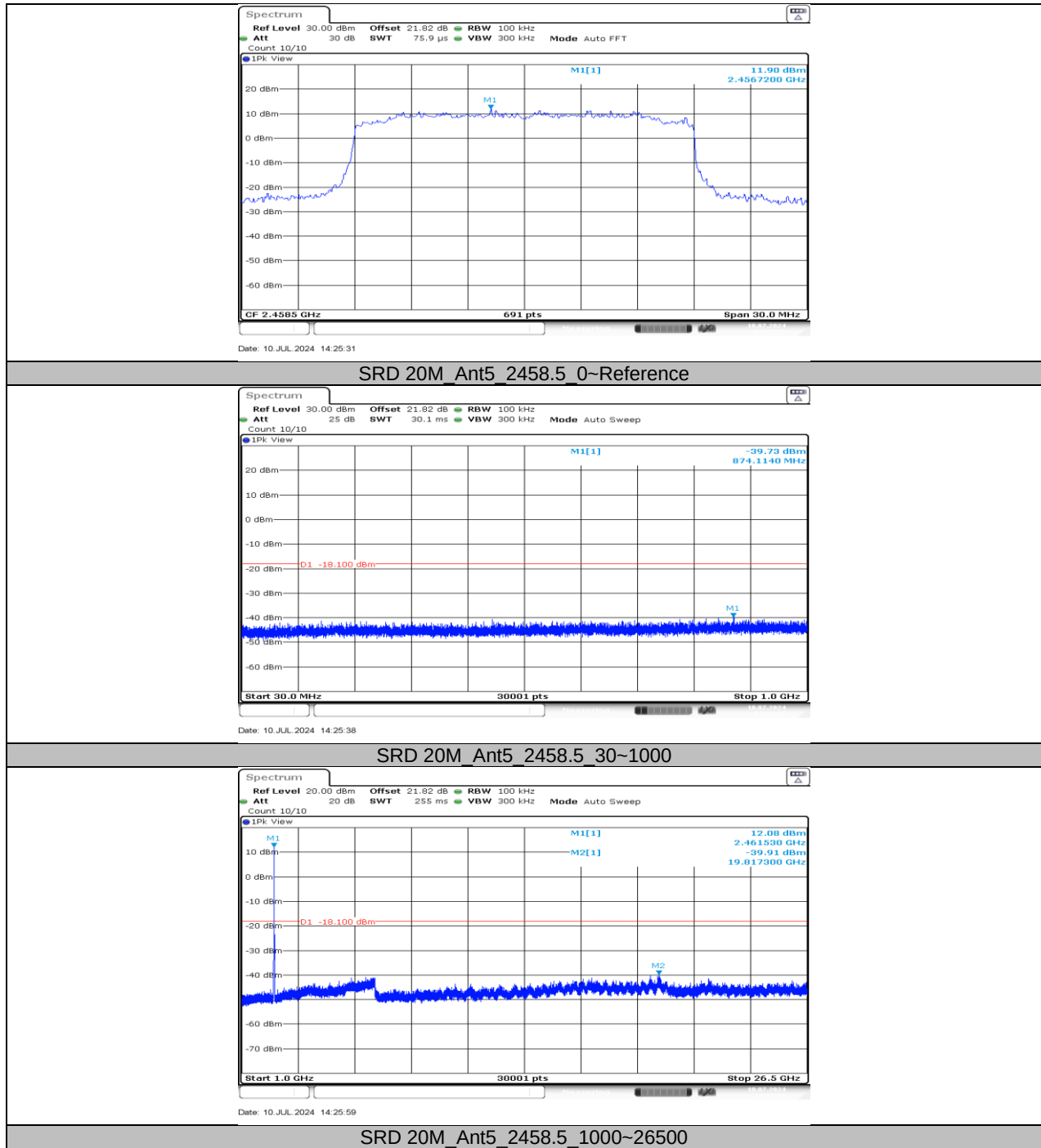
SRD 20M_Ant4_2458.5_0~Reference

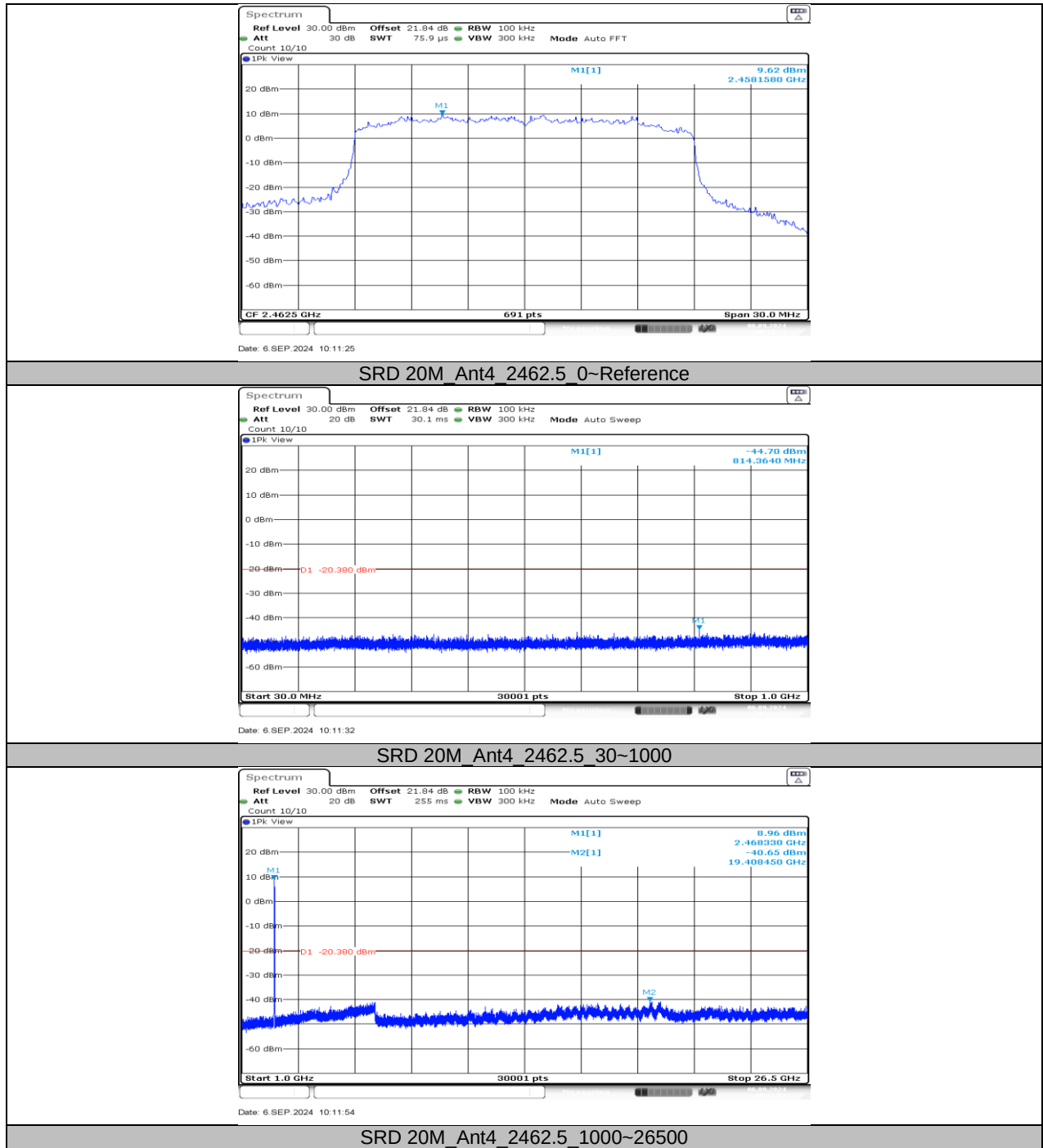


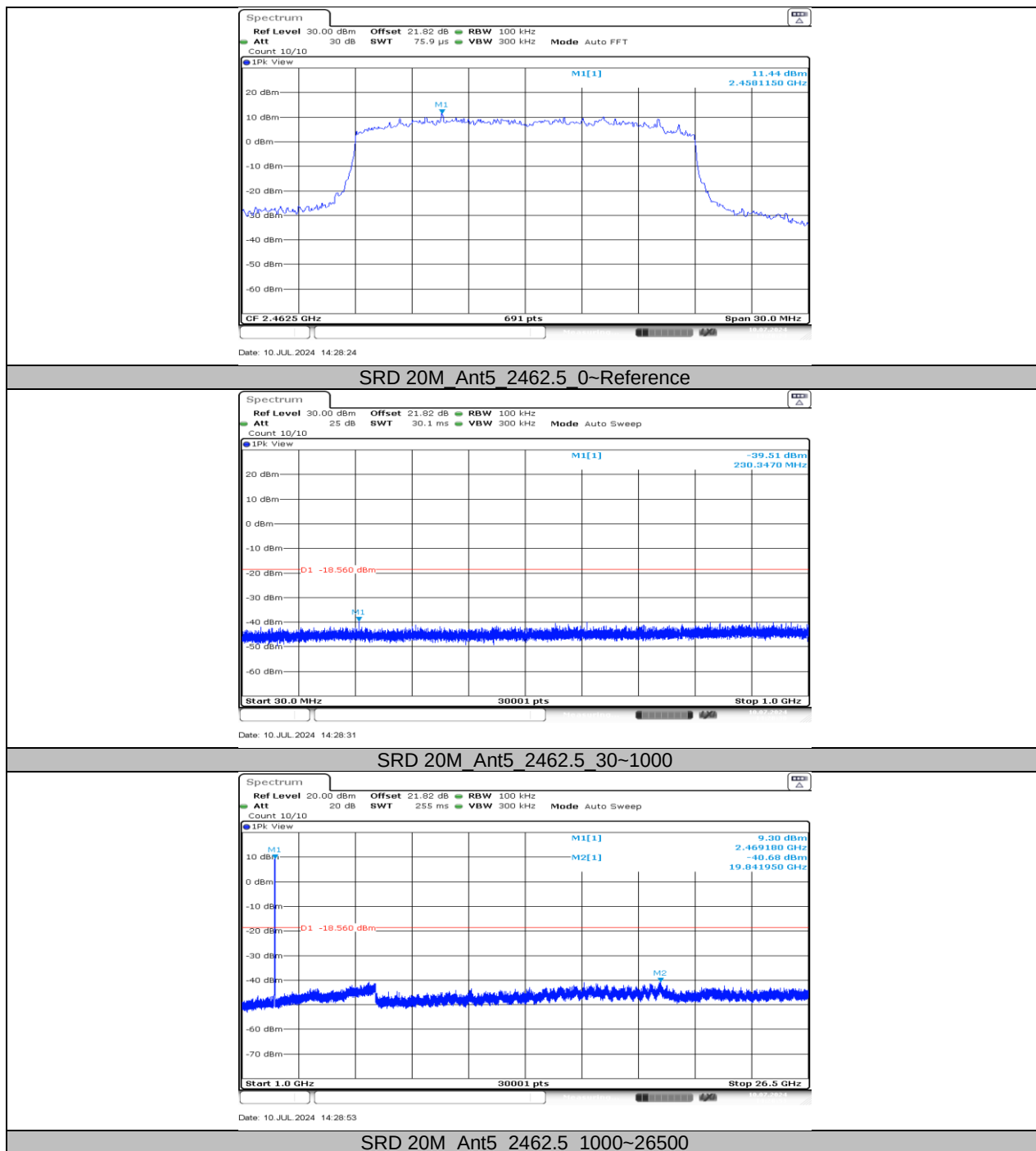
SRD 20M_Ant4_2458.5_30~1000

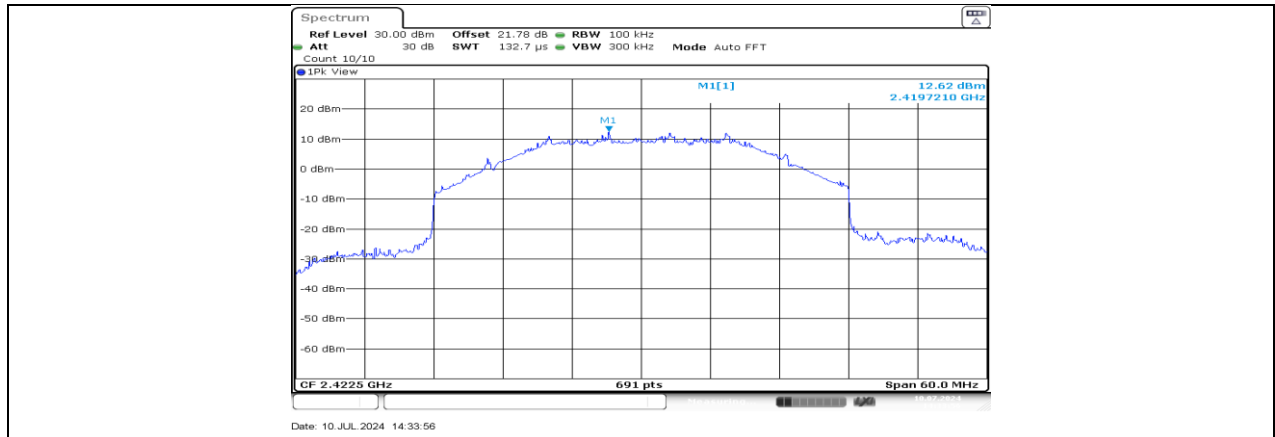


SRD 20M_Ant4_2458.5_1000~26500

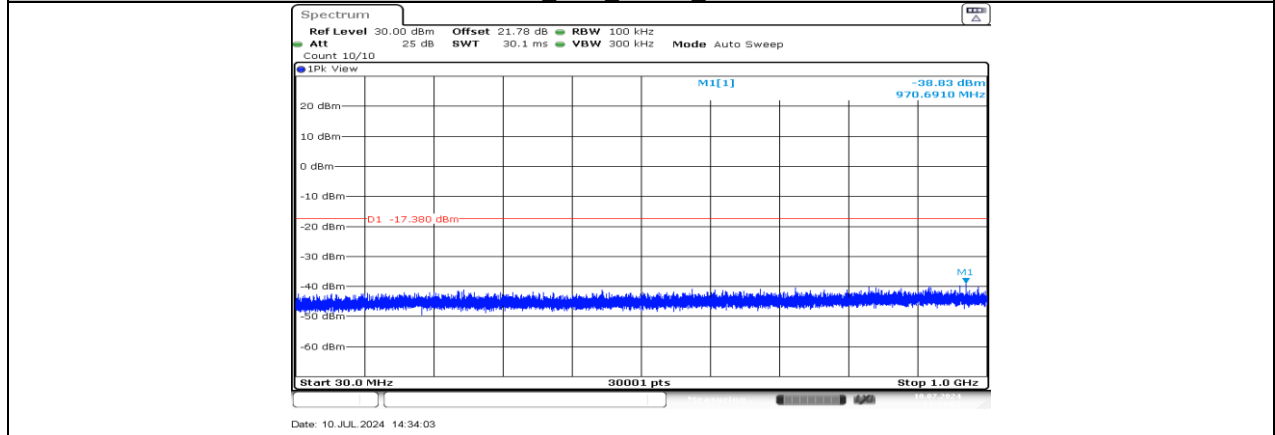




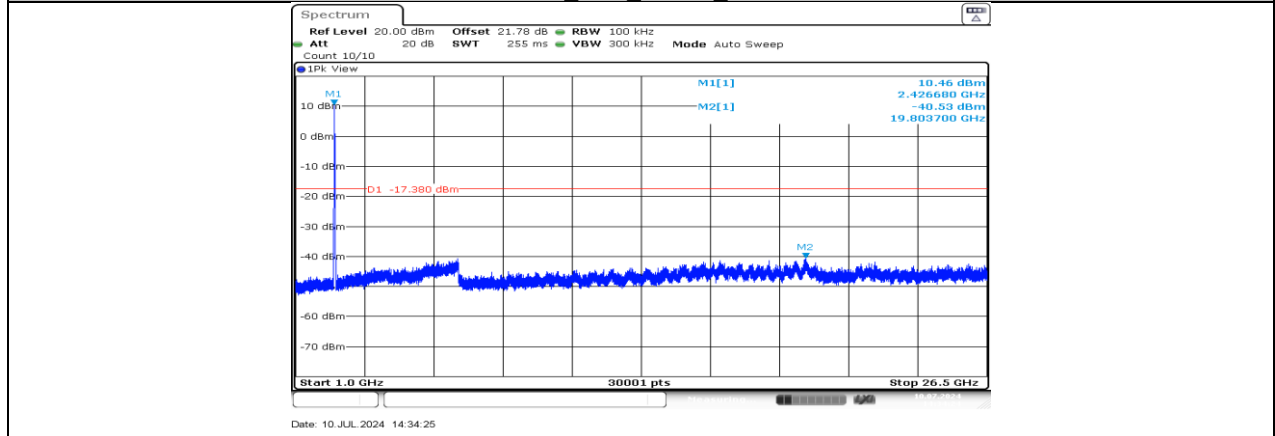




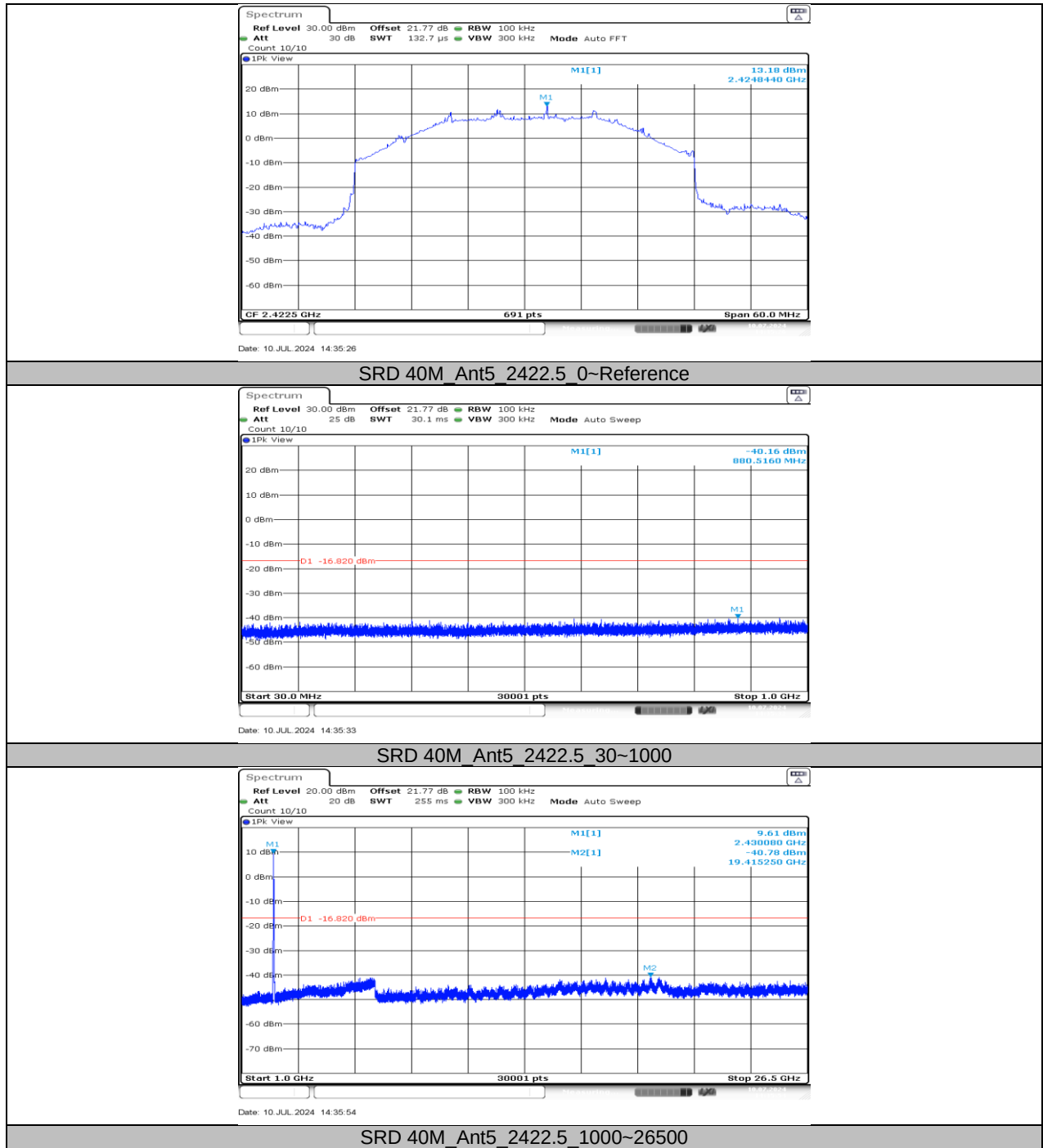
SRD 40M_Ant4_2422.5_0~Reference

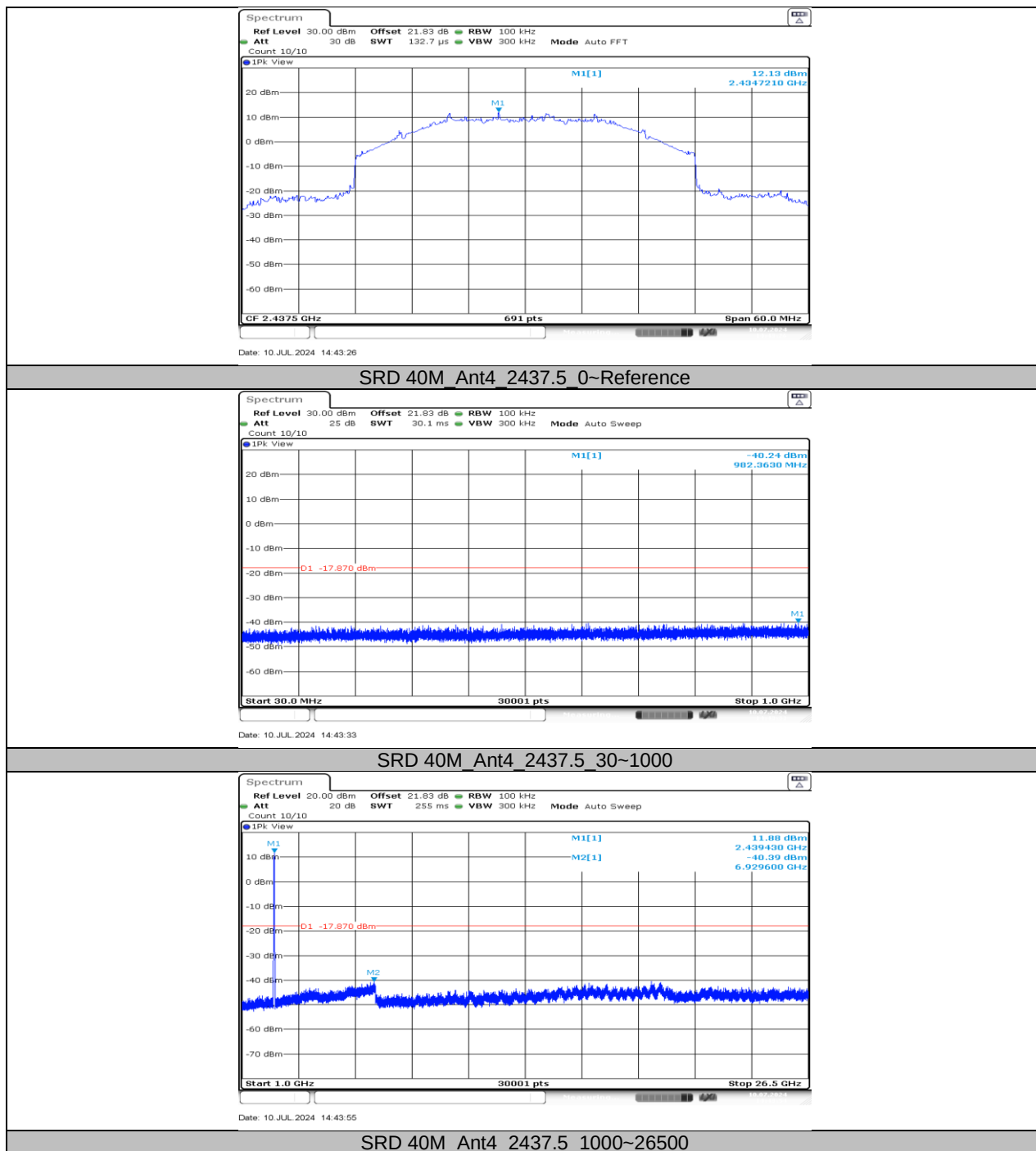


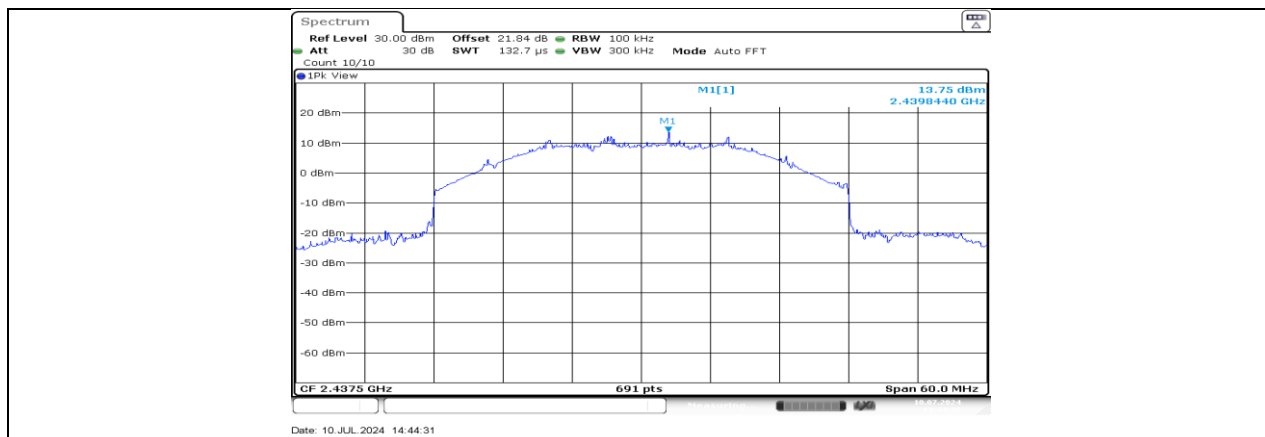
SRD 40M_Ant4_2422.5_30~1000



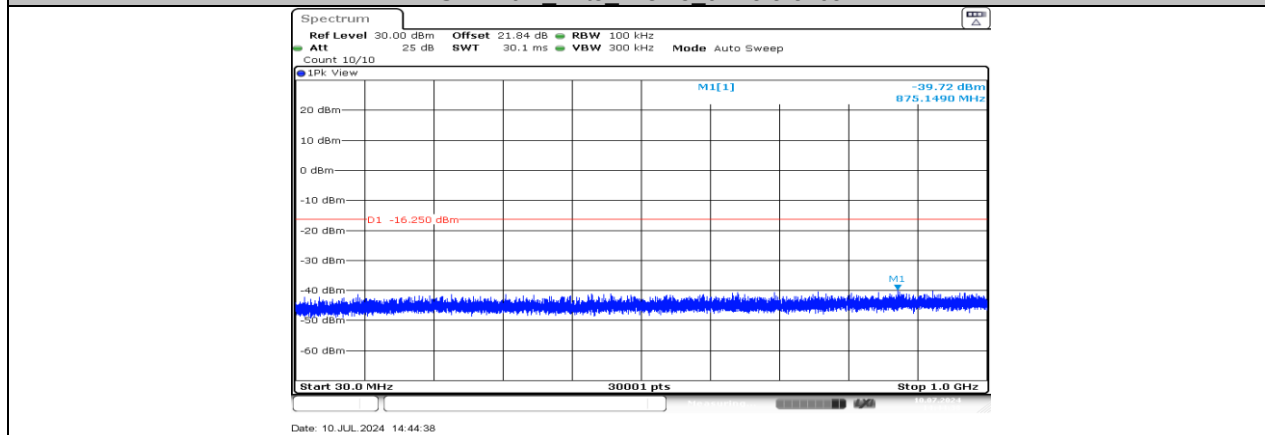
SRD 40M_Ant4_2422.5_1000~26500



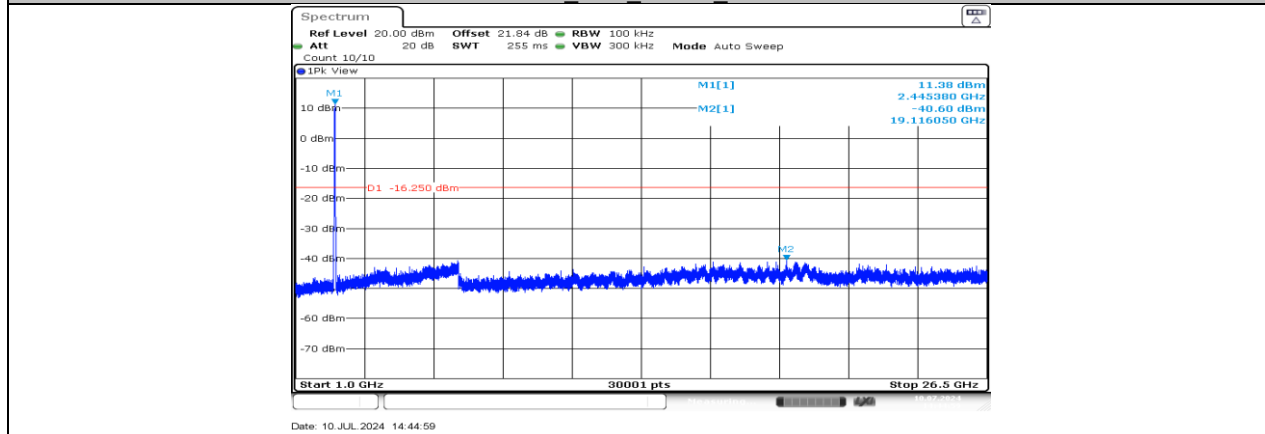




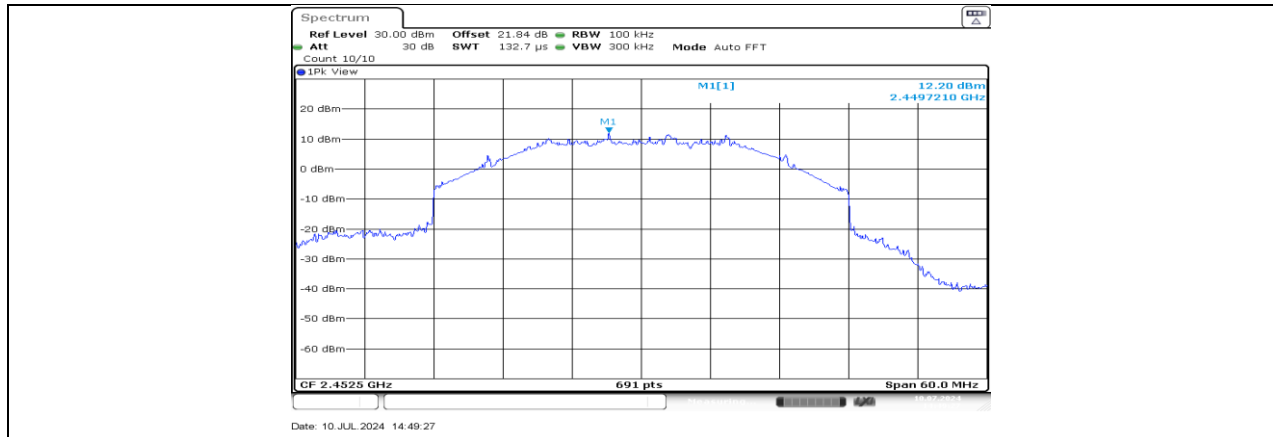
SRD 40M_Ant5_2437.5_0~Reference



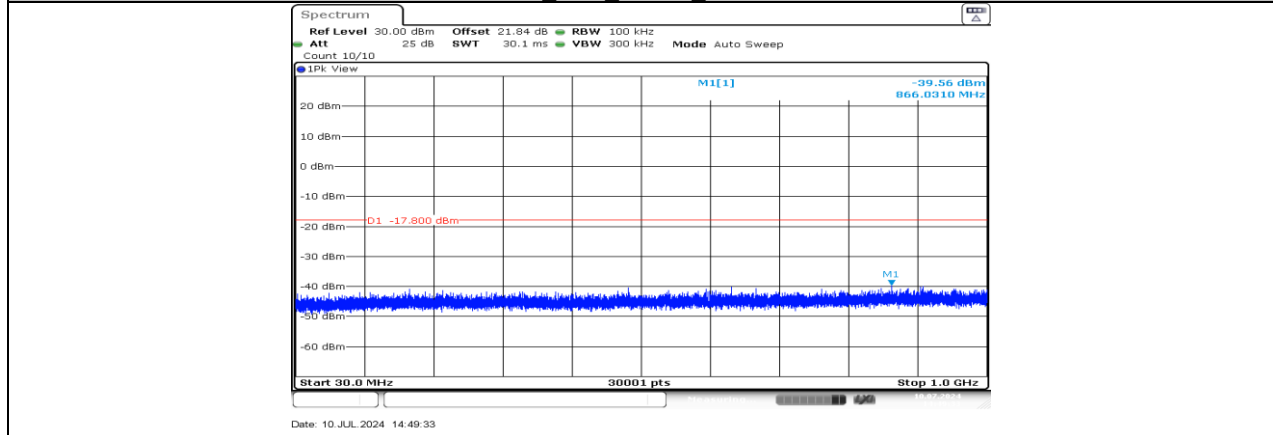
SRD 40M_Ant5_2437.5_30~1000



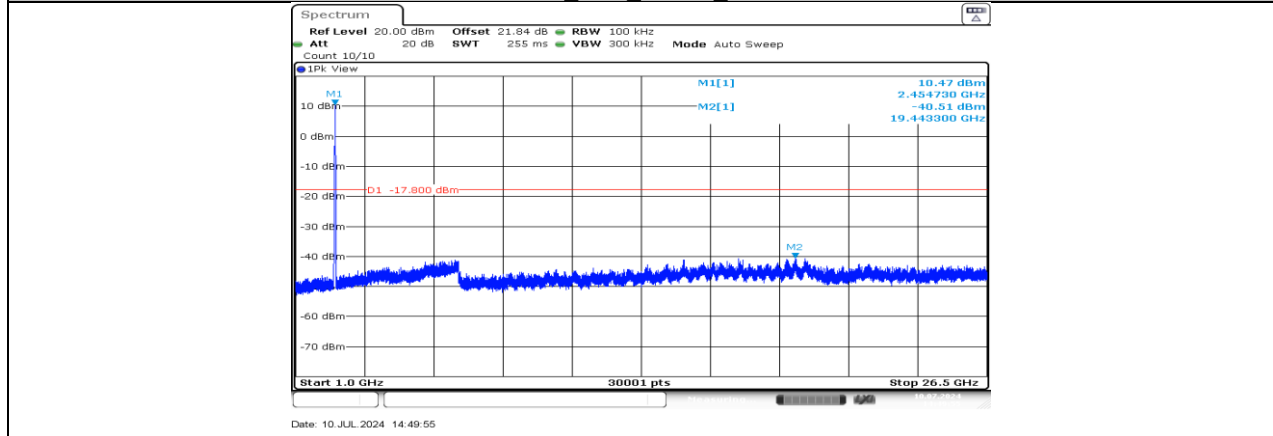
SRD 40M_Ant5_2437.5_1000~26500



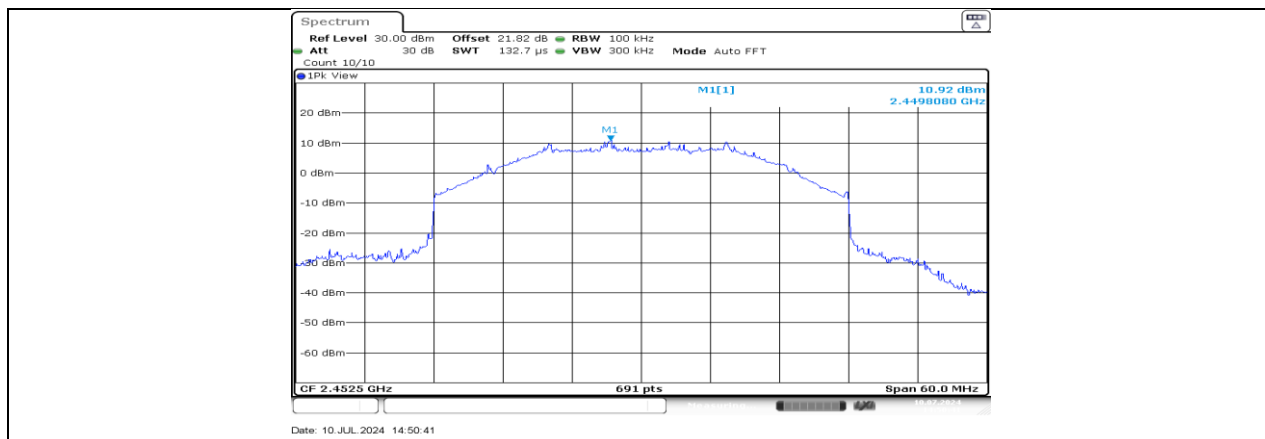
SRD 40M_Ant4_2452.5_0~Reference



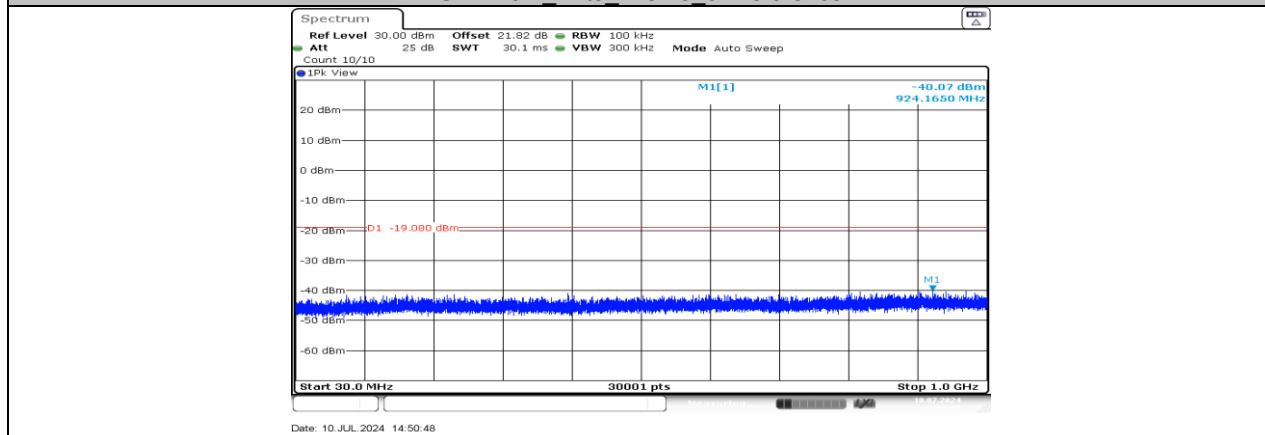
SRD 40M_Ant4_2452.5_30~1000



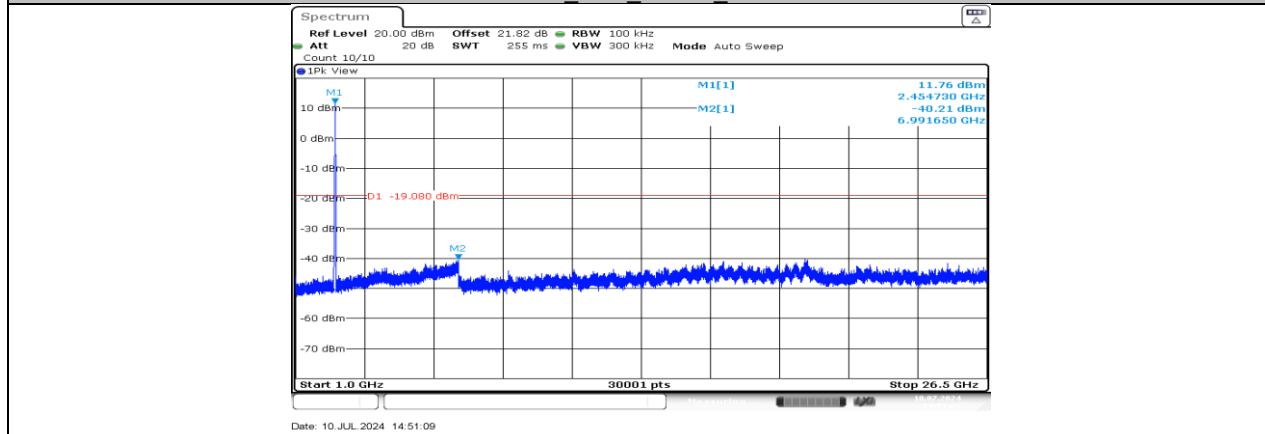
SRD 40M_Ant4_2452.5_1000~26500



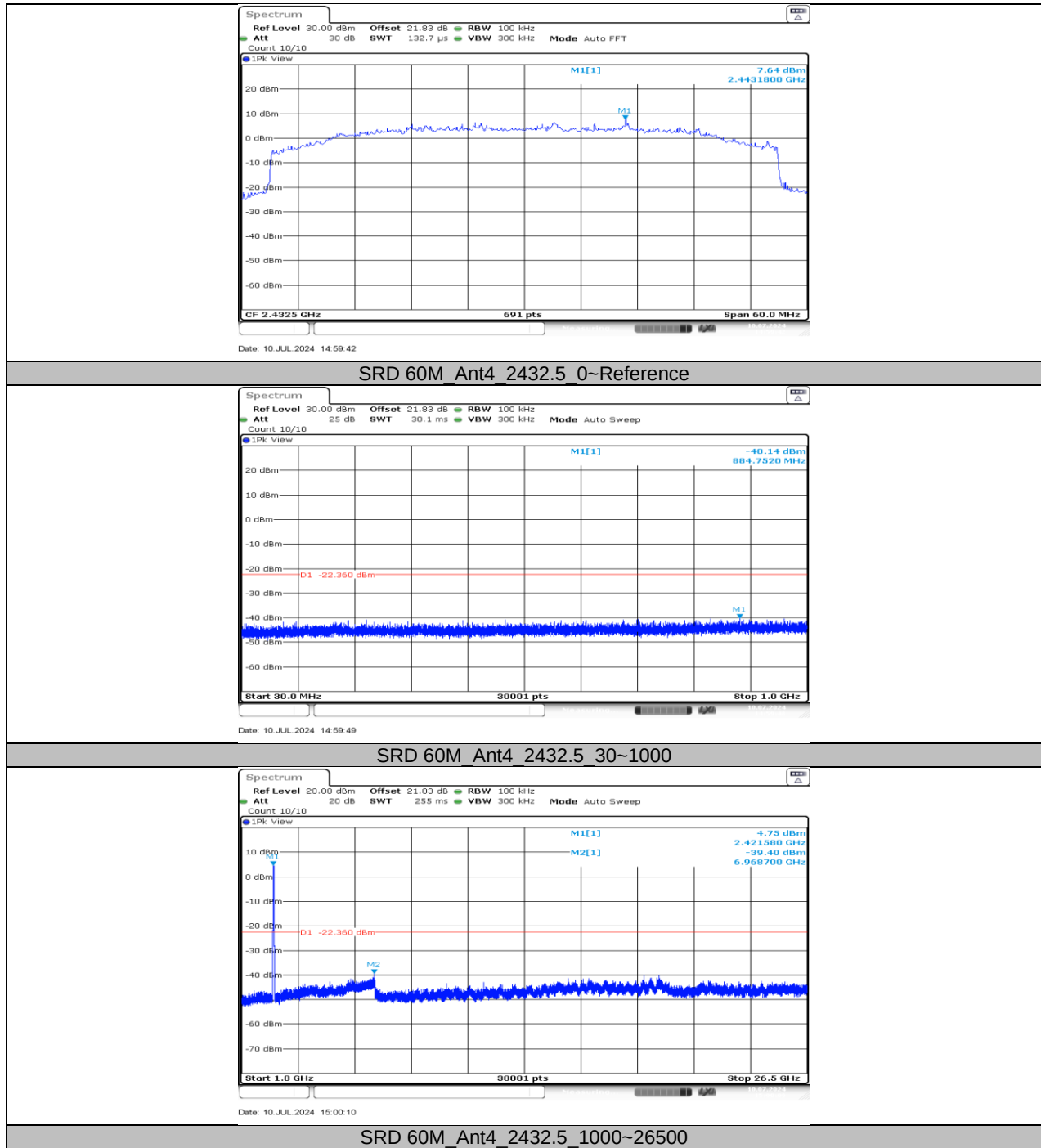
SRD 40M_Ant5_2452.5_0~Reference

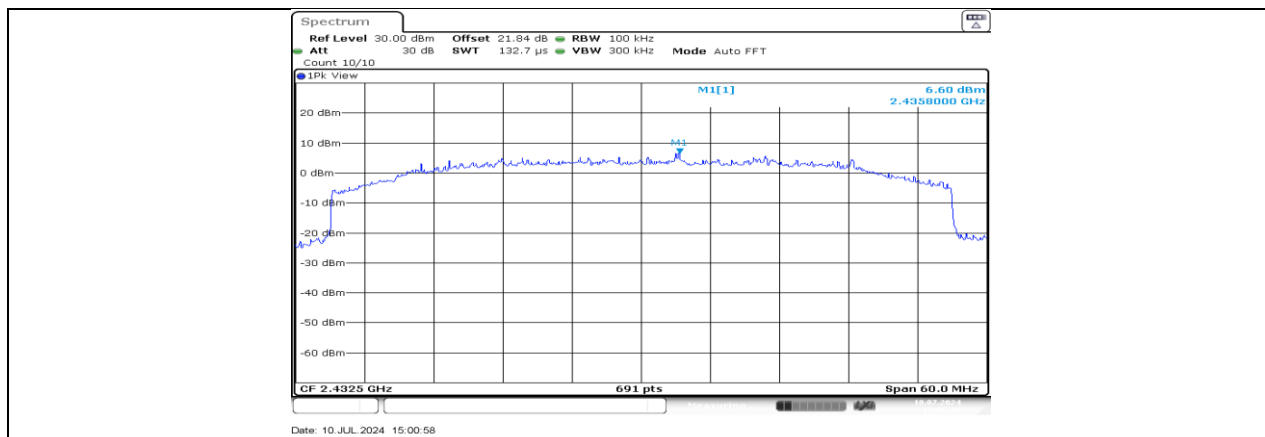


SRD 40M_Ant5_2452.5_30~1000

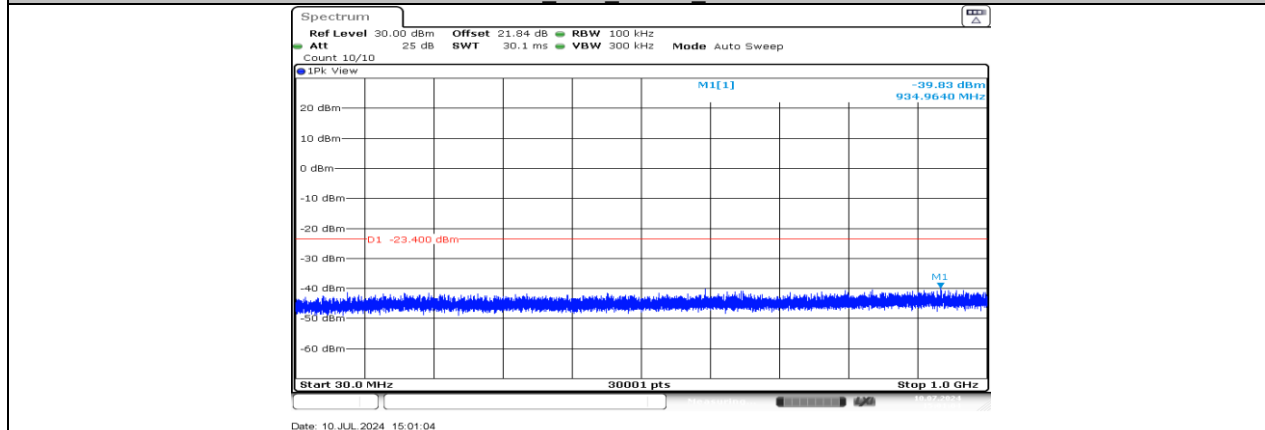


SRD 40M_Ant5_2452.5_1000~26500

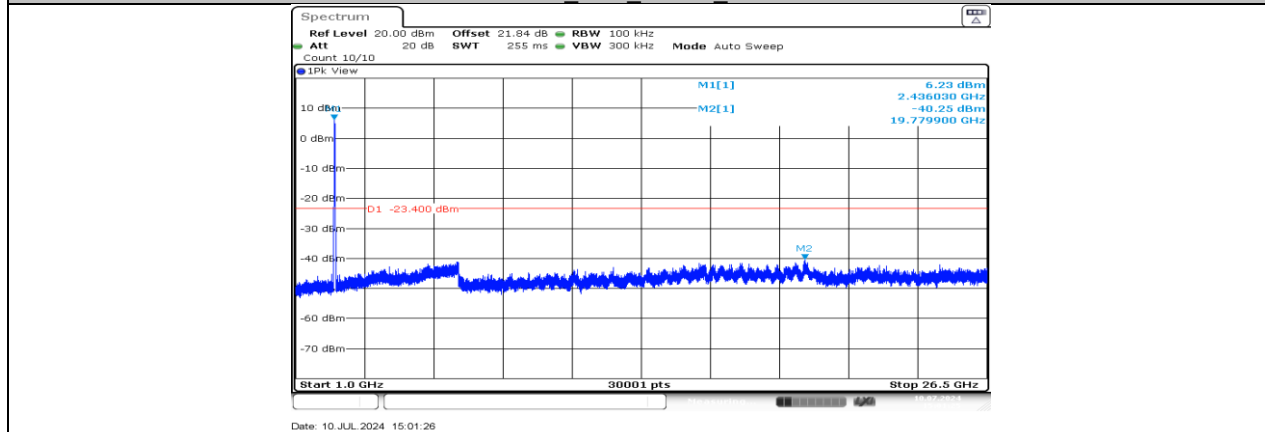




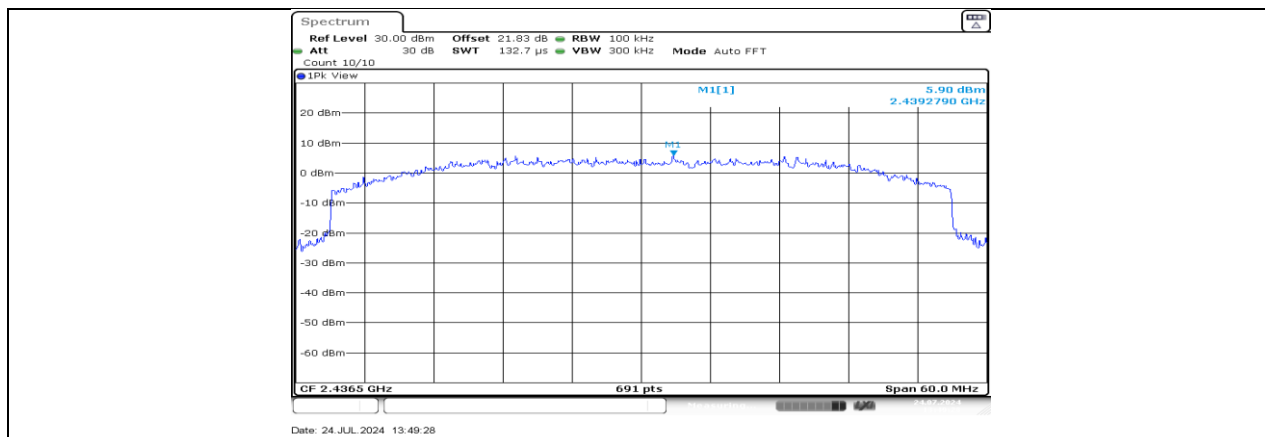
SRD 60M_Ant5_2432.5_0~Reference



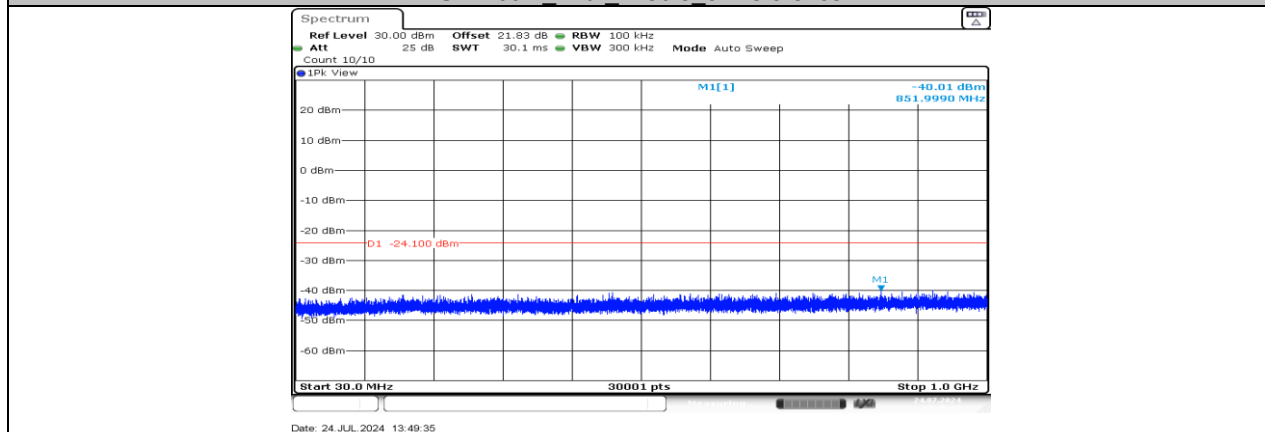
SRD 60M_Ant5_2432.5_30~1000



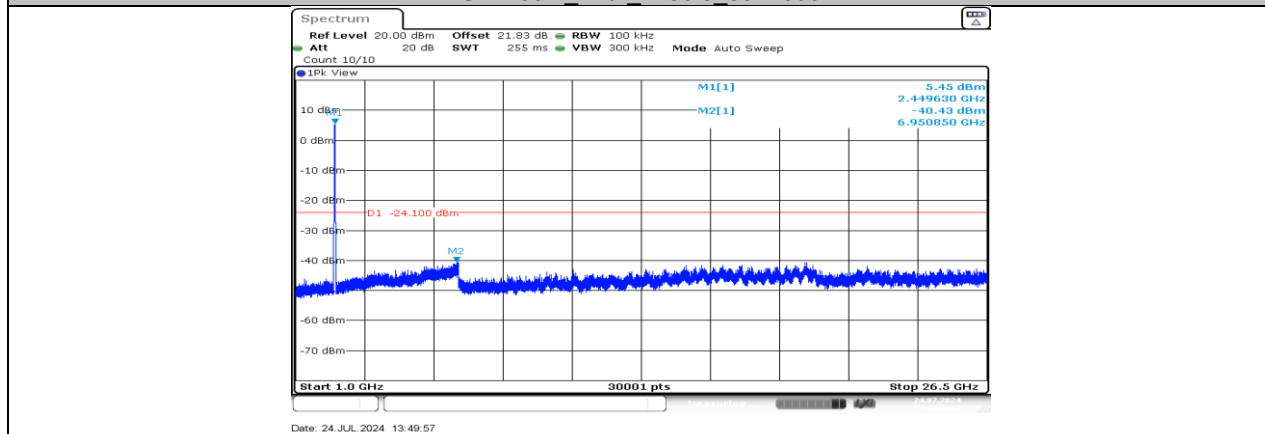
SRD 60M_Ant5_2432.5_1000~26500



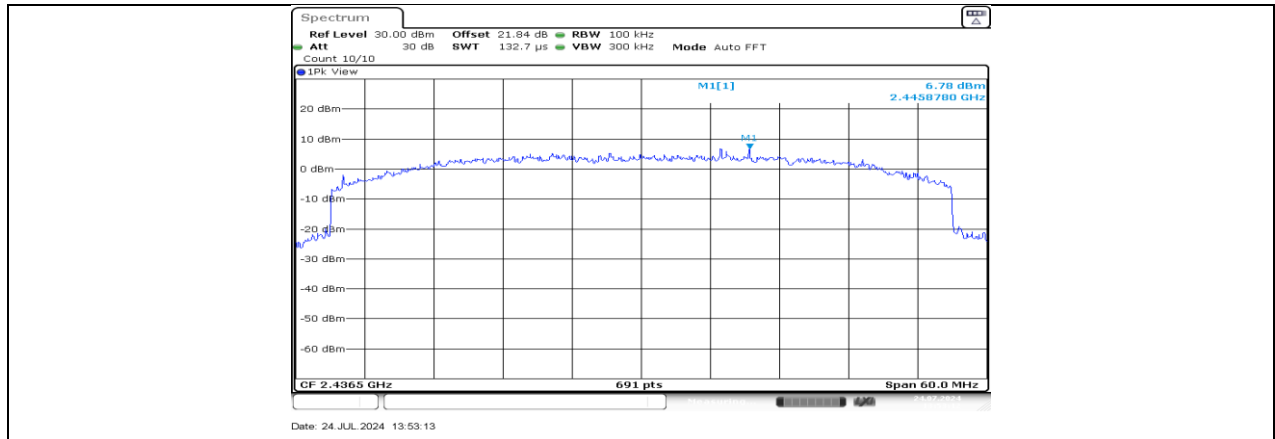
SRD 60M_Ant4_2436.5_0~Reference



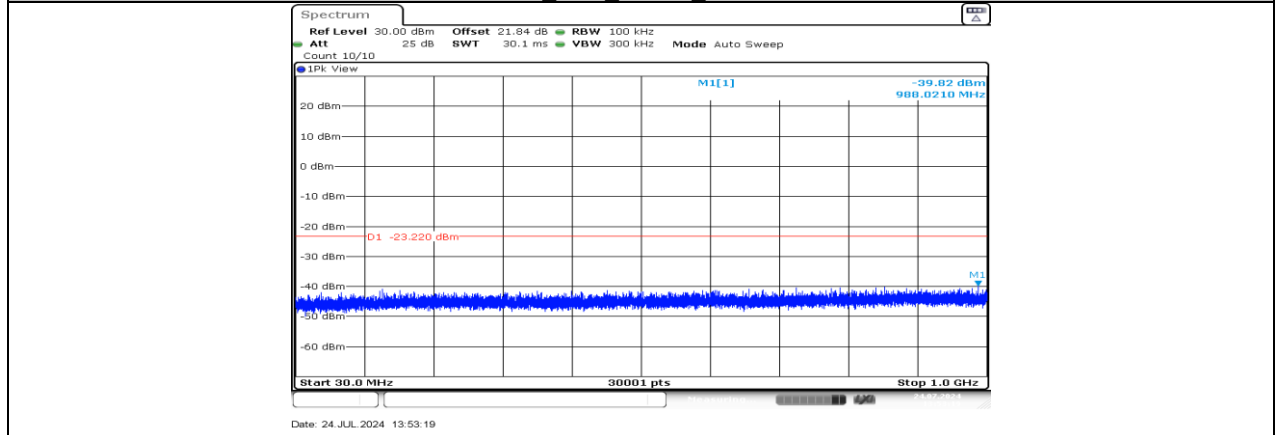
SRD 60M_Ant4_2436.5_30~1000



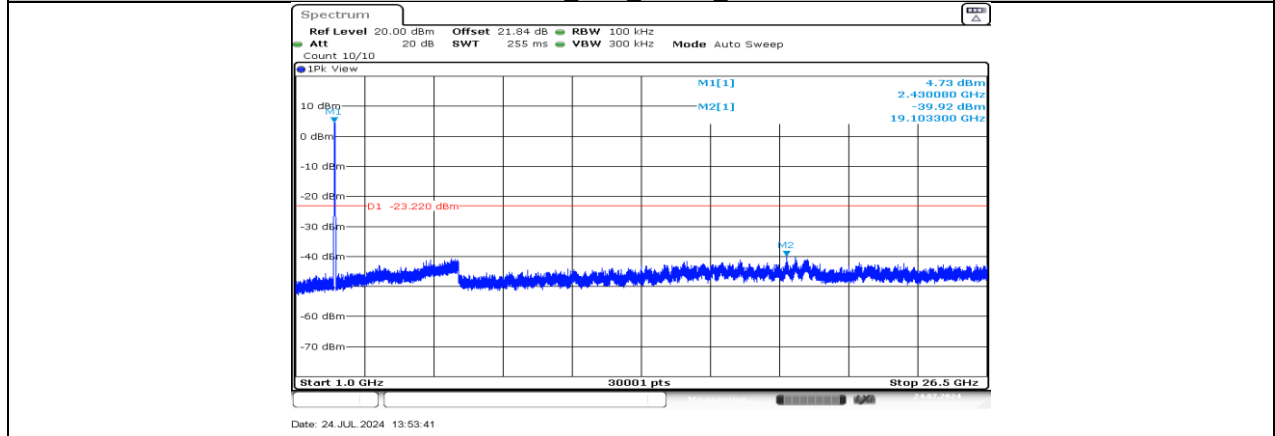
SRD 60M_Ant4_2436.5_1000~26500



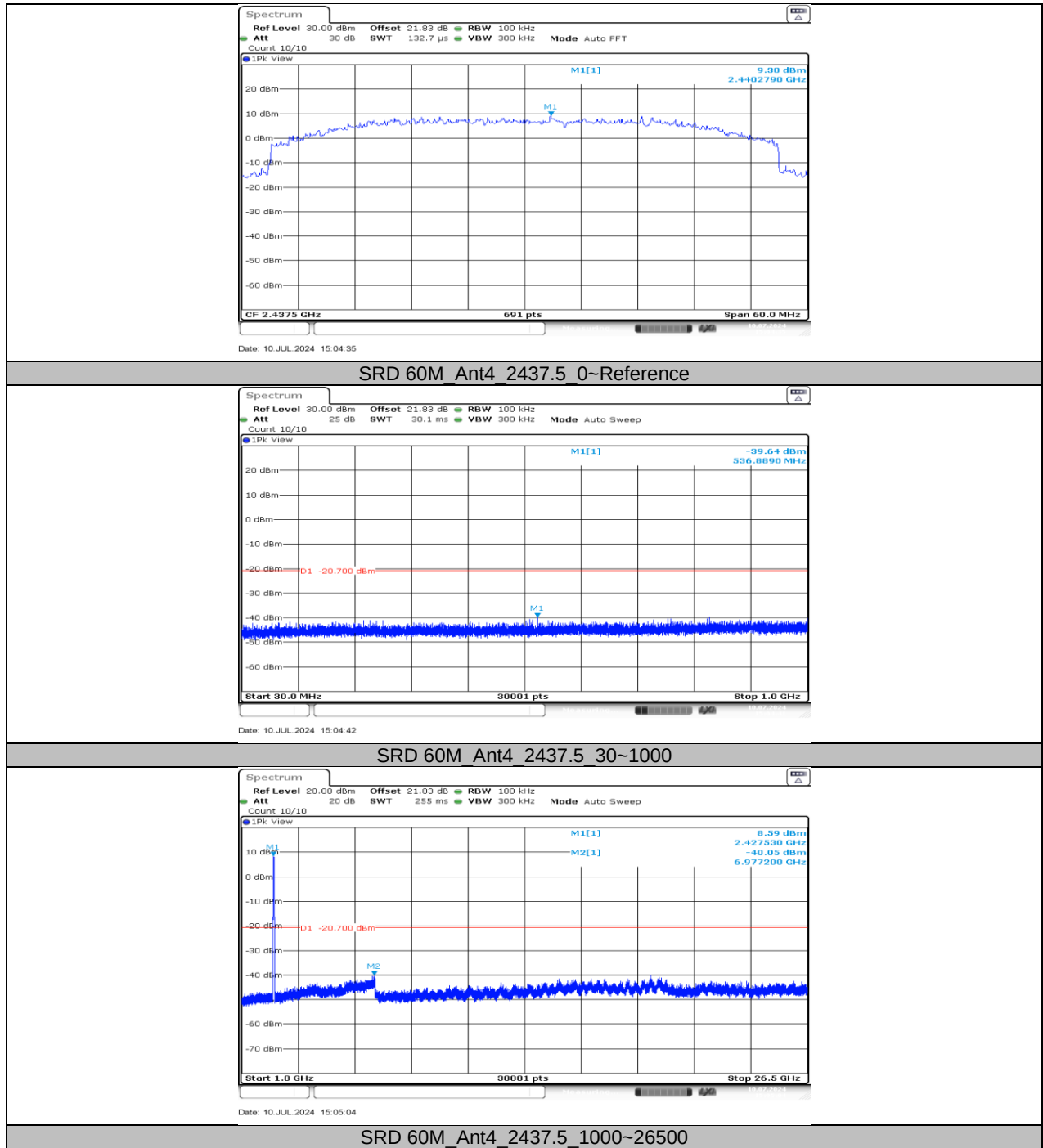
SRD 60M_Ant5_2436.5_0~Reference

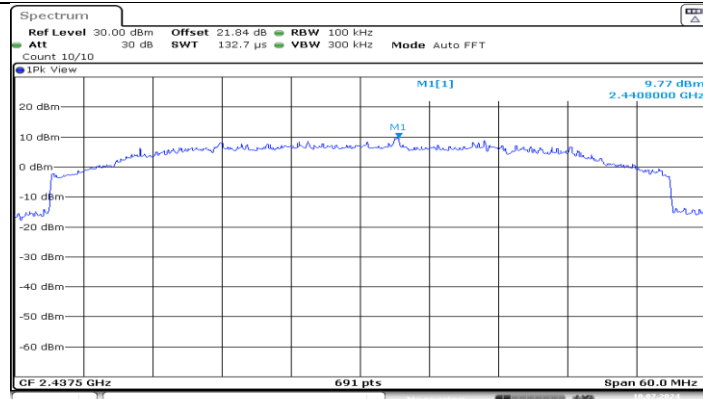


SRD 60M_Ant5_2436.5_30~1000



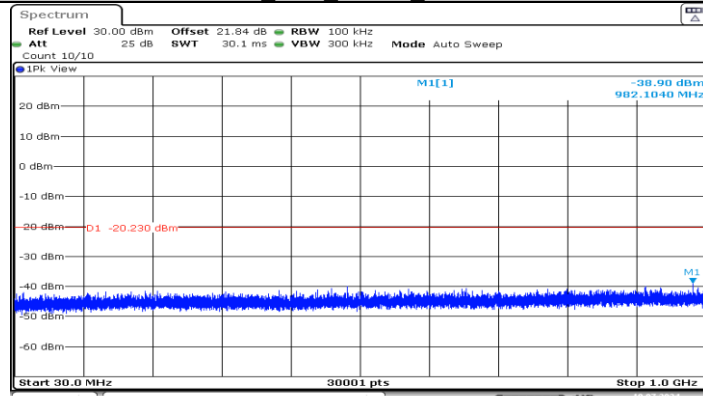
SRD 60M_Ant5_2436.5_1000~26500





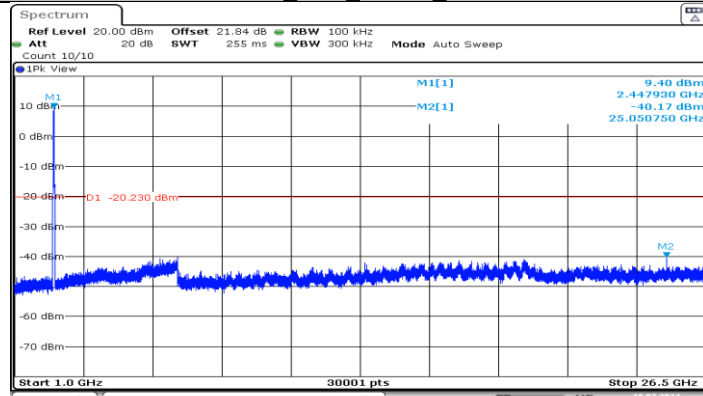
Date: 10 JUL 2024 15:09:03

SRD 60M_Ant5_2437.5_0~Reference



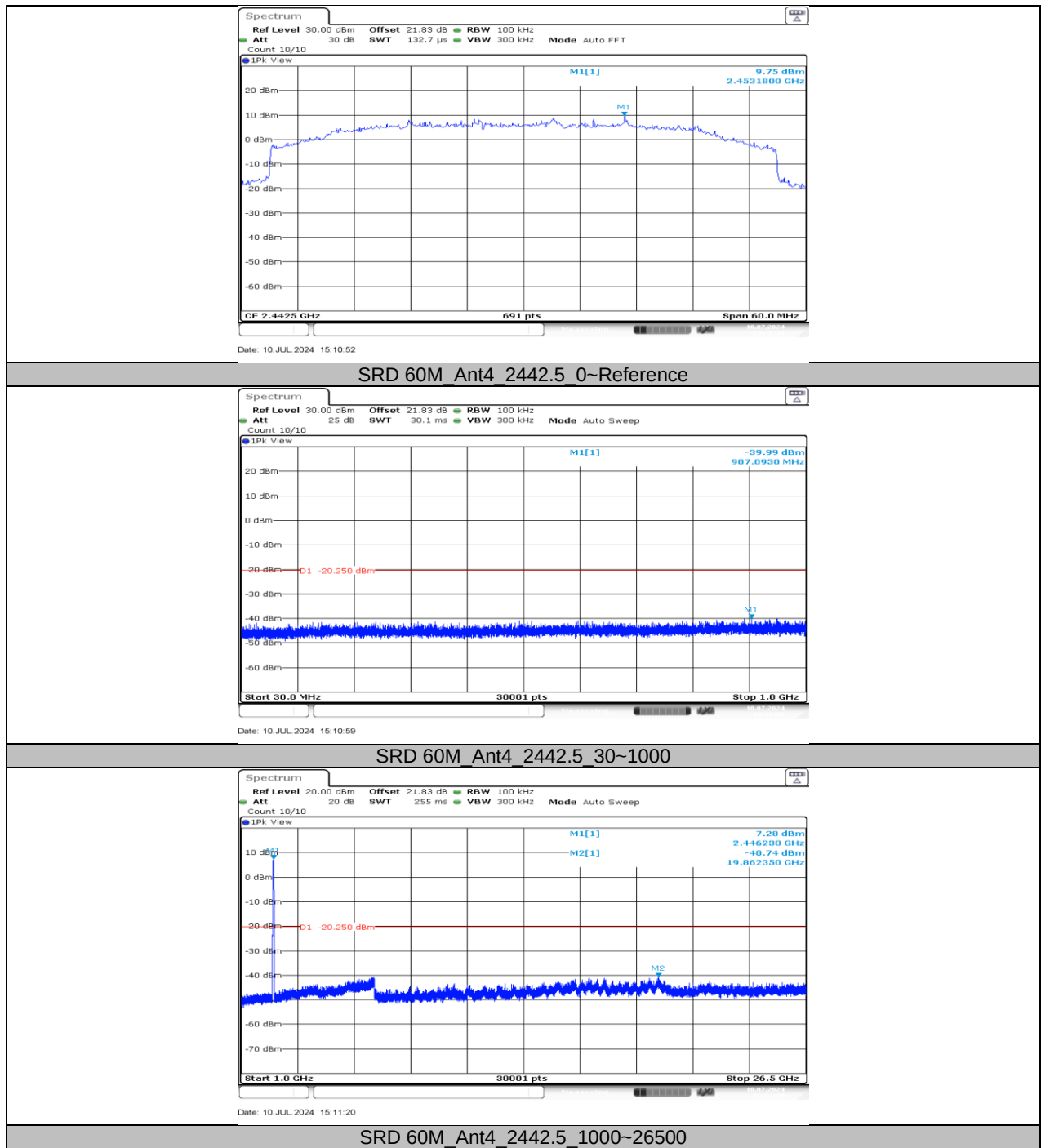
Date: 10 JUL 2024 15:09:09

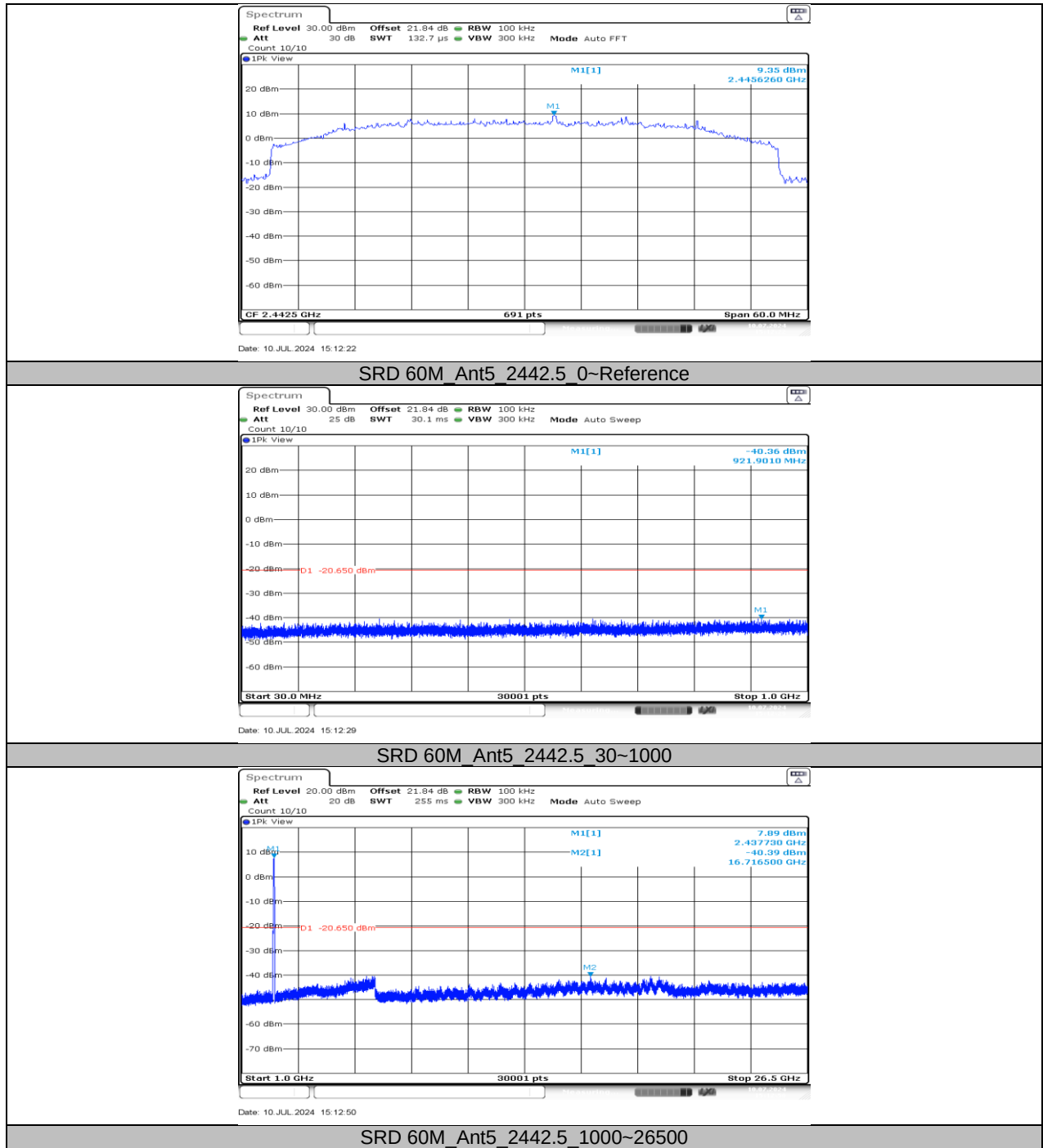
SRD 60M_Ant5_2437.5_30~1000



Date: 10 JUL 2024 15:09:31

SRD 60M_Ant5_2437.5_1000~26500





11.7. APPENDIX G: DUTY CYCLE**11.7.1. Test Result**

Test Mode	On Time (msec)	Period (msec)	Duty Cycle x (Linear)	Duty Cycle (%)	Duty Cycle Correction Factor (dB)	1/T Minimum VBW (kHz)	Final setting For VBW (kHz)
SRD 10M	50.00	50.00	1.0000	100.00	0.00	0.02	0.01
SRD 20M	50.00	50.00	1.0000	100.00	0.00	0.02	0.01
SRD 40M	50.00	50.00	1.0000	100.00	0.00	0.02	0.01
SRD 60M	50.00	50.00	1.0000	100.00	0.00	0.02	0.01

Note:

Duty Cycle Correction Factor= $10\log(1/x)$.

Where: x is Duty Cycle (Linear)

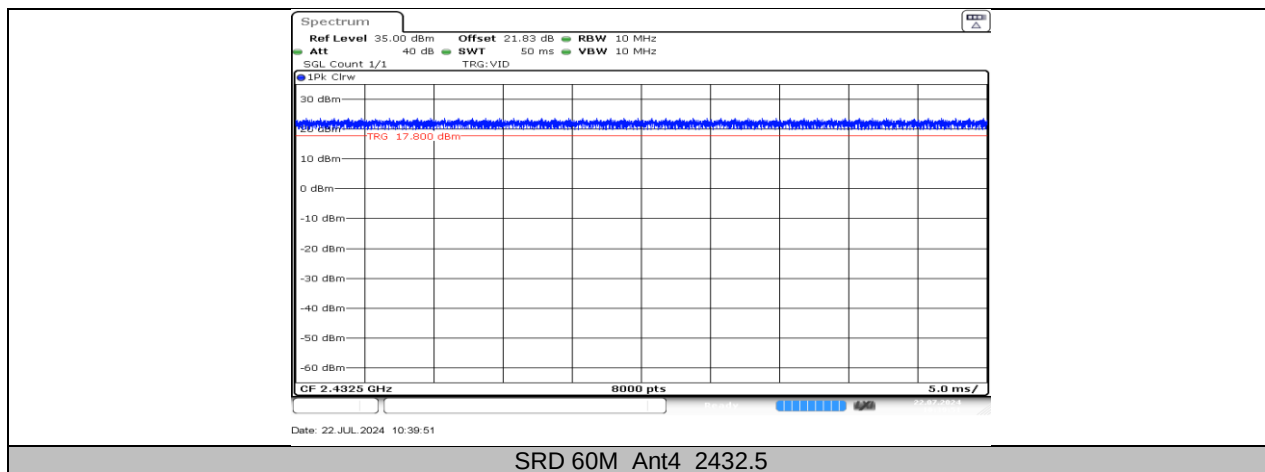
Where: T is On Time

If that calculated VBW is not available on the analyzer then the next higher value should be used.

Note: All the modes and antennas had been tested, but only the worst data was recorded in the report.

11.7.2. Test Graphs





END OF REPORT