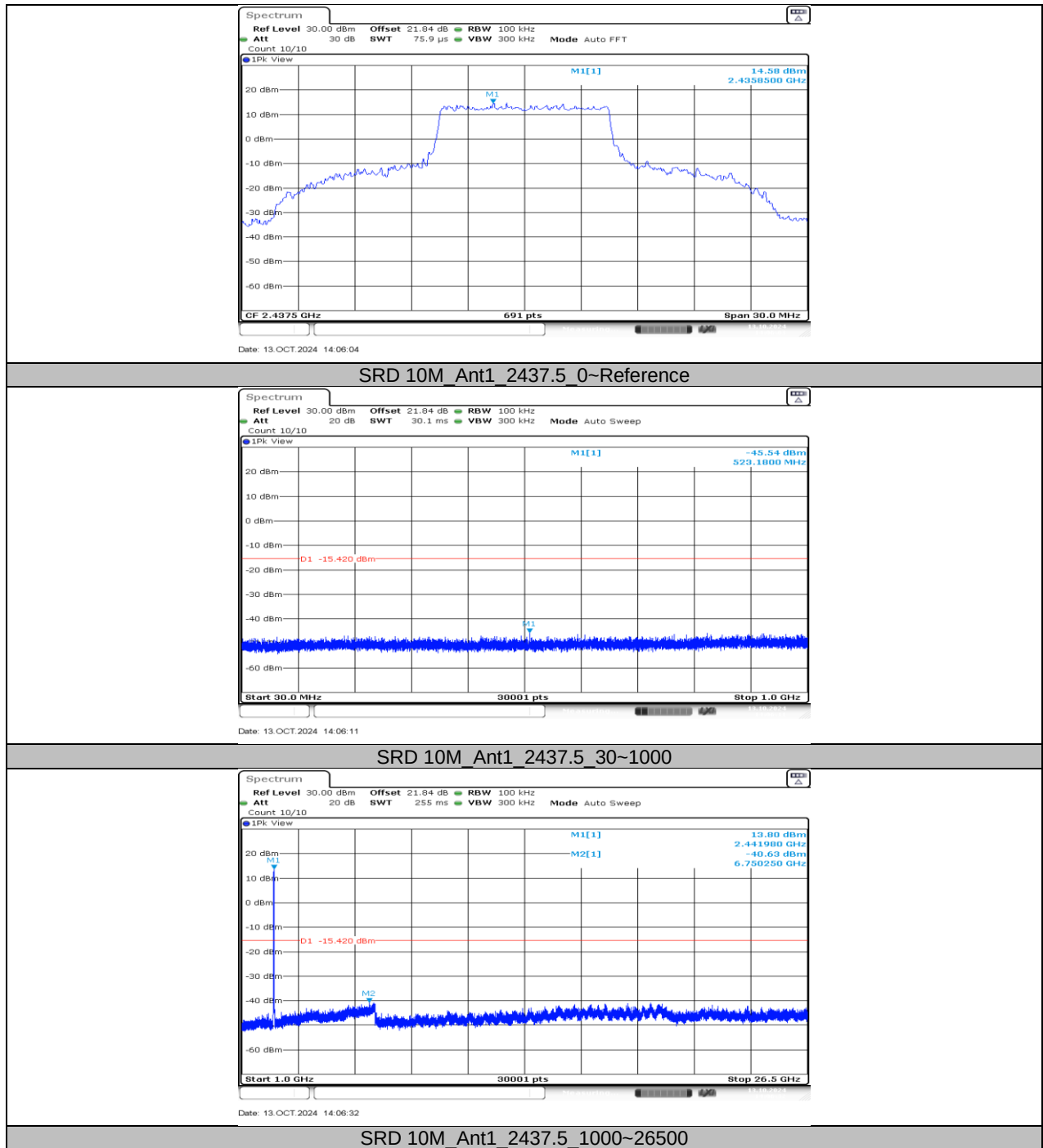
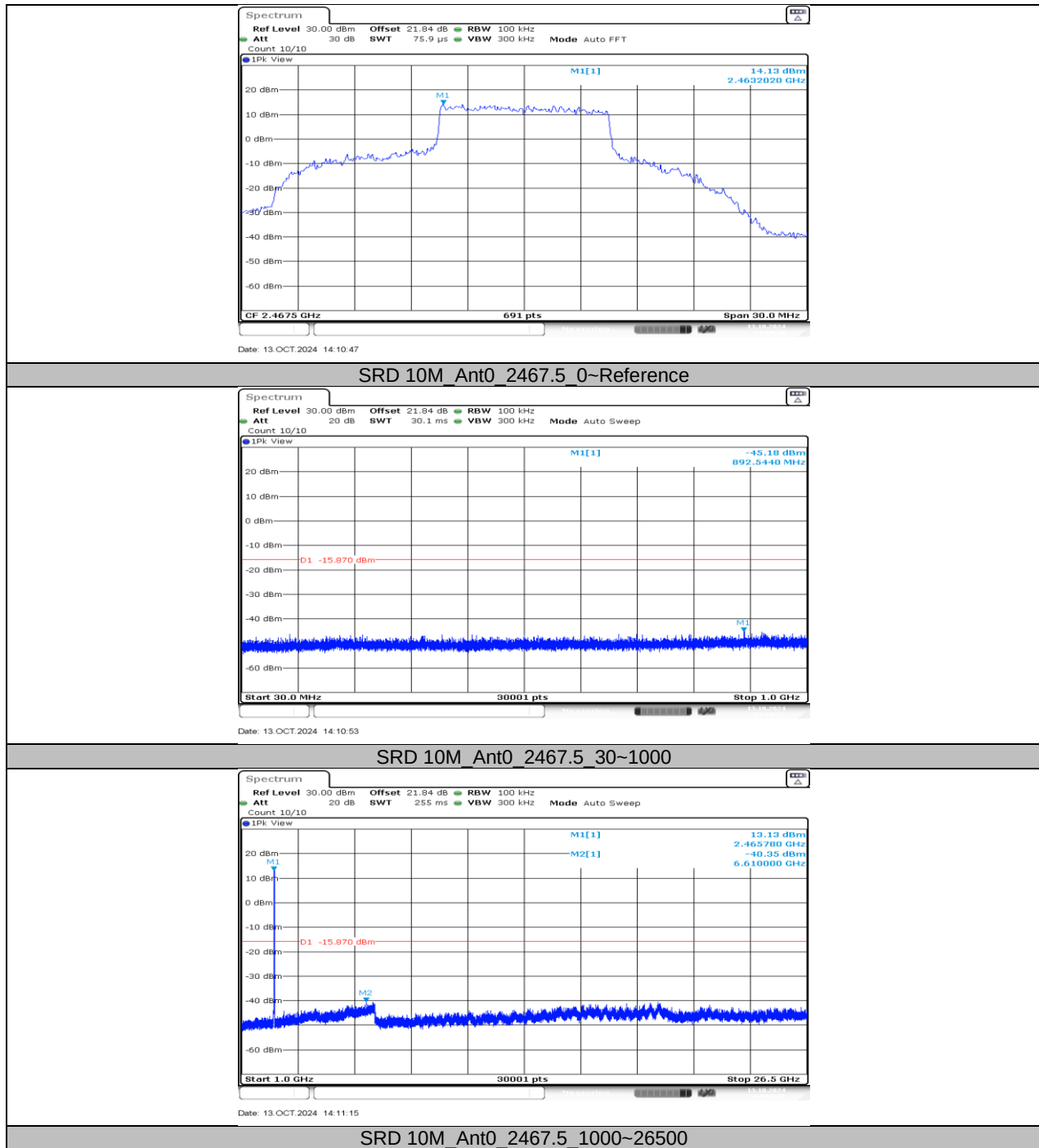
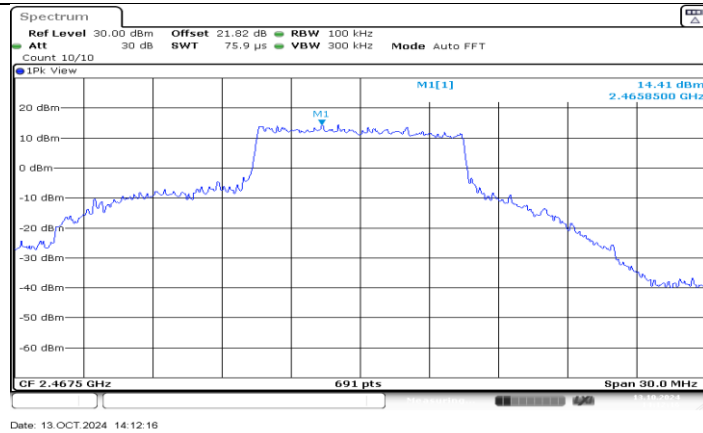
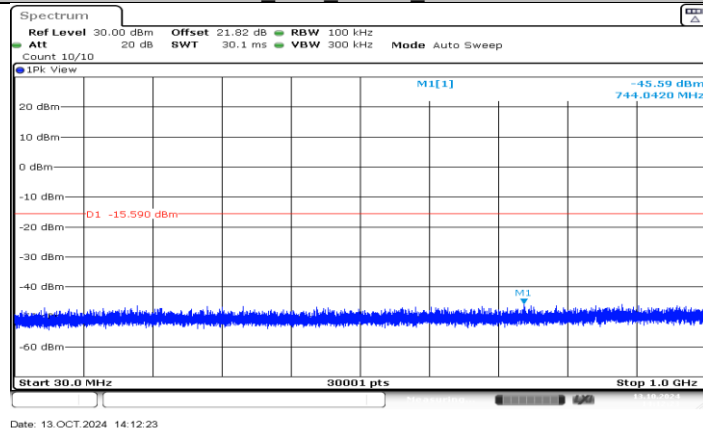




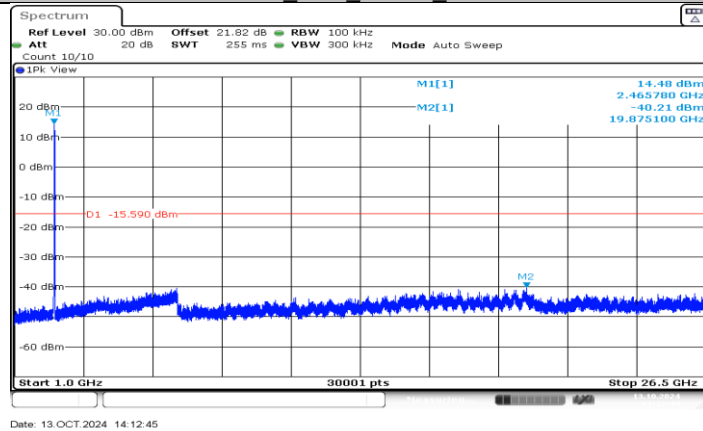




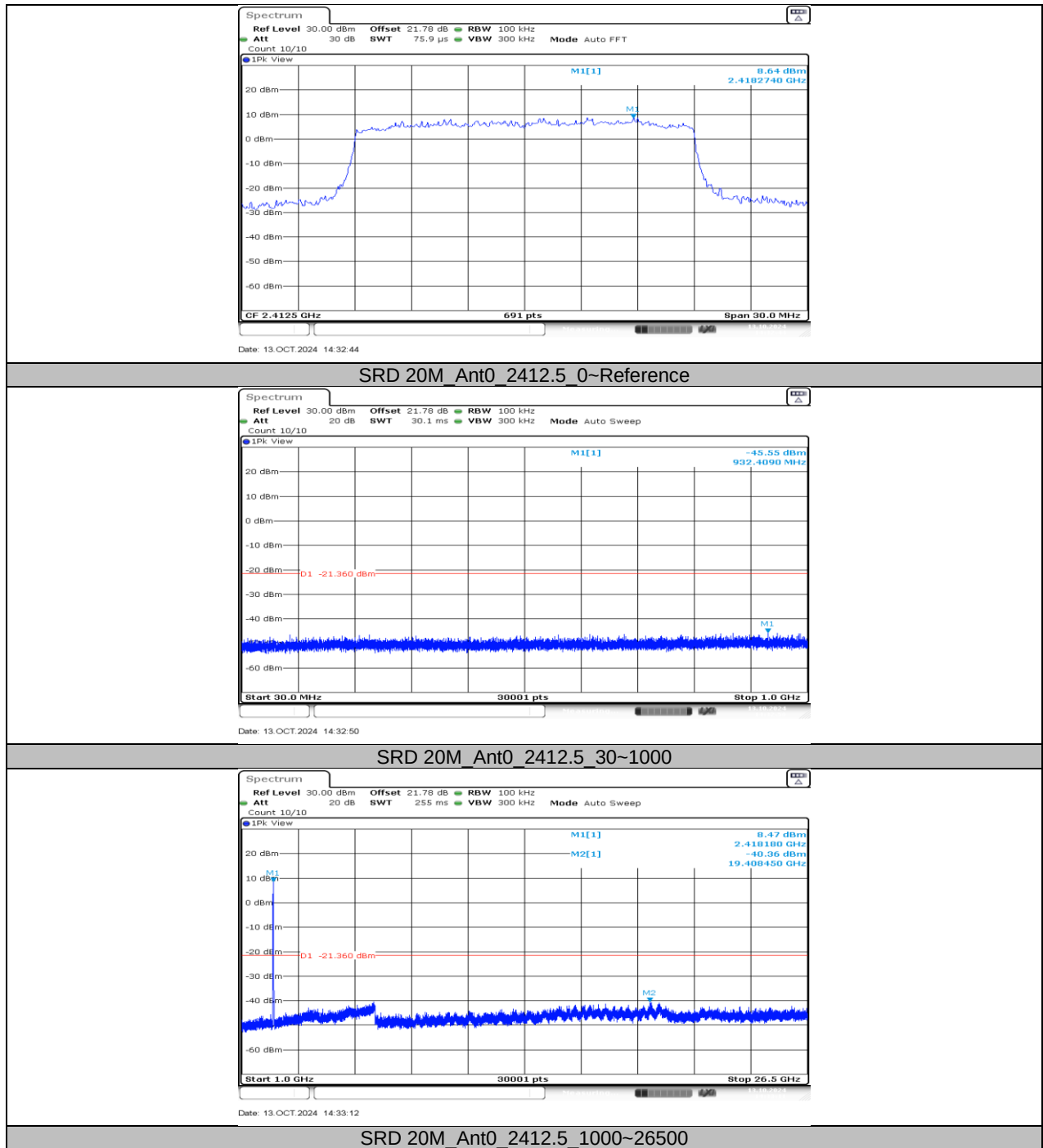
SRD 10M_Ant1_2467.5_0~Reference

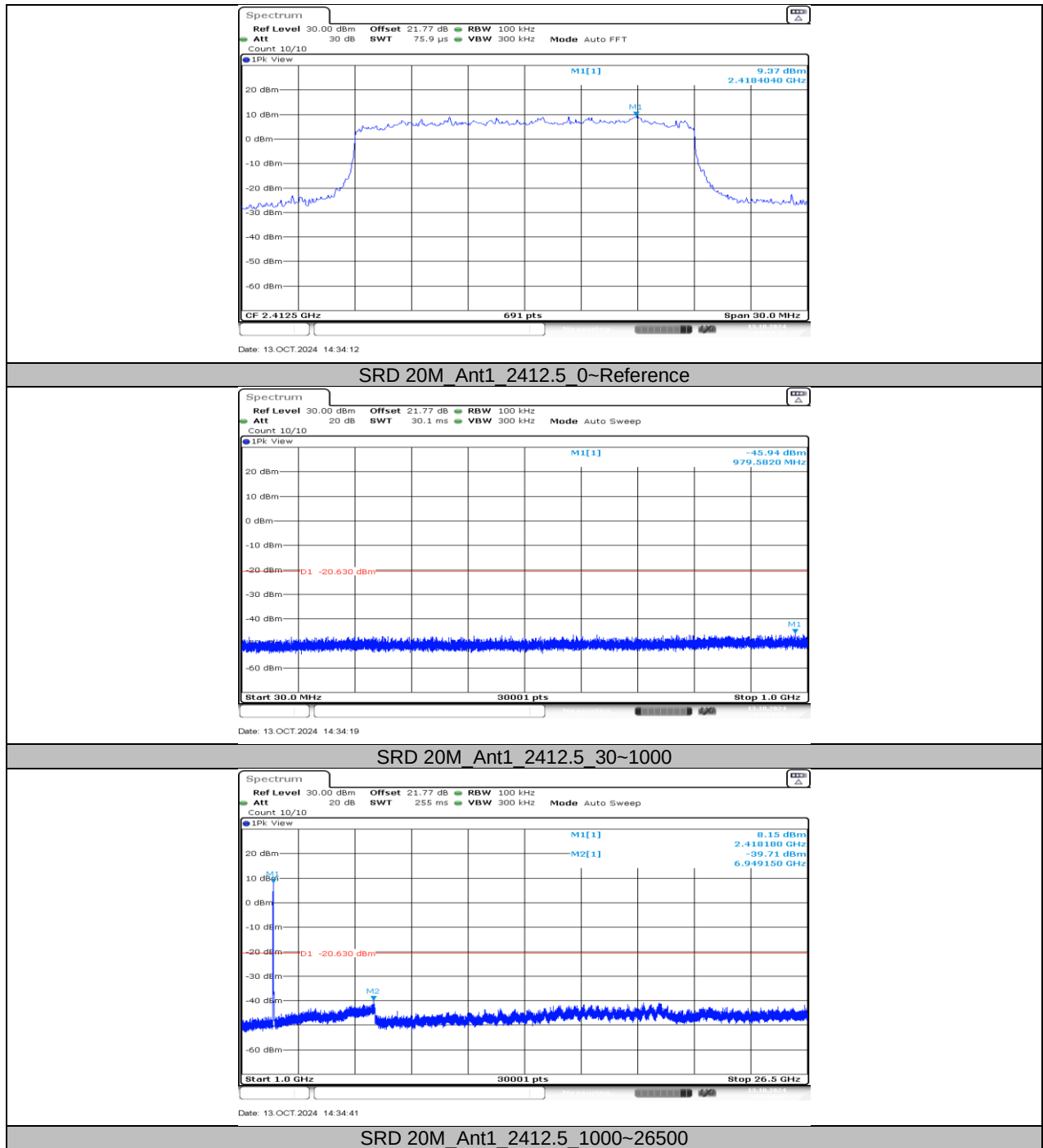


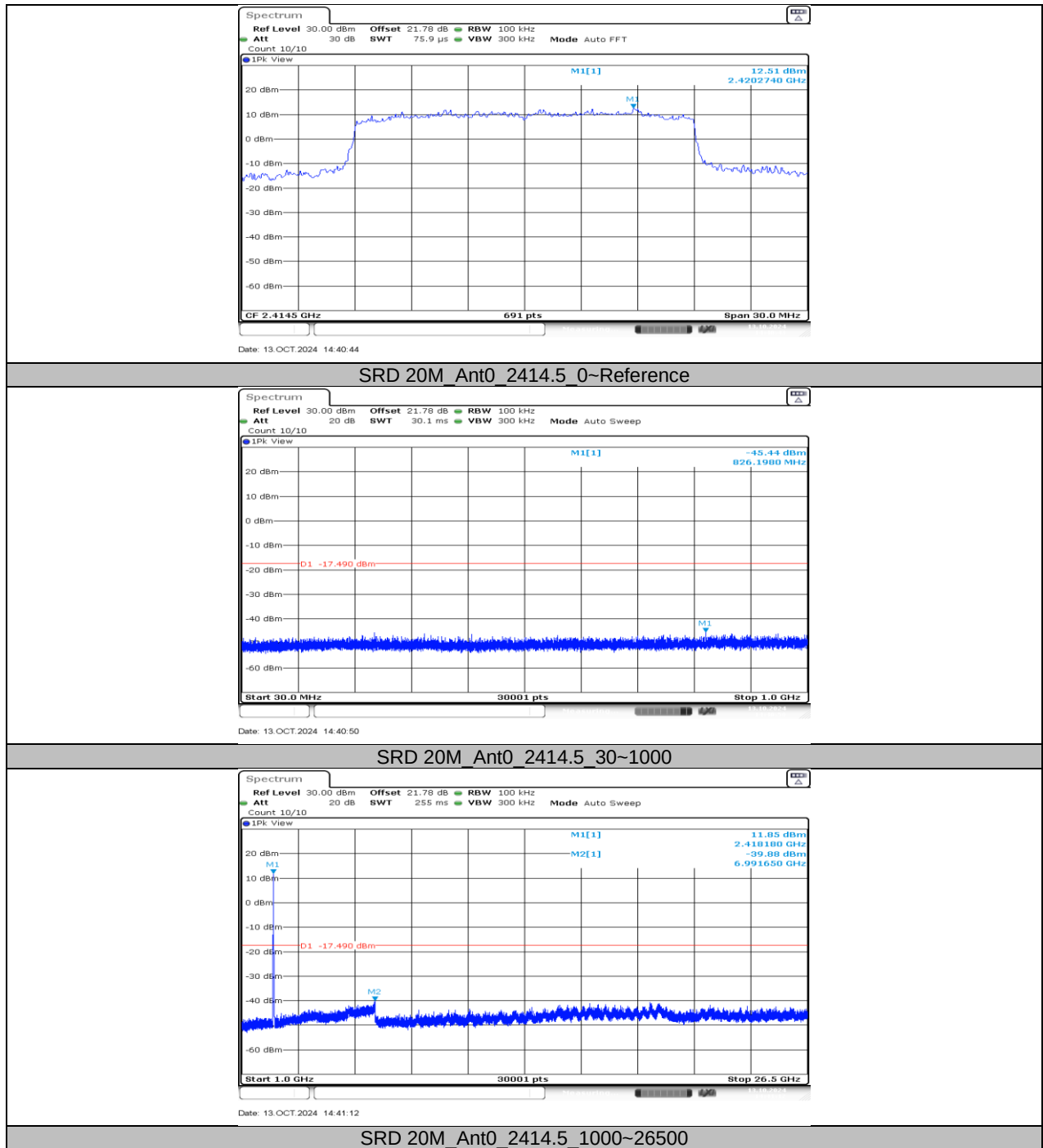
SRD 10M_Ant1_2467.5_30~1000

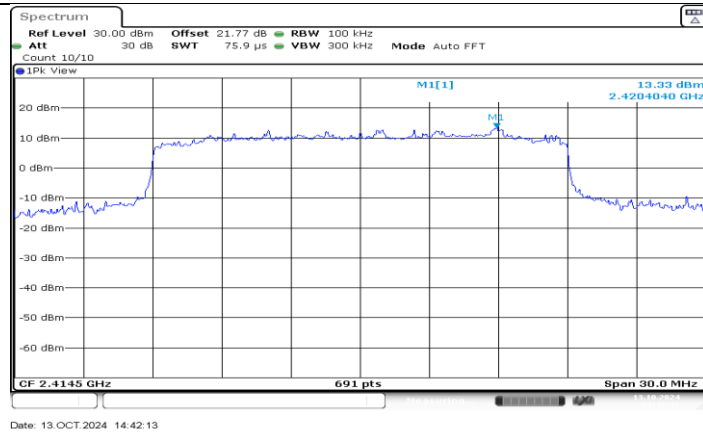


SRD 10M_Ant1_2467.5_1000~26500

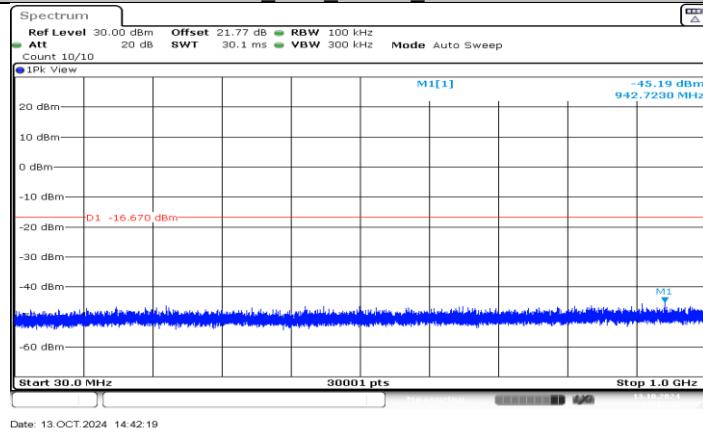




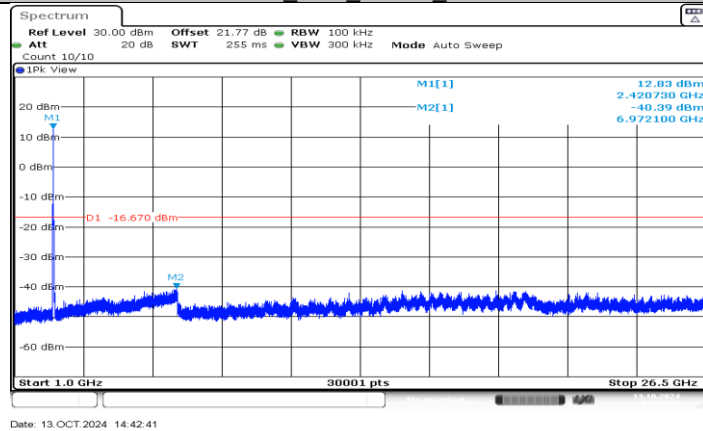




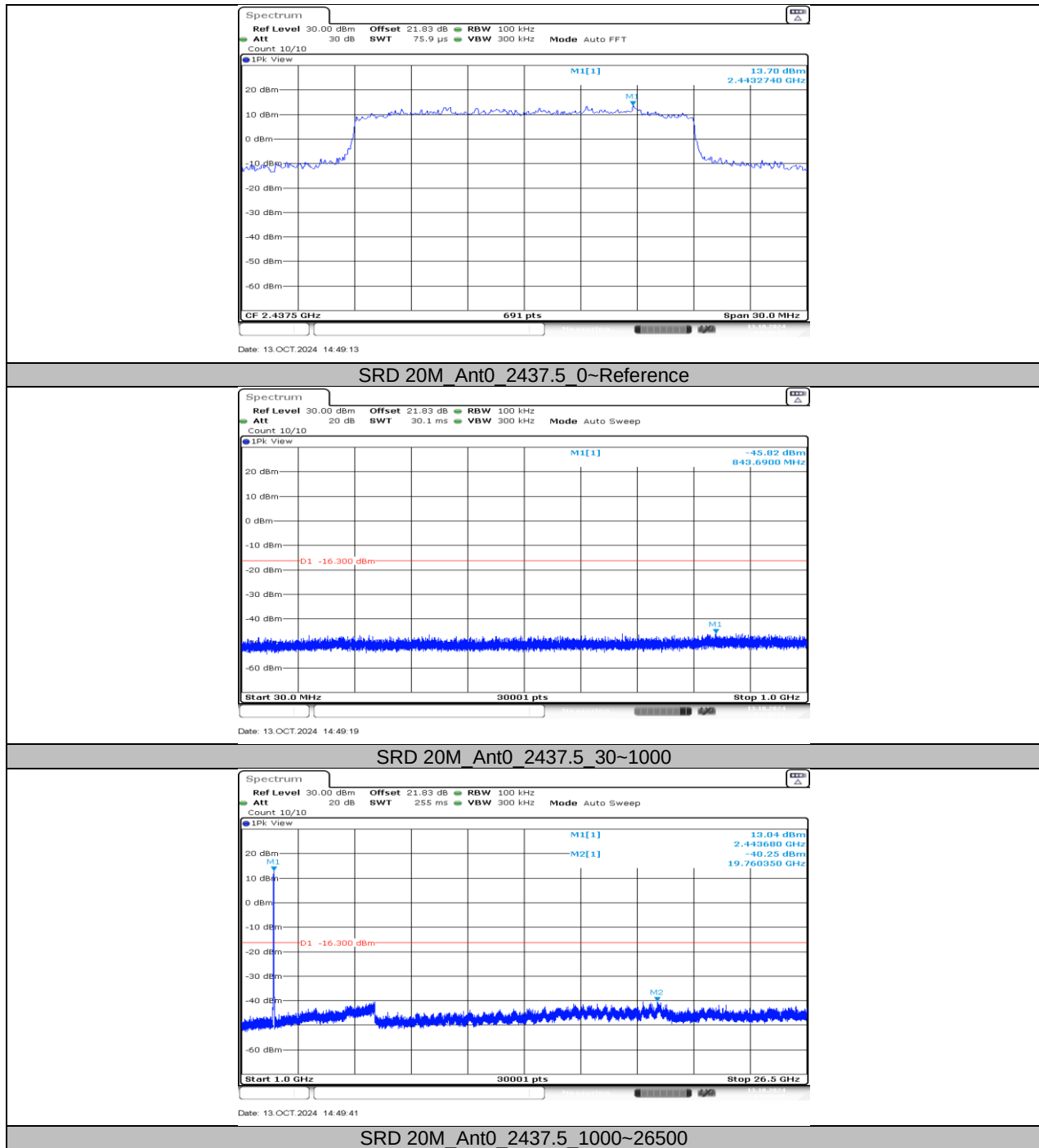
SRD 20M_Ant1_2414.5_0~Reference

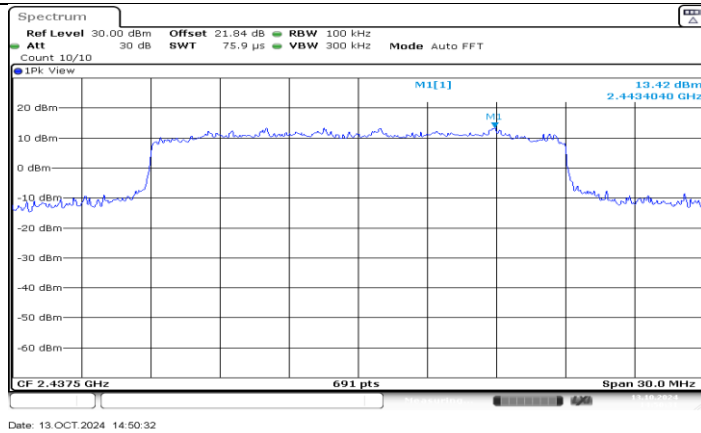


SRD 20M_Ant1_2414.5_30~1000

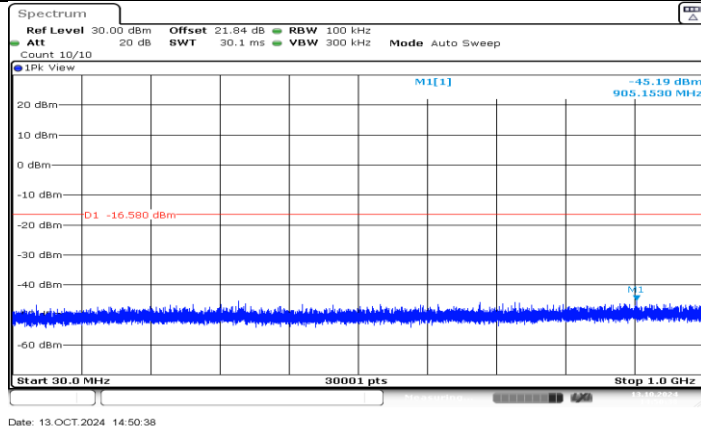


SRD 20M_Ant1_2414.5_1000~26500

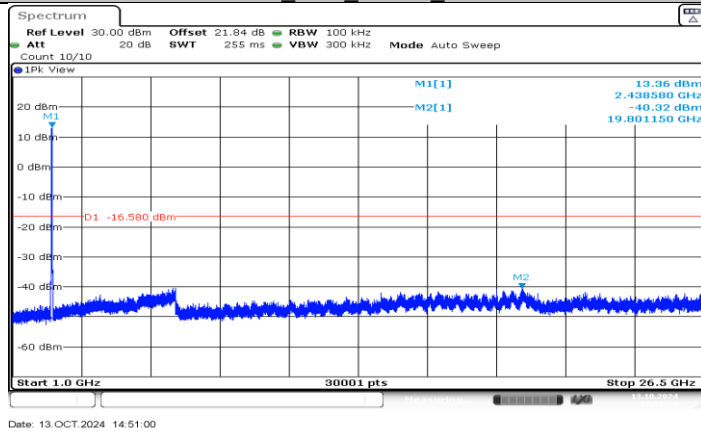




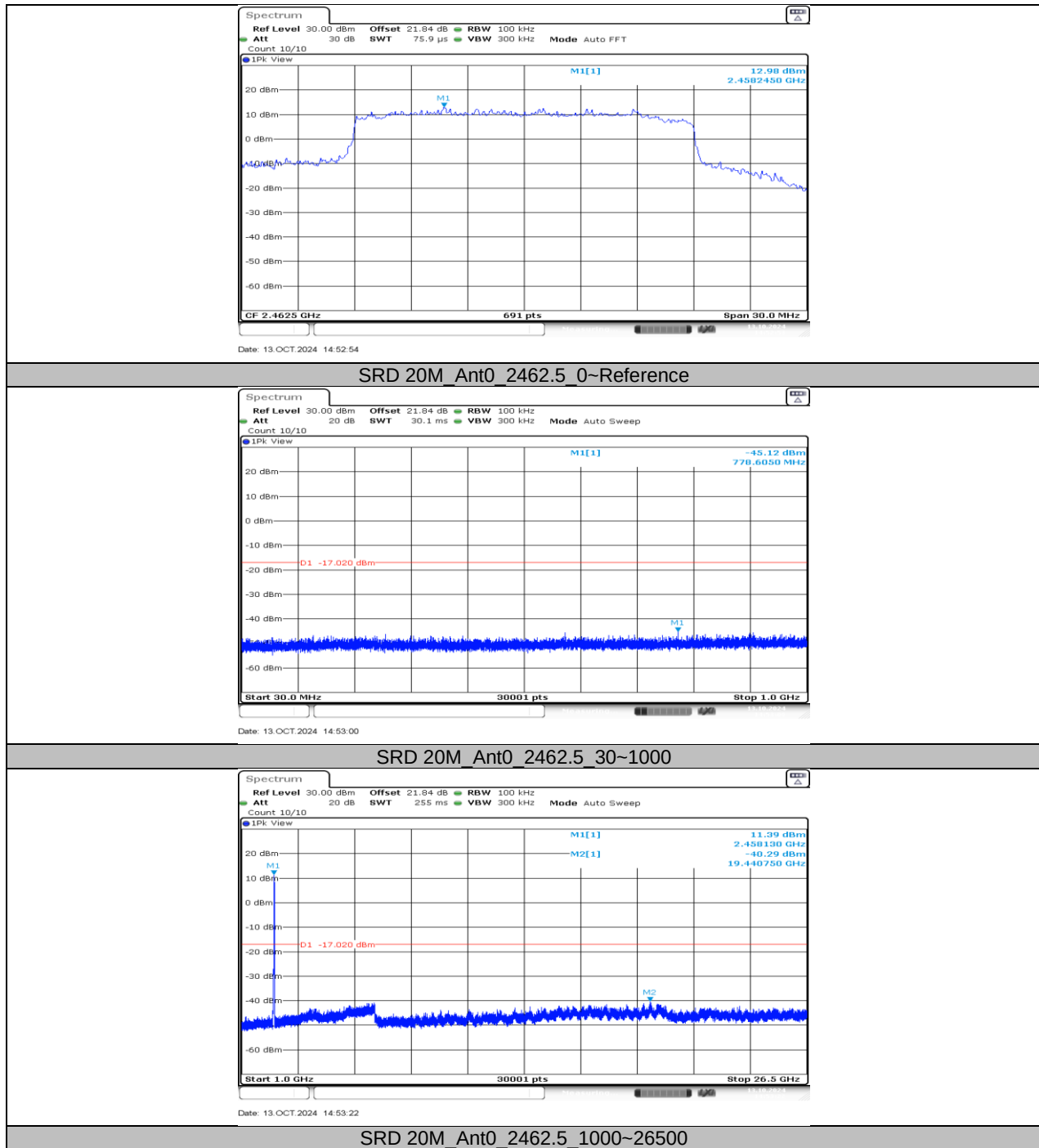
SRD 20M_Ant1_2437.5_0~Reference

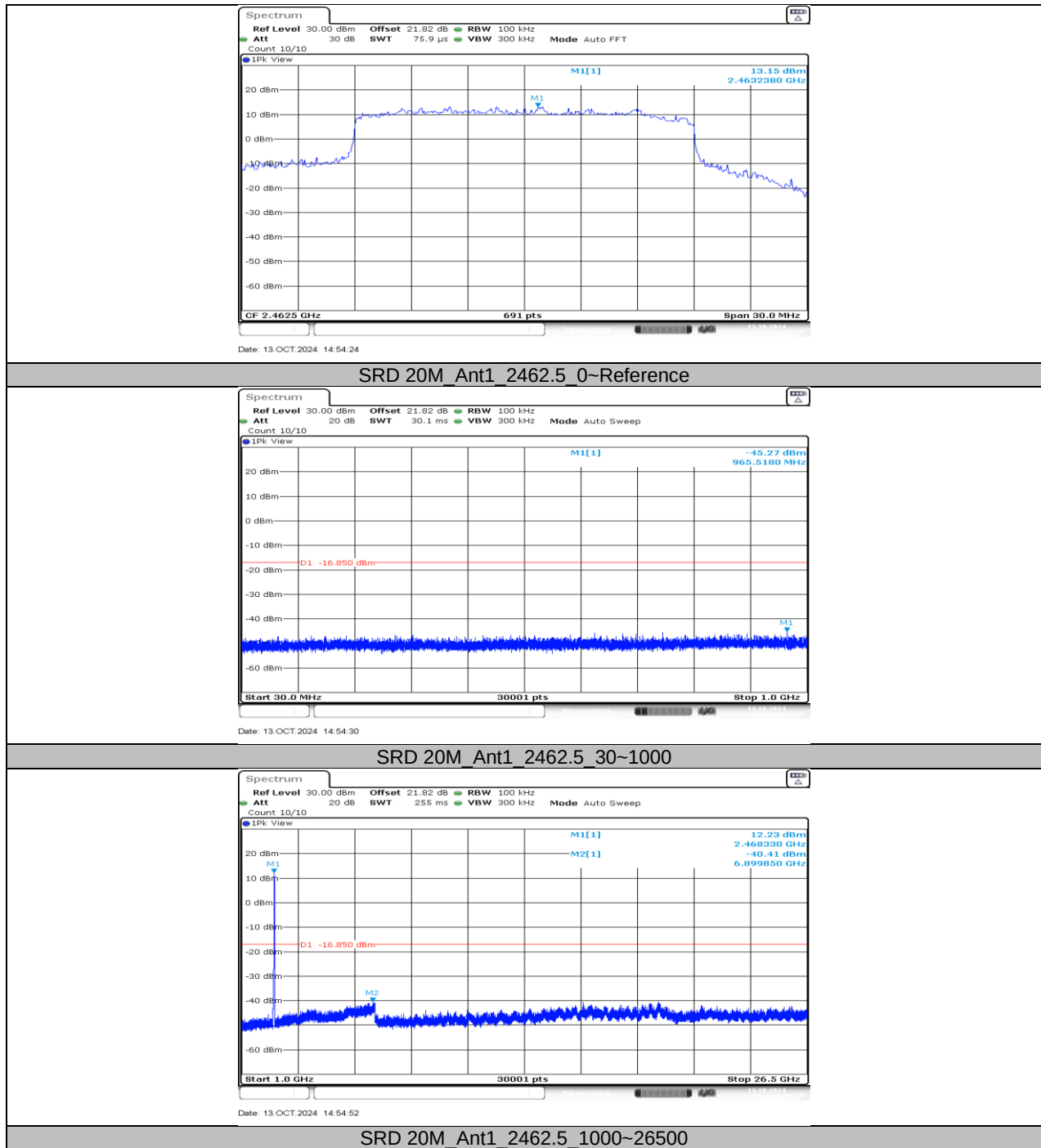


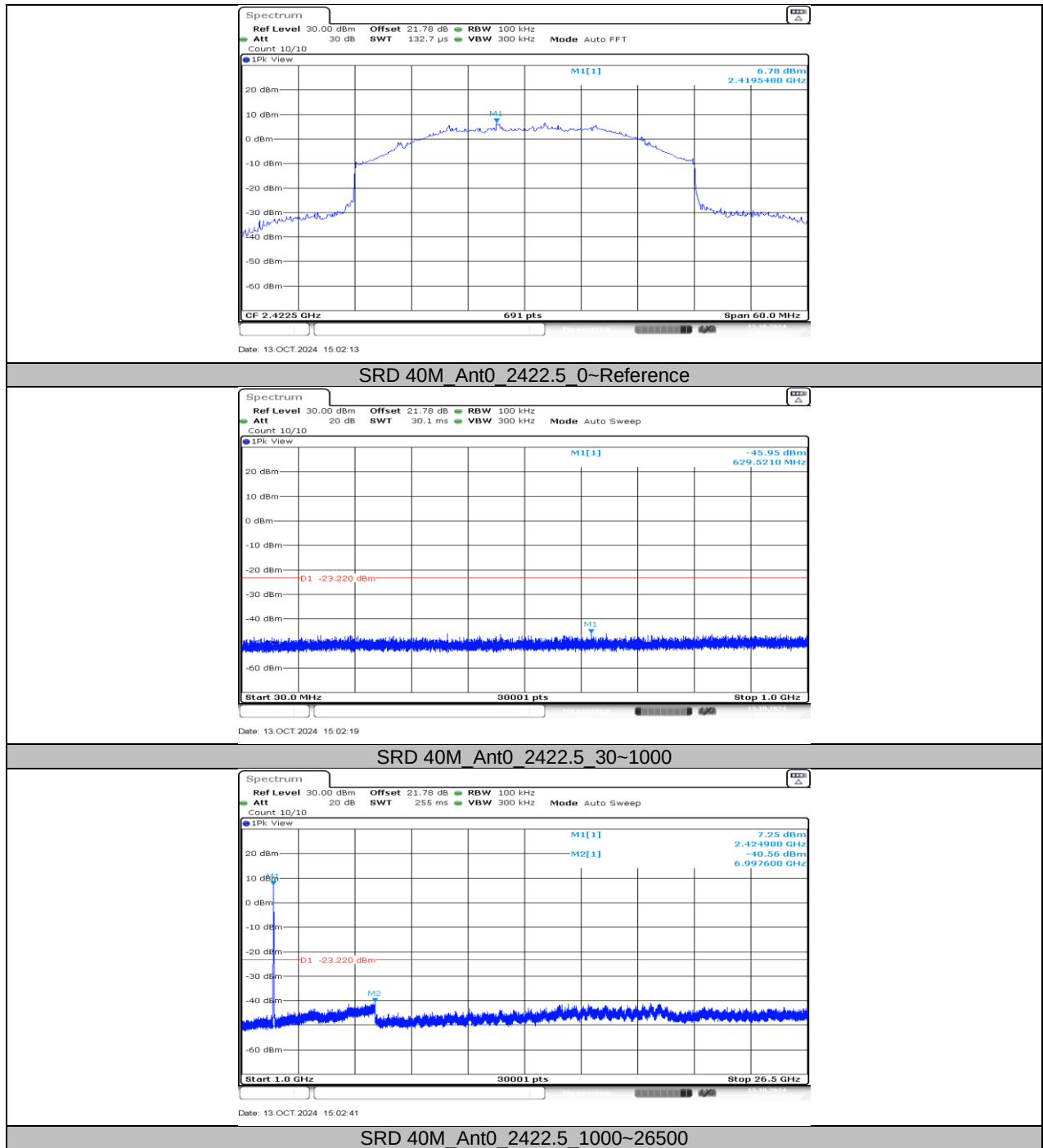
SRD 20M_Ant1_2437.5_30~1000

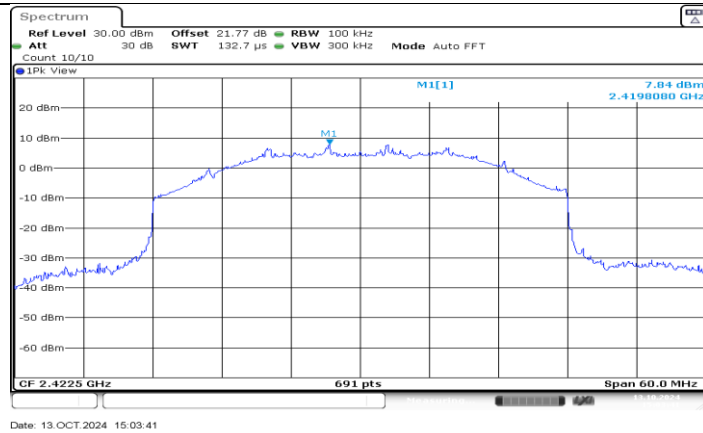


SRD 20M_Ant1_2437.5_1000~26500

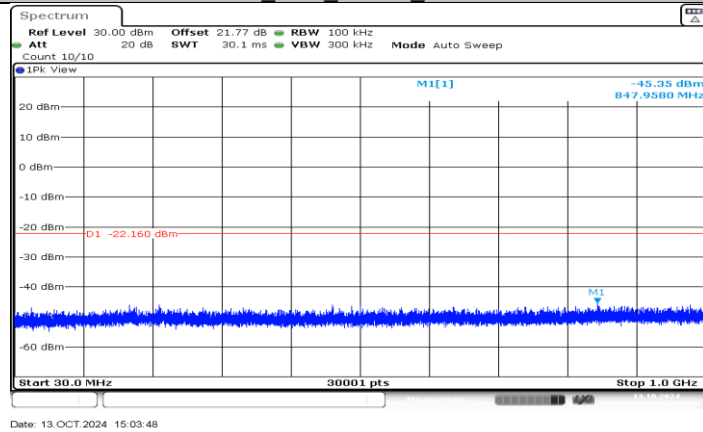




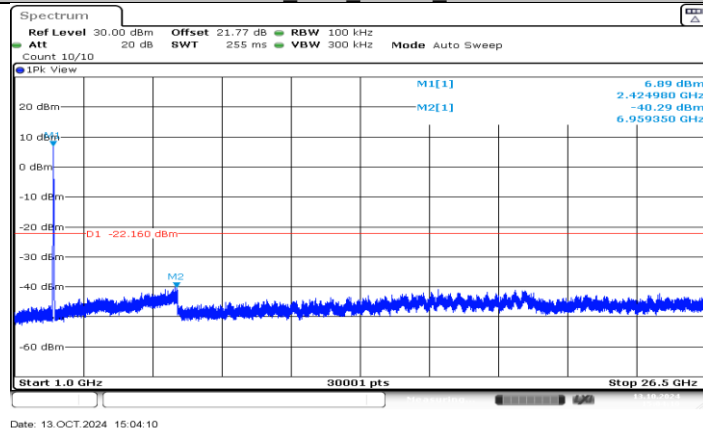




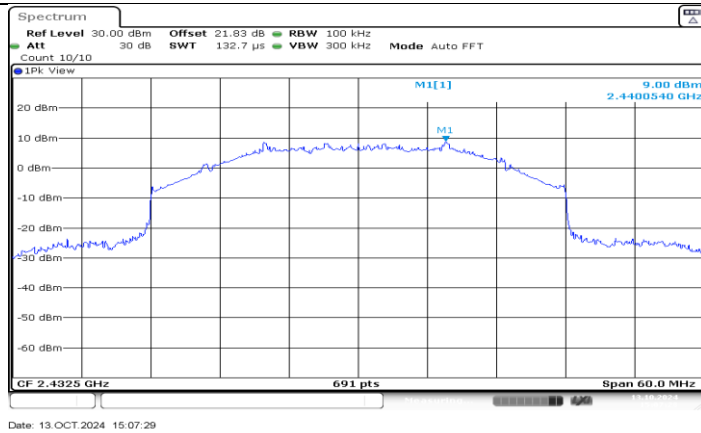
SRD 40M_Ant1_2422.5_0~Reference



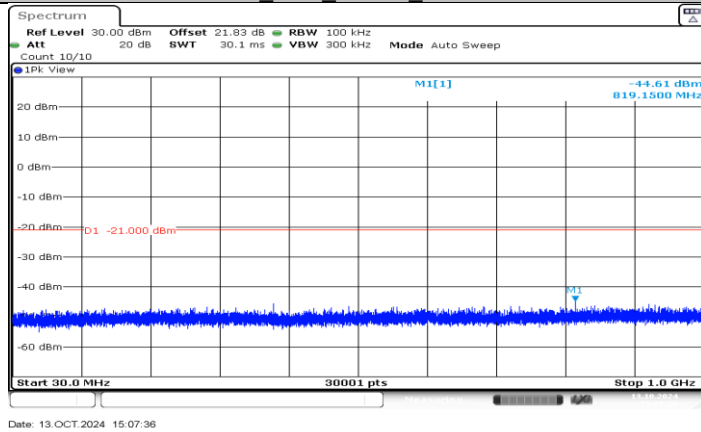
SRD 40M_Ant1_2422.5_30~1000



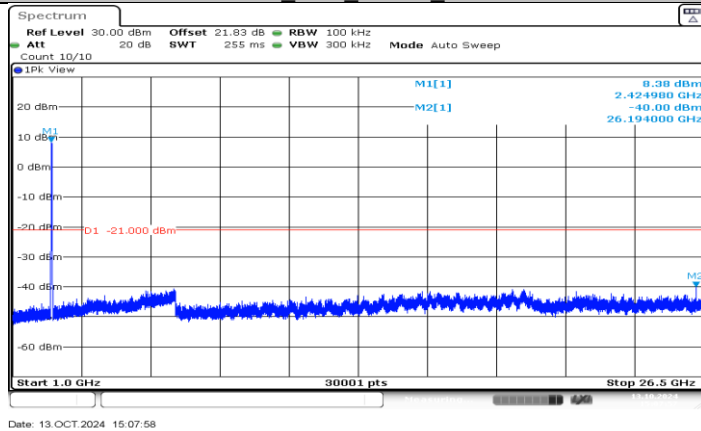
SRD 40M_Ant1_2422.5_1000~26500



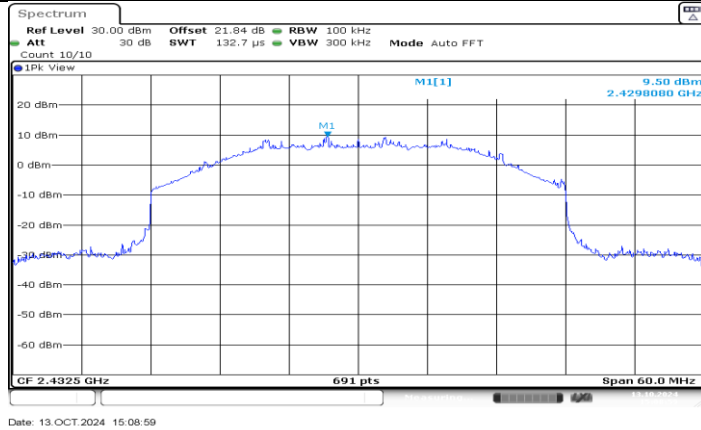
SRD 40M_Ant0_2432.5_0~Reference



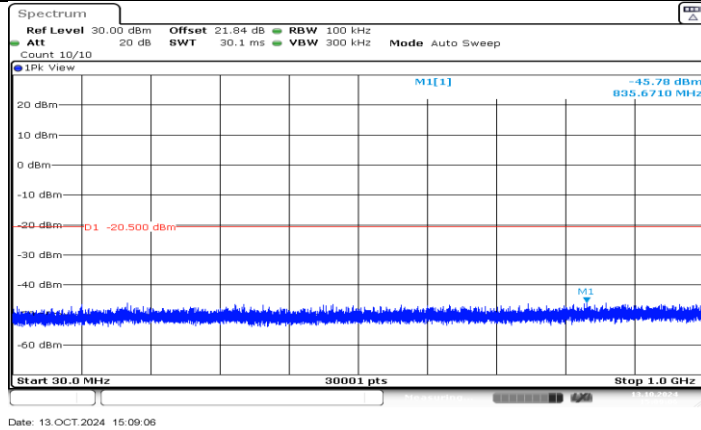
SRD 40M_Ant0_2432.5_30~1000



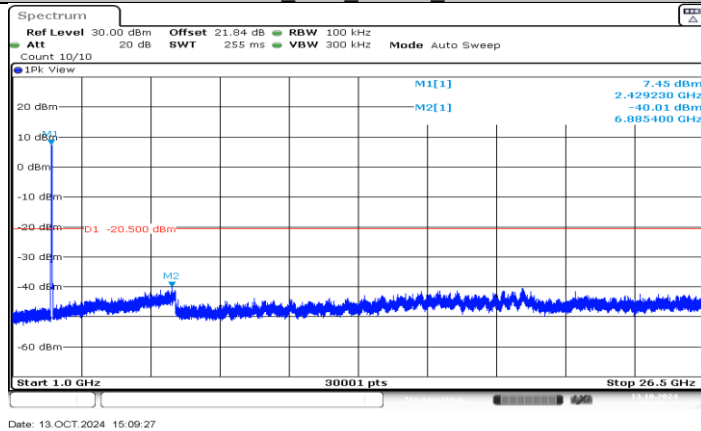
SRD 40M_Ant0_2432.5_1000~26500



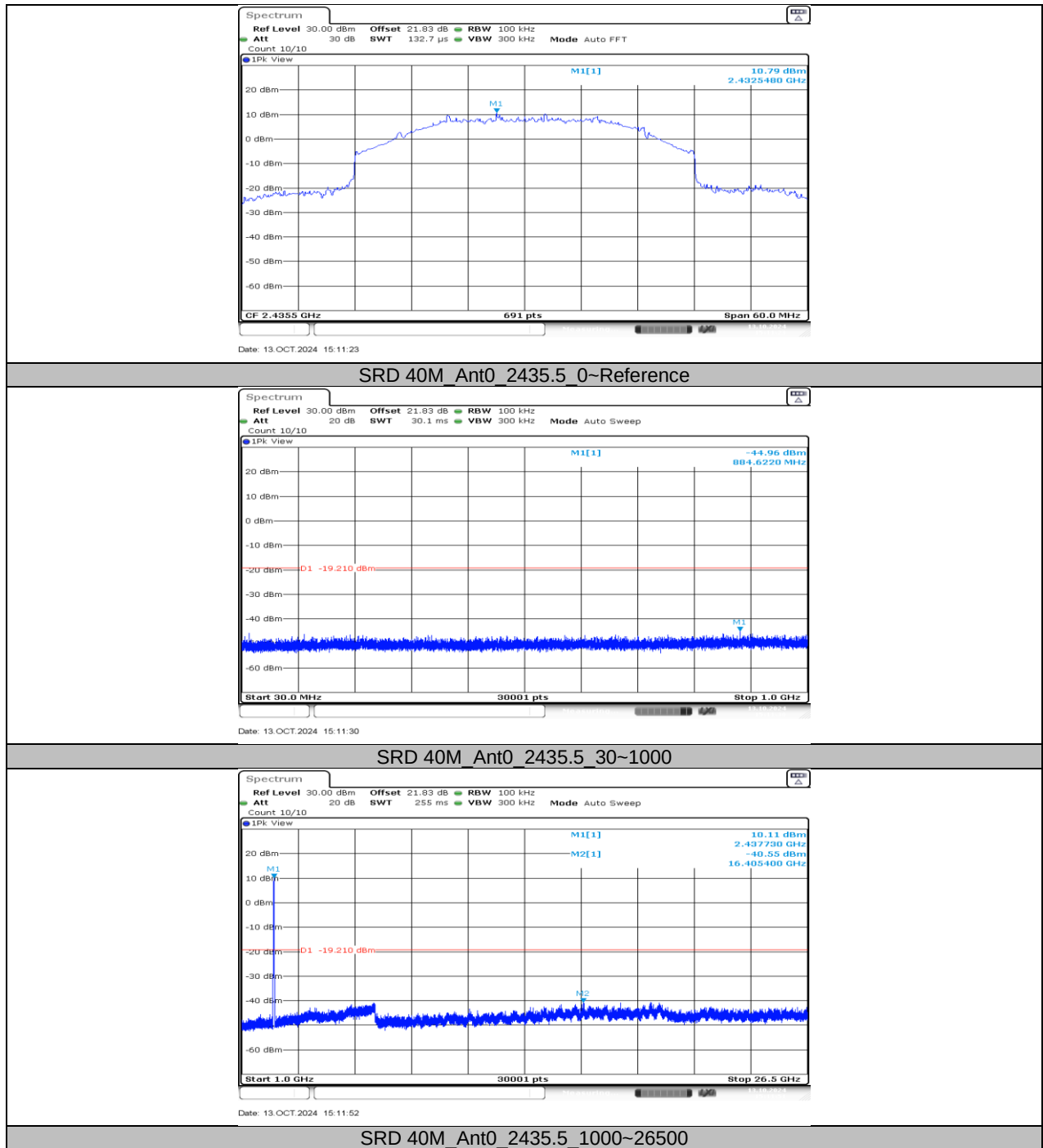
SRD 40M_Ant1_2432.5_0~Reference

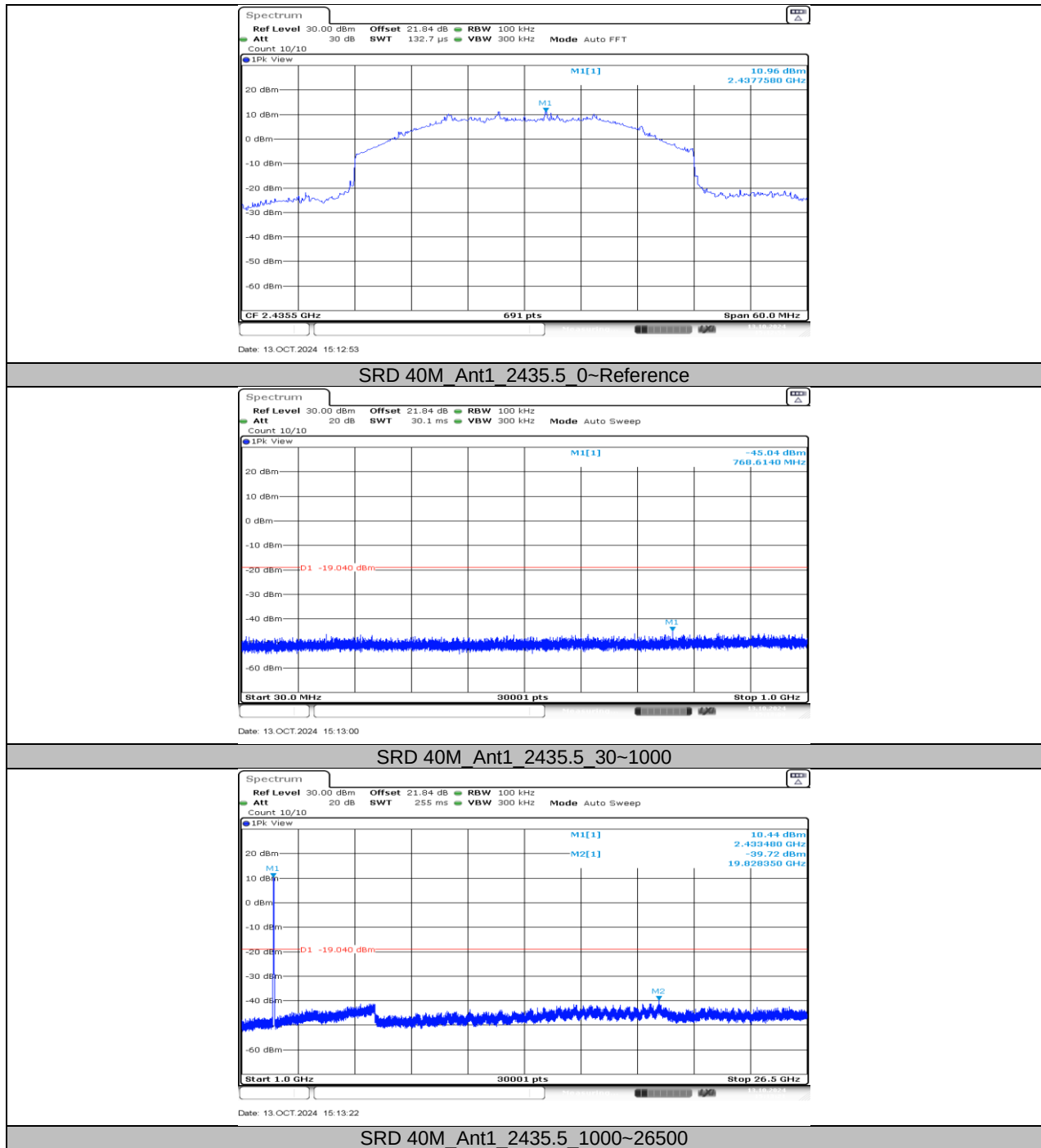


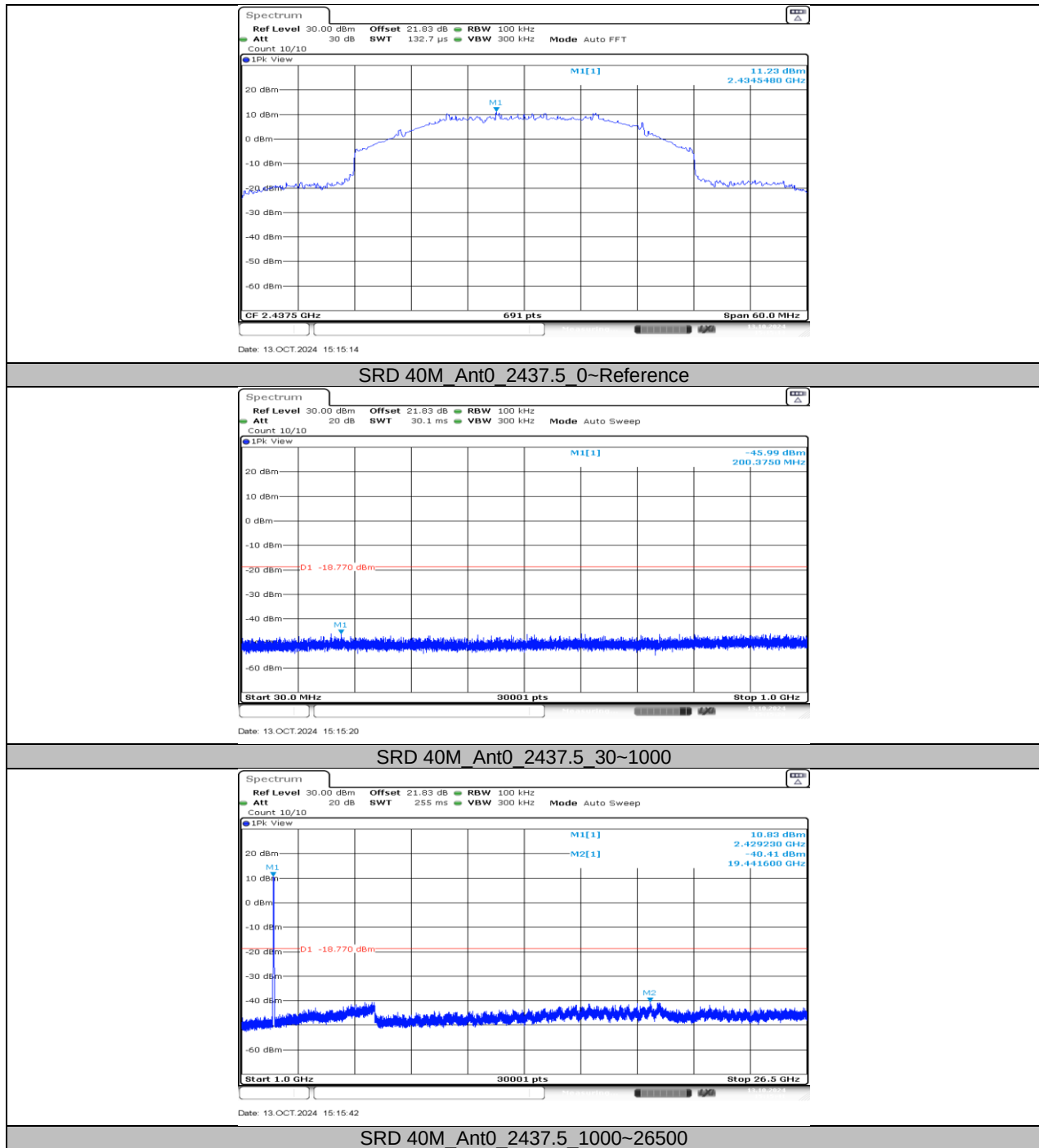
SRD 40M_Ant1_2432.5_30~1000

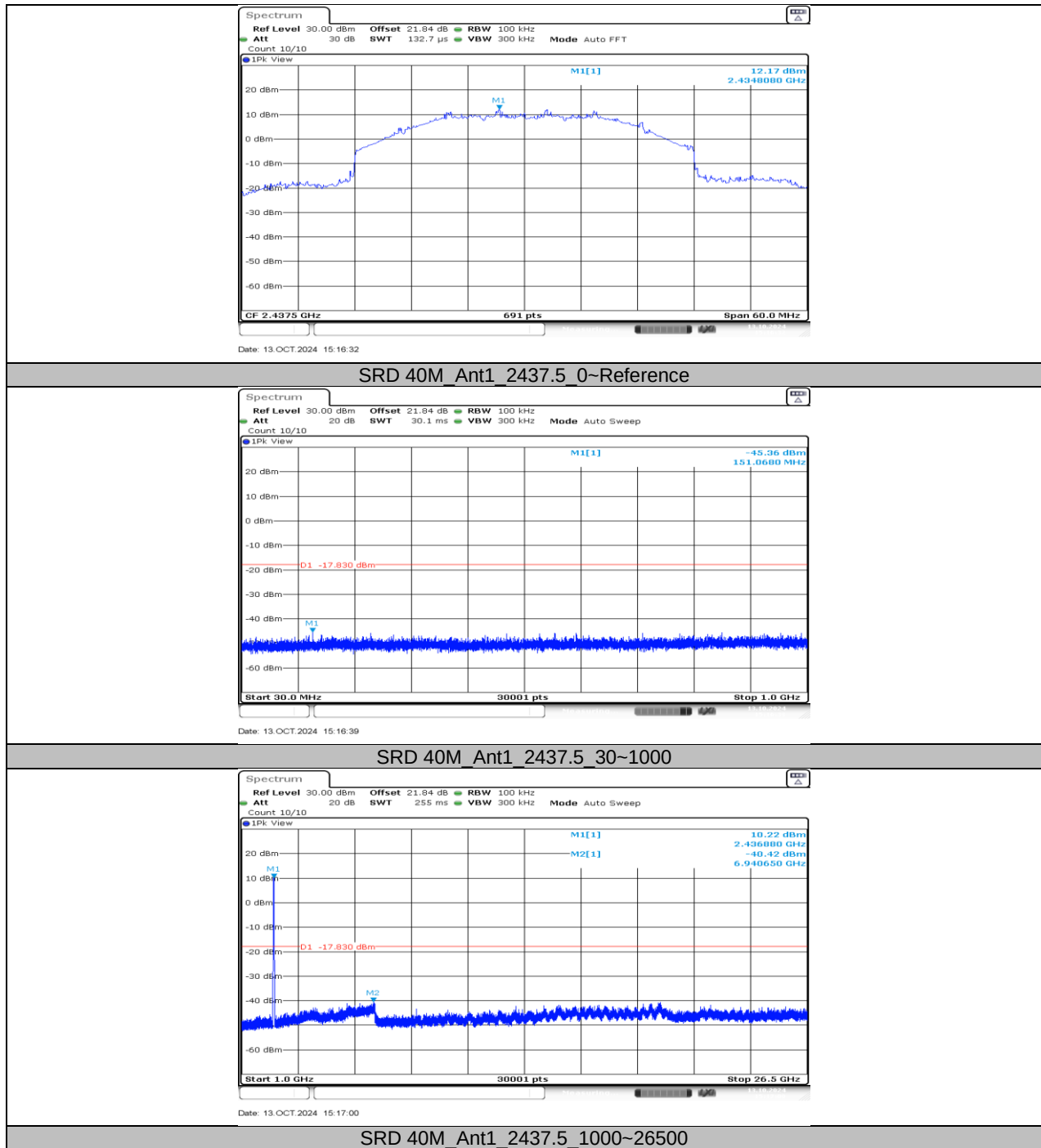


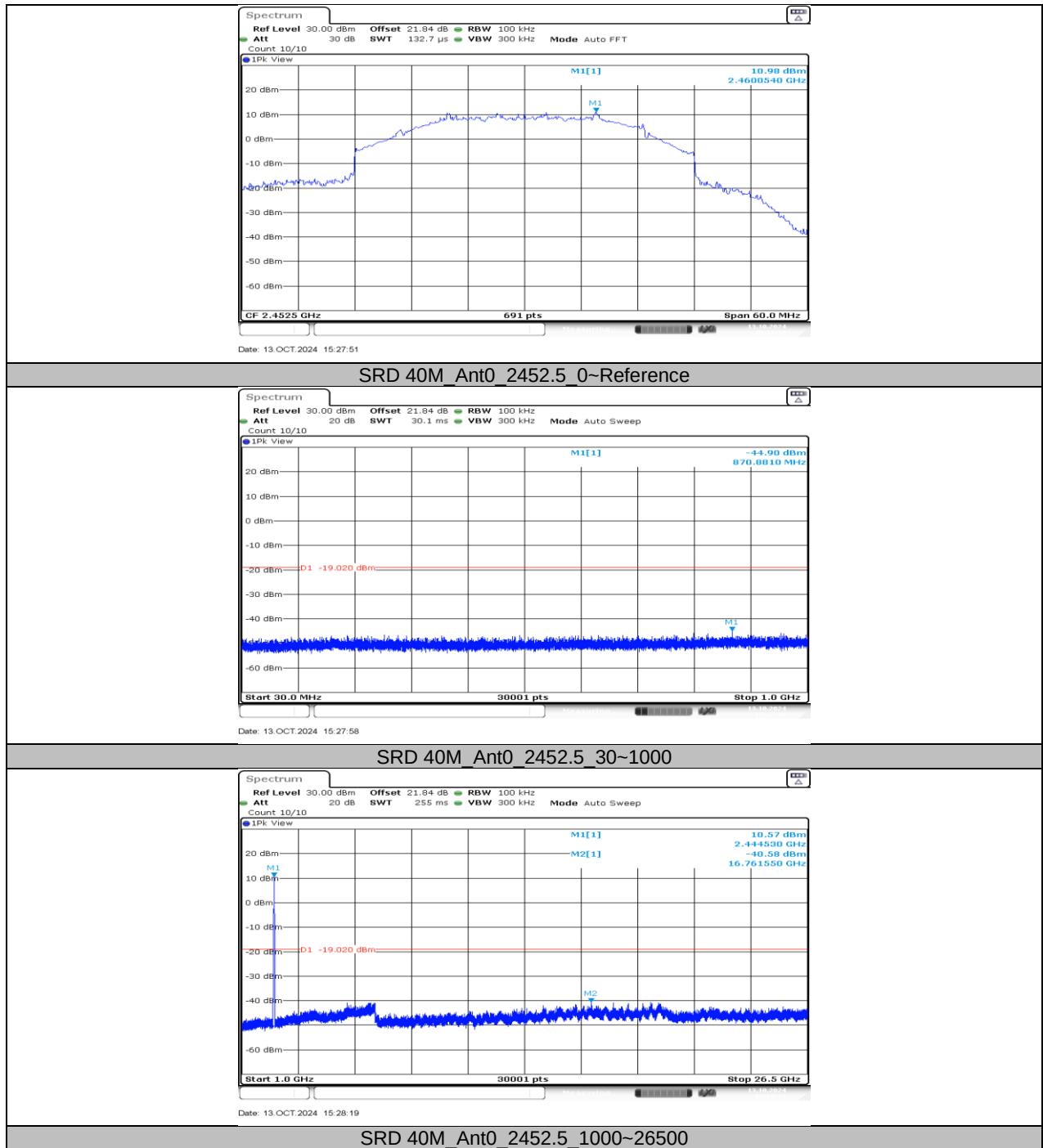
SRD 40M_Ant1_2432.5_1000~26500

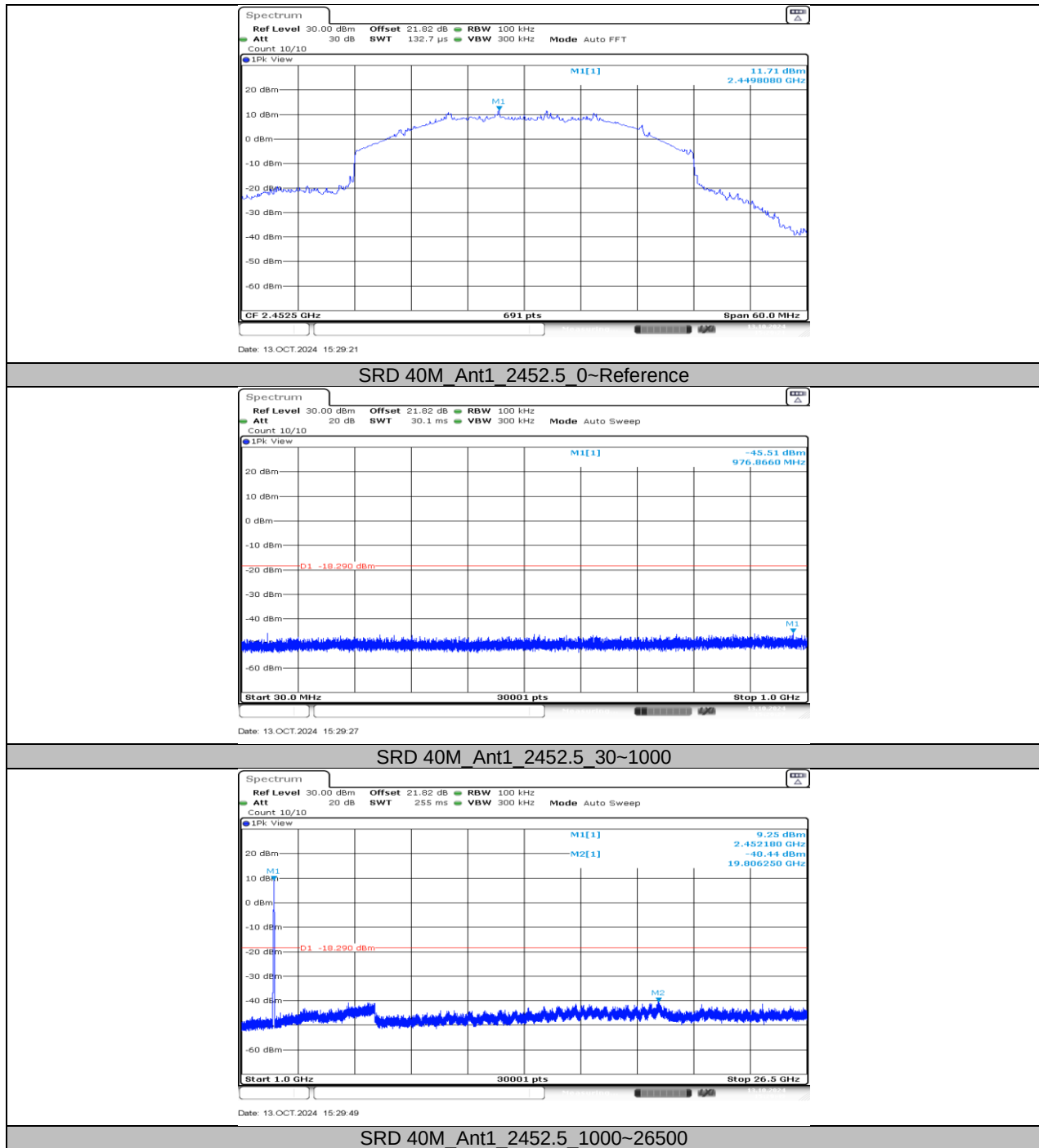


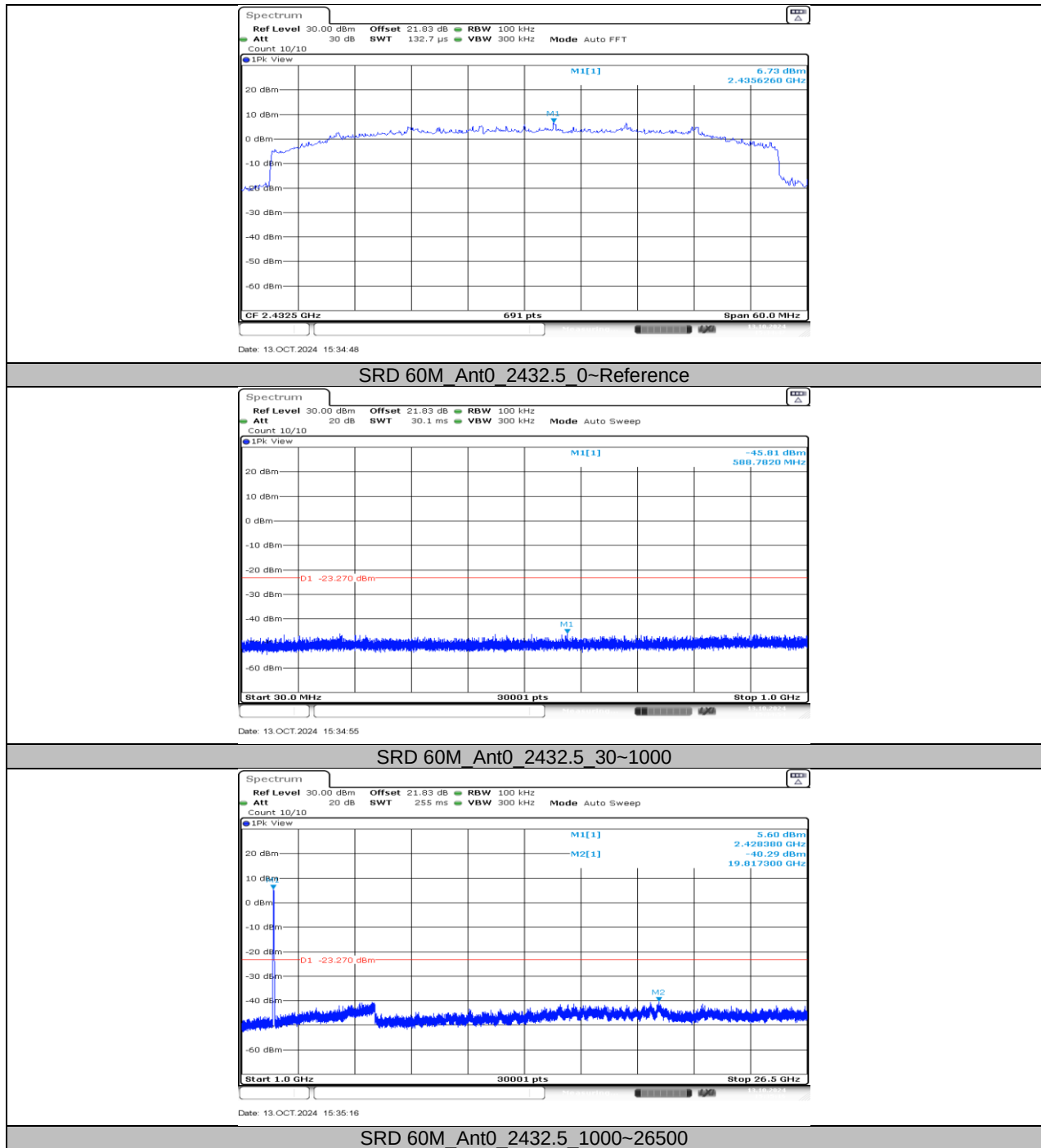


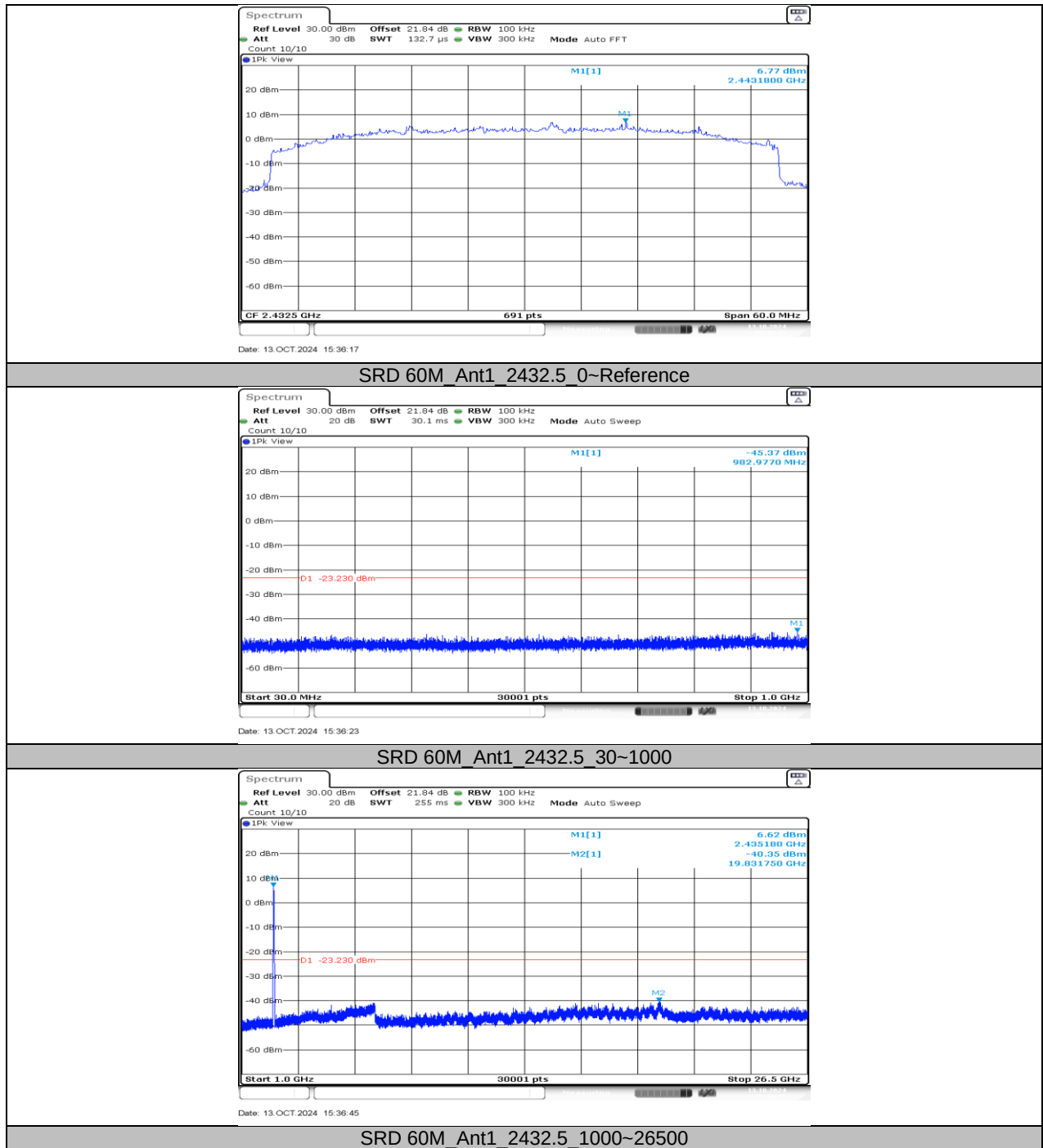


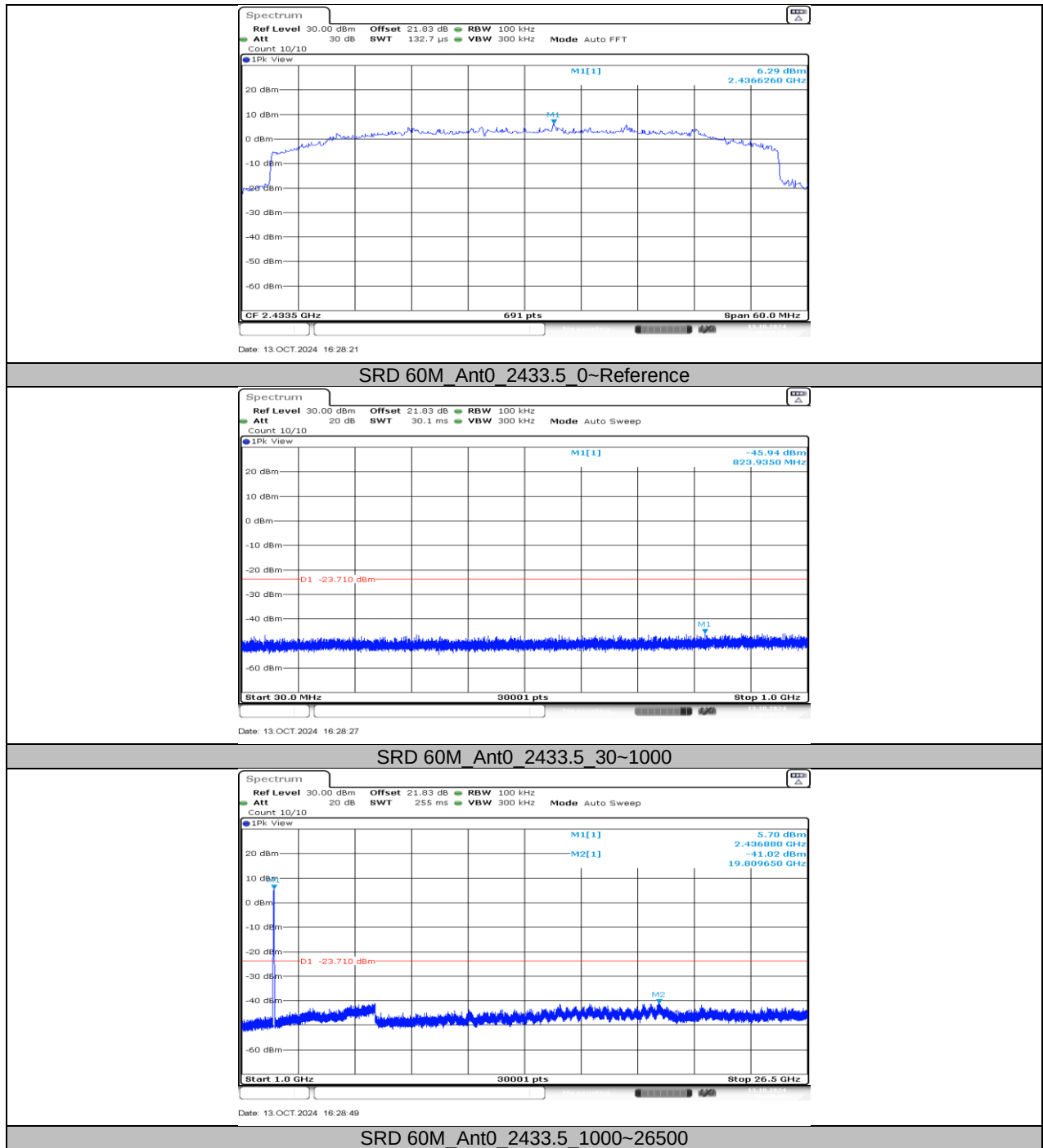


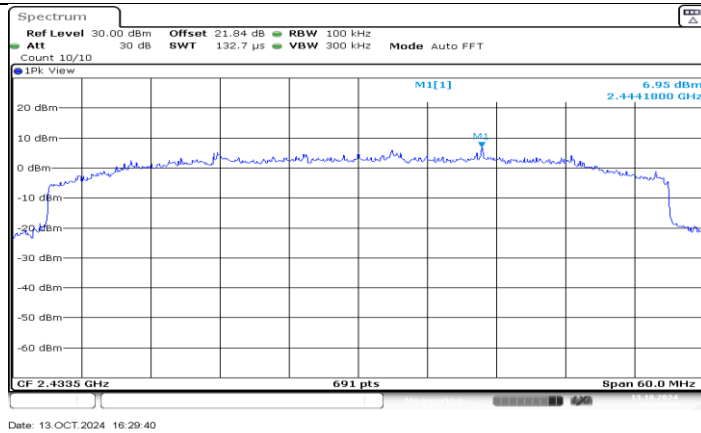




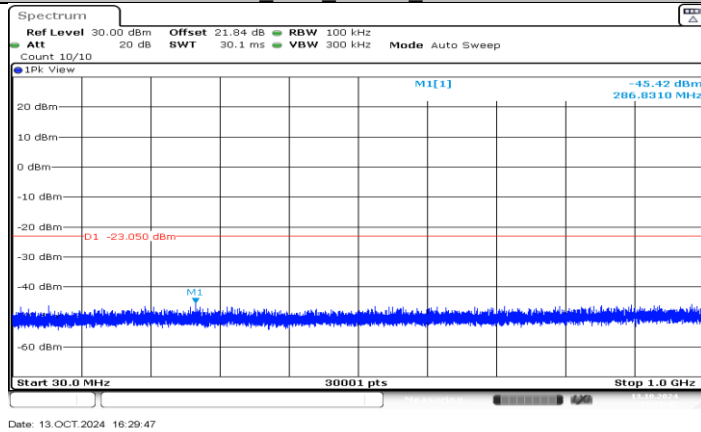




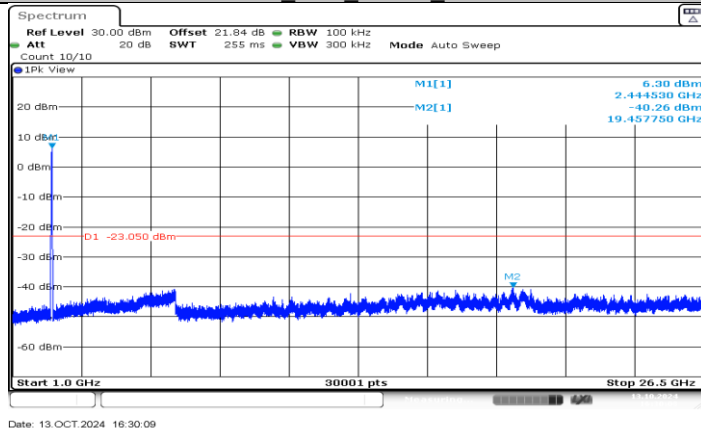




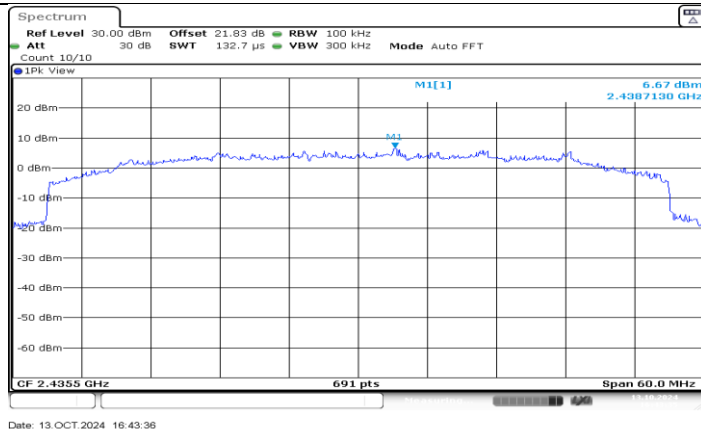
SRD 60M_Ant1_2433.5_0~Reference



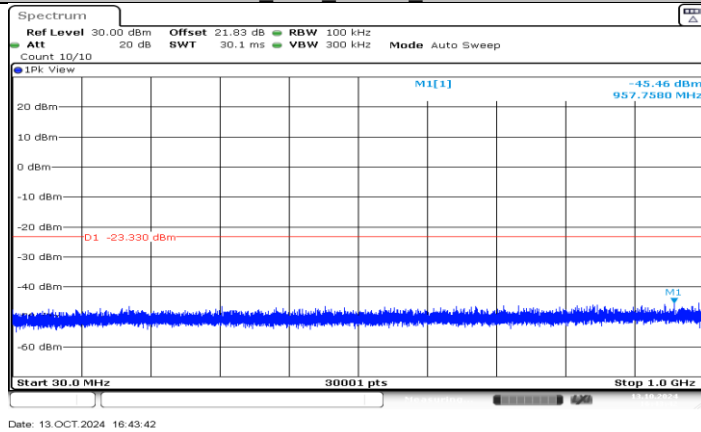
SRD 60M_Ant1_2433.5_30~1000



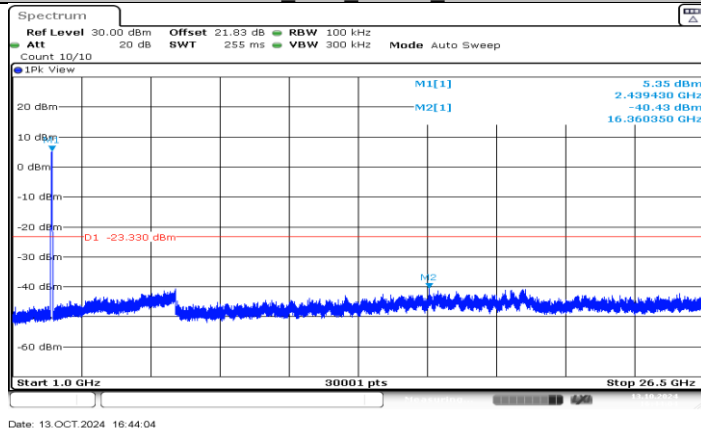
SRD 60M_Ant1_2433.5_1000~26500



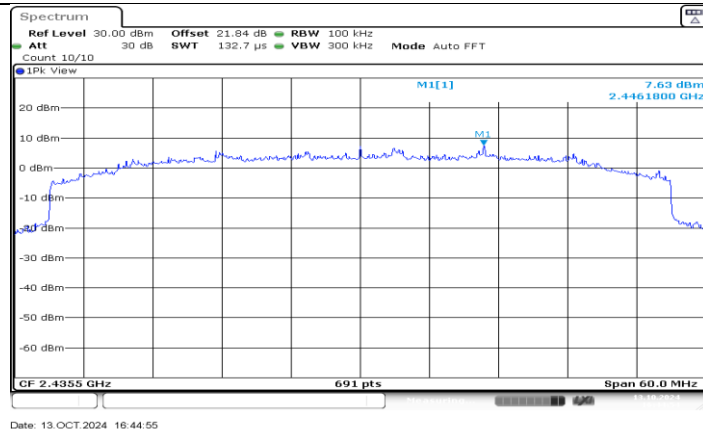
SRD 60M_Ant0_2435.5_0~Reference



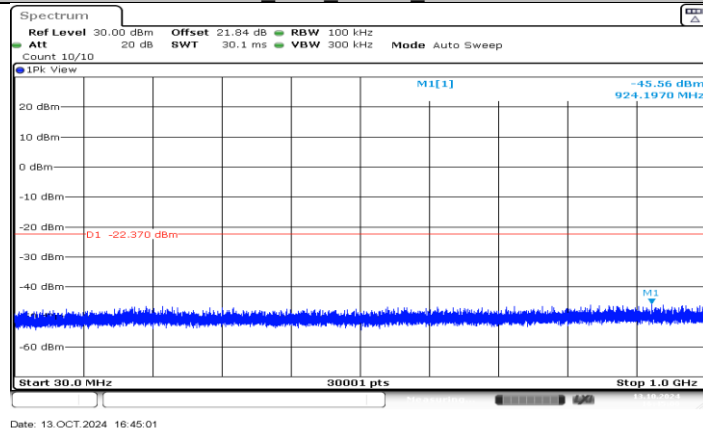
SRD 60M_Ant0_2435.5_30~1000



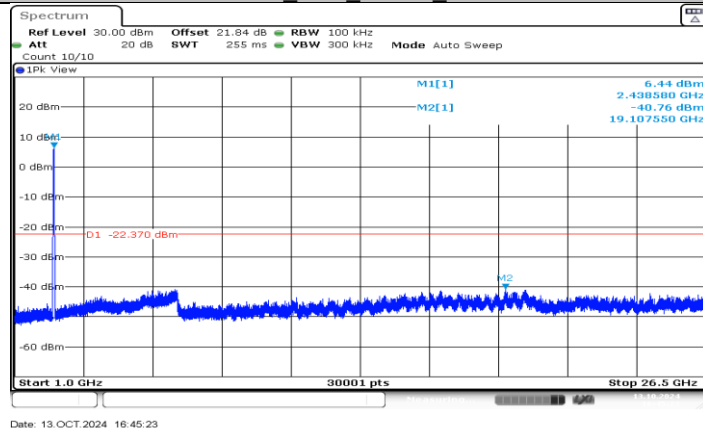
SRD 60M_Ant0_2435.5_1000~26500



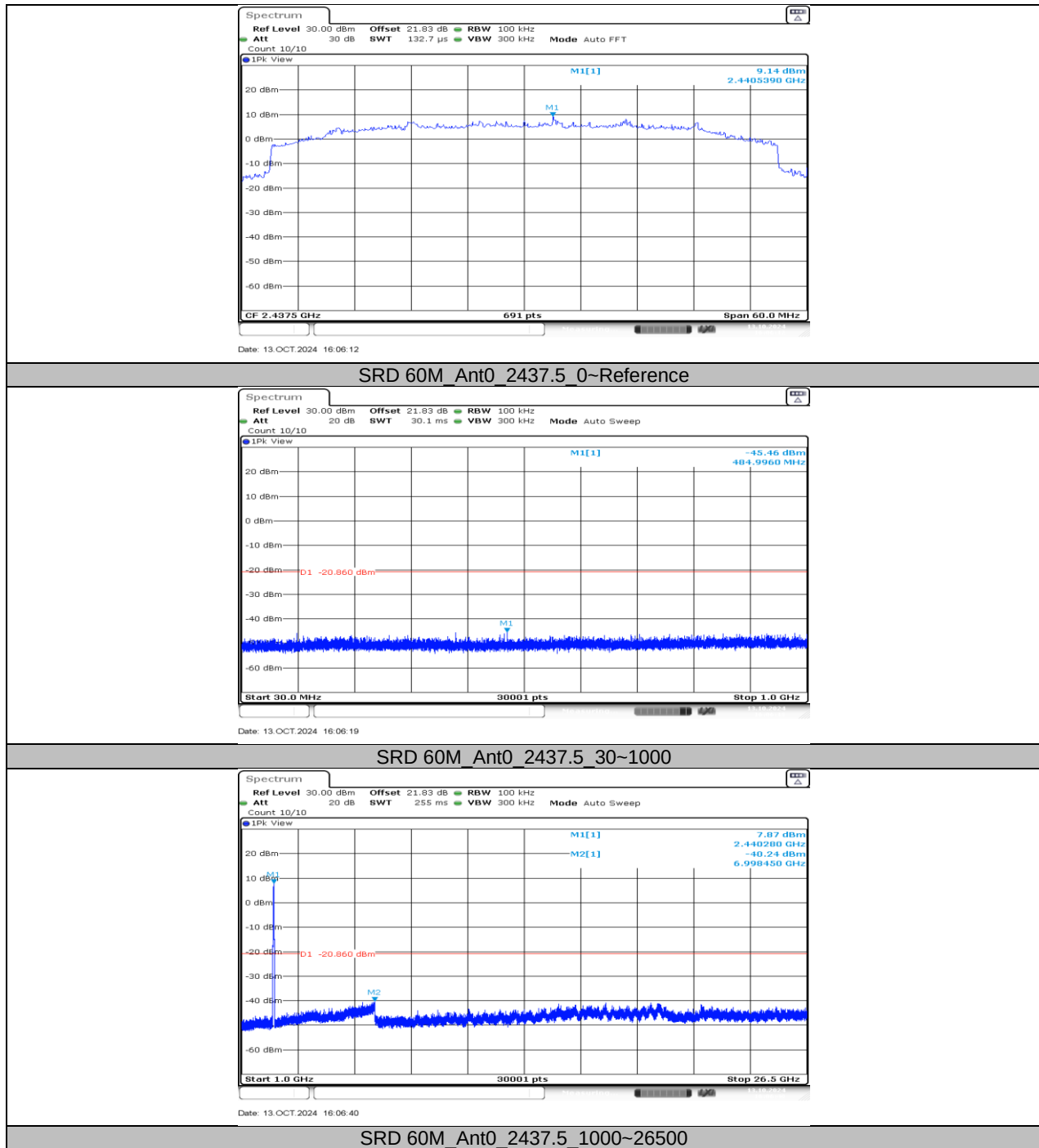
SRD 60M_Ant1_2435.5_0~Reference

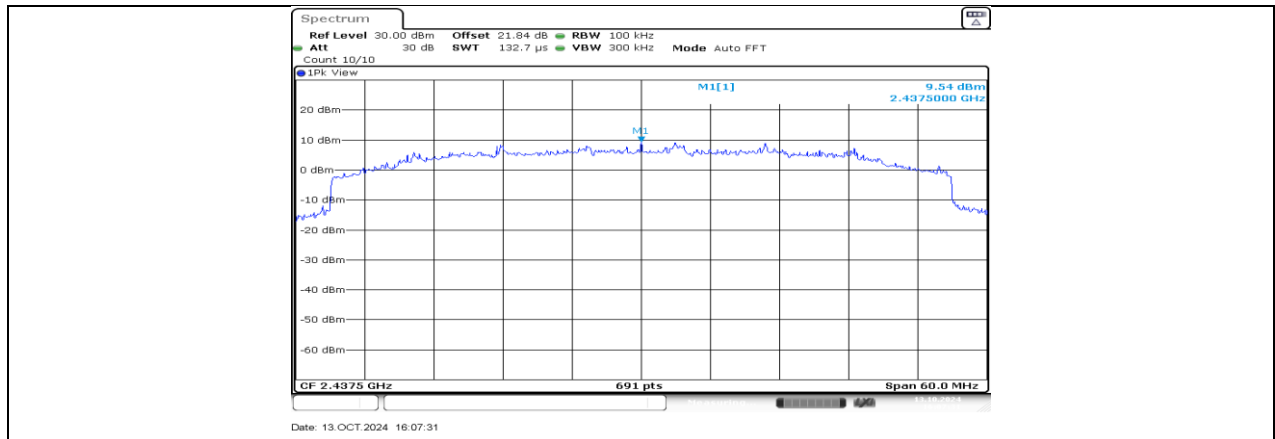


SRD 60M_Ant1_2435.5_30~1000

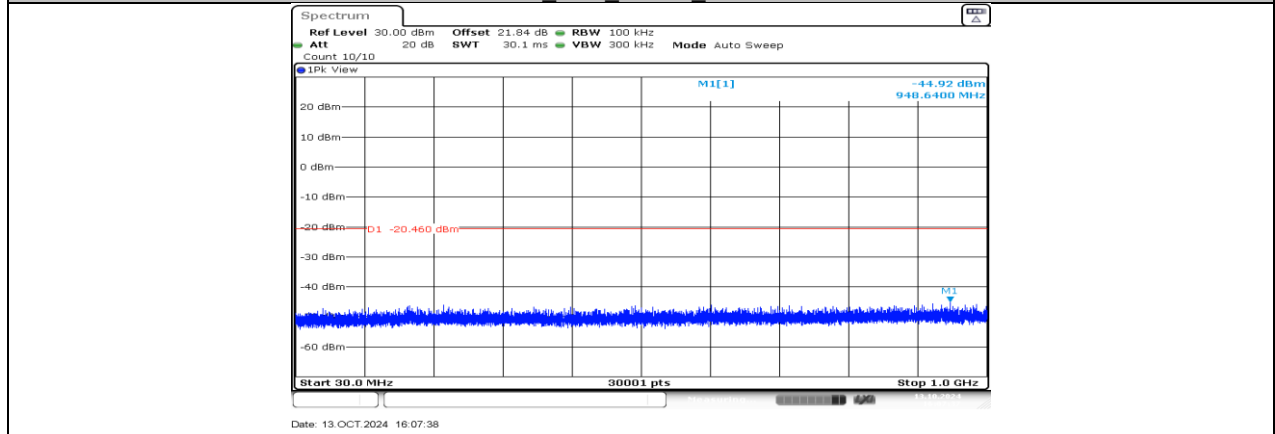


SRD 60M_Ant1_2435.5_1000~26500

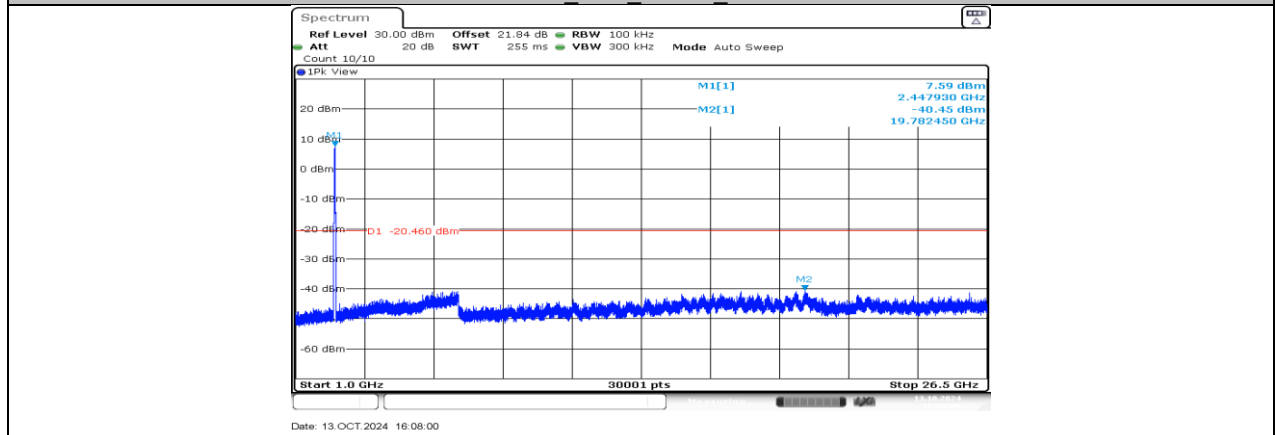




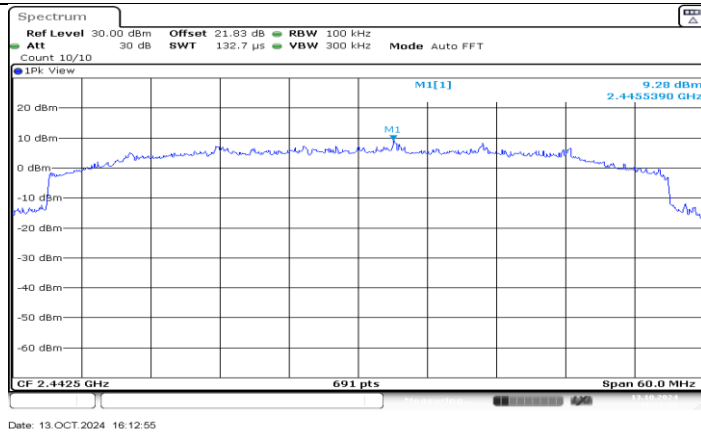
SRD 60M_Ant1_2437.5_0~Reference



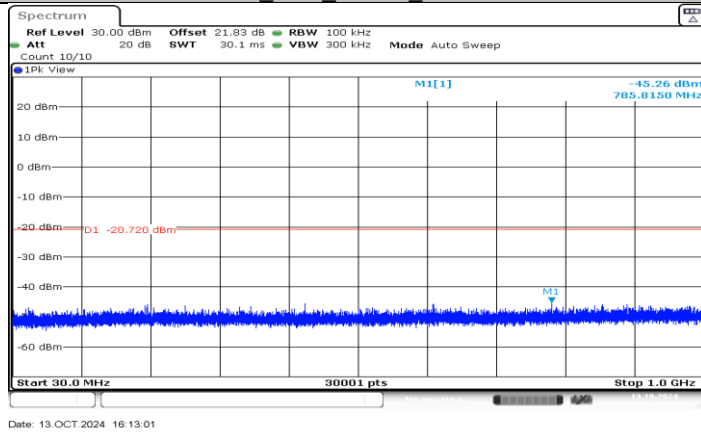
SRD 60M_Ant1_2437.5_30~1000



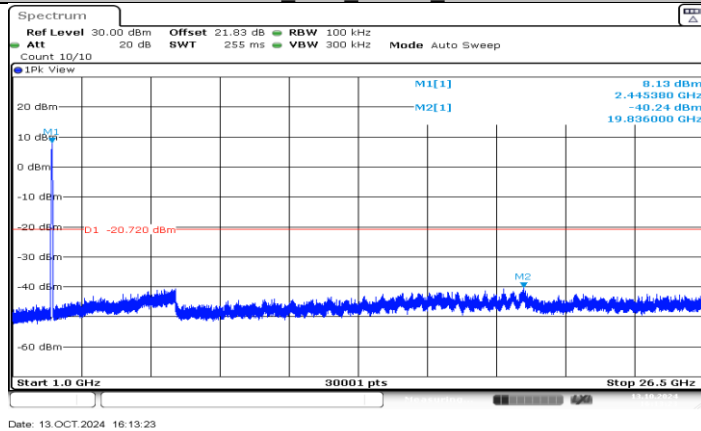
SRD 60M_Ant1_2437.5_1000~26500



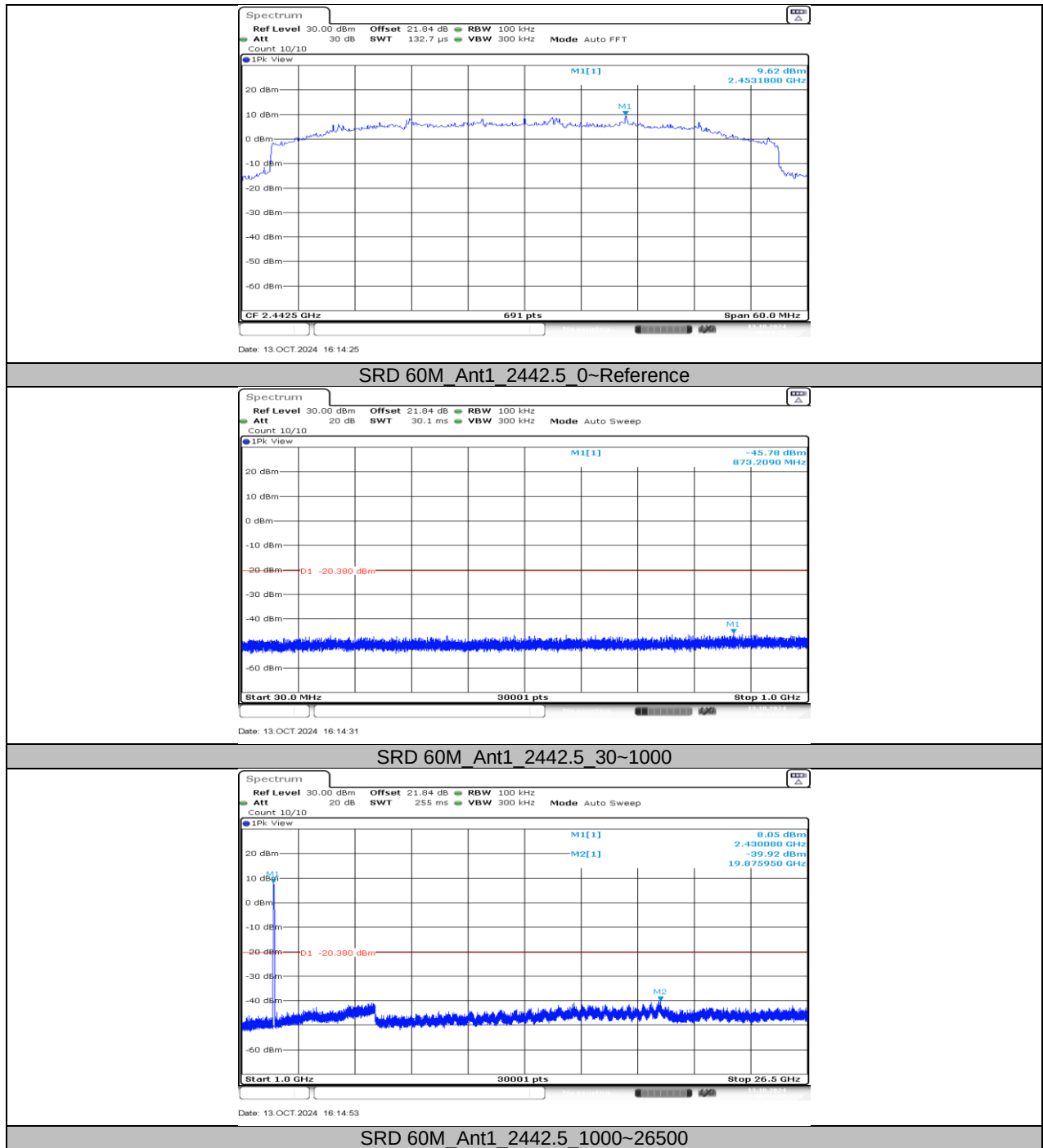
SRD 60M_Ant0_2442.5_0~Reference



SRD 60M_Ant0_2442.5_30~1000



SRD 60M_Ant0_2442.5_1000~26500



11.7. APPENDIX G: DUTY CYCLE**11.7.1. Test Result**

Test Mode	On Time (msec)	Period (msec)	Duty Cycle x (Linear)	Duty Cycle (%)	Duty Cycle Correction Factor (dB)	1/T Minimum VBW (kHz)	Final setting For VBW (kHz)
SRD 10M	105.00	105.00	1.0000	100.00	0.00	0.01	N/A
SRD 20M	105.00	105.00	1.0000	100.00	0.00	0.01	N/A
SRD 40M	105.00	105.00	1.0000	100.00	0.00	0.01	N/A
SRD 60M	105.00	105.00	1.0000	100.00	0.00	0.01	N/A

Note:

Duty Cycle Correction Factor=10log (1/x).

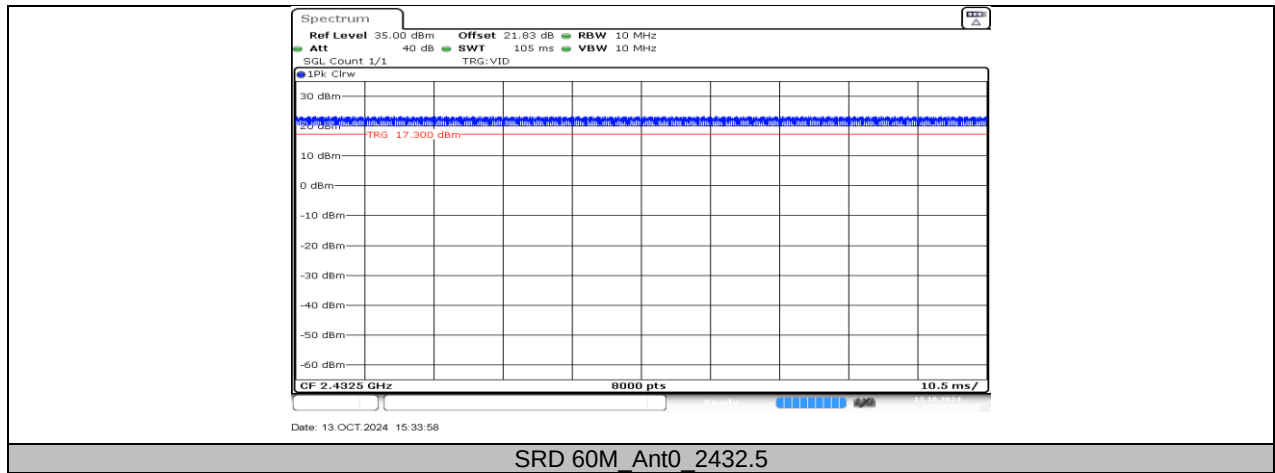
Where: x is Duty Cycle (Linear)

Where: T is On Time

If that calculated VBW is not available on the analyzer then the next higher value should be used.

11.7.2. Test Graphs





END OF REPORT