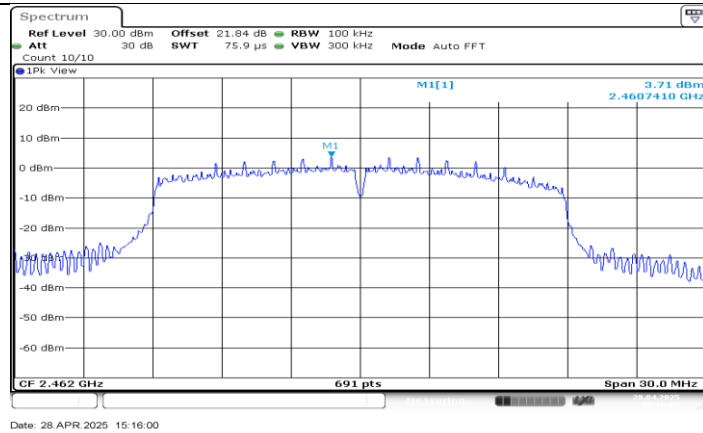
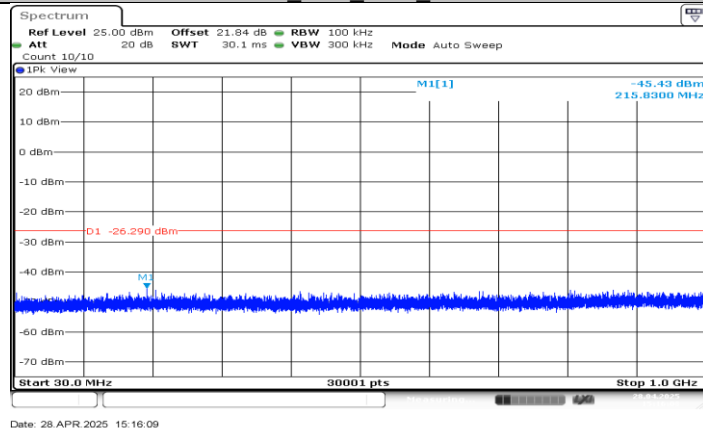
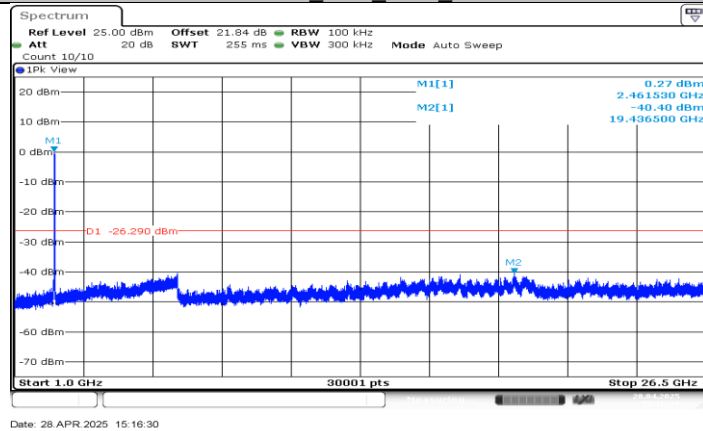




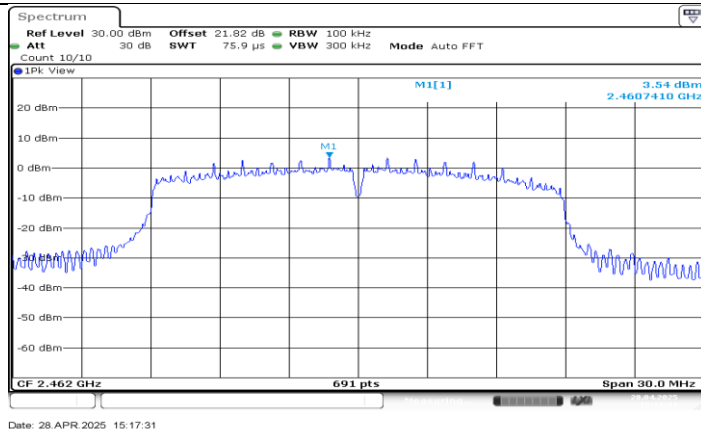
11N20MIMO_Ant1_2462_0~Reference



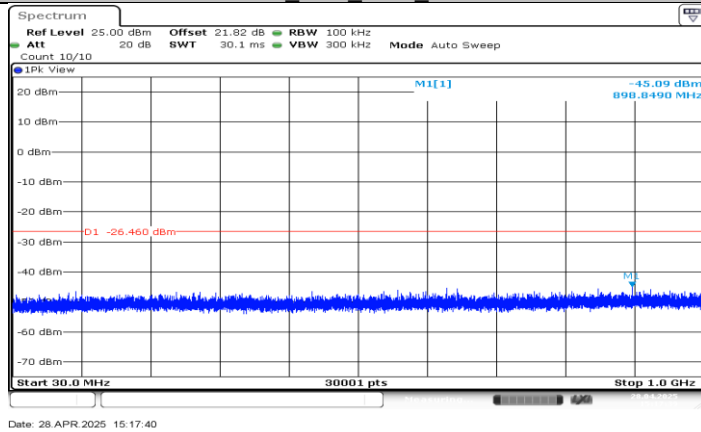
11N20MIMO_Ant1_2462_30~1000



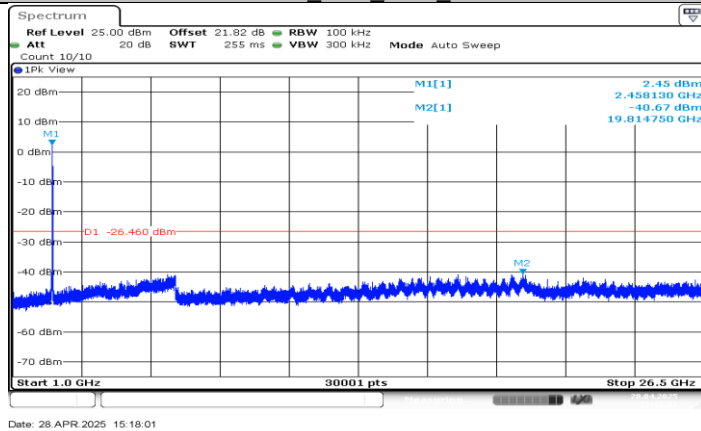
11N20MIMO_Ant1_2462_1000~26500



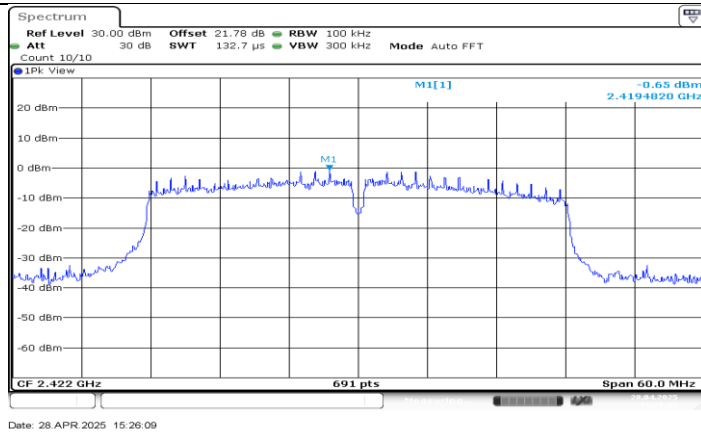
11N20MIMO_Ant3_2462_0~Reference



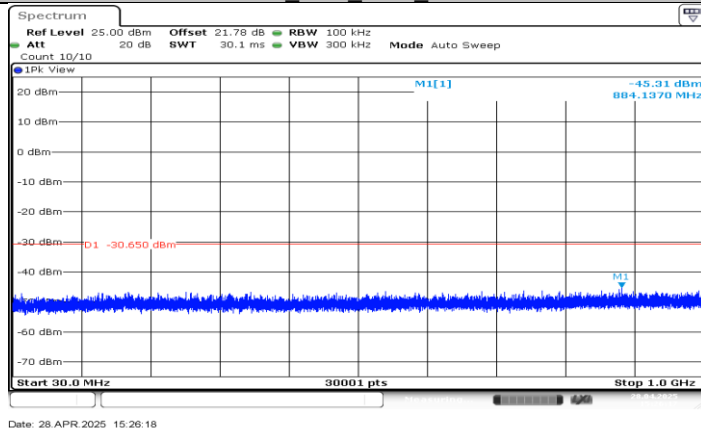
11N20MIMO_Ant3_2462_30~1000



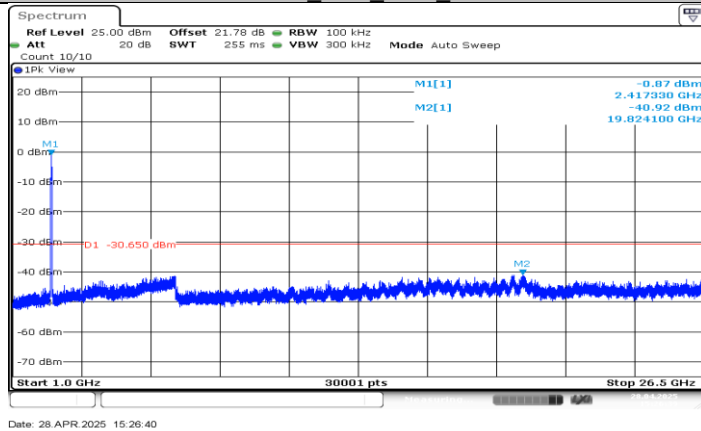
11N20MIMO_Ant3_2462_1000~26500



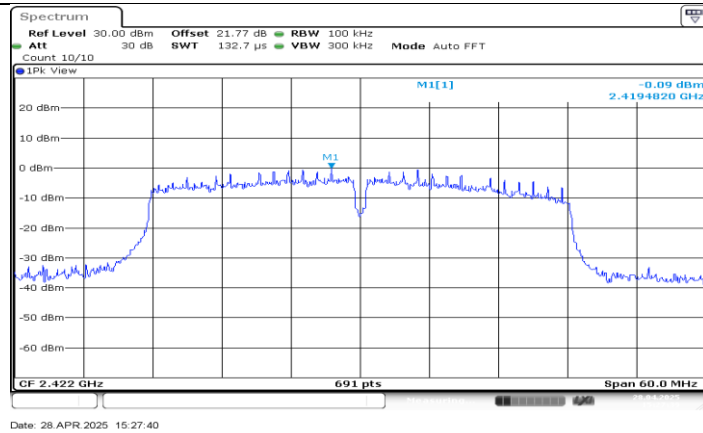
11N40MIMO_Ant1_2422_0~Reference



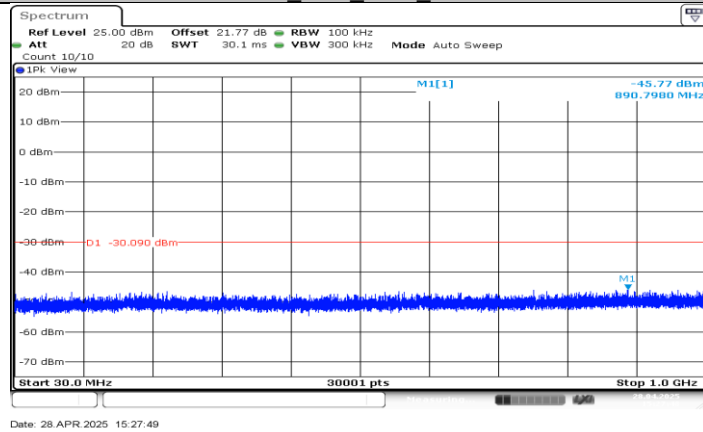
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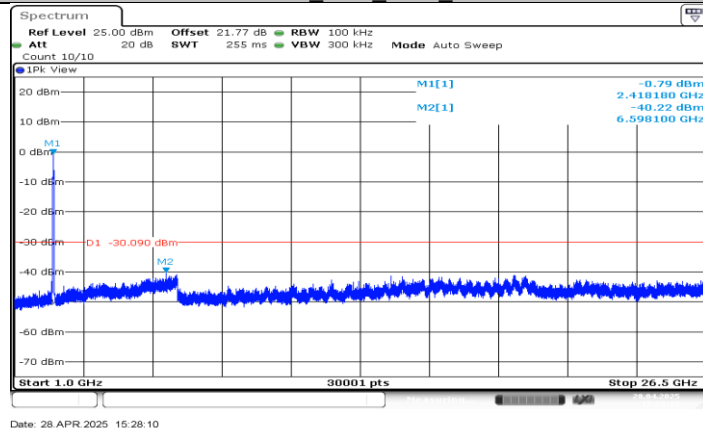
11N40MIMO_Ant1_2422_1000~26500



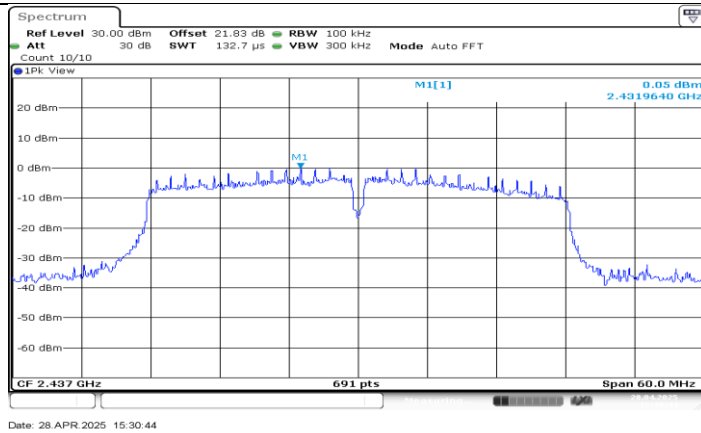
11N40MIMO_Ant3_2422_0~Reference



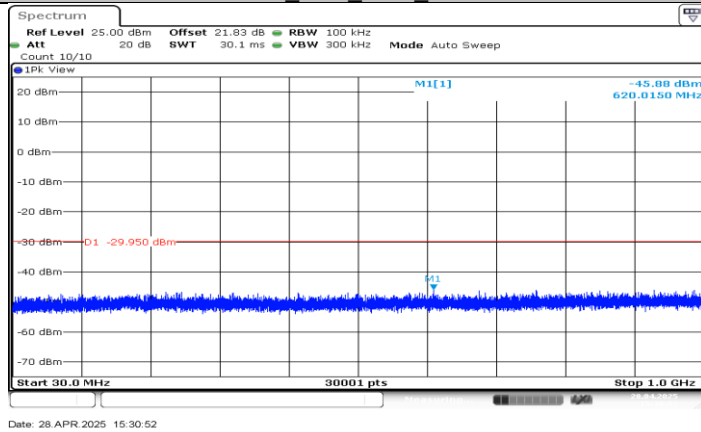
11N40MIMO_Ant3_2422_30~1000



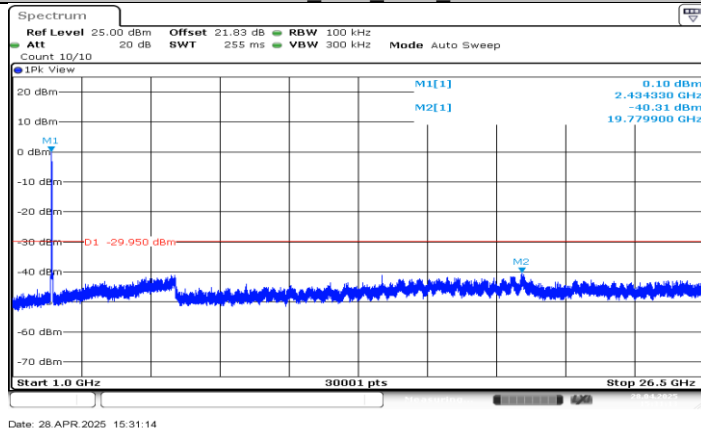
11N40MIMO_Ant3_2422_1000~26500



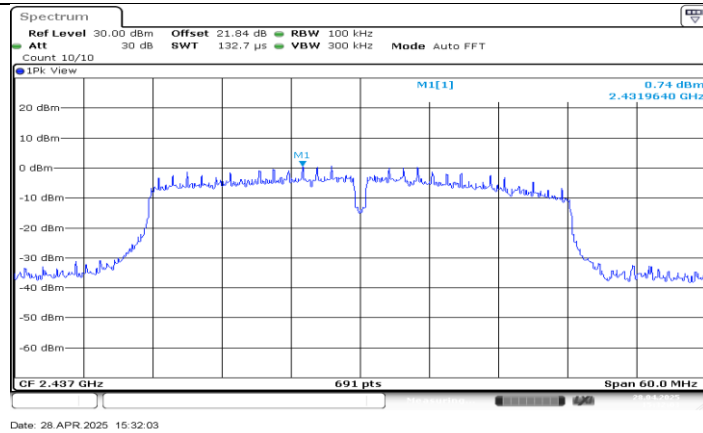
11N40MIMO_Ant1_2437_0~Reference



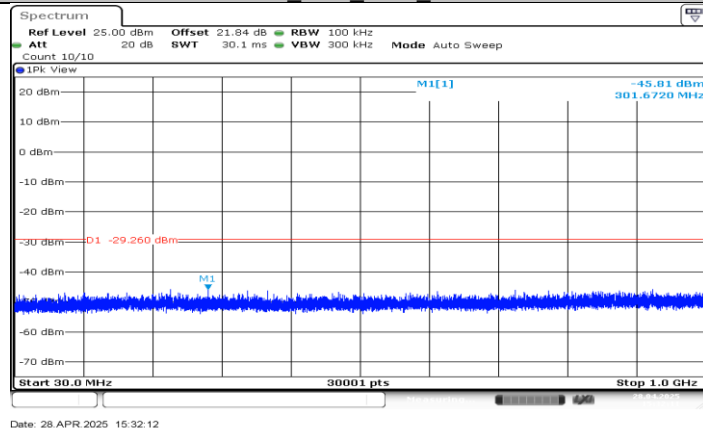
11N40MIMO_Ant1_2437_30~1000



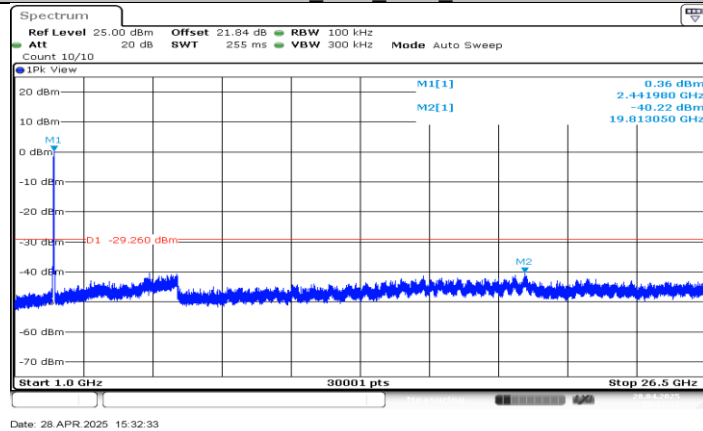
11N40MIMO_Ant1_2437_1000~26500



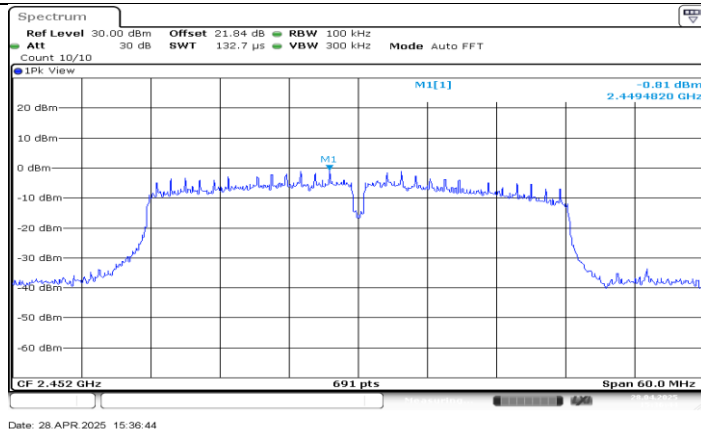
11N40MIMO_Ant3_2437_0~Reference



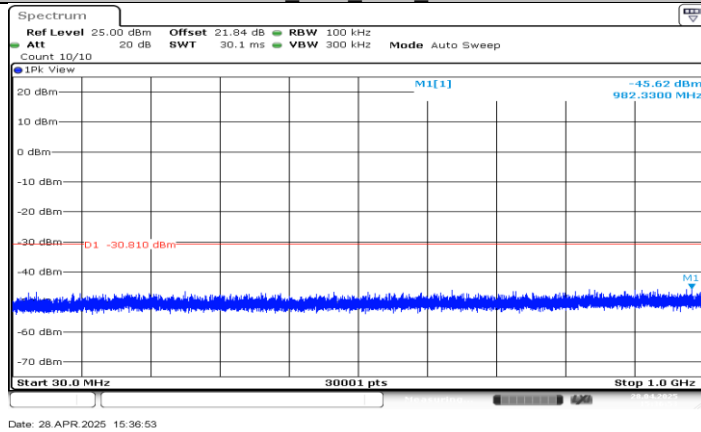
11N40MIMO_Ant3_2437_30~1000



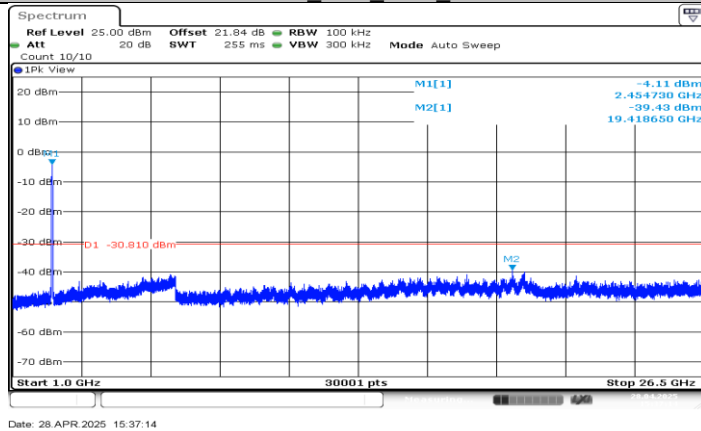
11N40MIMO_Ant3_2437_1000~26500



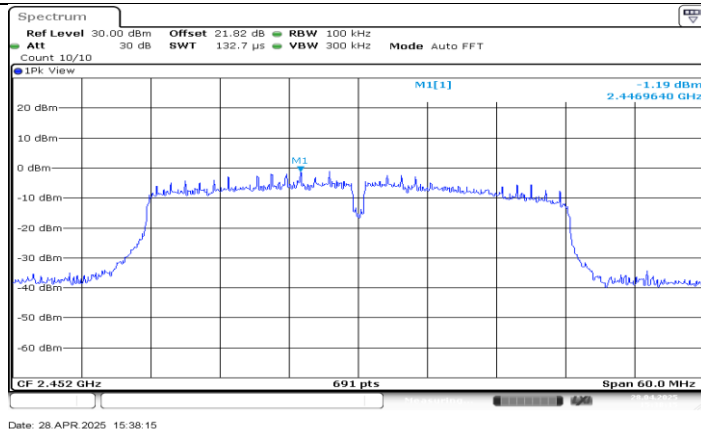
11N40MIMO_Ant1_2452_0~Reference



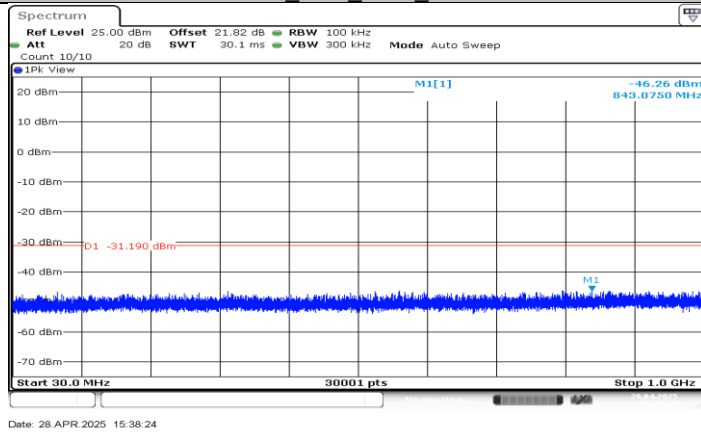
11N40MIMO_Ant1_2452_30~1000



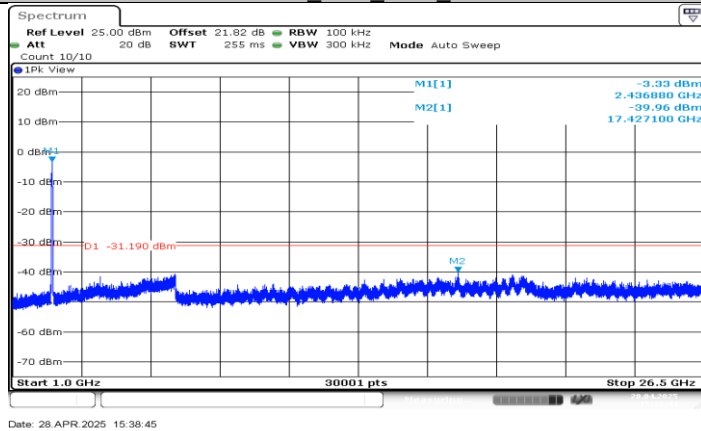
11N40MIMO_Ant1_2452_1000~26500



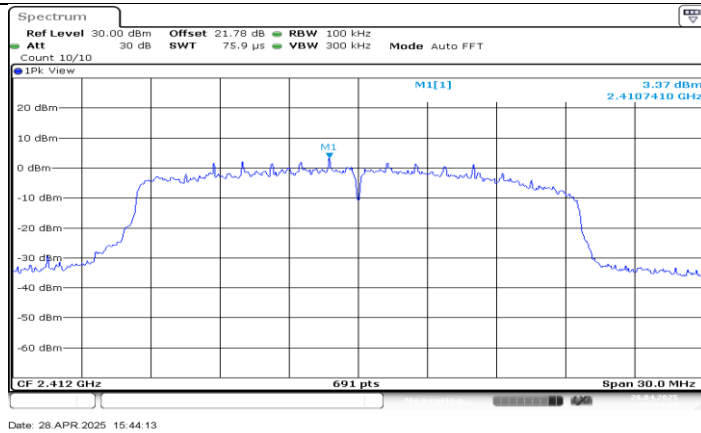
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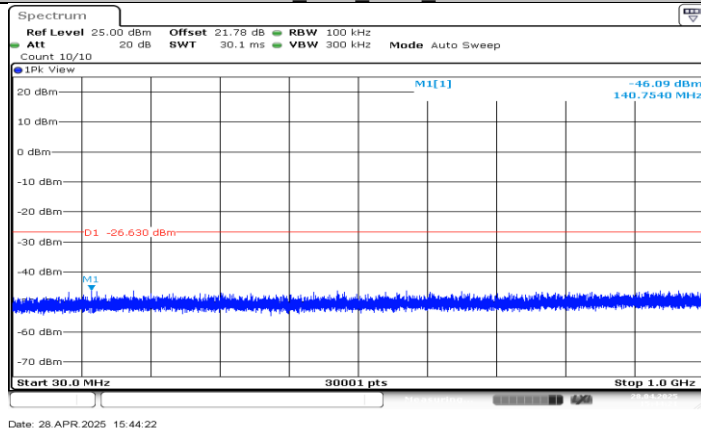
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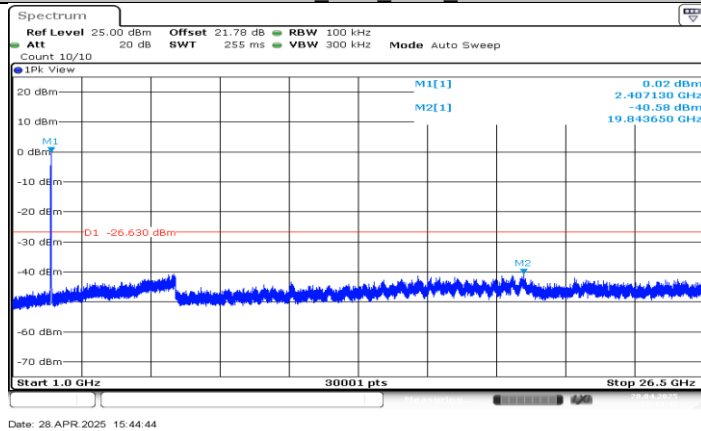
11N40MIMO_Ant3_2452_1000~26500



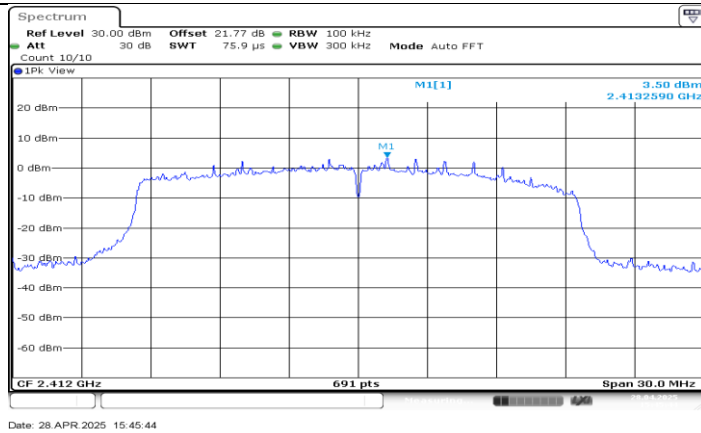
11AX20MIMO_Ant1_2412_0~Reference



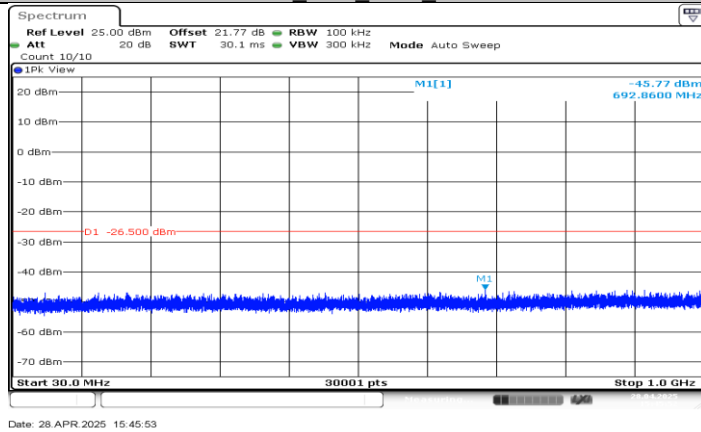
11AX20MIMO_Ant1_2412_30~1000



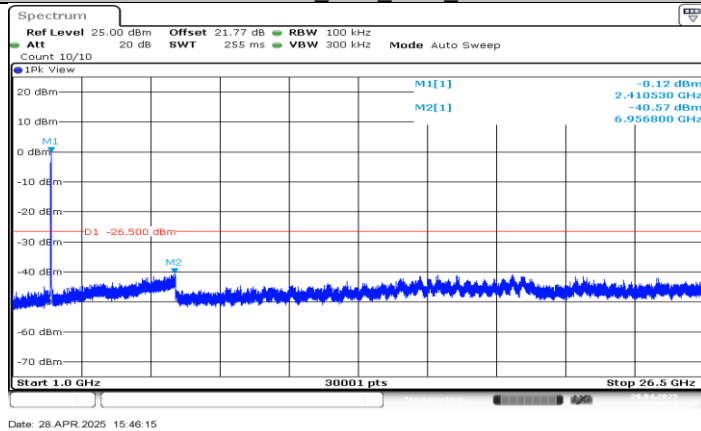
11AX20MIMO_Ant1_2412_1000~26500



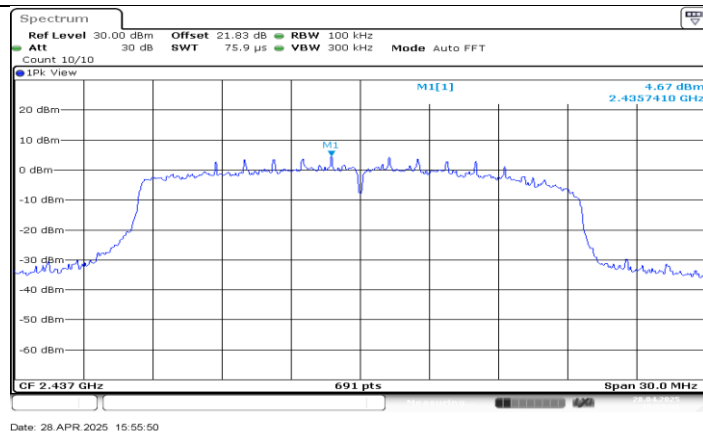
11AX20MIMO_Ant3_2412_0~Reference



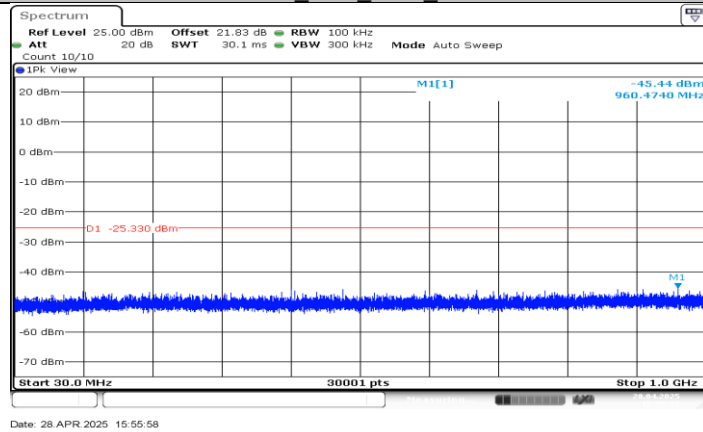
11AX20MIMO_Ant3_2412_30~1000



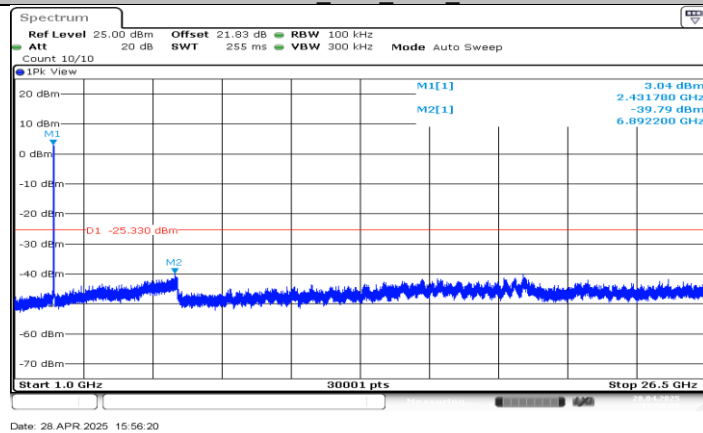
11AX20MIMO_Ant3_2412_1000~26500



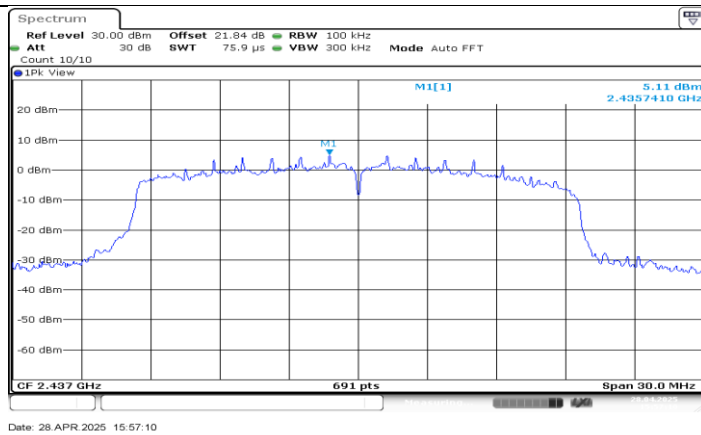
11AX20MIMO_Ant1_2437_0~Reference



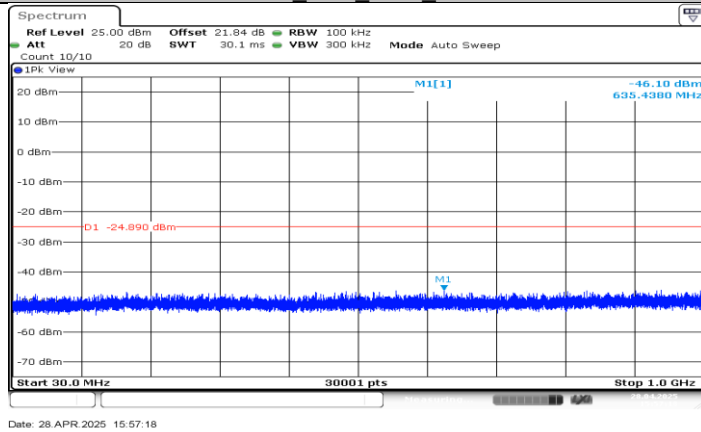
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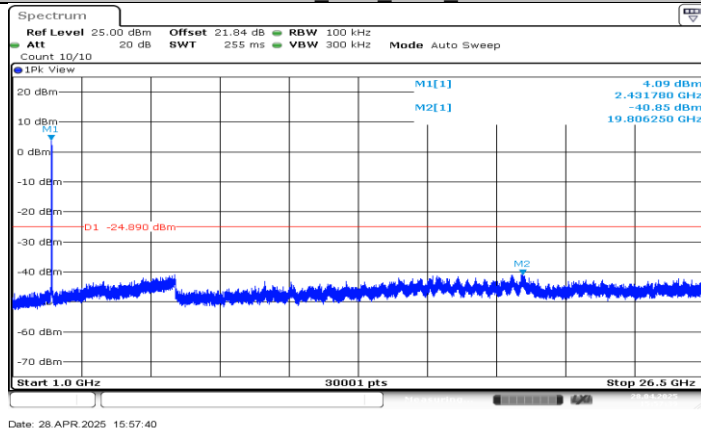
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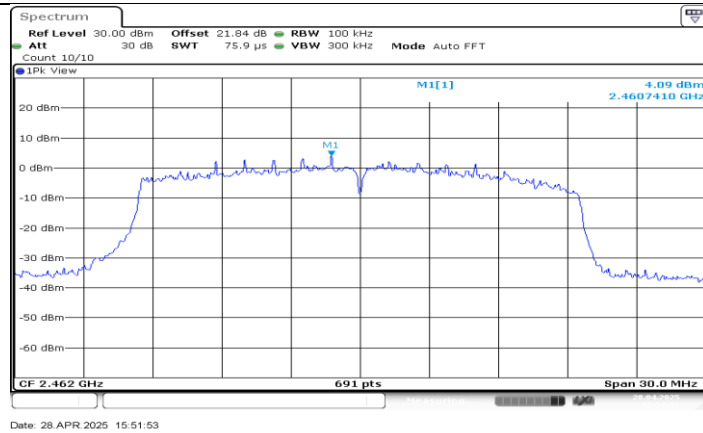
11AX20MIMO_Ant3_2437_0~Reference



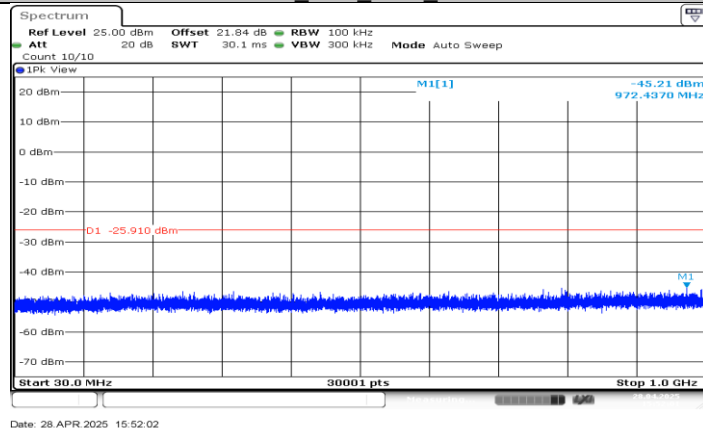
11AX20MIMO_Ant3_2437_30~1000



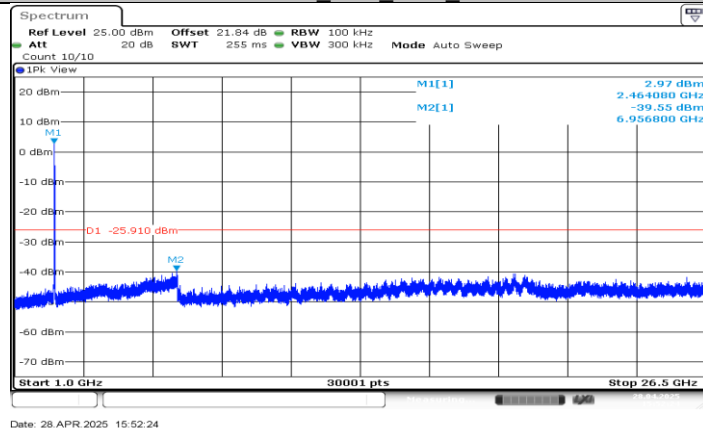
11AX20MIMO_Ant3_2437_1000~26500



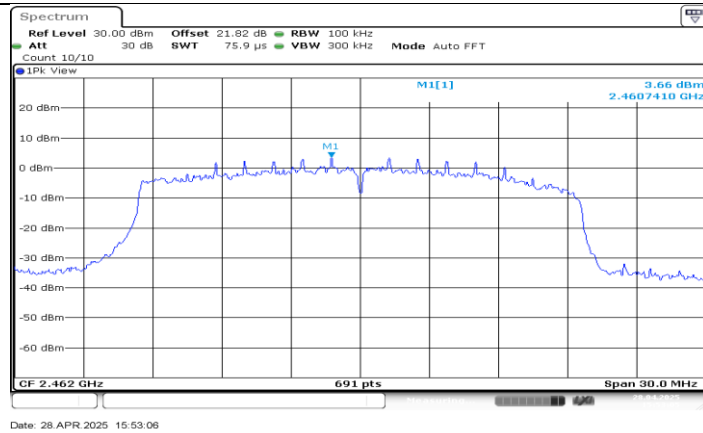
11AX20MIMO_Ant1_2462_0~Reference



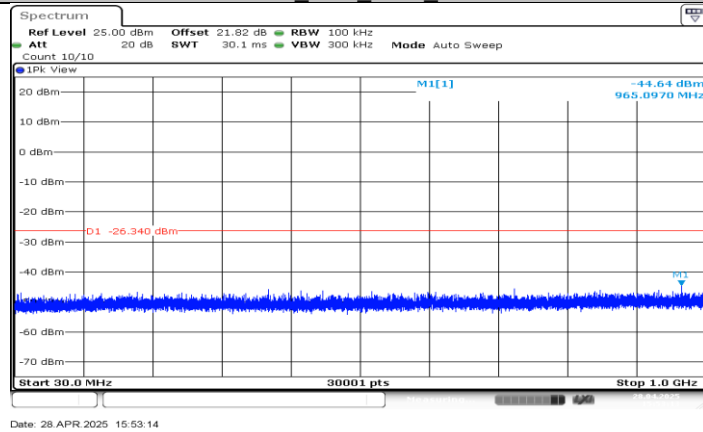
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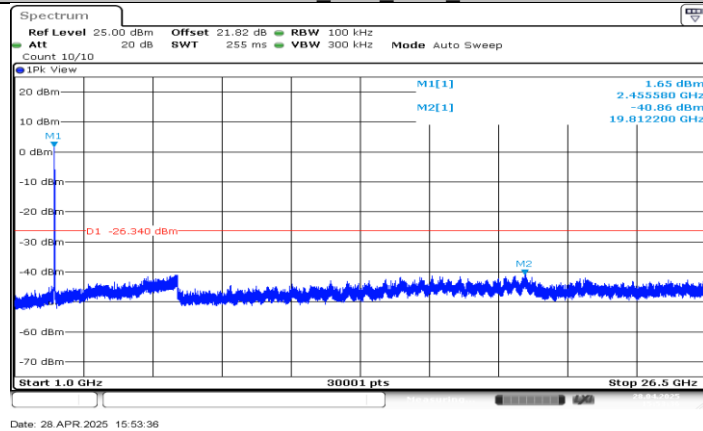
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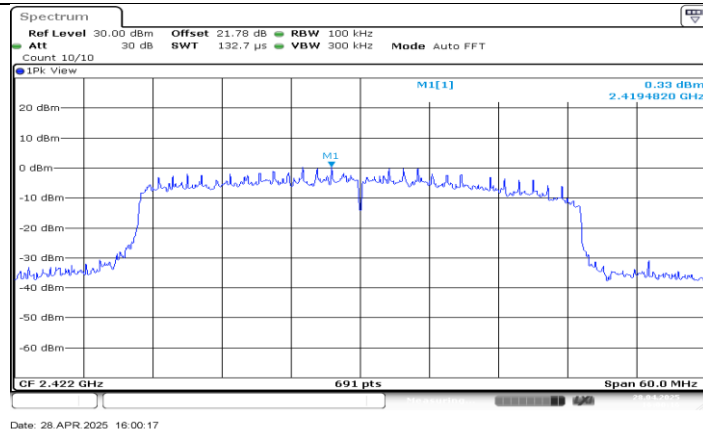
11AX20MIMO_Ant3_2462_0~Reference



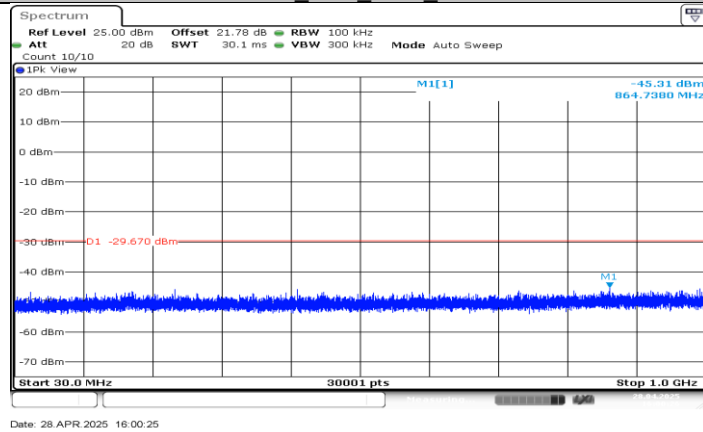
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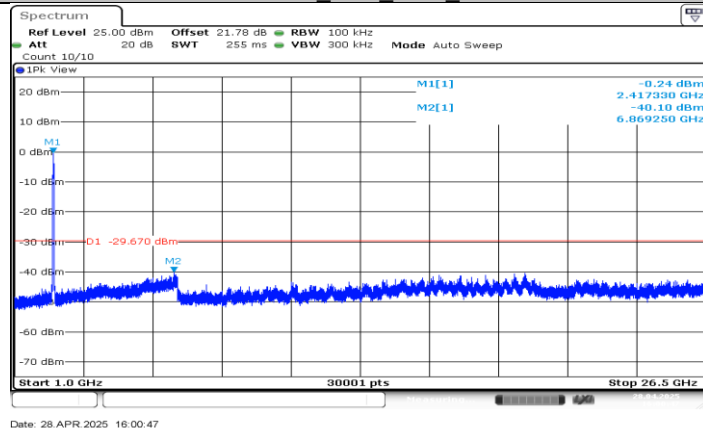
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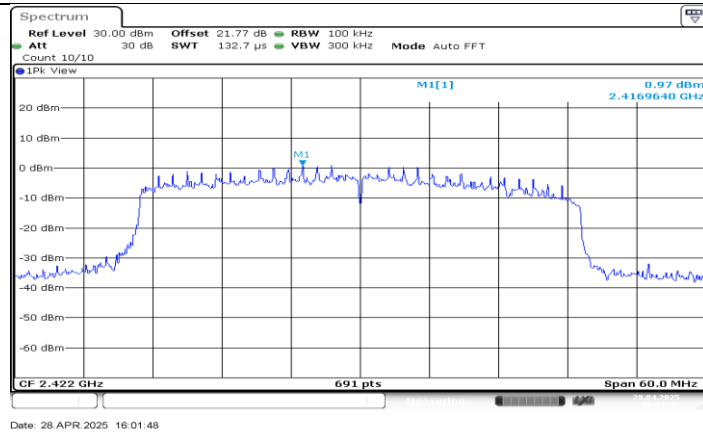
11AX40MIMO_Ant1_2422_0~Reference



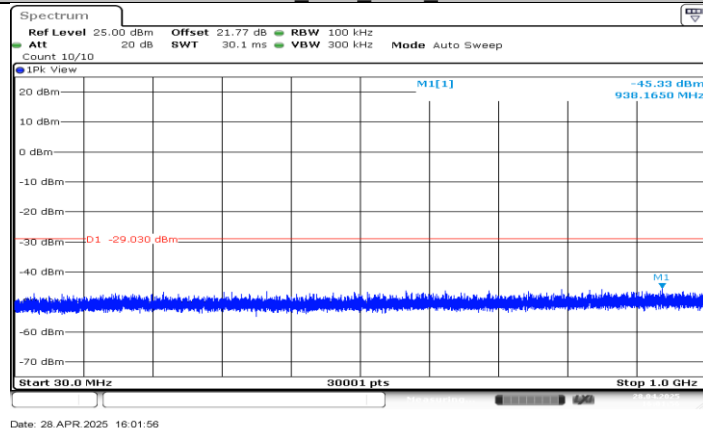
11AX40MIMO_Ant1_2422_30~1000



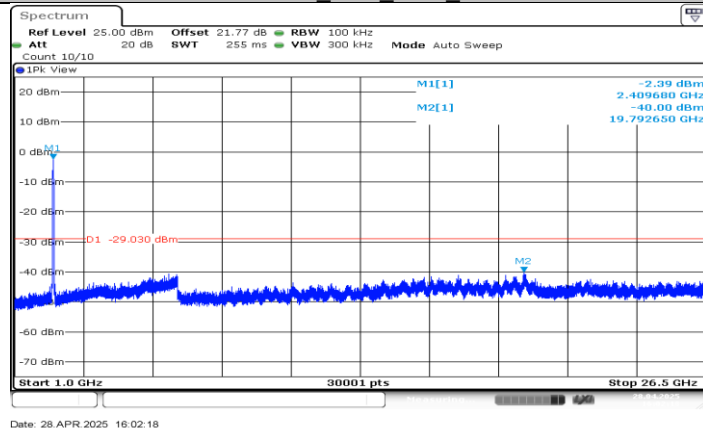
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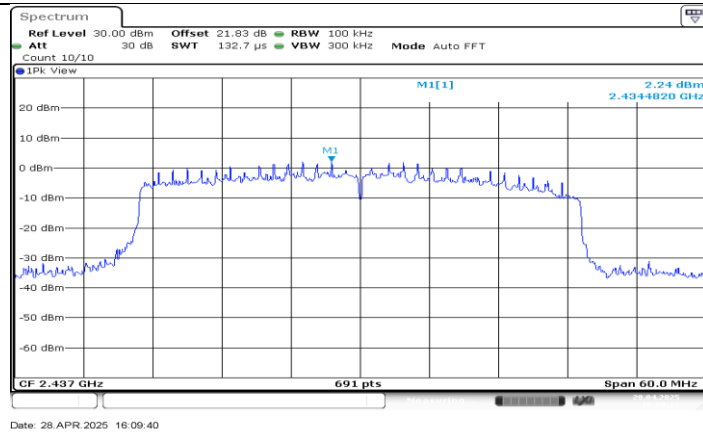
11AX40MIMO_Ant3_2422_0~Reference



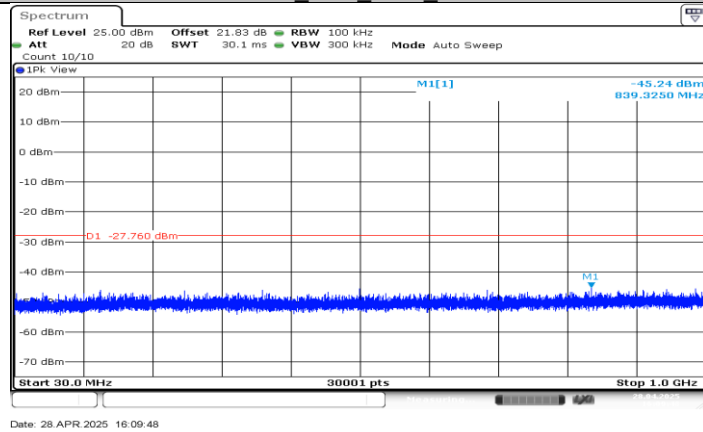
11AX40MIMO_Ant3_2422_30~1000



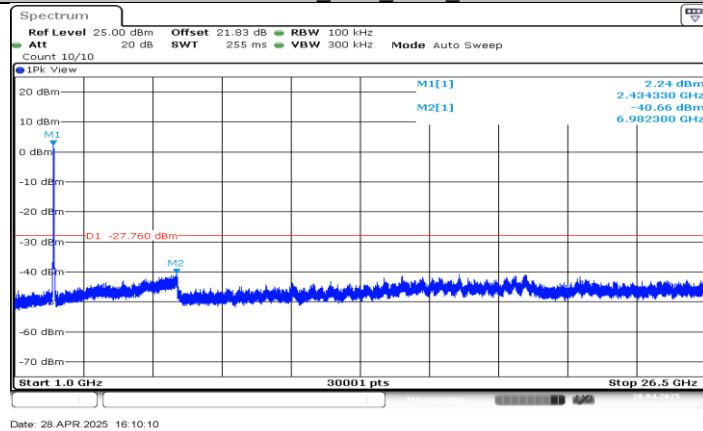
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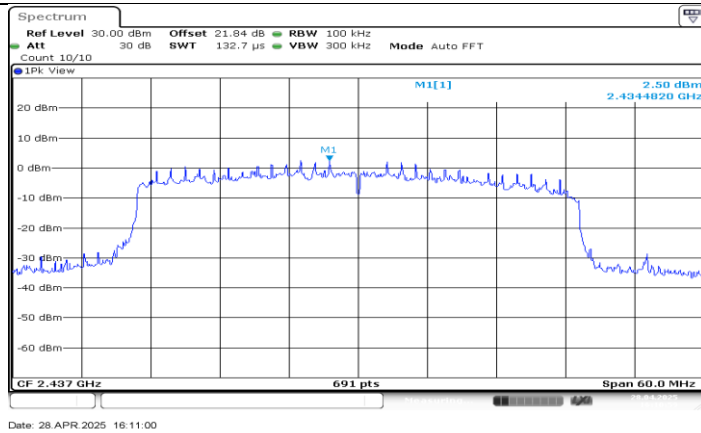
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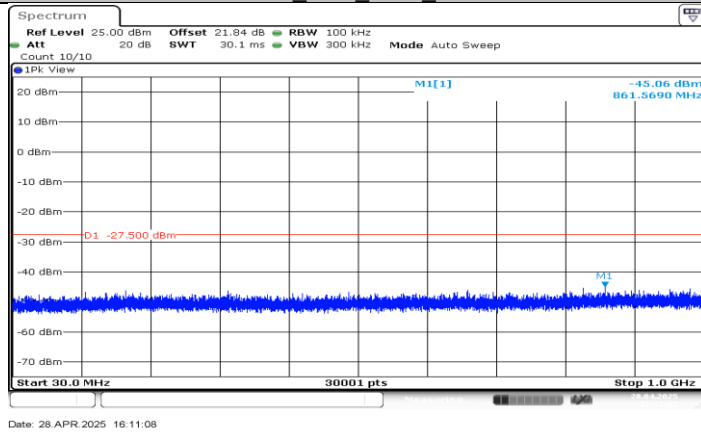
11AX40MIMO_Ant1_2437_30~1000



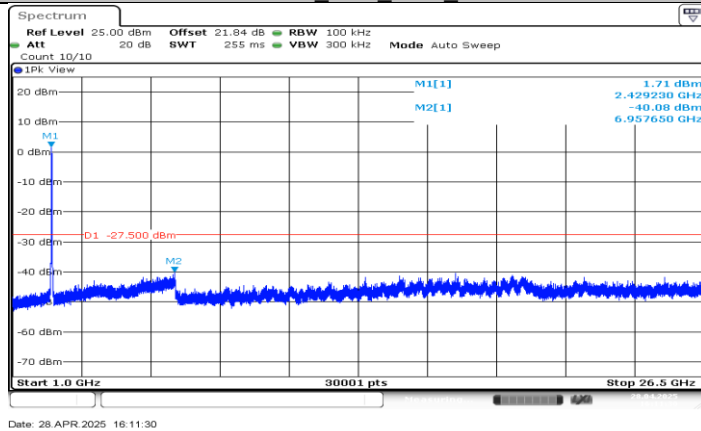
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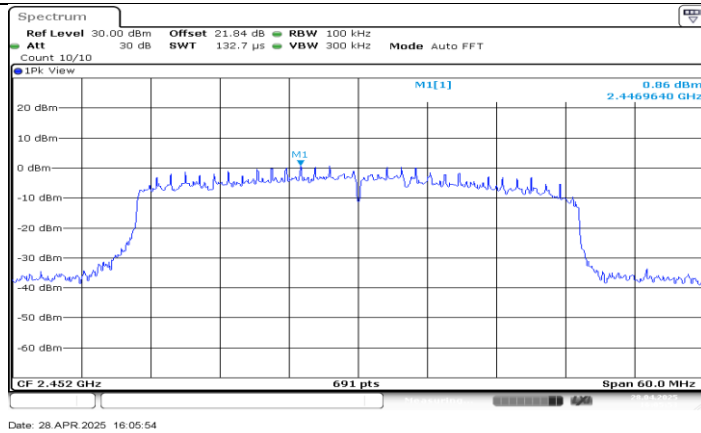
11AX40MIMO_Ant3_2437_0~Reference



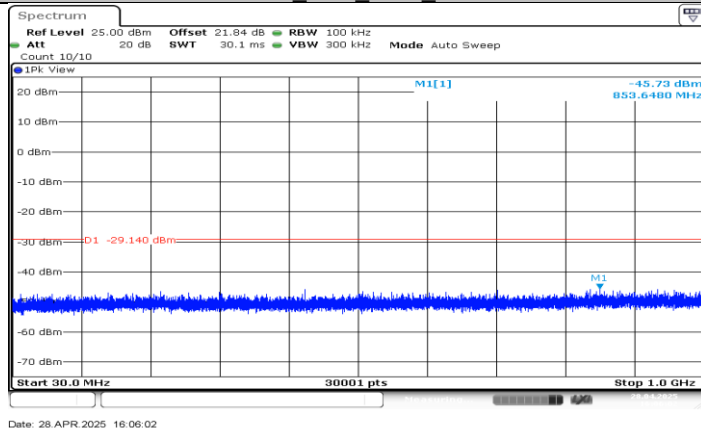
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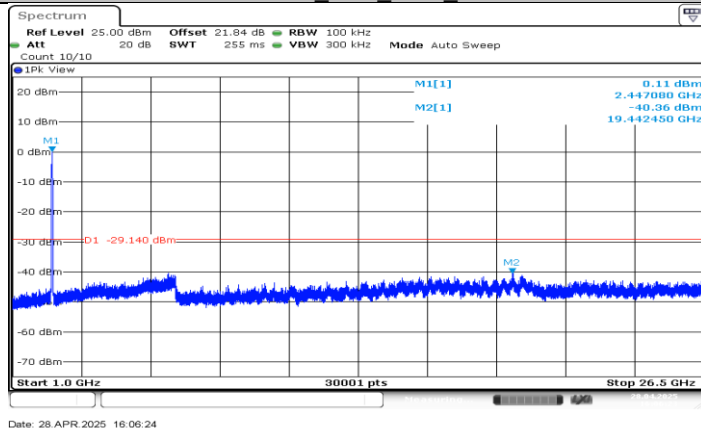
11AX40MIMO_Ant3_2437_1000~26500



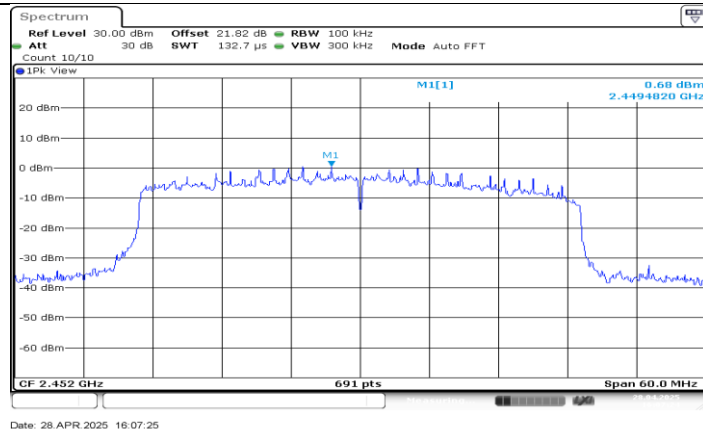
11AX40MIMO_Ant1_2452_0~Reference



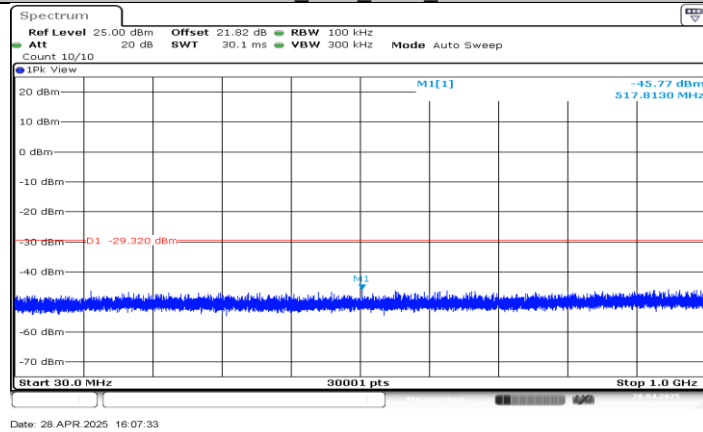
11AX40MIMO_Ant1_2452_30~1000



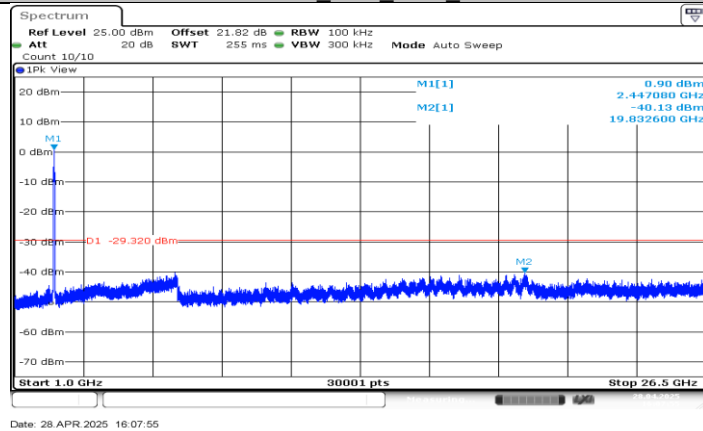
11AX40MIMO_Ant1_2452_1000~26500



11AX40MIMO_Ant3_2452_0~Reference



11AX40MIMO_Ant3_2452_30~1000



11AX40MIMO_Ant3_2452_1000~26500

11.7. APPENDIX G: DUTY CYCLE**11.7.1. Test Result**

Test Mode	On Time (msec)	Period (msec)	Duty Cycle x (Linear)	Duty Cycle (%)	Duty Cycle Correction Factor (dB)	1/T Minimum VBW (kHz)	Final setting For VBW (kHz)
11B-CDD	8.37	8.53	0.9812	98.12	0.08	0.12	0.01
11G-CDD	1.38	1.53	0.9020	90.20	0.45	0.72	1
11N20MIMO	1.3	1.42	0.9155	91.55	0.38	0.77	1
11N40MIMO	0.65	0.77	0.8442	84.42	0.74	1.54	2
11AX20MIMO	1	1.13	0.8850	88.50	0.53	1.00	1
11AX40MIMO	0.63	0.75	0.8400	84.00	0.76	1.59	2

Note:

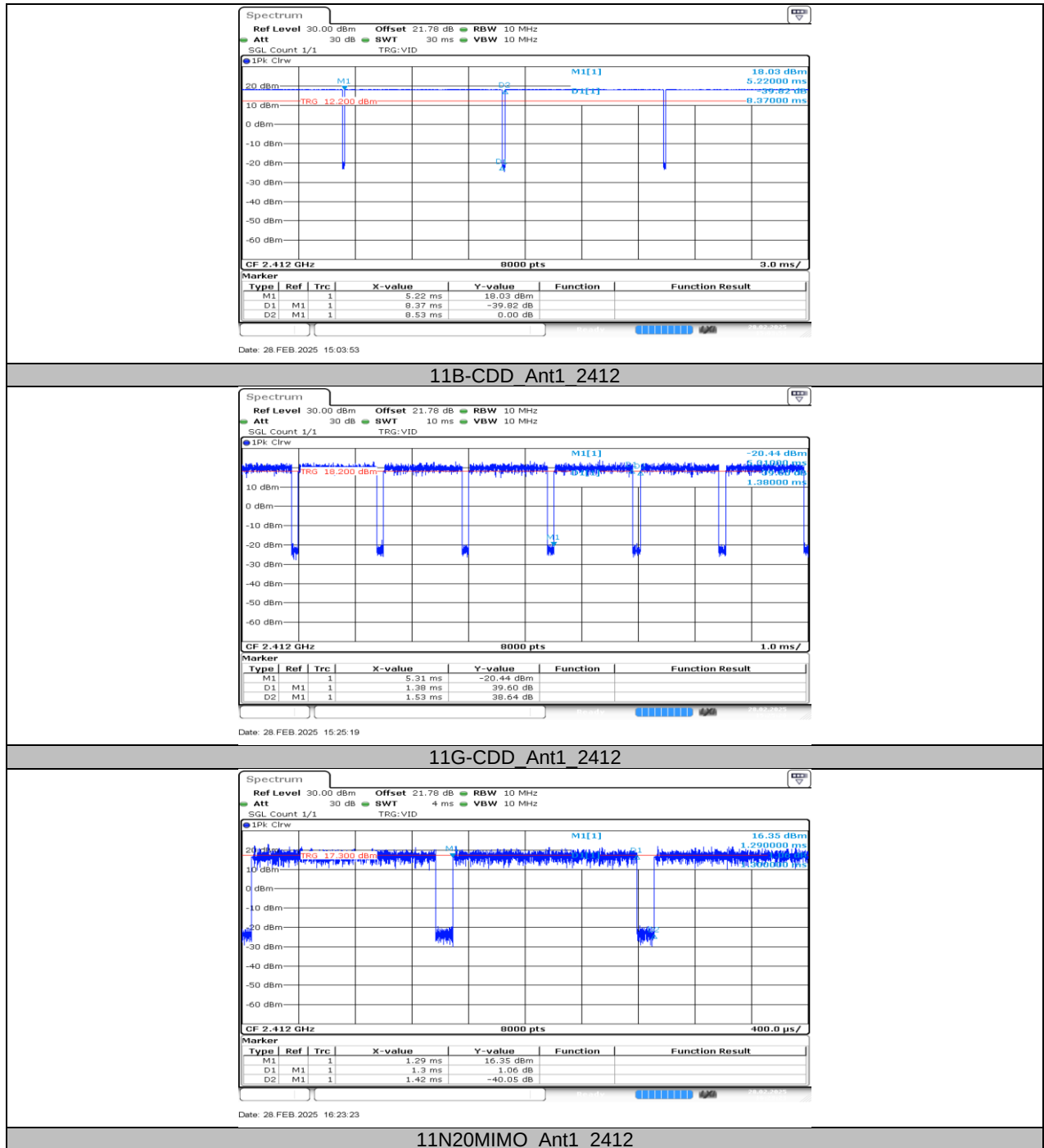
Duty Cycle Correction Factor=10log (1/x).

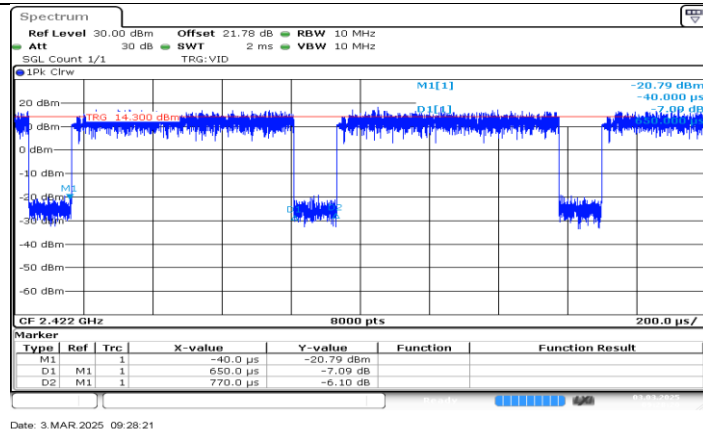
Where: x is Duty Cycle (Linear)

Where: T is On Time

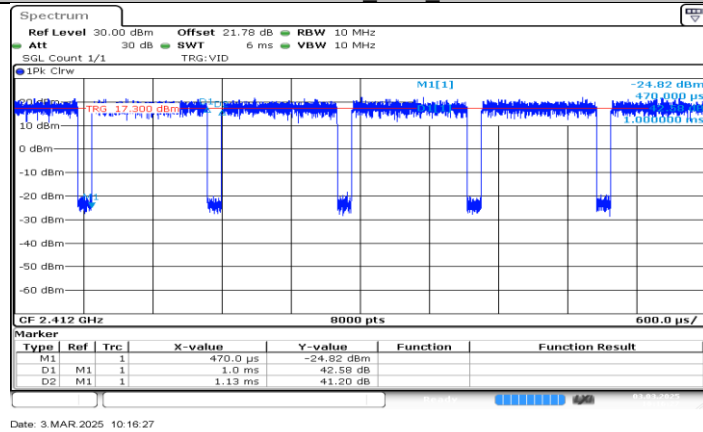
If that calculated VBW is not available on the analyzer then the next higher value should be used.

11.7.2. Test Graphs

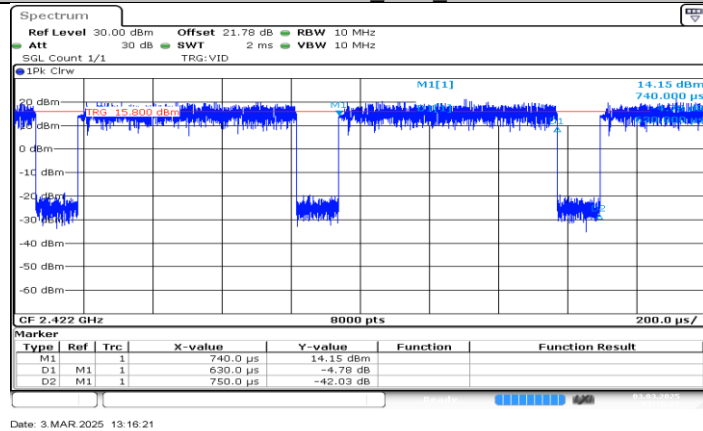




11N40MIMO_Ant1_2422



11AX20MIMO_Ant1_2412



11AX40MIMO_Ant1_2422

END OF REPORT