



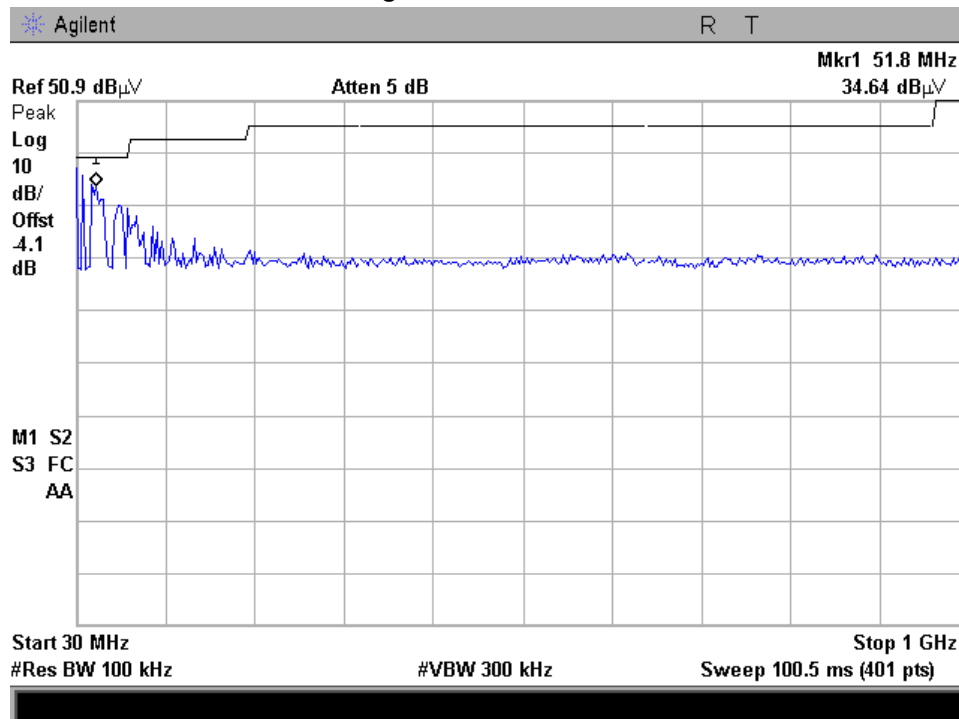
Annex A

Undesirable Emissions Conducted

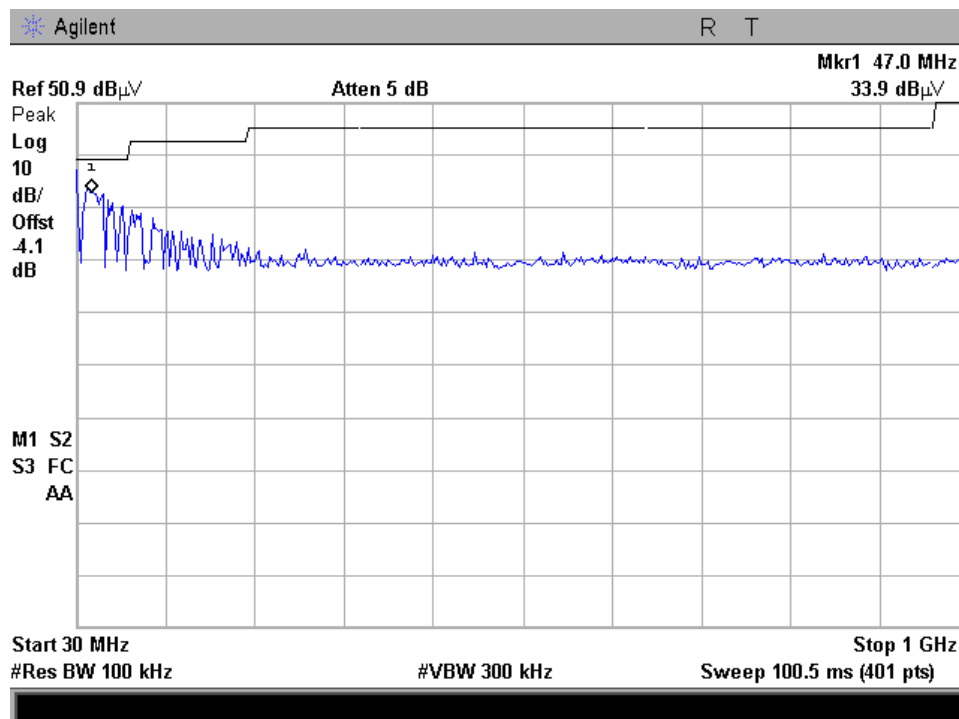
UNII-2A and UNII-2C



UNII-2A
10MHz High Ch 30-1000MHz Port J8

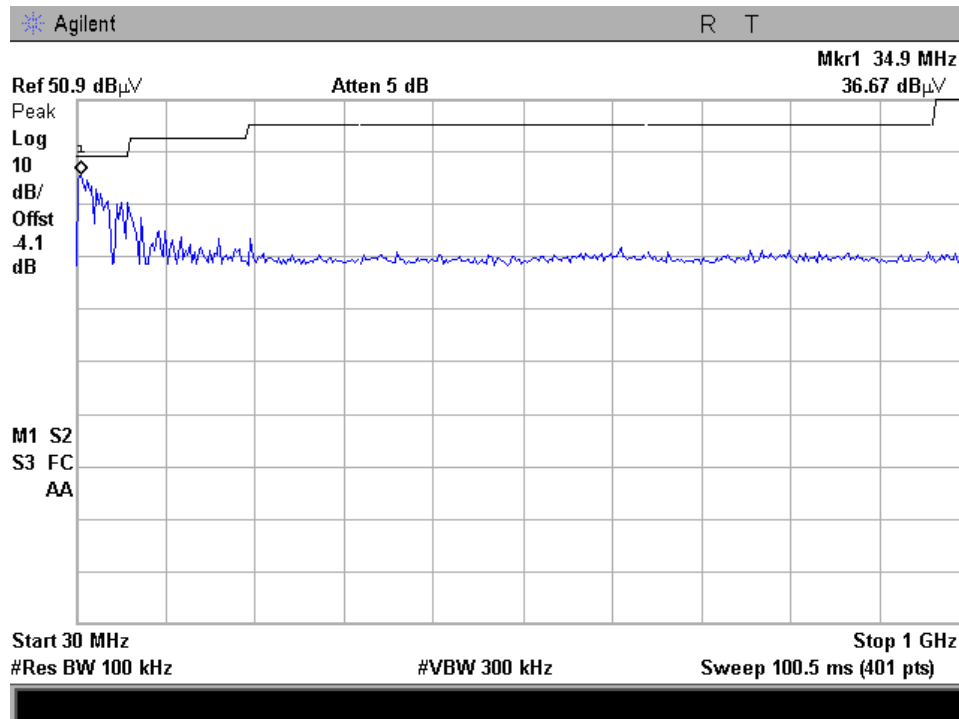


10MHz High Ch 30-1000MHz Port J7

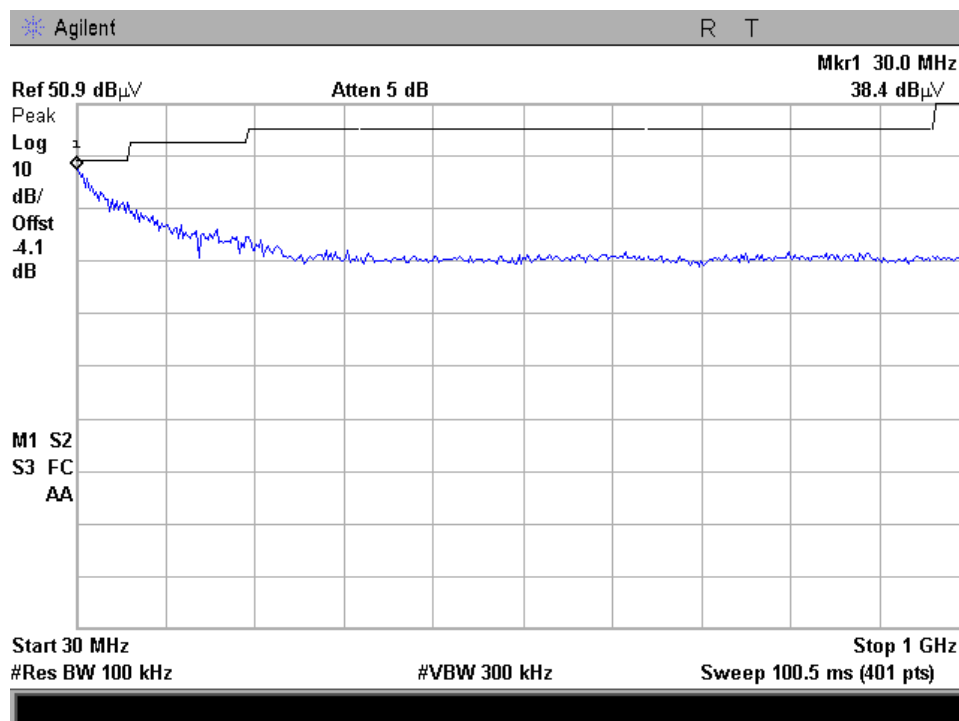




10MHz Low Ch 30-1000MHz Port J8

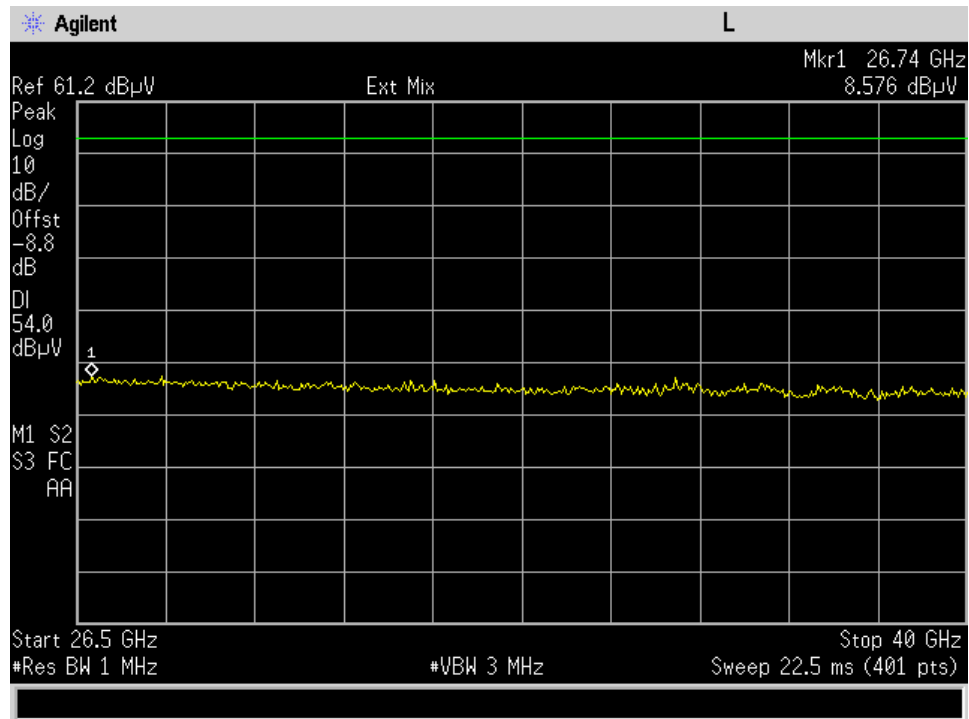


10MHz Low Ch 30-1000MHz Port J7

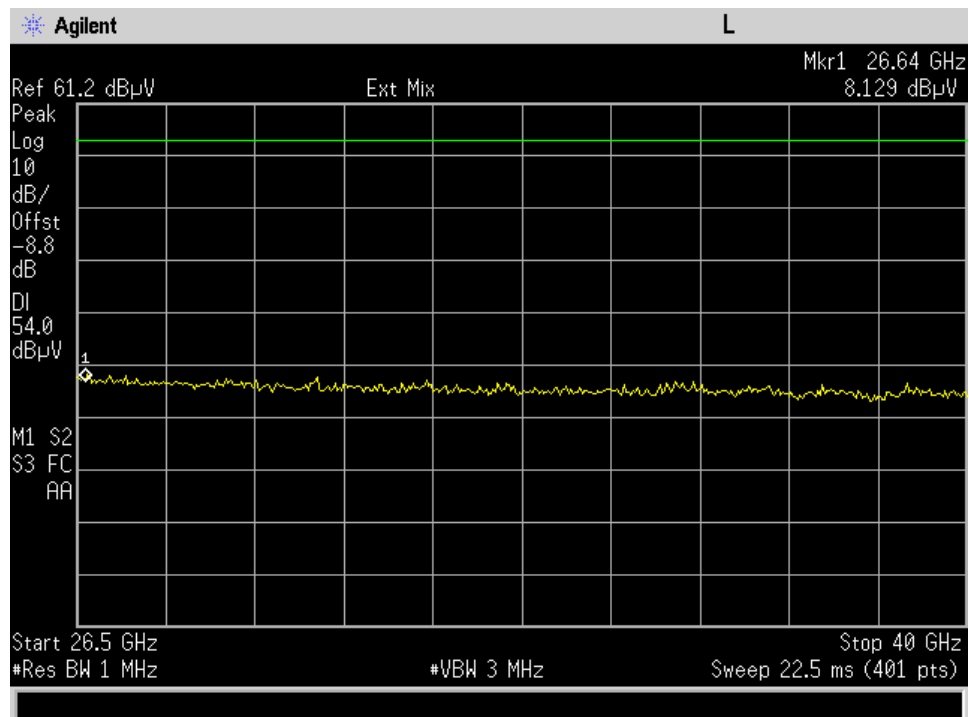




10MHz Mid Ch 26.5-40GHz Port J8

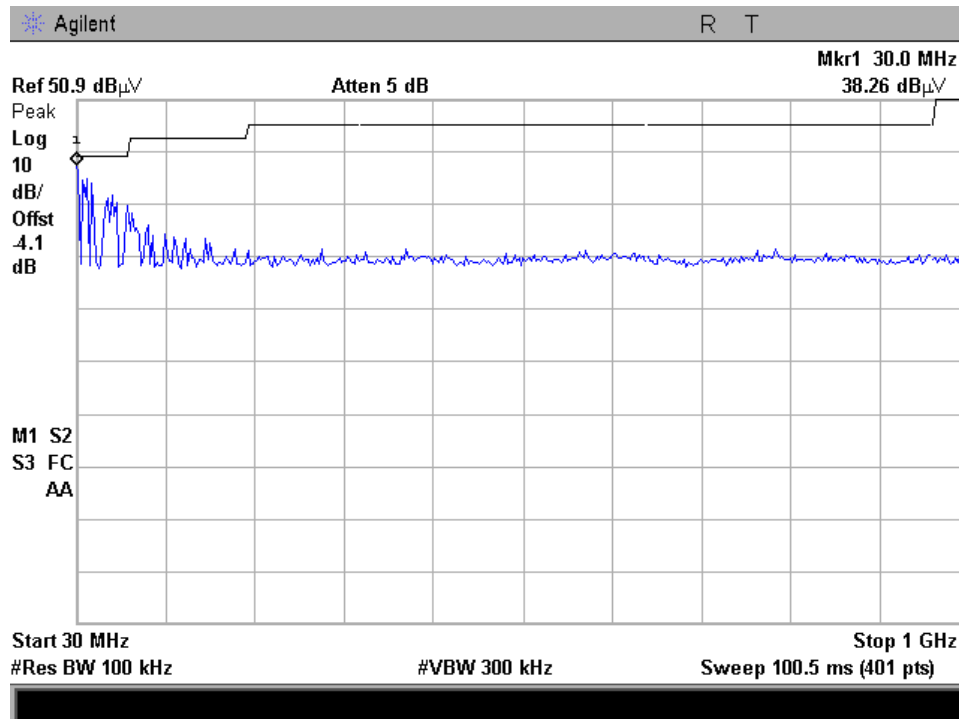


10MHz Mid Ch 26.5-40GHz Port J7

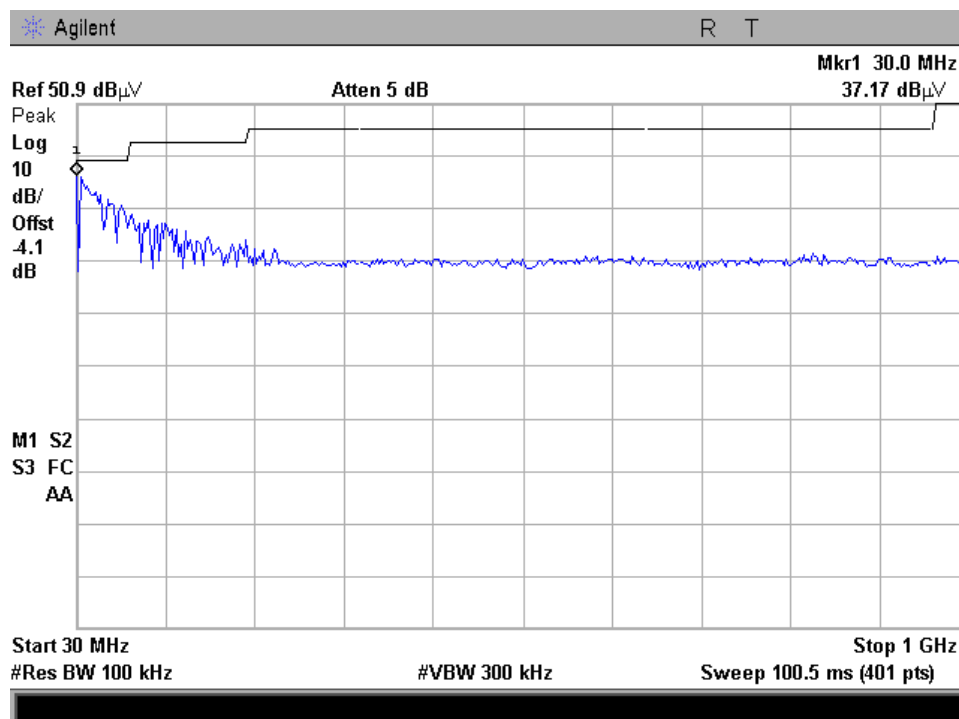




10MHz Mid Ch 30-1000MHz Port J8

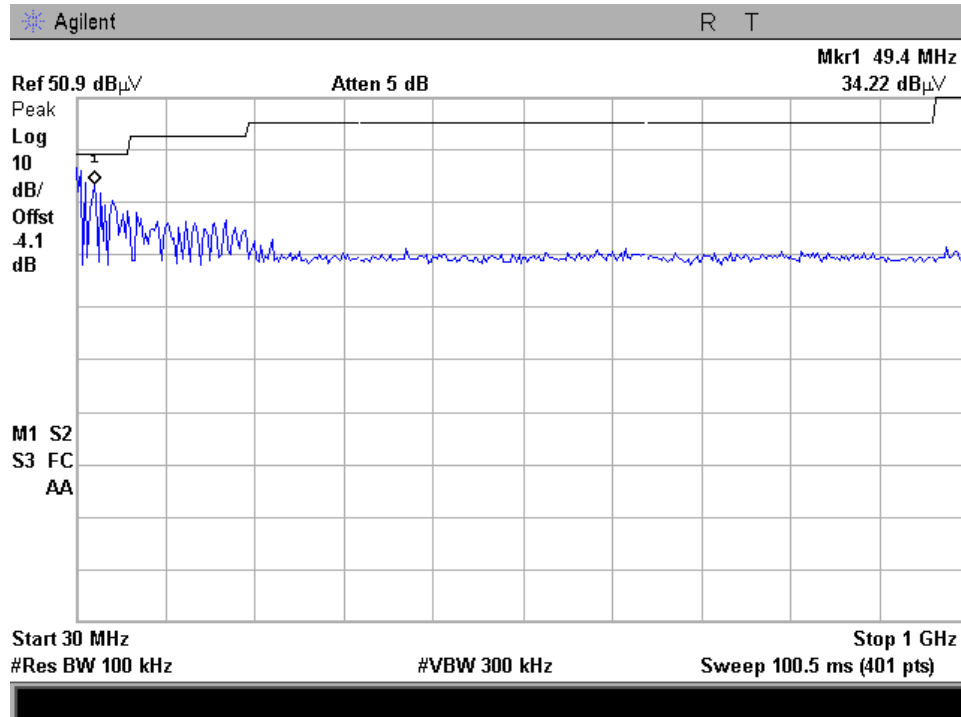


10MHz Mid Ch 30-1000MHz Port J7

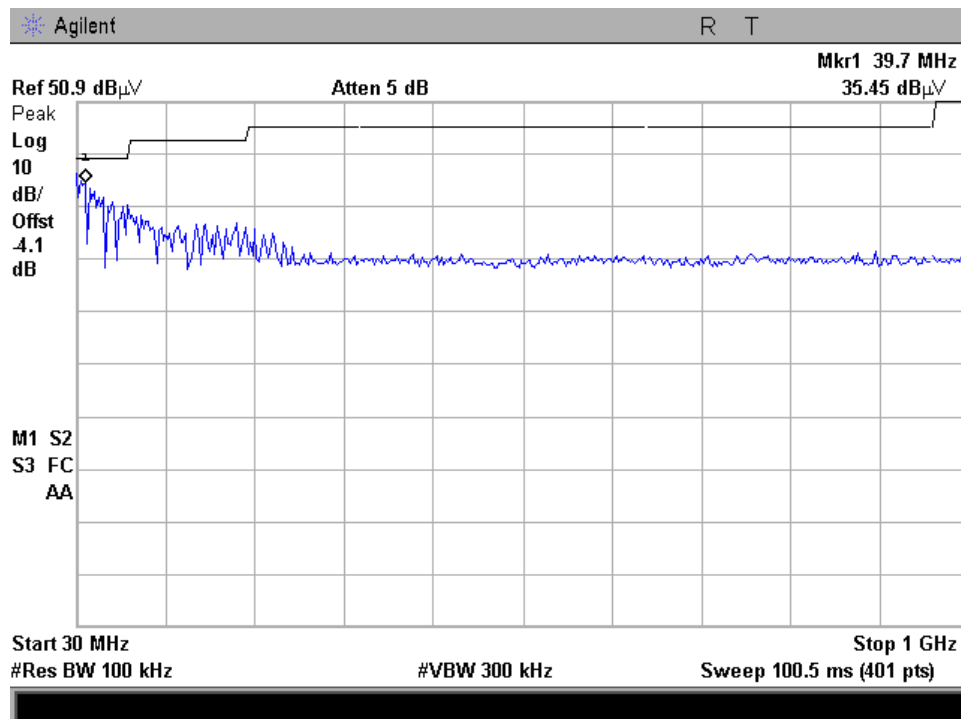




20MHz High Ch 30-1000MHz Port J8

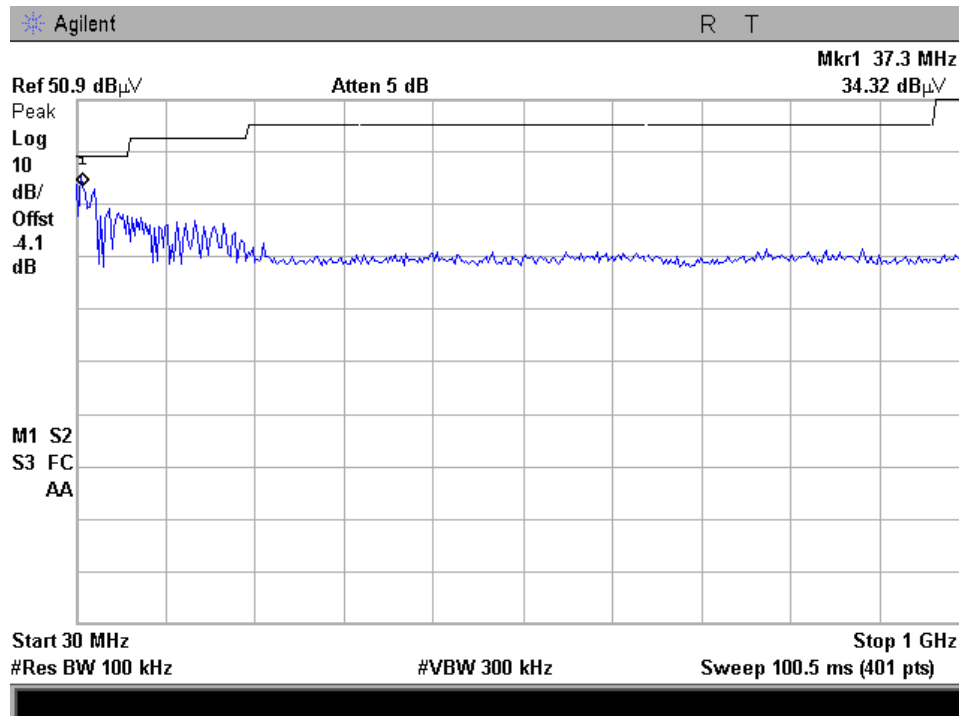


20MHz High Ch 30-1000MHz Port J7

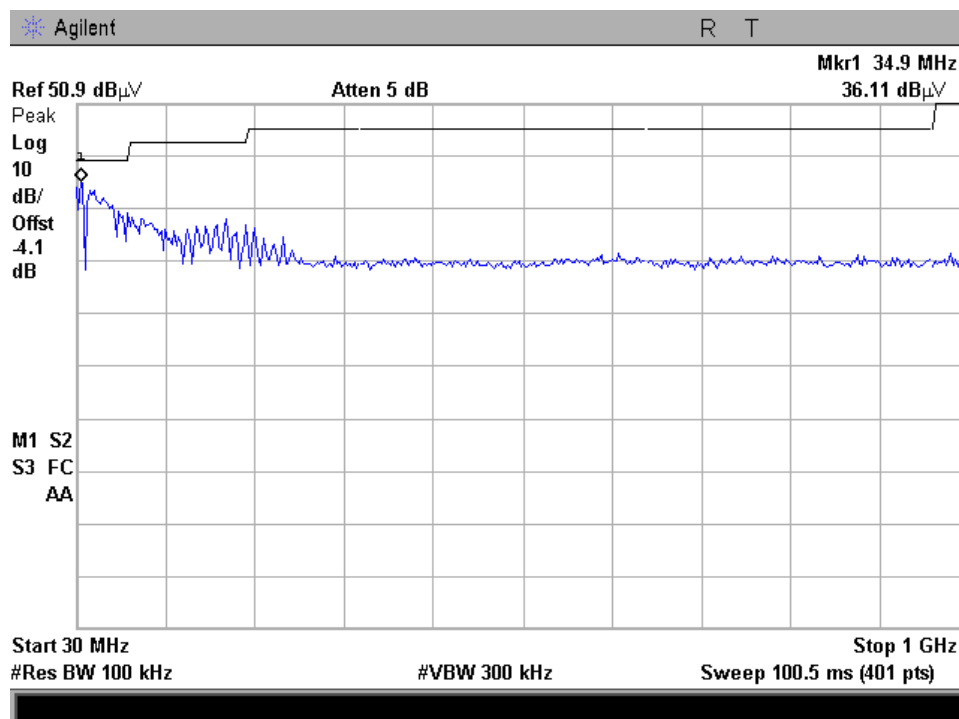




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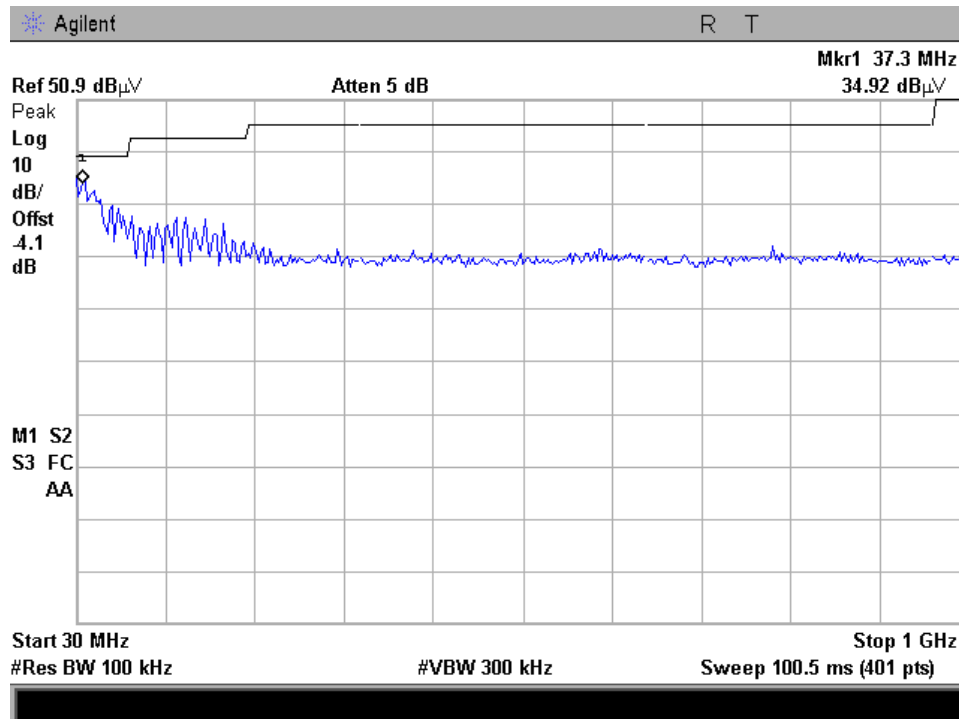


20MHz Low Ch 30-1000MHz Port J7

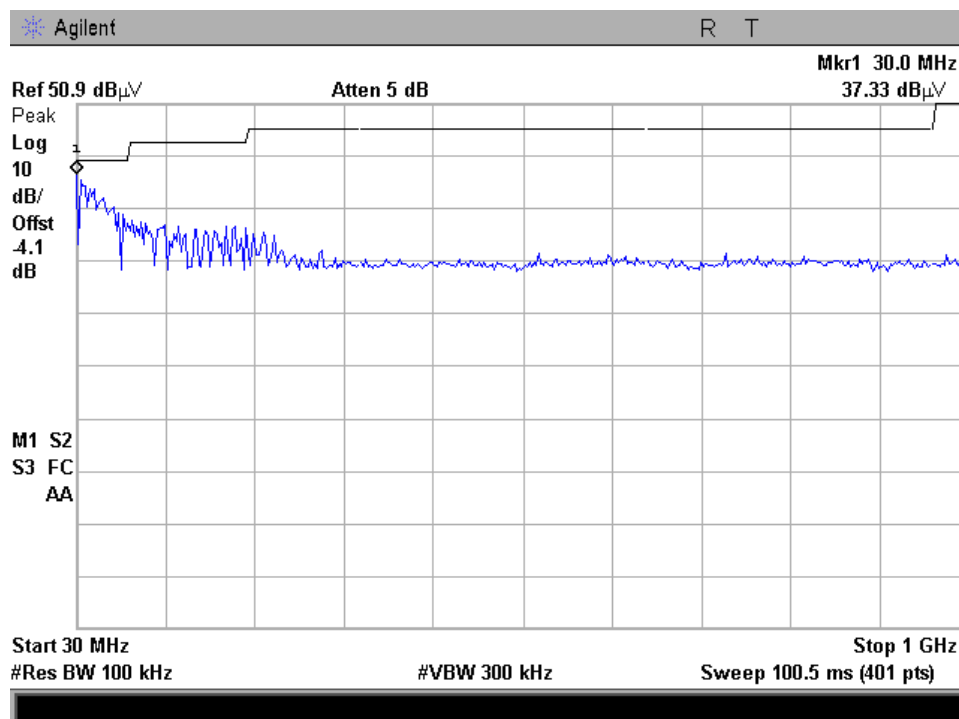




20MHz Mid Ch 30-1000MHz Port J8

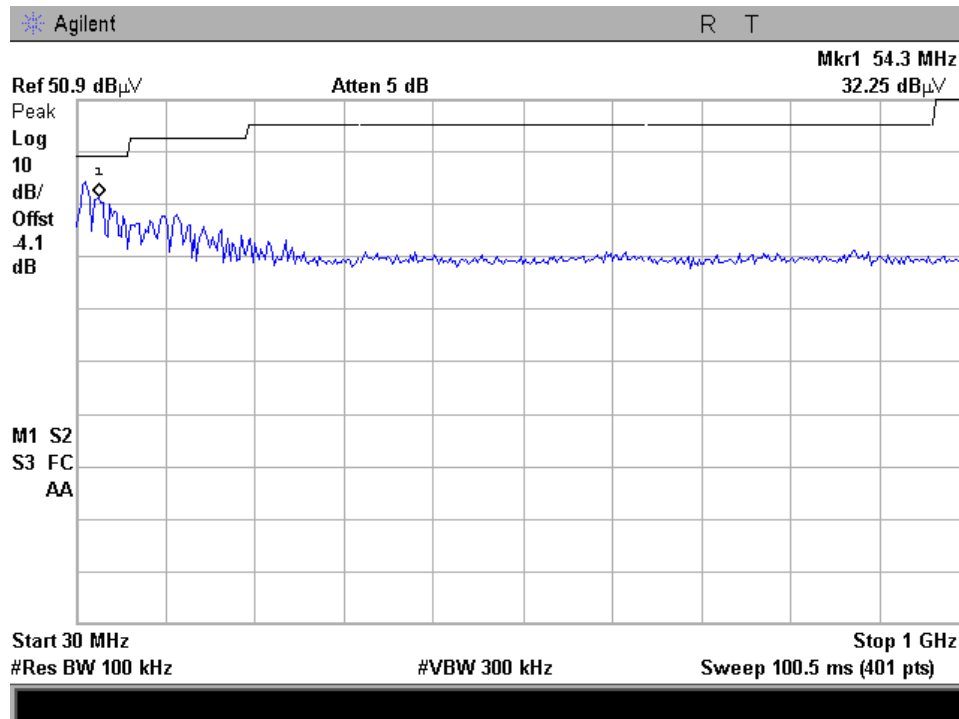


20MHz Mid Ch 30-1000MHz Port J7

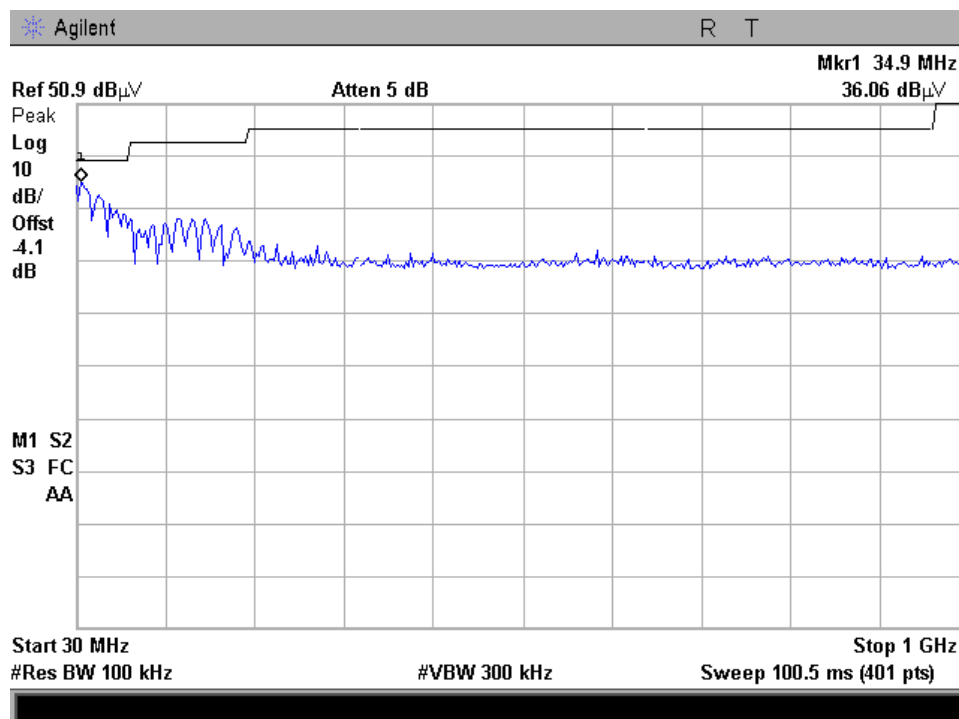




30MHz High Ch 30-1000MHz Port J8

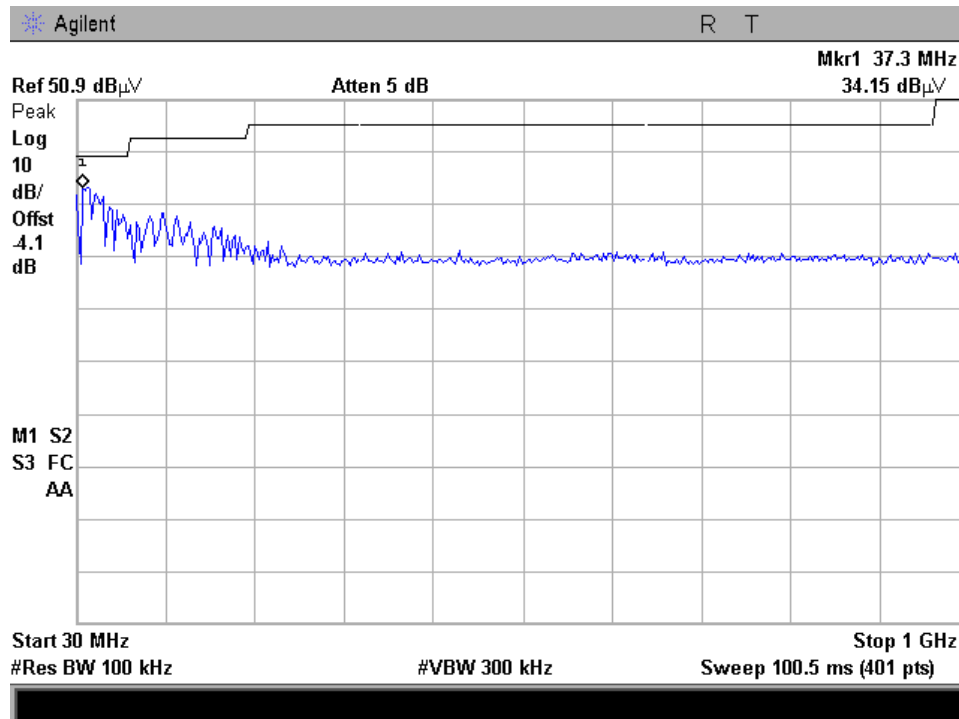


30MHz High Ch 30-1000MHz Port J7

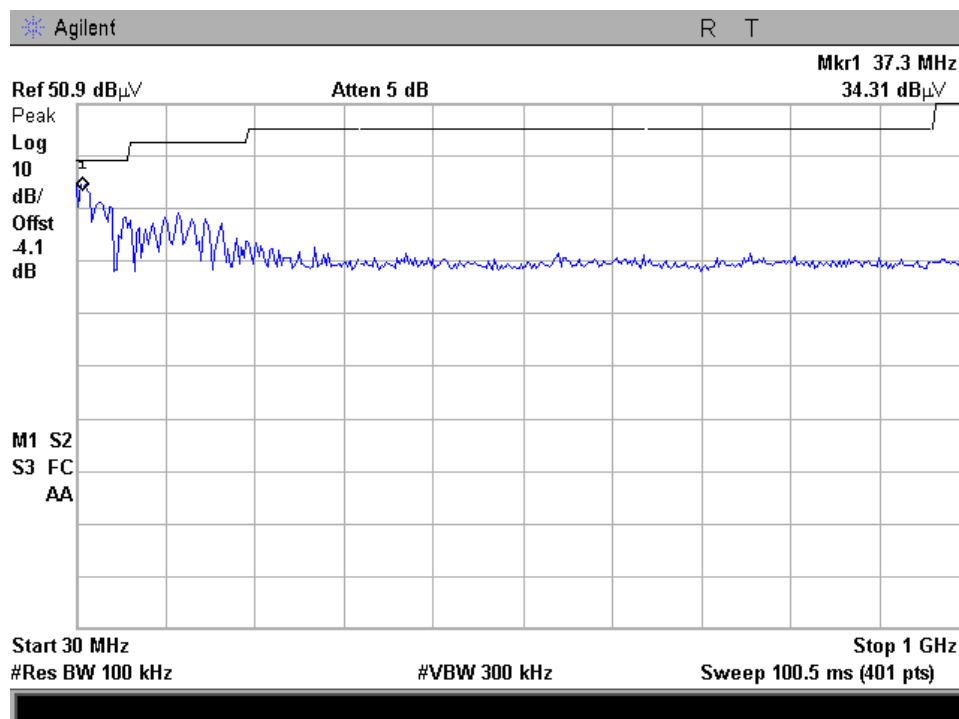




30MHz Low Ch 30-1000MHz Port J8

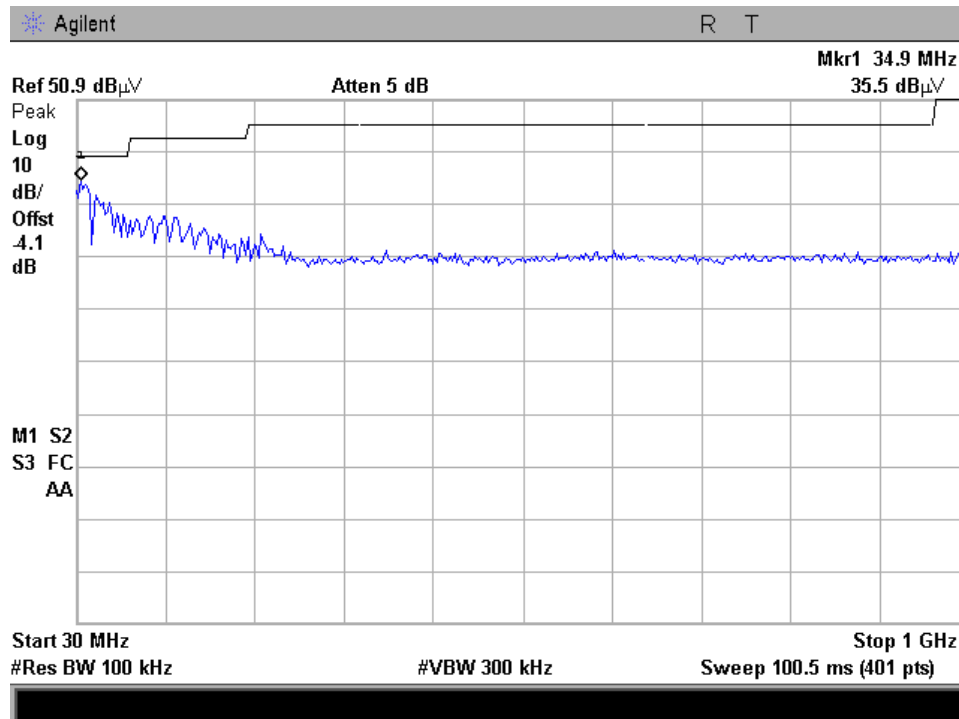


30MHz Low Ch 30-1000MHz Port J7

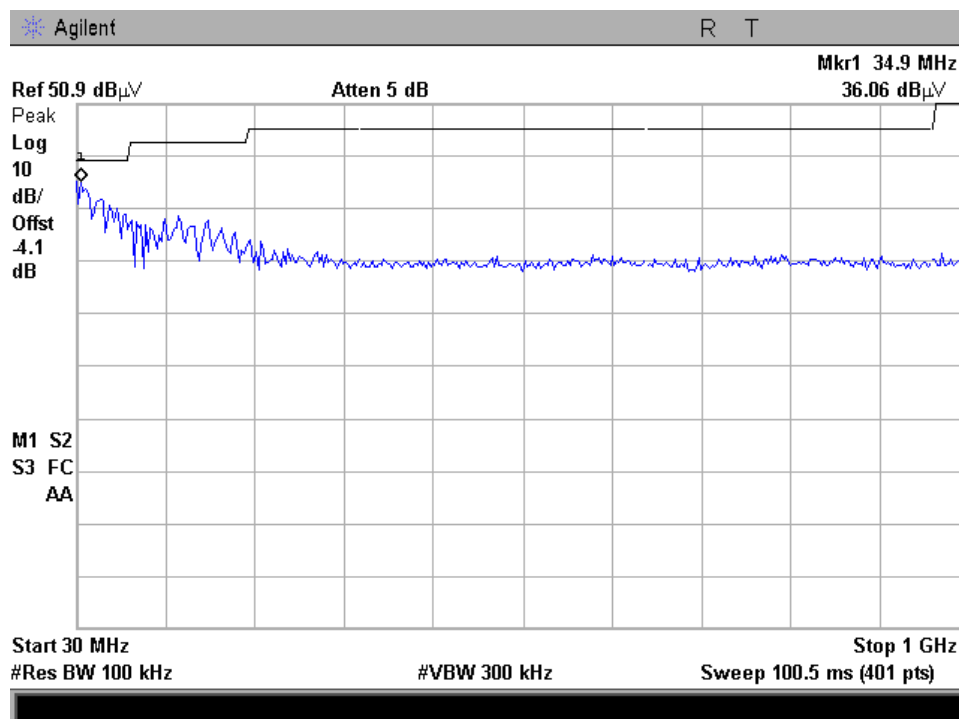




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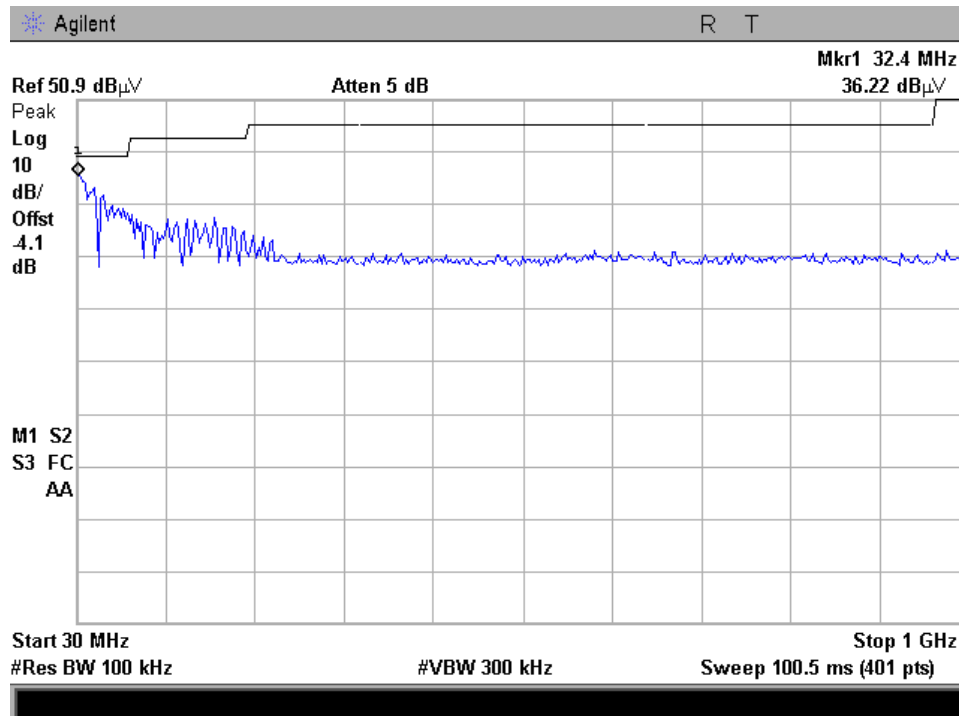


30MHz Mid Ch 30-1000MHz Port J7

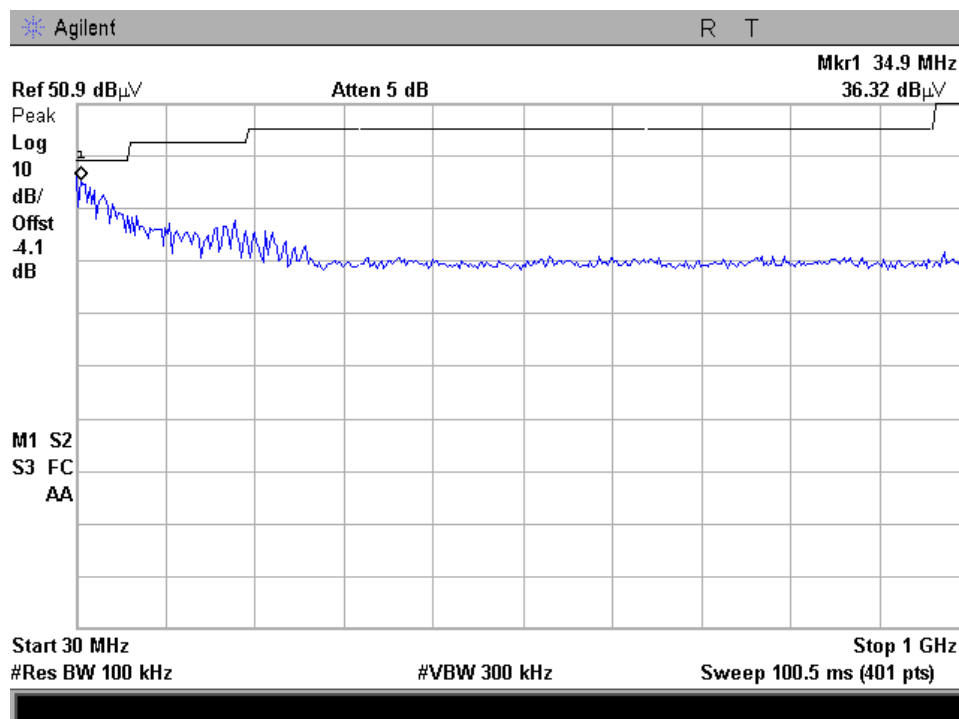




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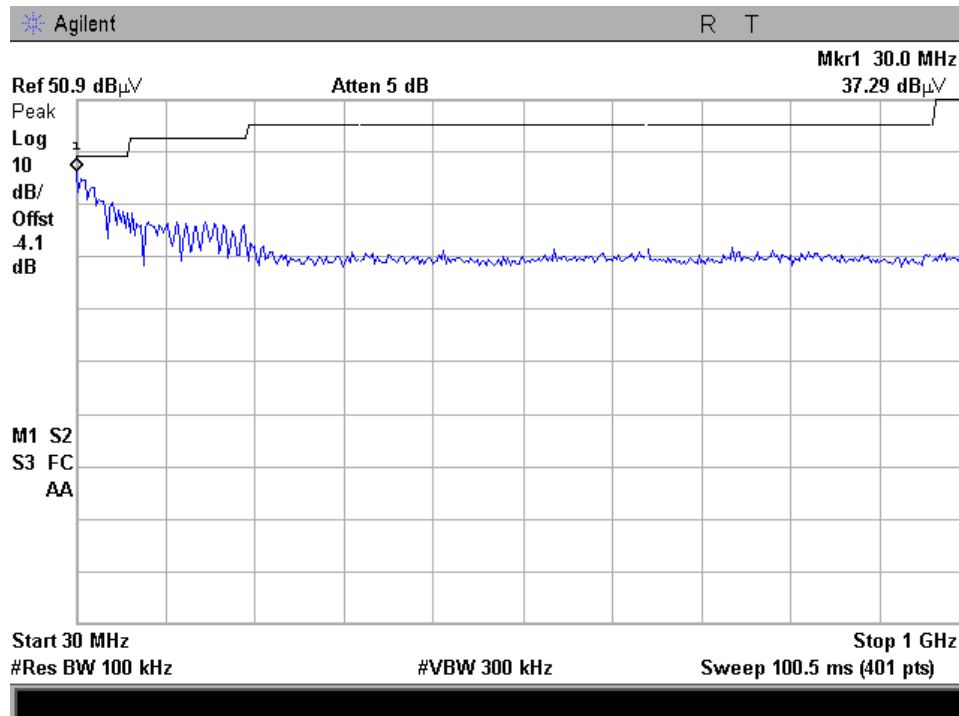


40MHz High Ch 30-1000MHz Port J7

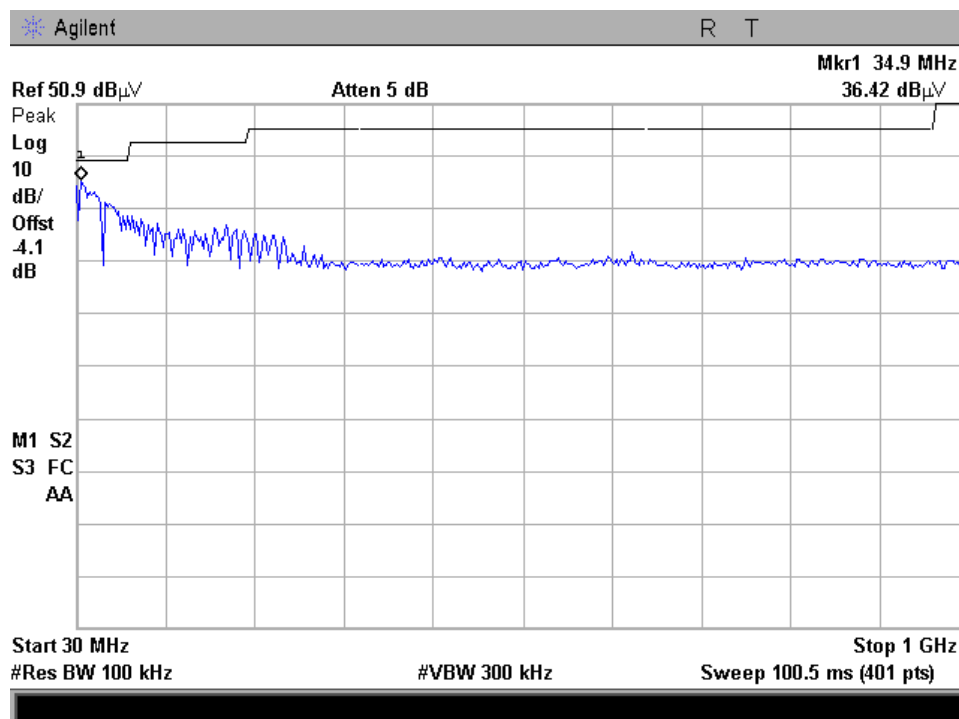




40MHz Low Ch 30-1000MHz Port J8

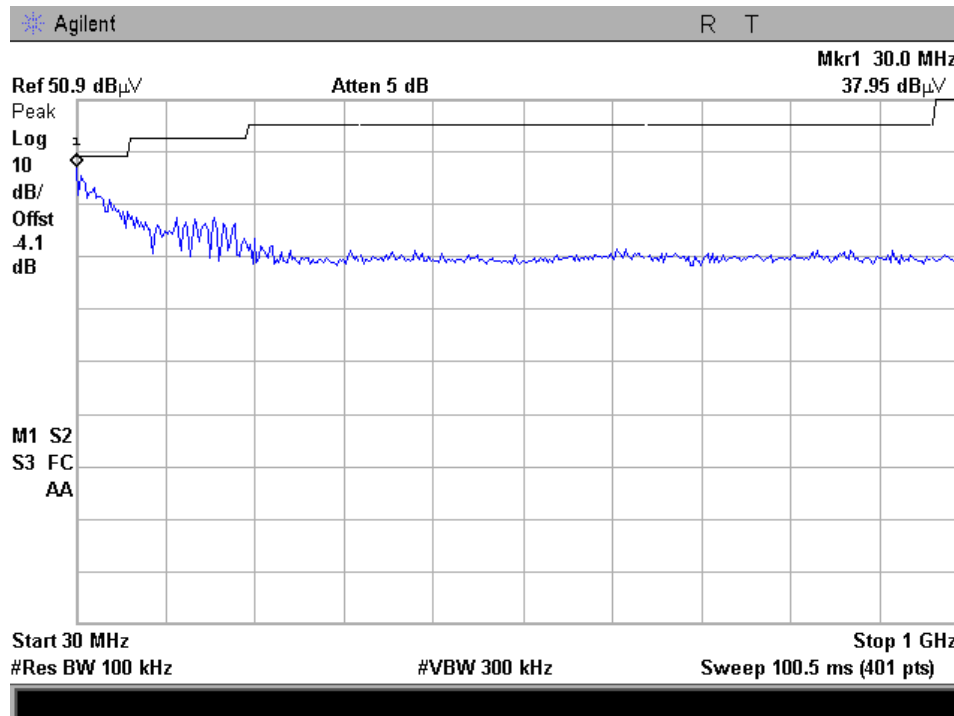


40MHz Low Ch 30-1000MHz Port J7

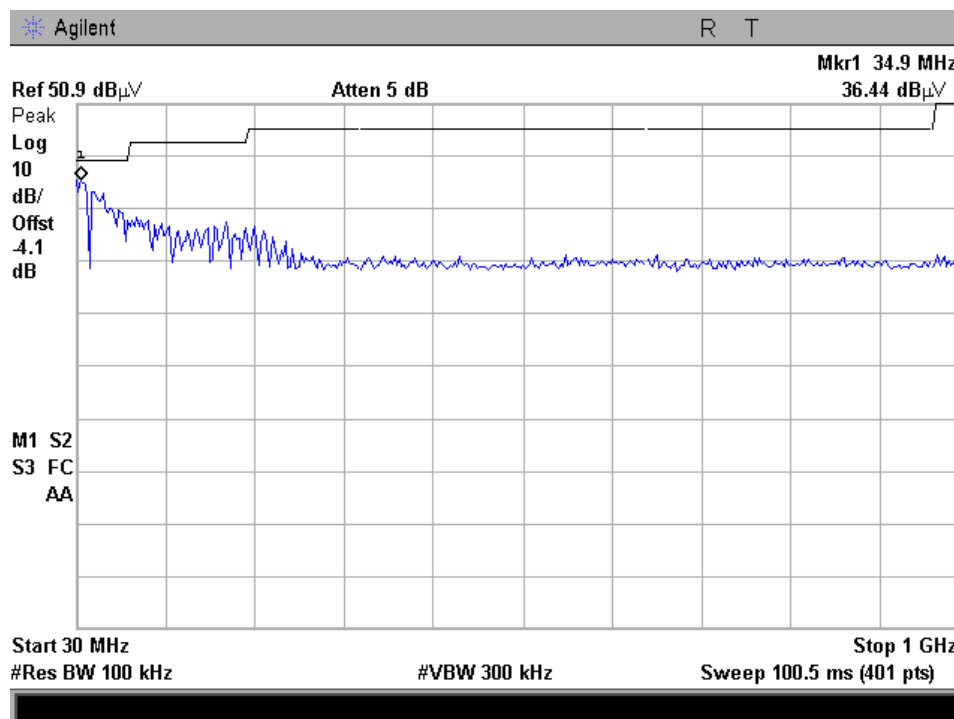




40MHz Mid Ch 30-1000MHz Port J8

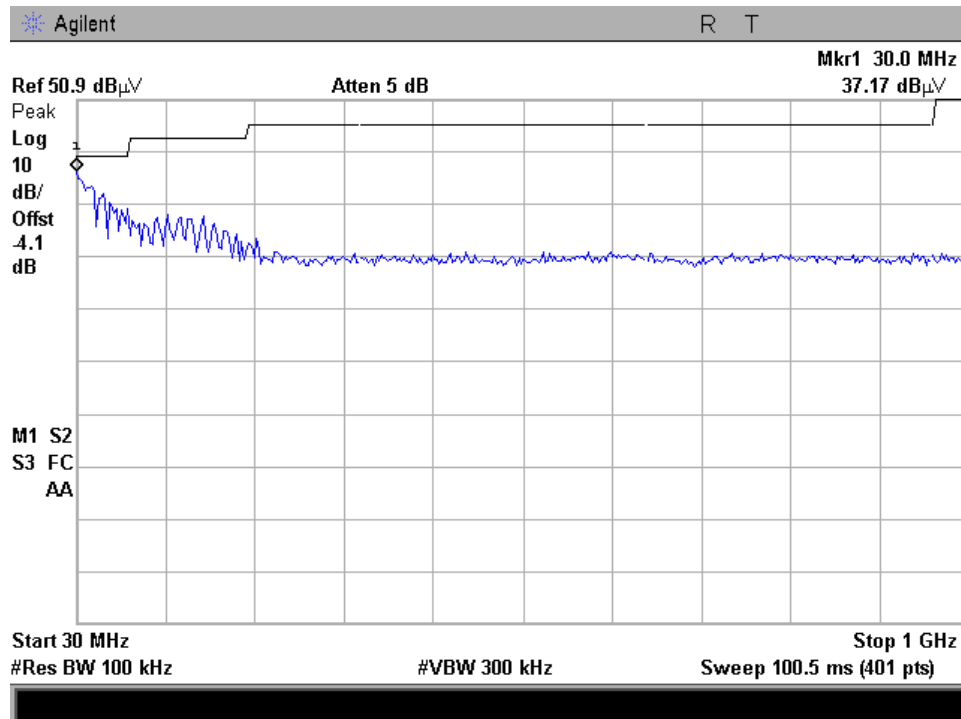


40MHz Mid Ch 30-1000MHz Port J7

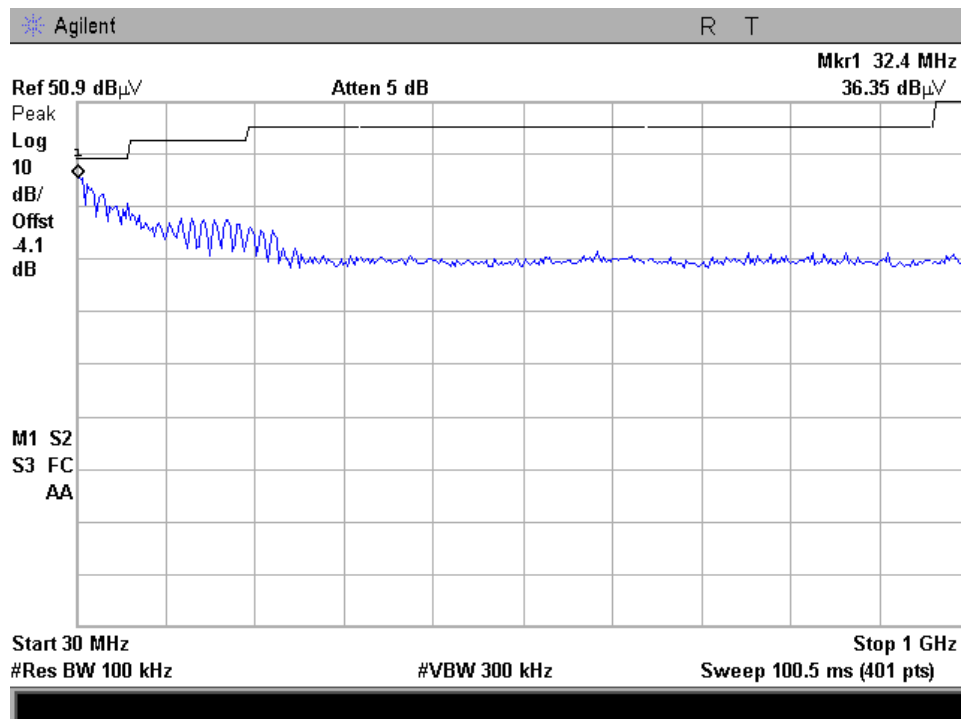




50MHz High Ch 30-1000MHz Port J8

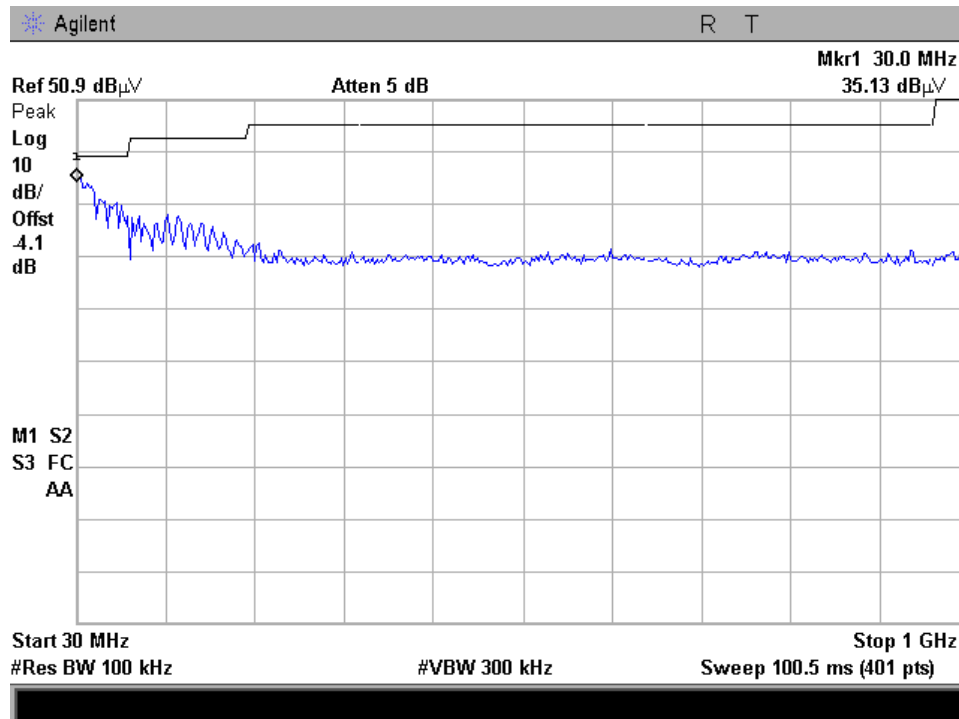


50MHz High Ch 30-1000MHz Port J7

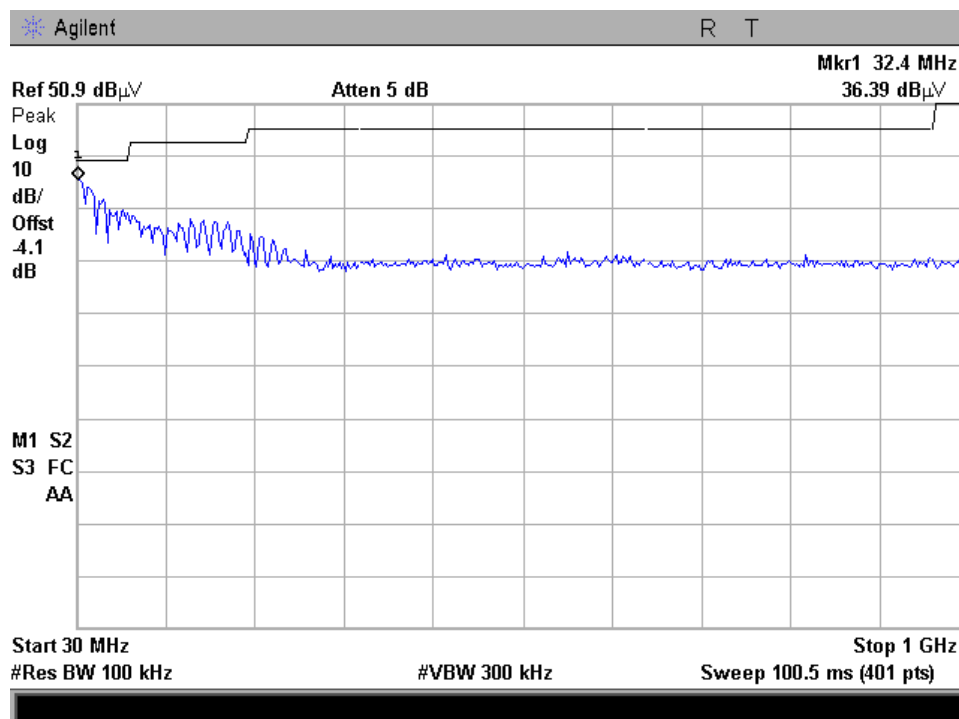




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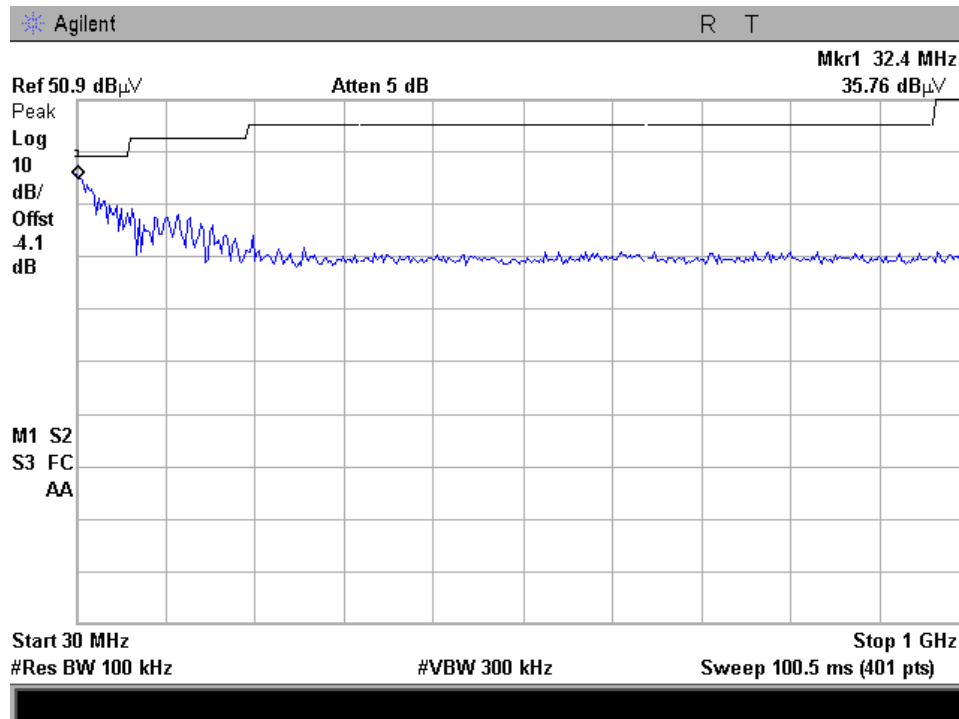


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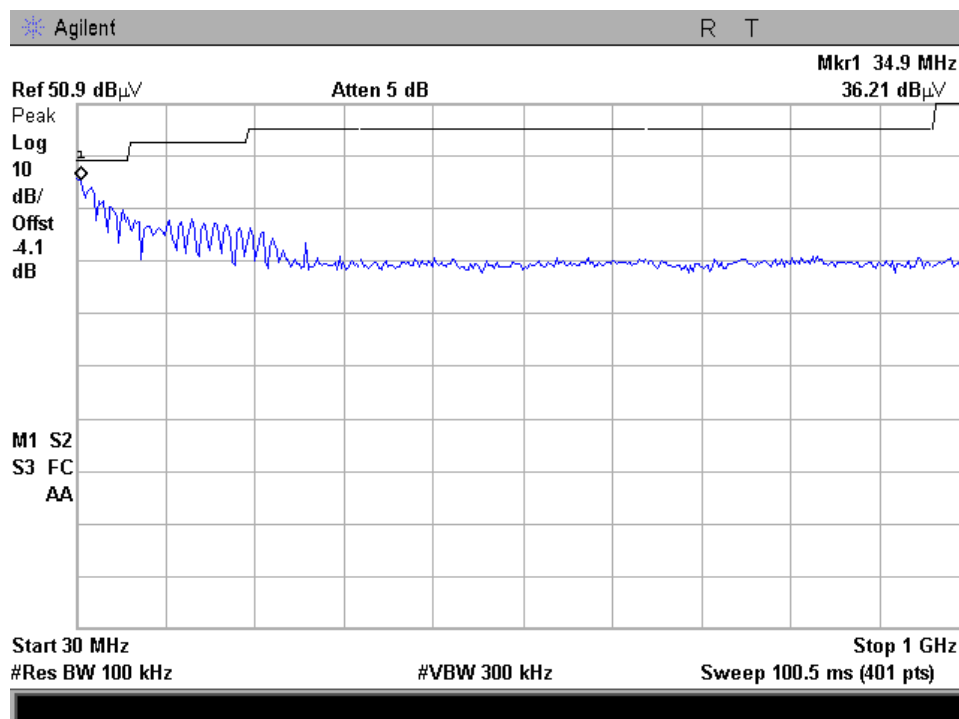




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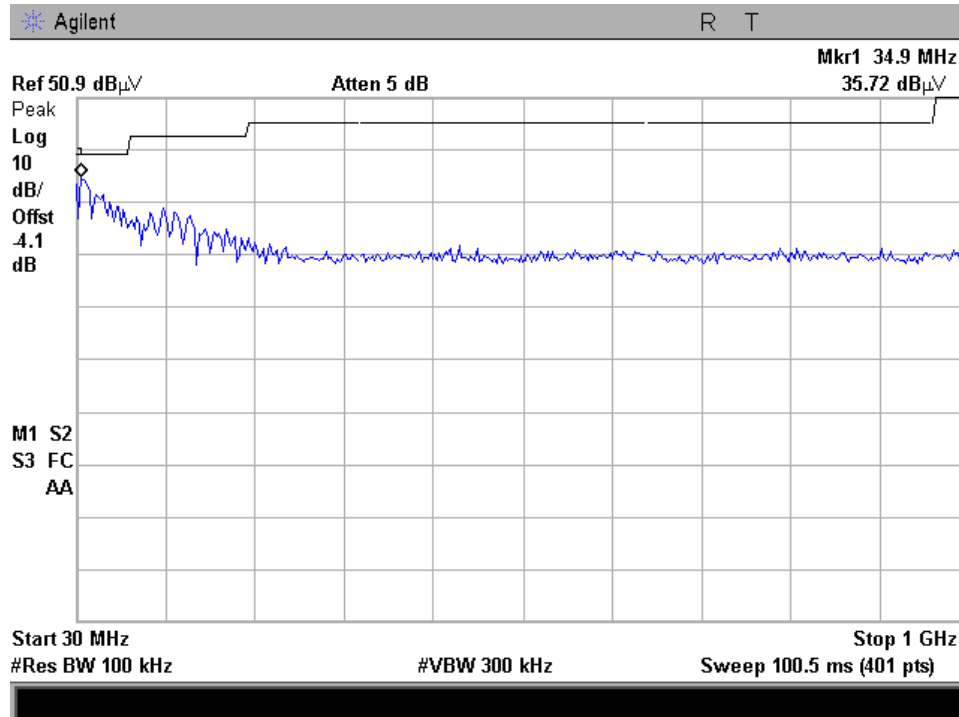


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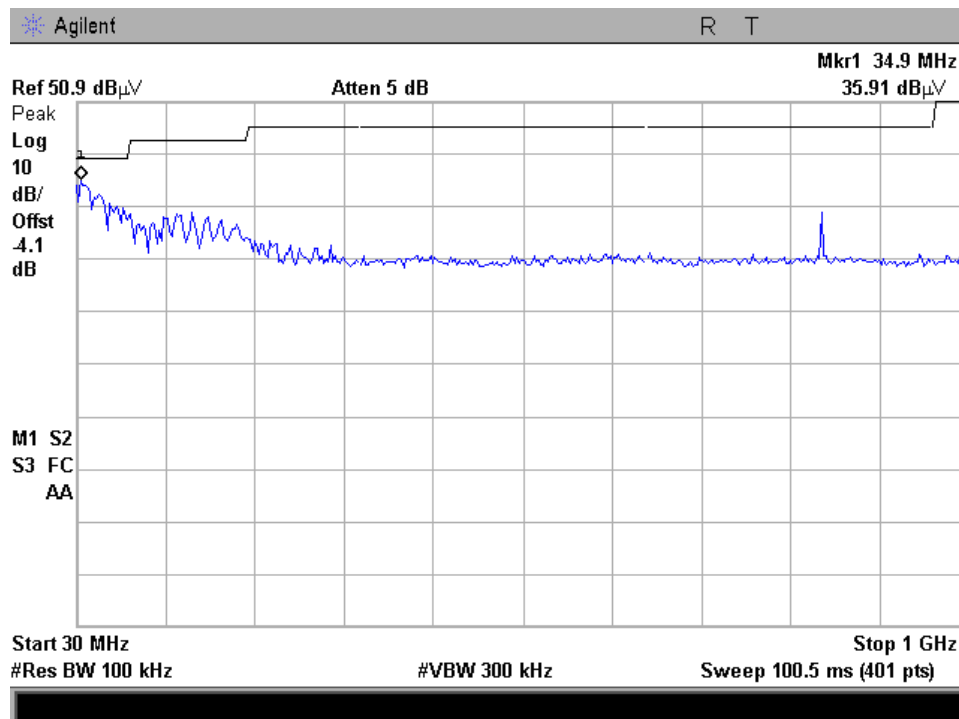




60MHz High Ch 30-1000MHz Port J8

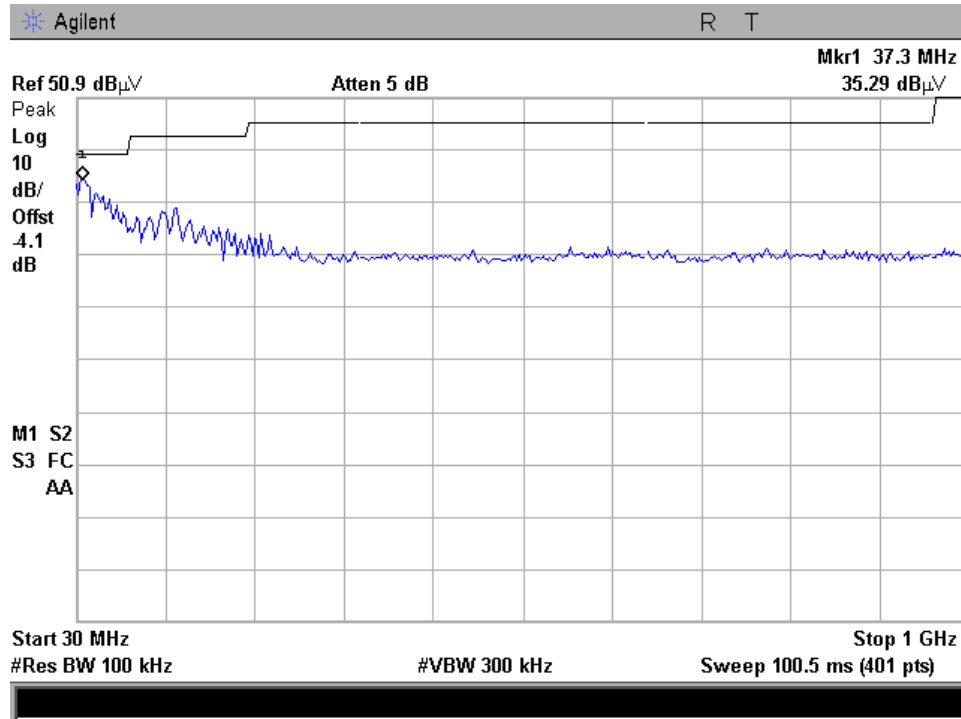


60MHz High Ch 30-1000MHz Port J7

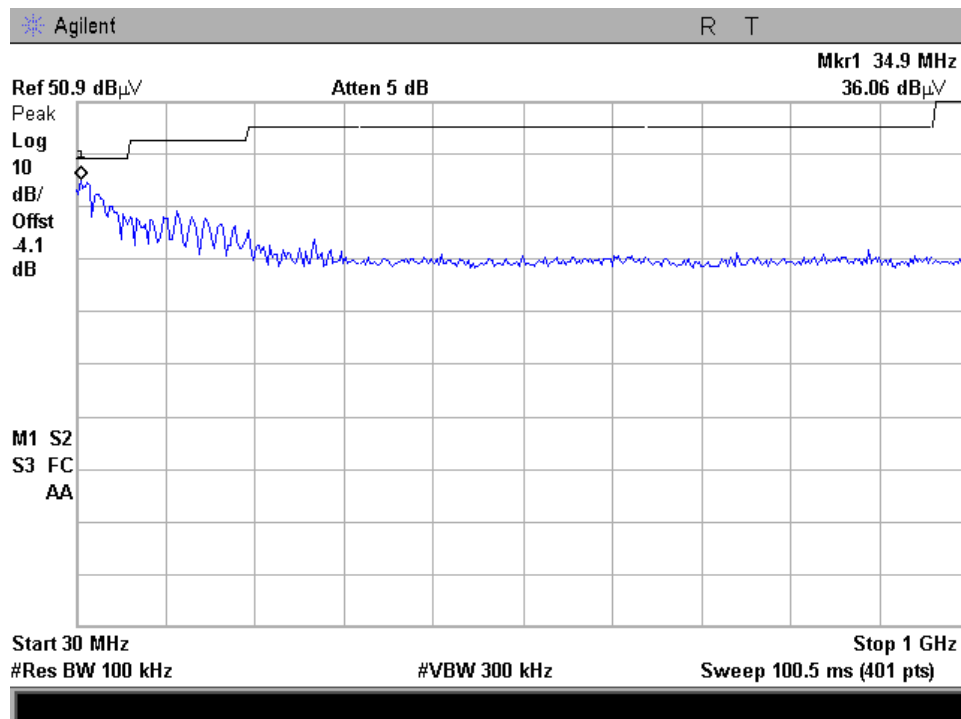




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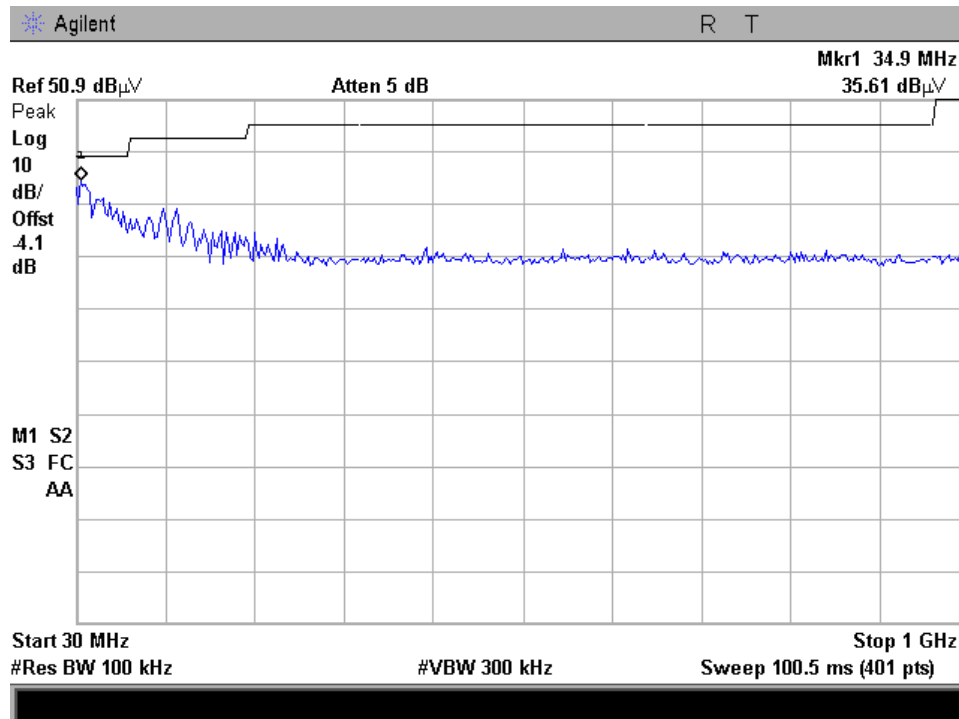


60MHz Low Ch 30-1000MHz Port J7

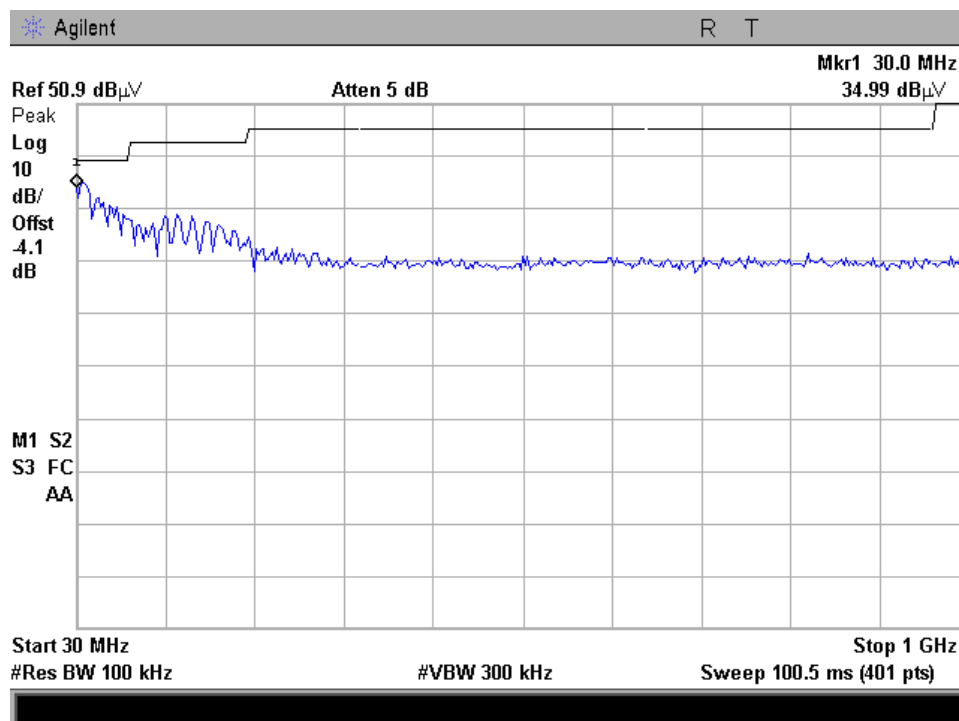




60MHz Mid Ch 30-1000MHz Port J8

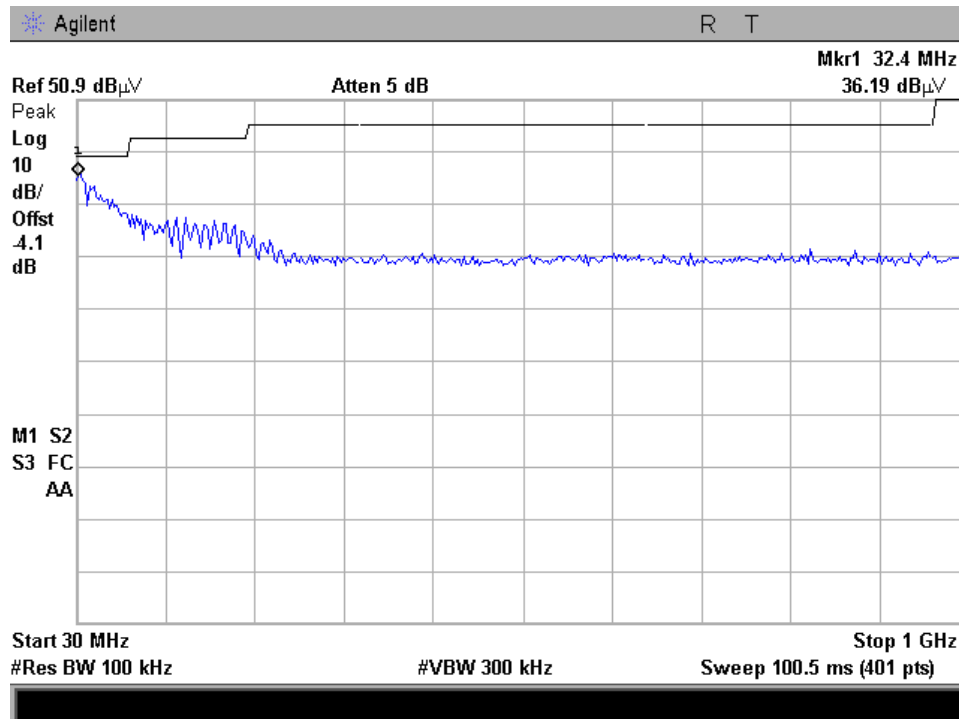


60MHz Mid Ch 30-1000MHz Port J7

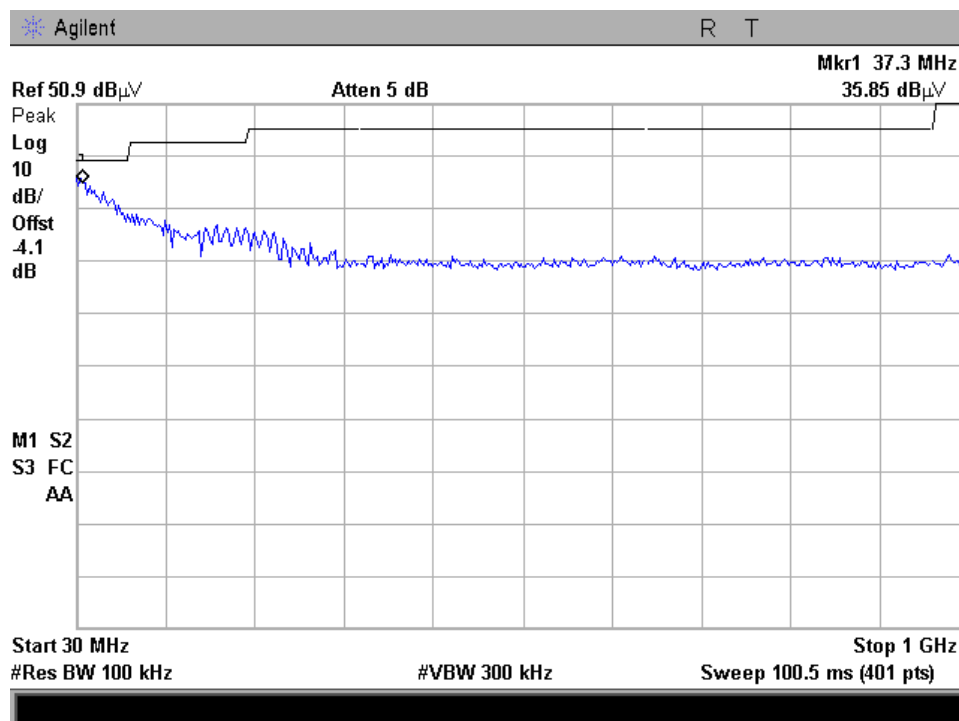




80MHz High Ch 30-1000MHz Port J8

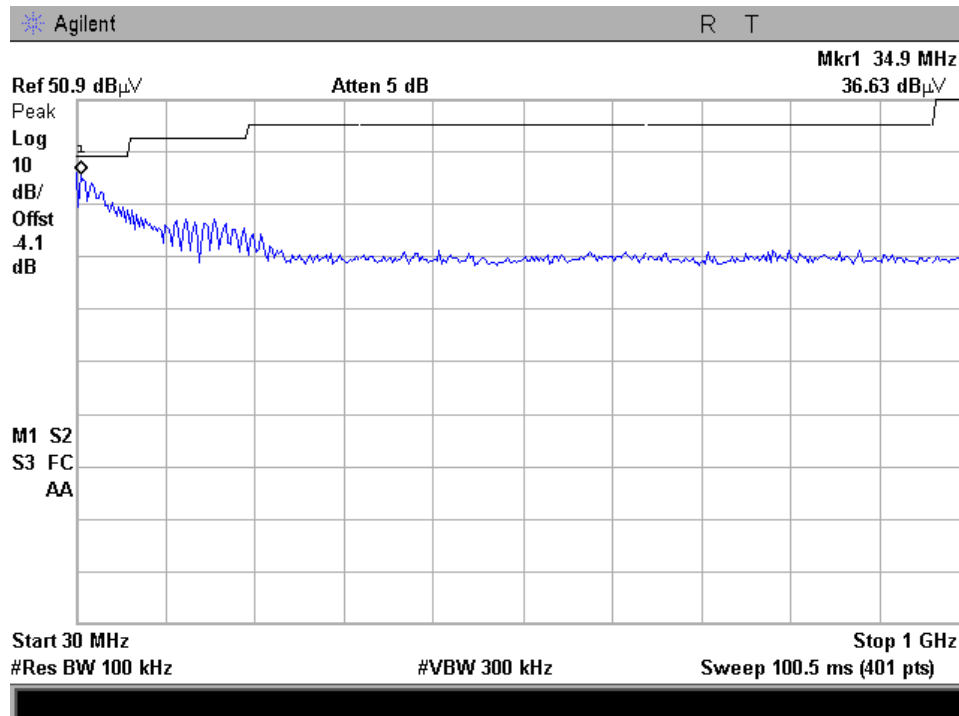


80MHz High Ch 30-1000MHz Port J7

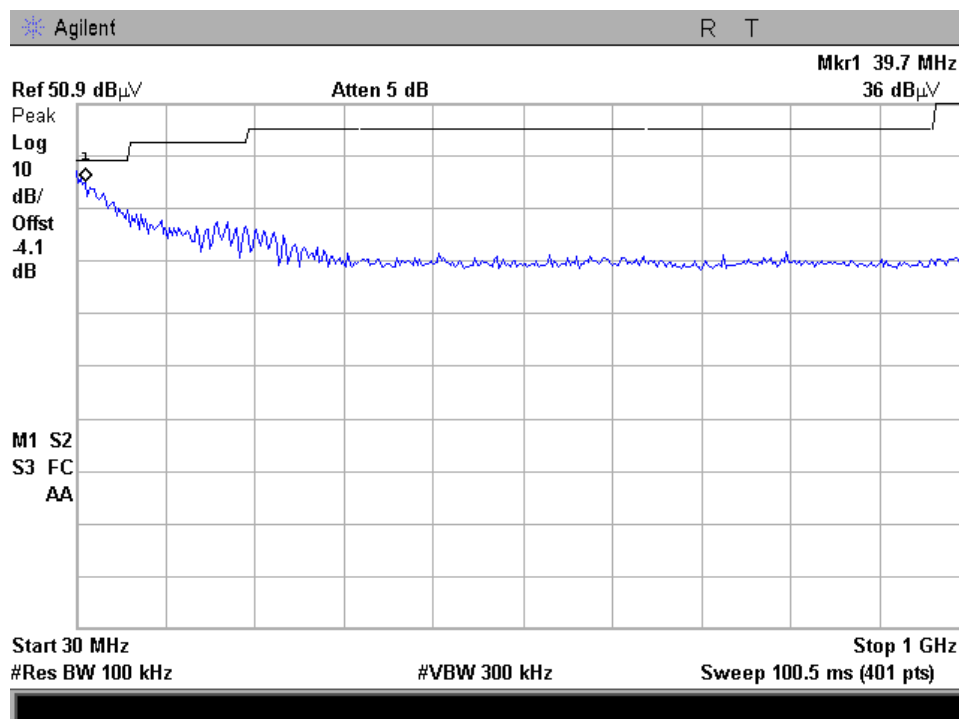




80MHz Low Ch 30-1000MHz Port J8

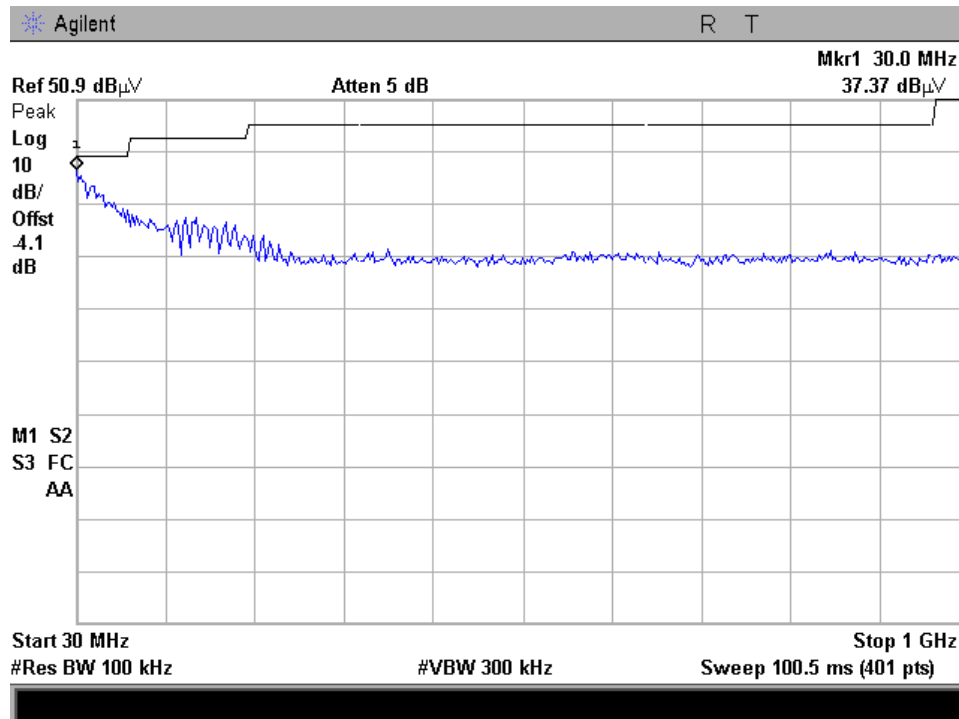


80MHz Low Ch 30-1000MHz Port J7

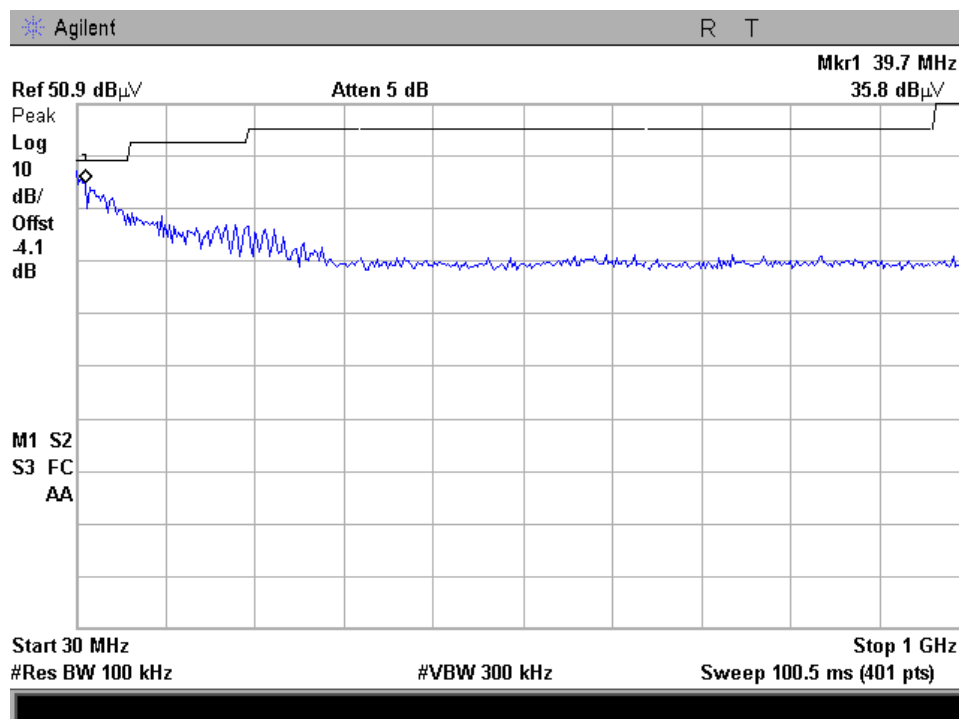




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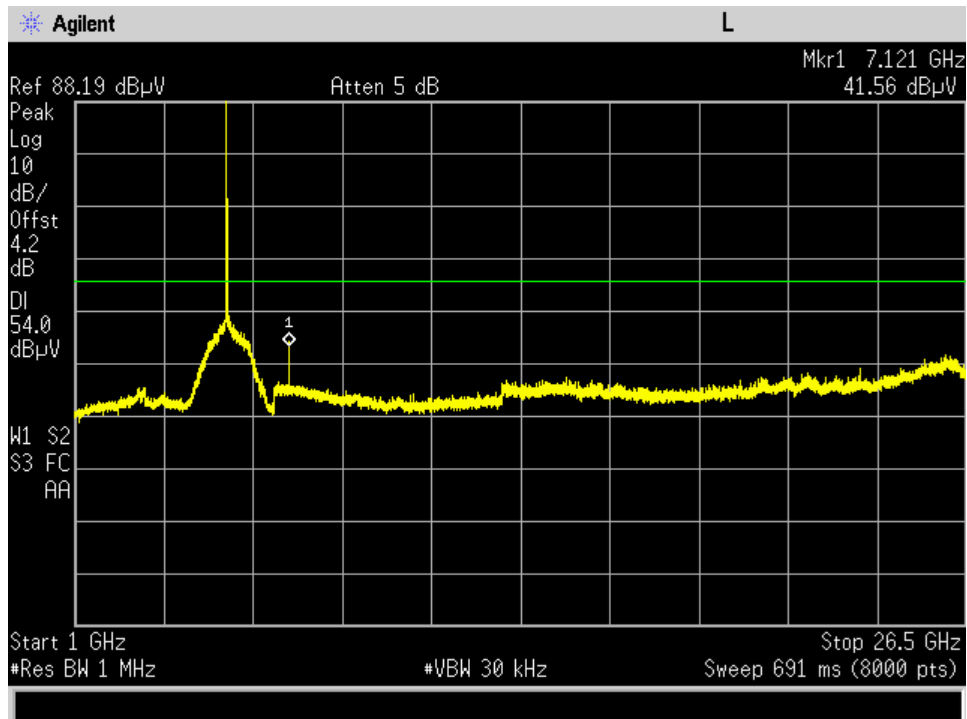


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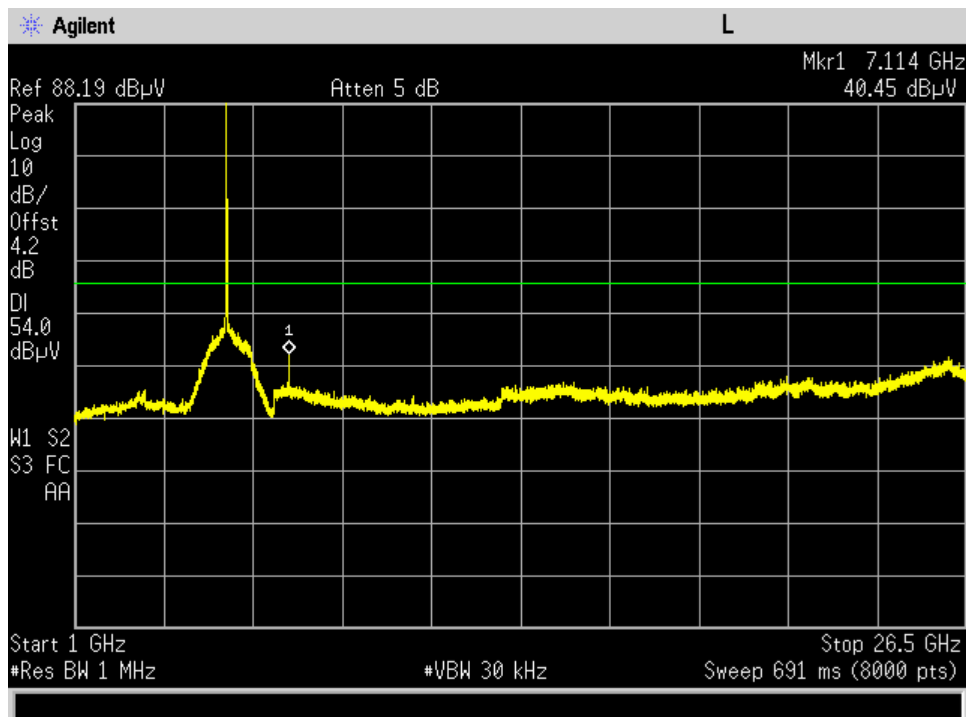




high ch port 1 10MHz BW 1-26.5GHz

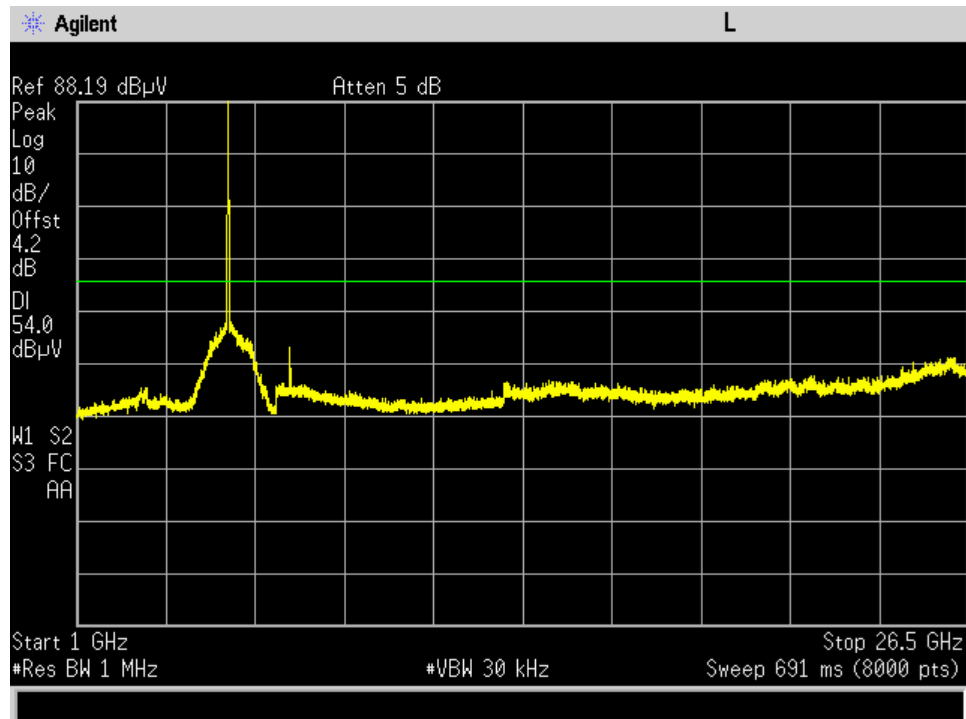


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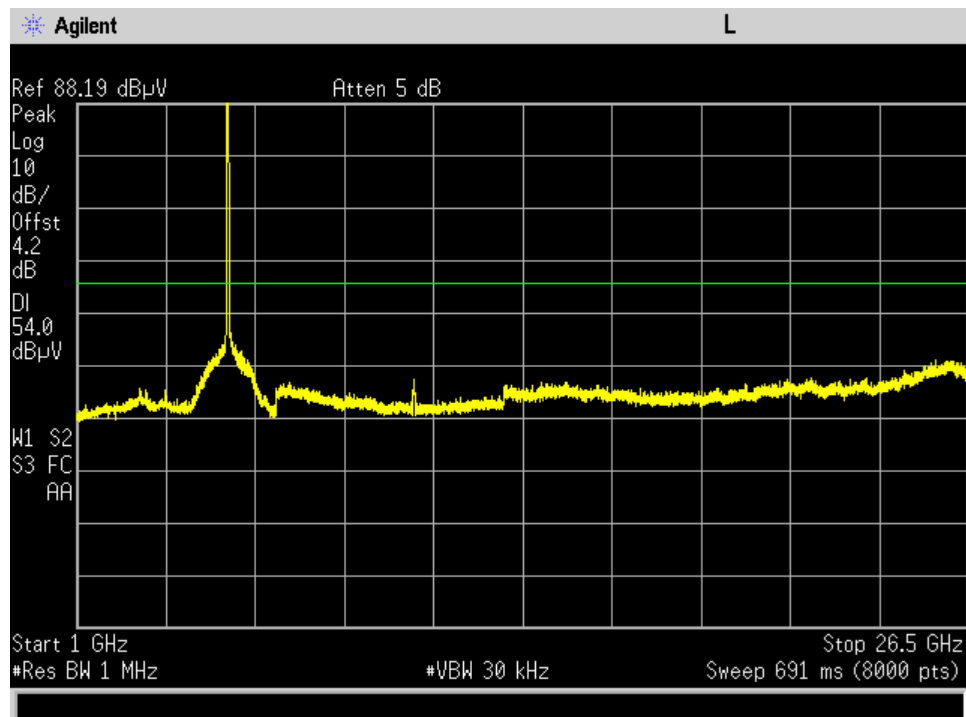




high ch port 1 30MHz BW 1-26.5GHz

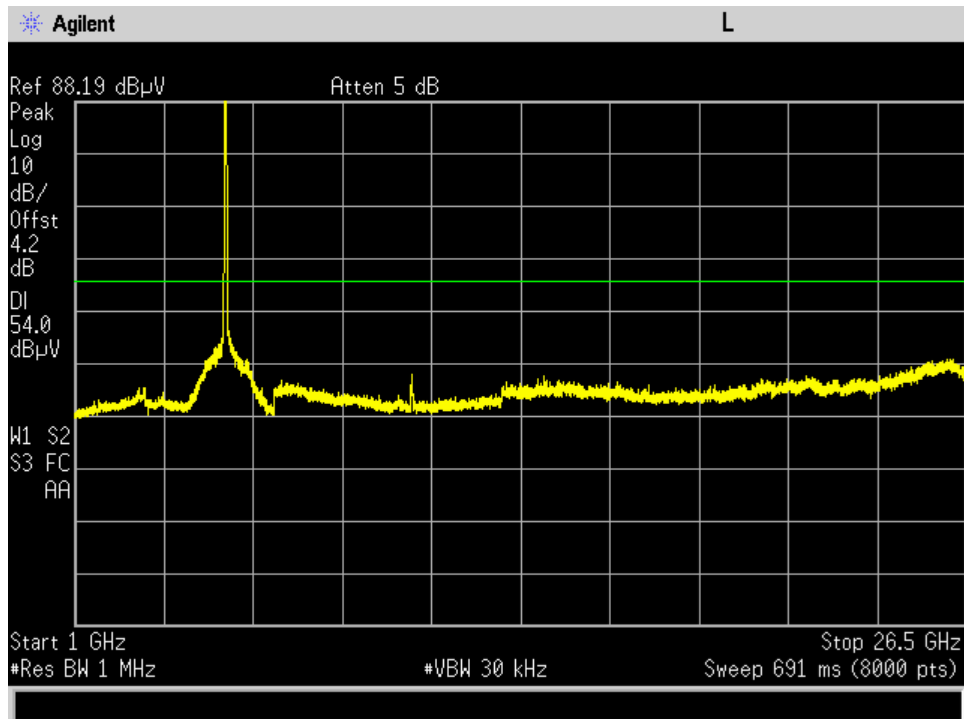


high ch port 1 40MHz BW 1-26.5GHz

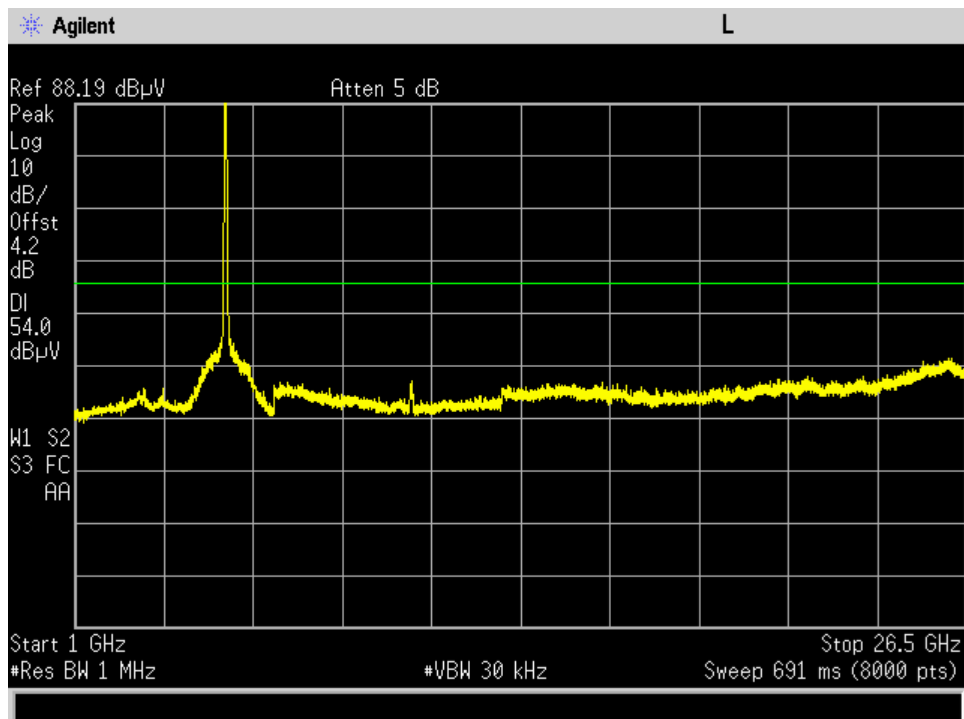




high ch port 1 50MHz BW 1-26.5GHz

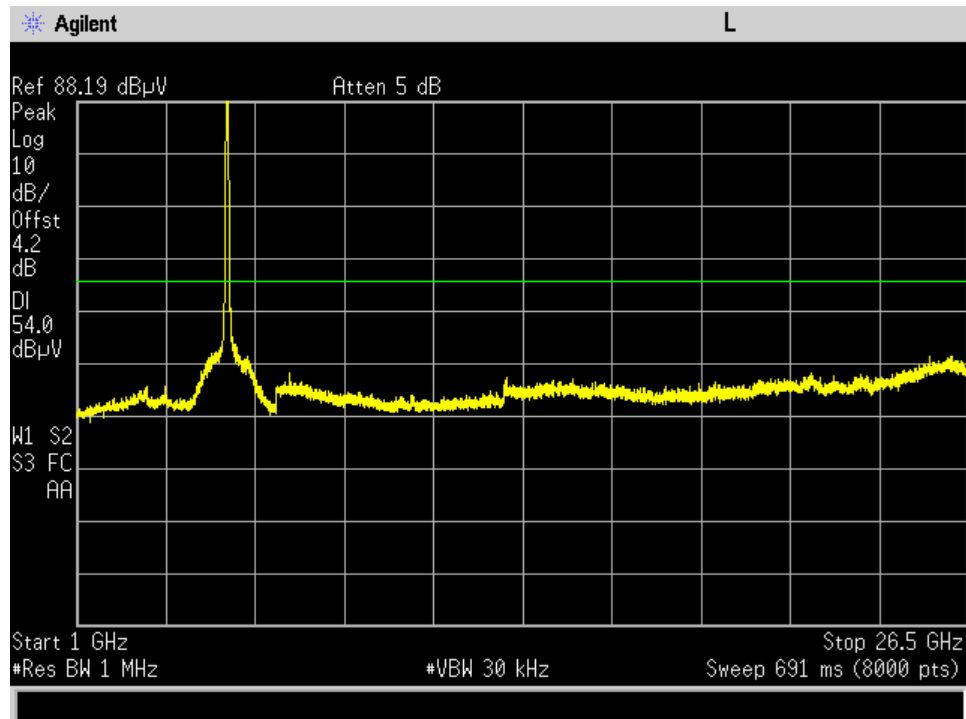


high ch port 1 60MHz BW 1-26.5GHz

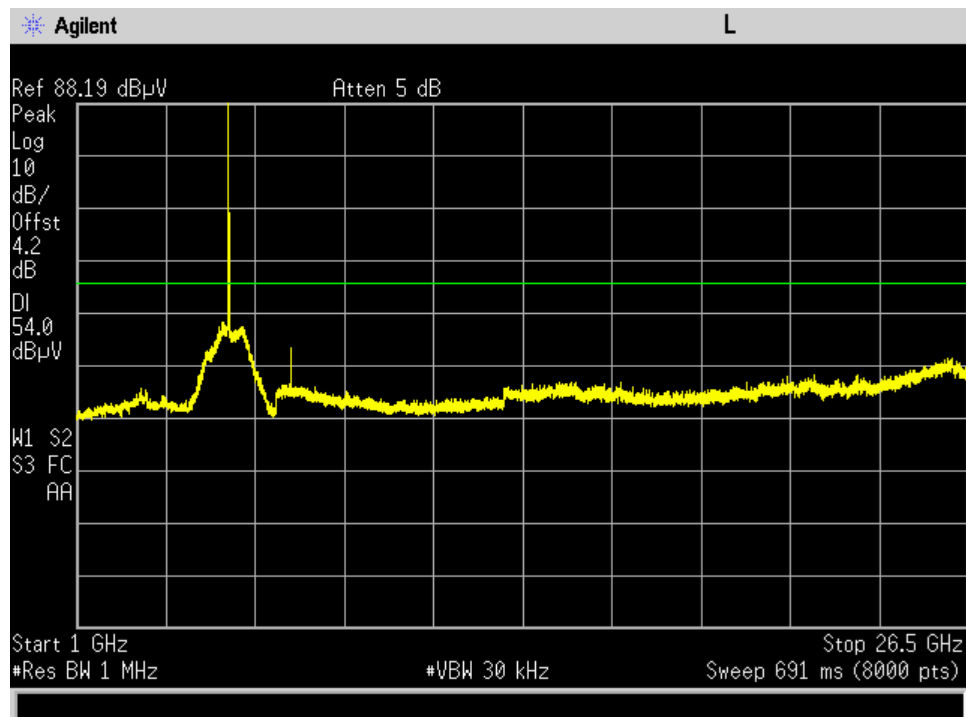




high ch port 1 80MHz BW 1-26.5GHz

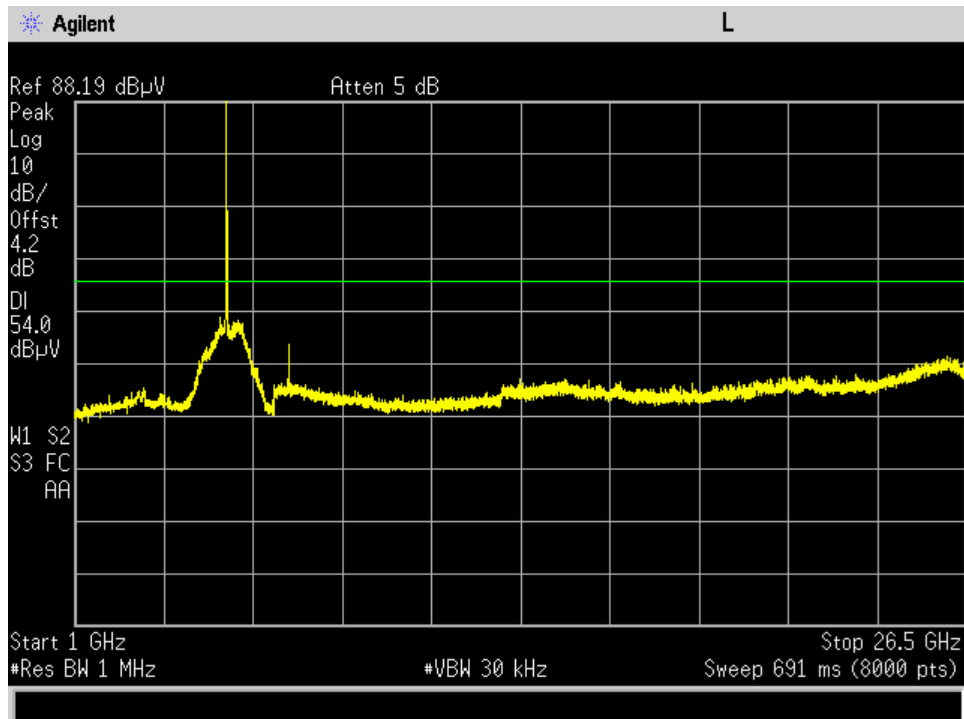


high ch port 2 10MHz BW 1-26.5GHz

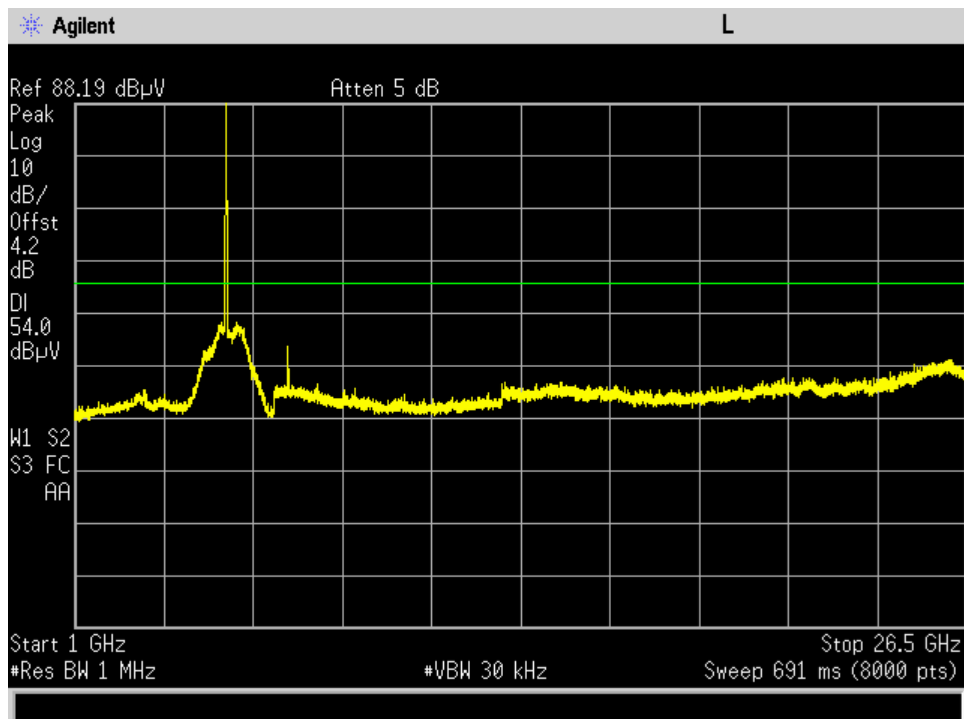




high ch port 2 20MHz BW 1-26.5GHz

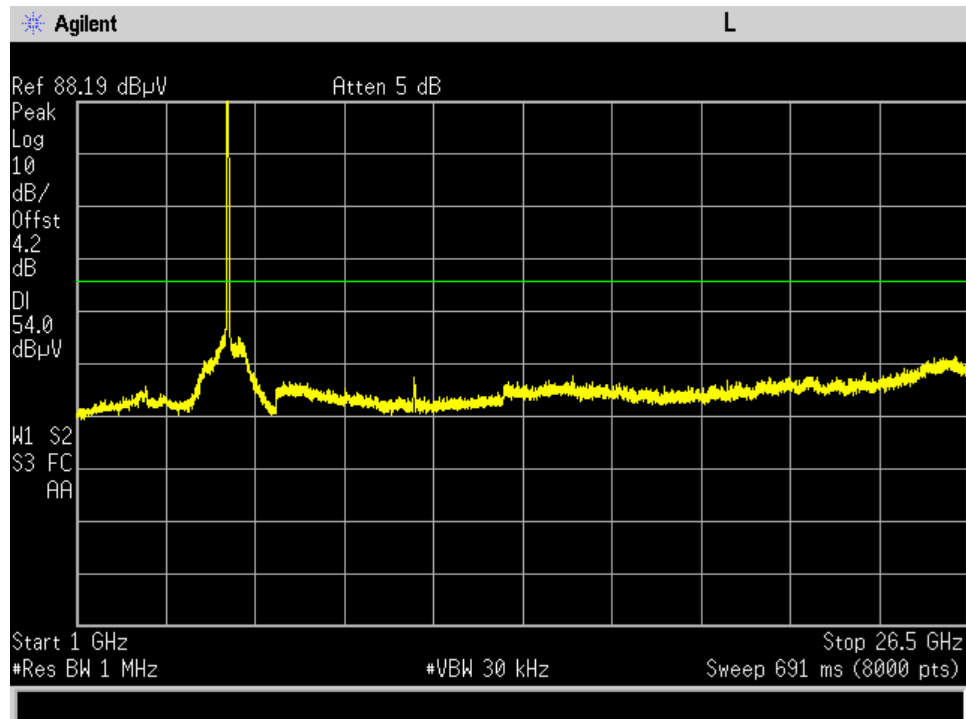


high ch port 2 30MHz BW 1-26.5GHz

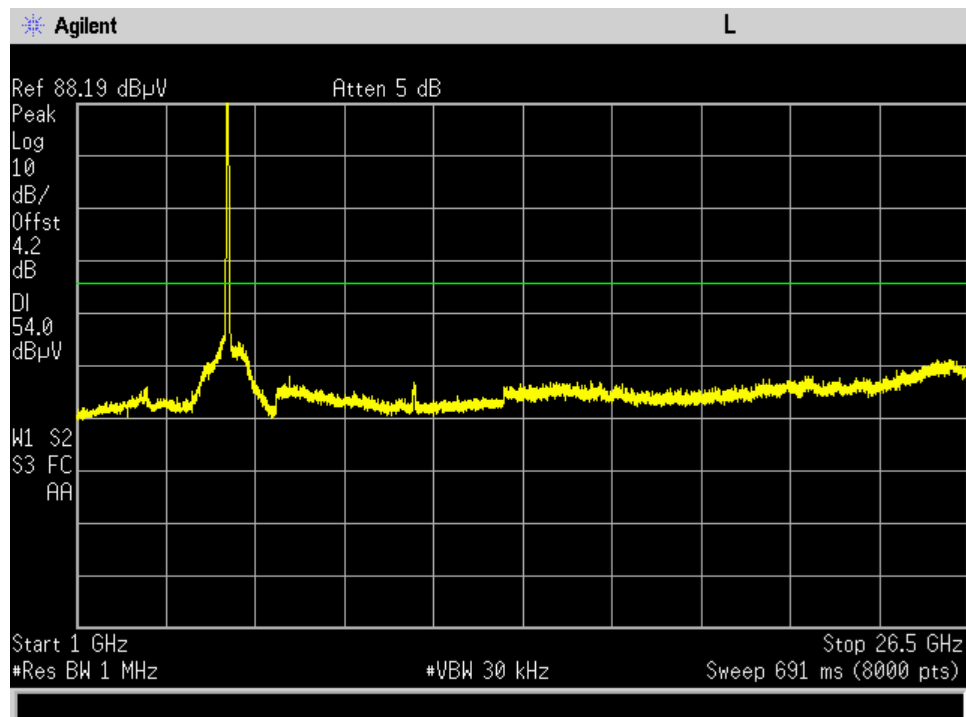




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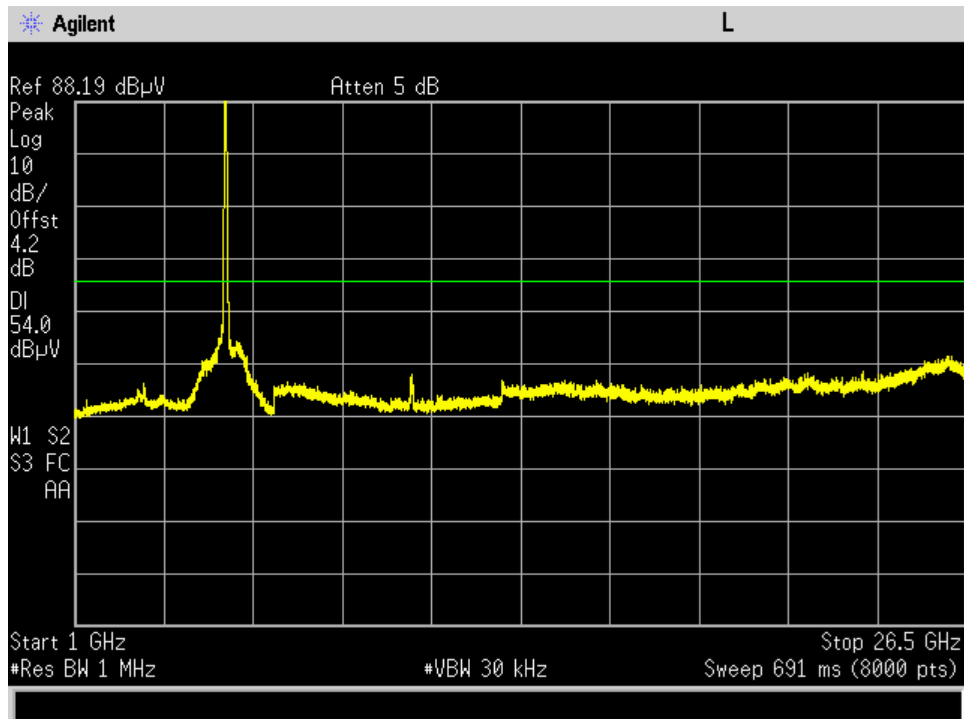


high ch port 2 50MHz BW 1-26.5GHz

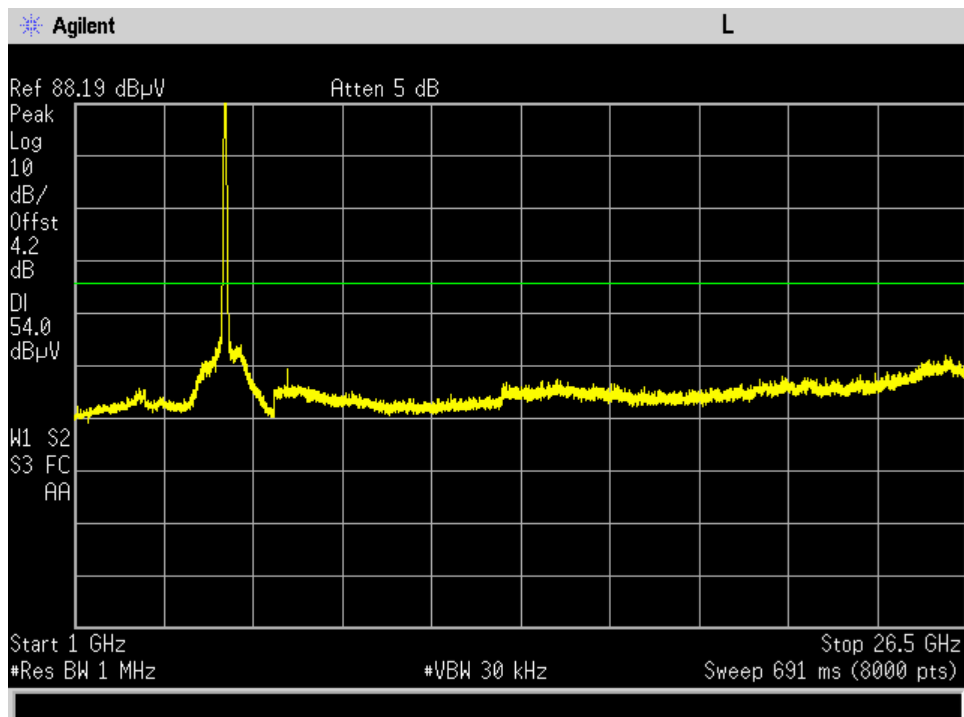




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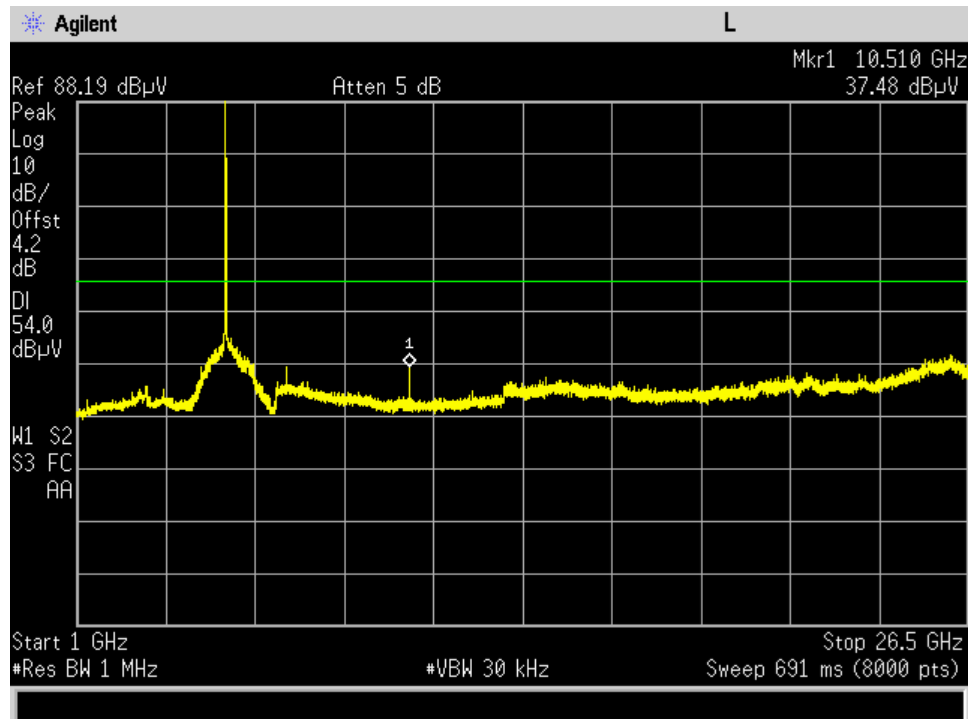


high ch port 2 80MHz BW 1-26.5GHz

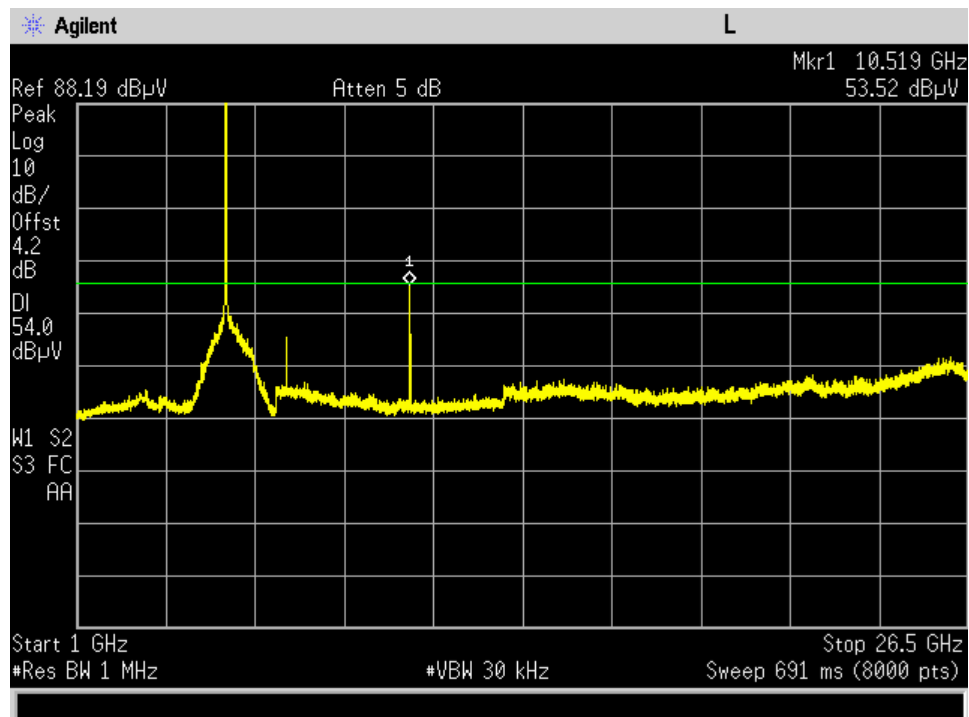




low ch port 1 10MHz BW 1-26.5GHz

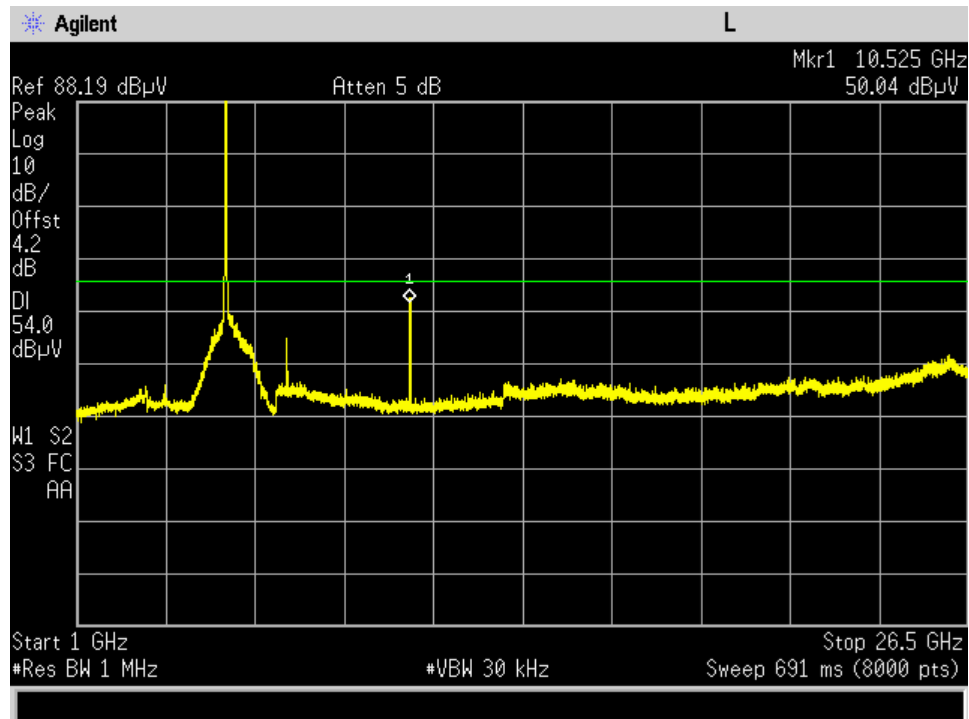


low ch port 1 20MHz BW 1-26.5GHz

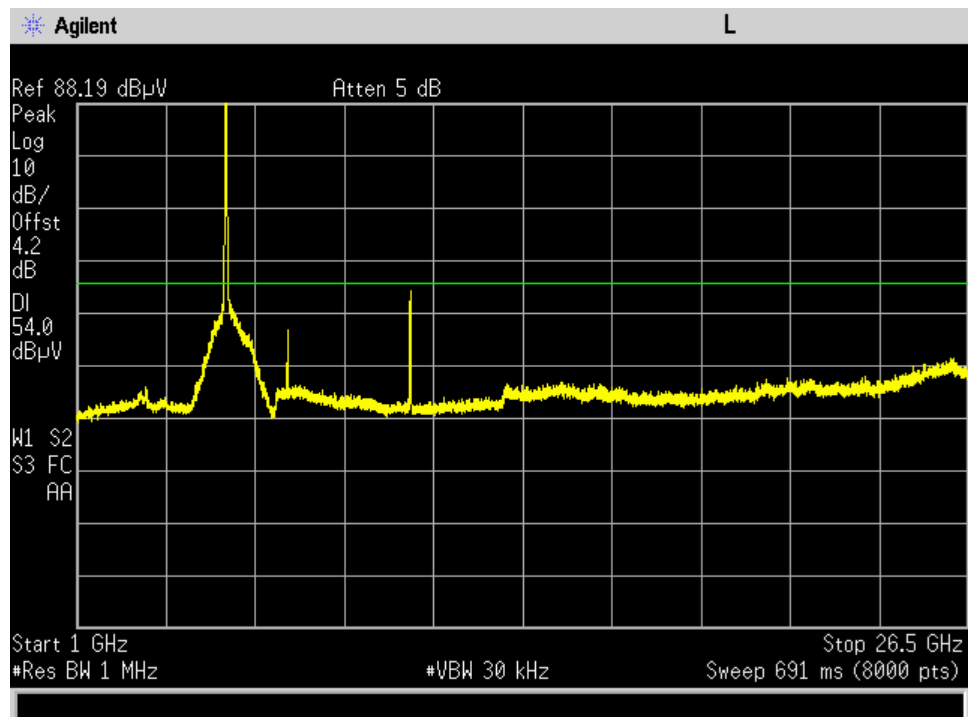




low ch port 1 30MHz BW 1-26.5GHz

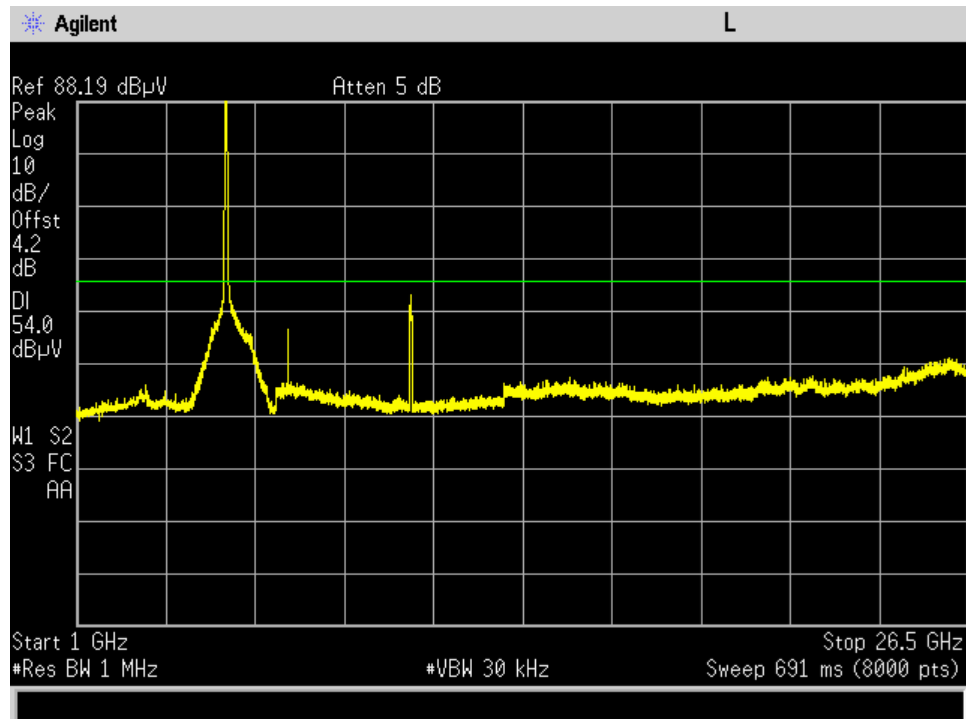


low ch port 1 40MHz BW 1-26.5GHz

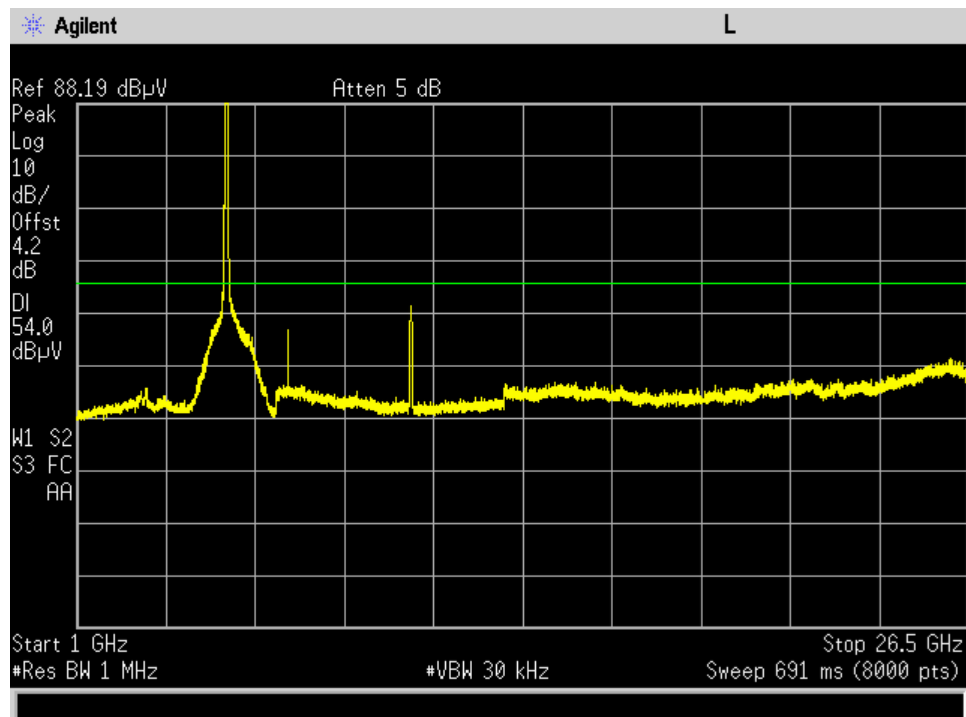




low ch port 1 50MHz BW 1-26.5GHz

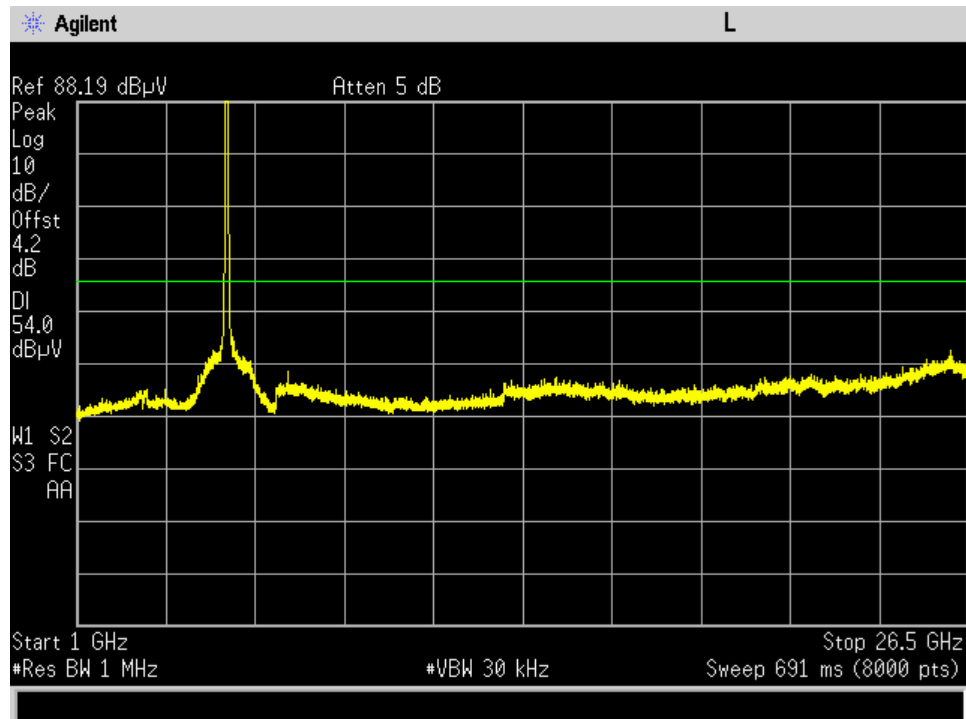


low ch port 1 60MHz BW 1-26.5GHz

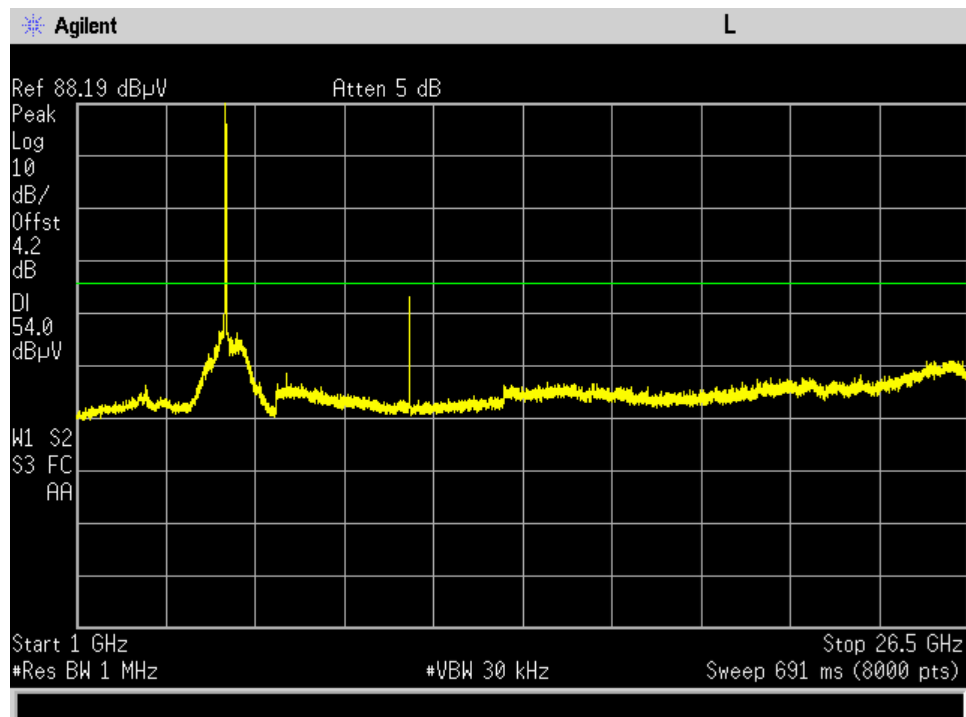




low ch port 1 80MHz BW 1-26.5GHz

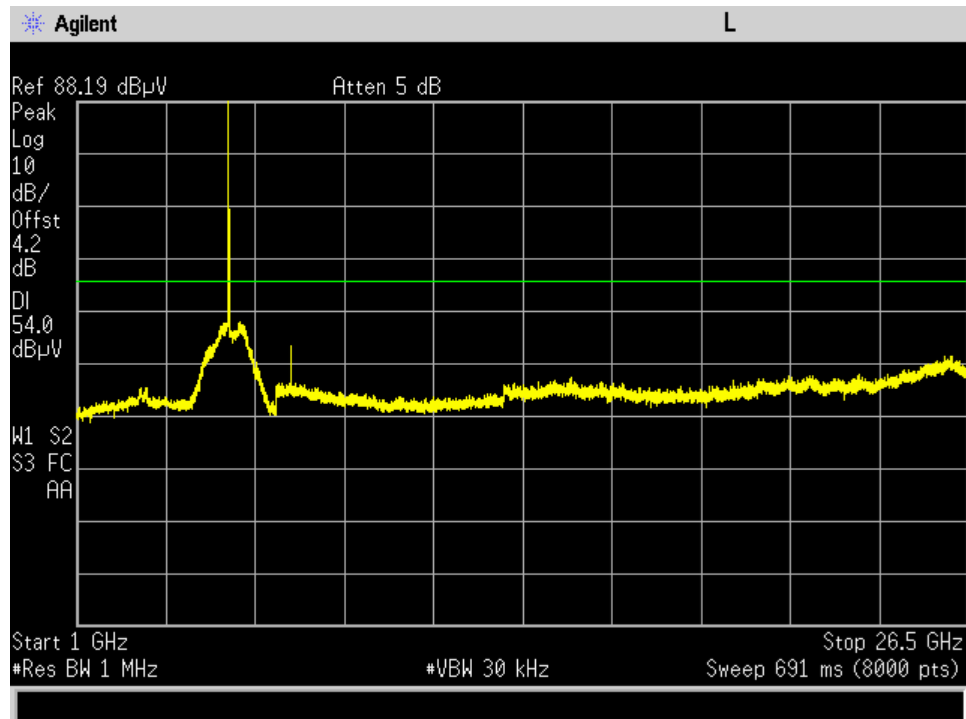


low ch port 2 10MHz BW 1-26.5GHz

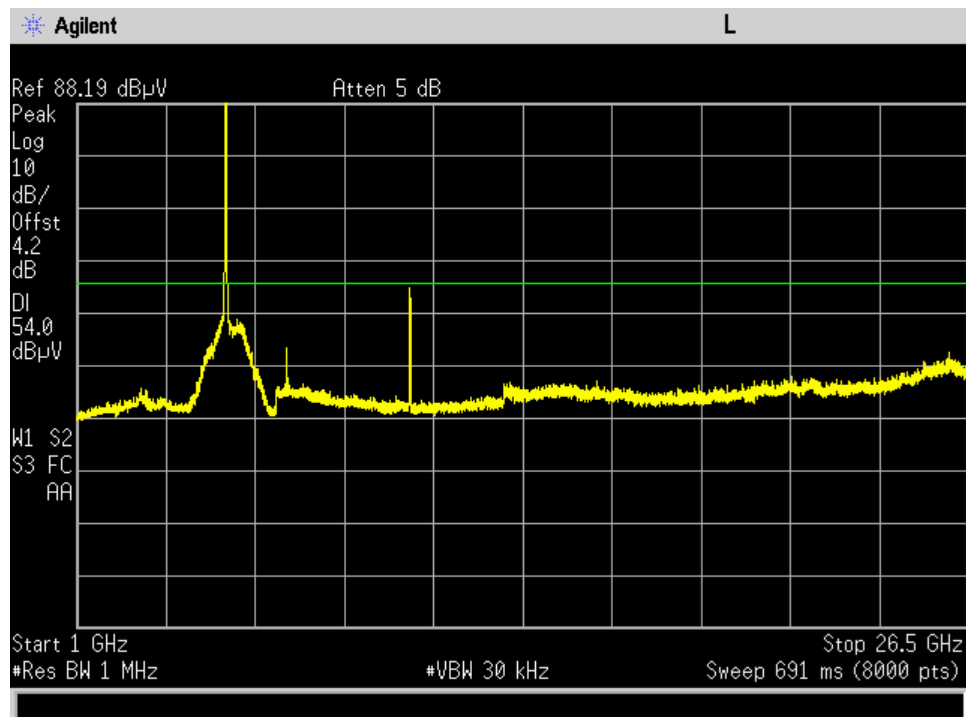




low ch port 2 20MHz BW 1-26.5GHz

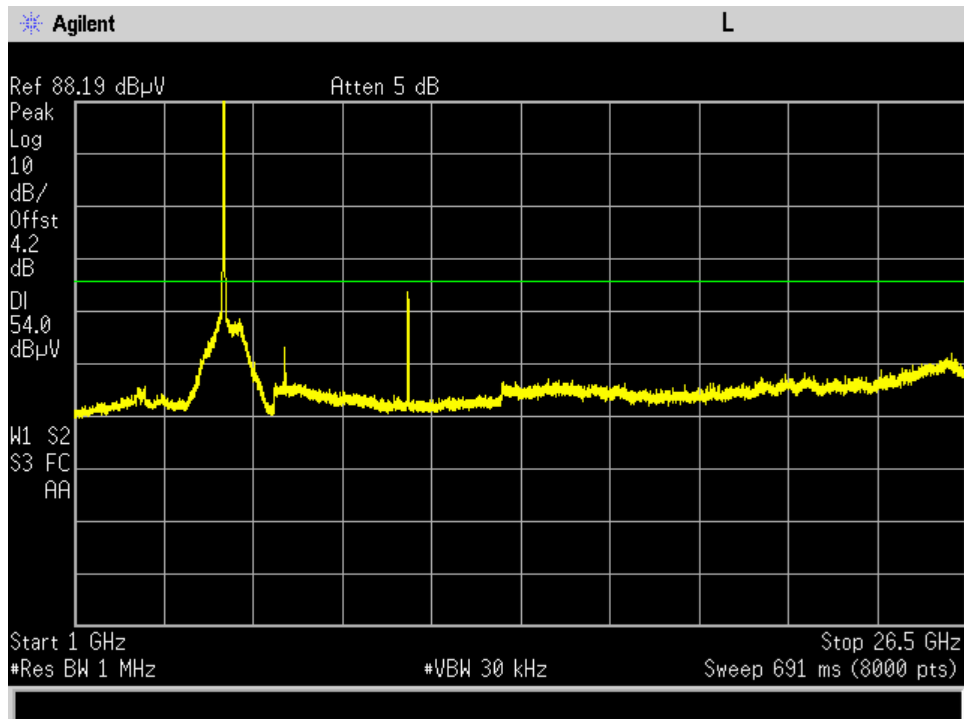


low ch port 2 30MHz BW 1-26.5GHz

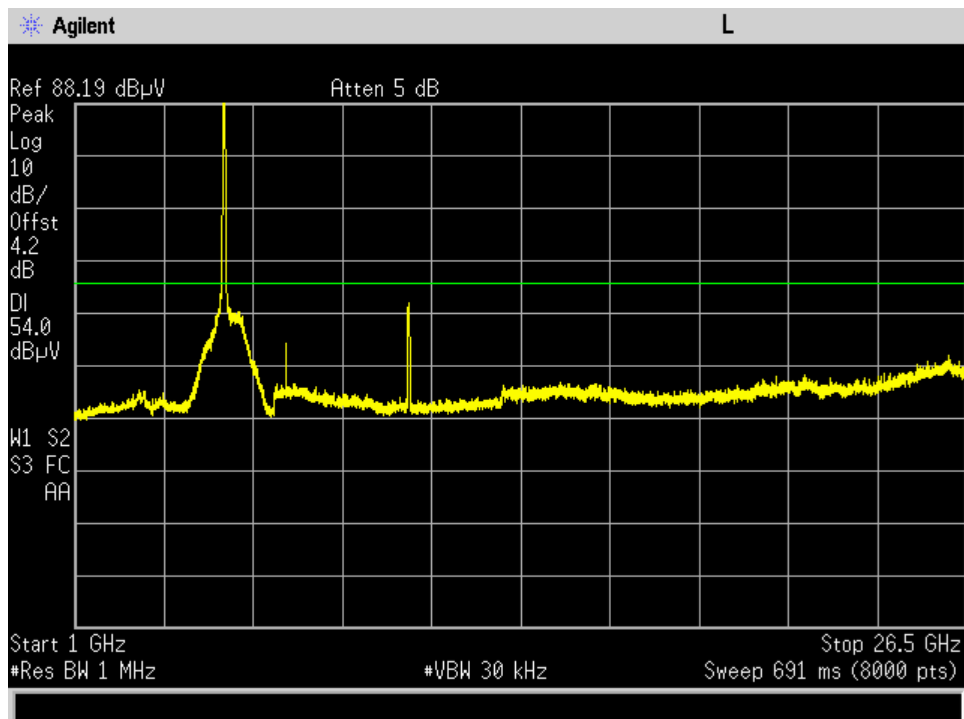




low ch port 2 40MHz BW 1-26.5GHz

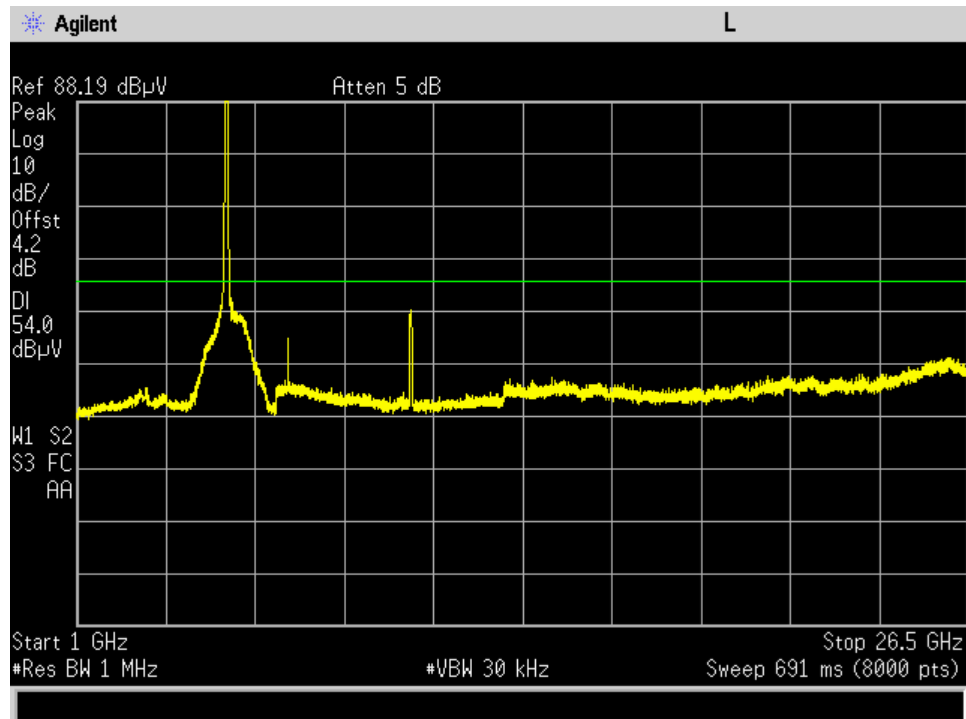


low ch port 2 50MHz BW 1-26.5GHz

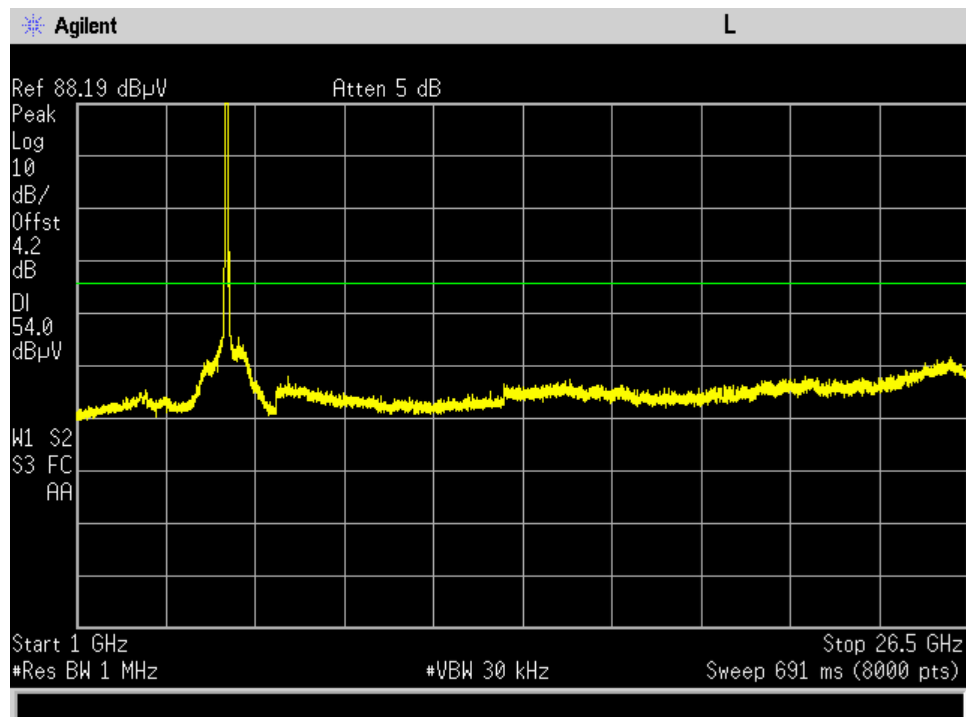




low ch port 2 60MHz BW 1-26.5GHz

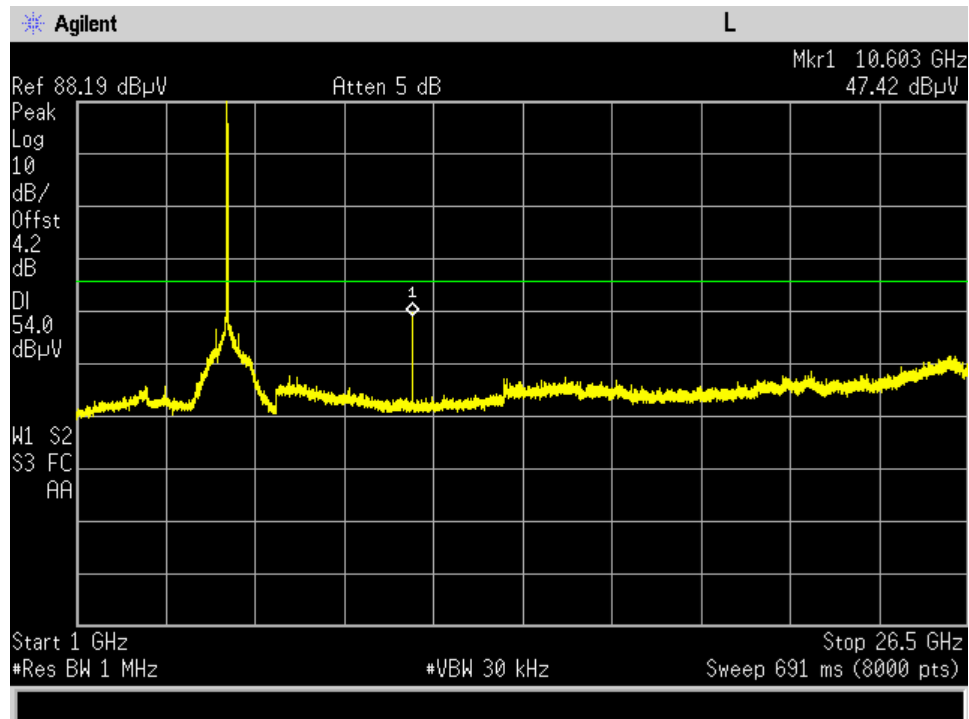


low ch port 2 80MHz BW 1-26.5GHz

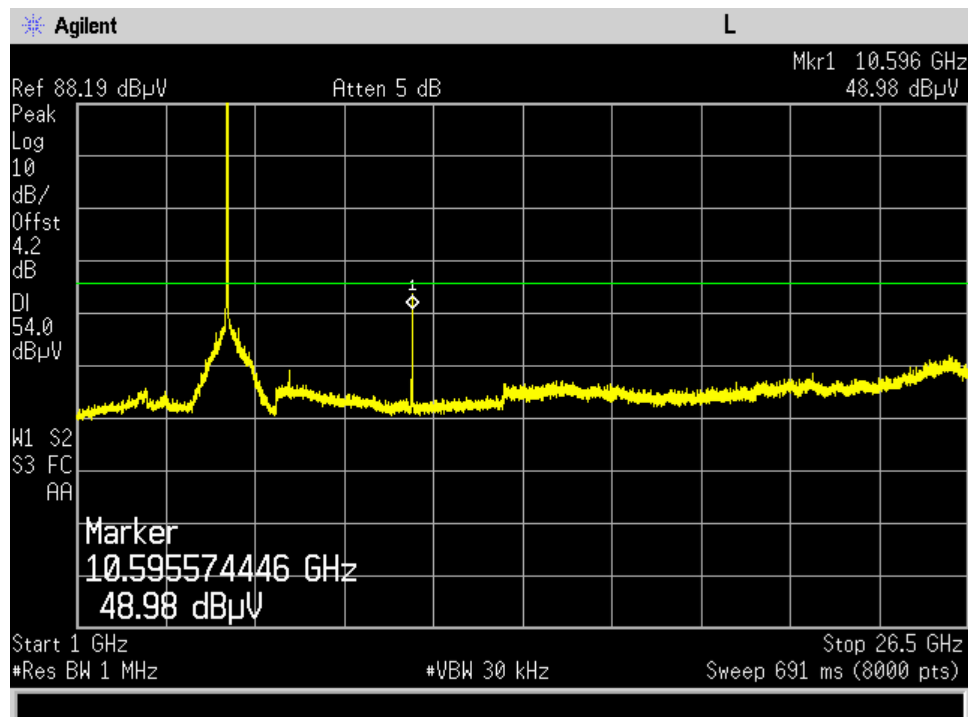




mid ch port 1 10MHz BW 1-26.5GHz

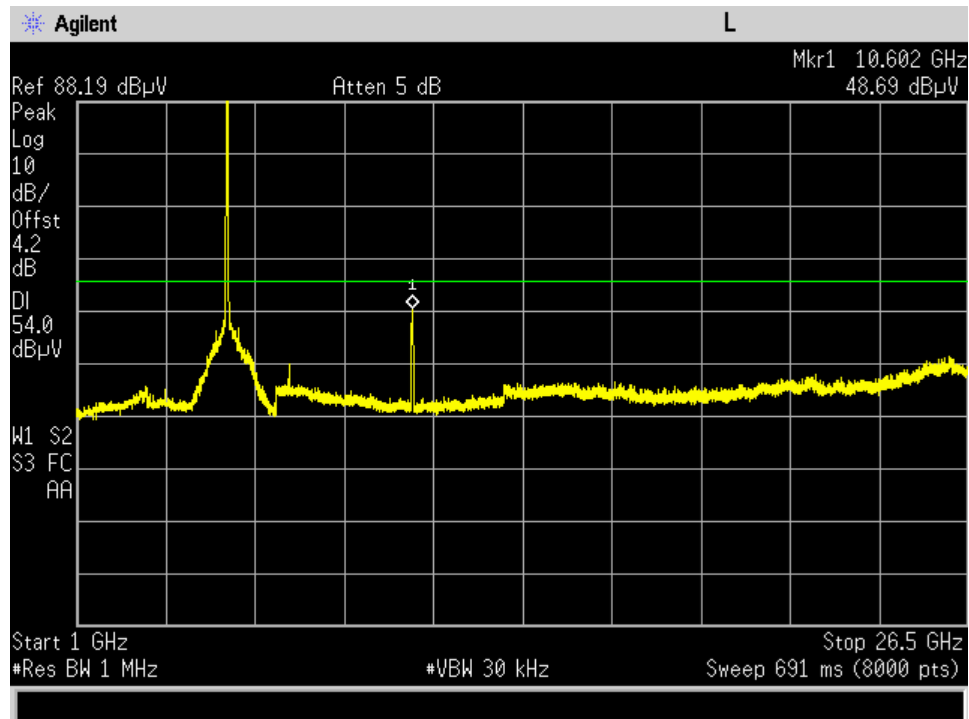


mid ch port 1 20MHz BW 1-26.5GHz

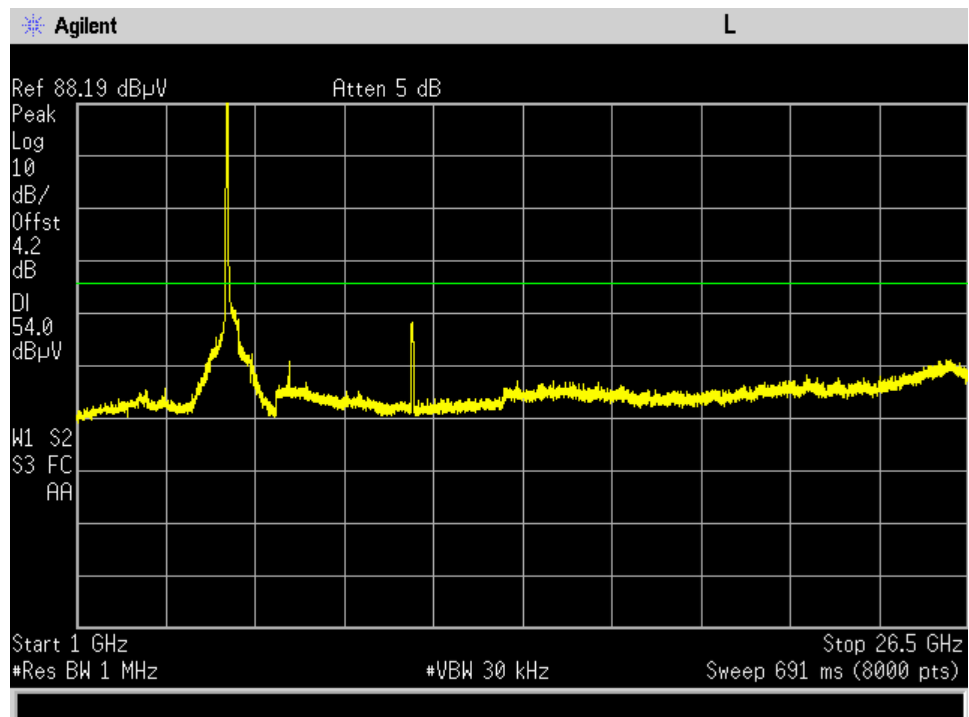




mid ch port 1 30MHz BW 1-26.5GHz

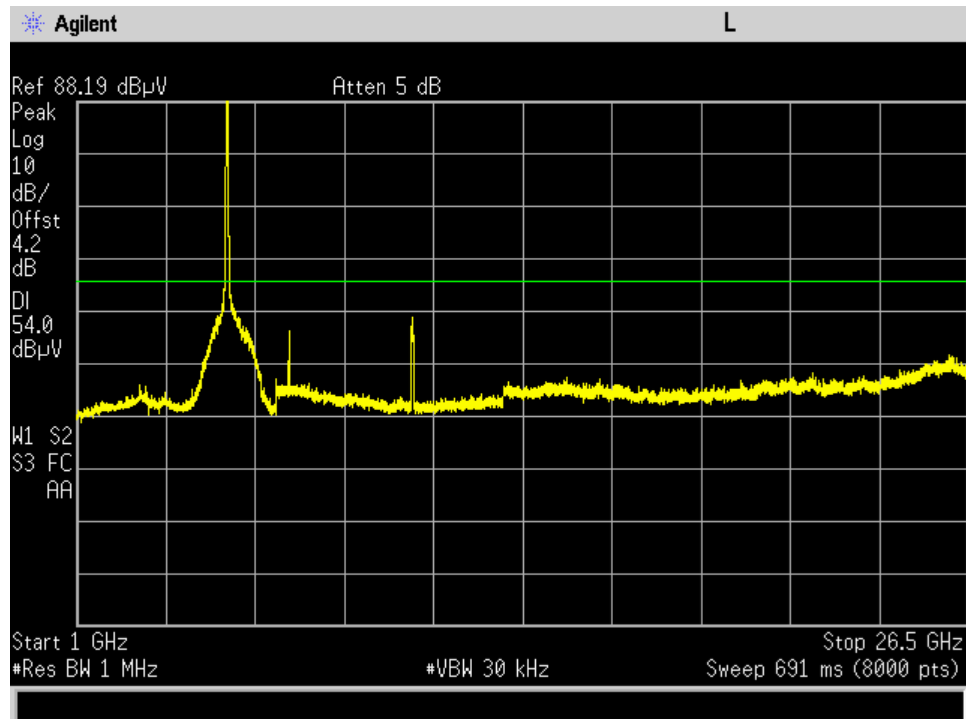


mid ch port 1 40MHz BW 1-26.5GHz

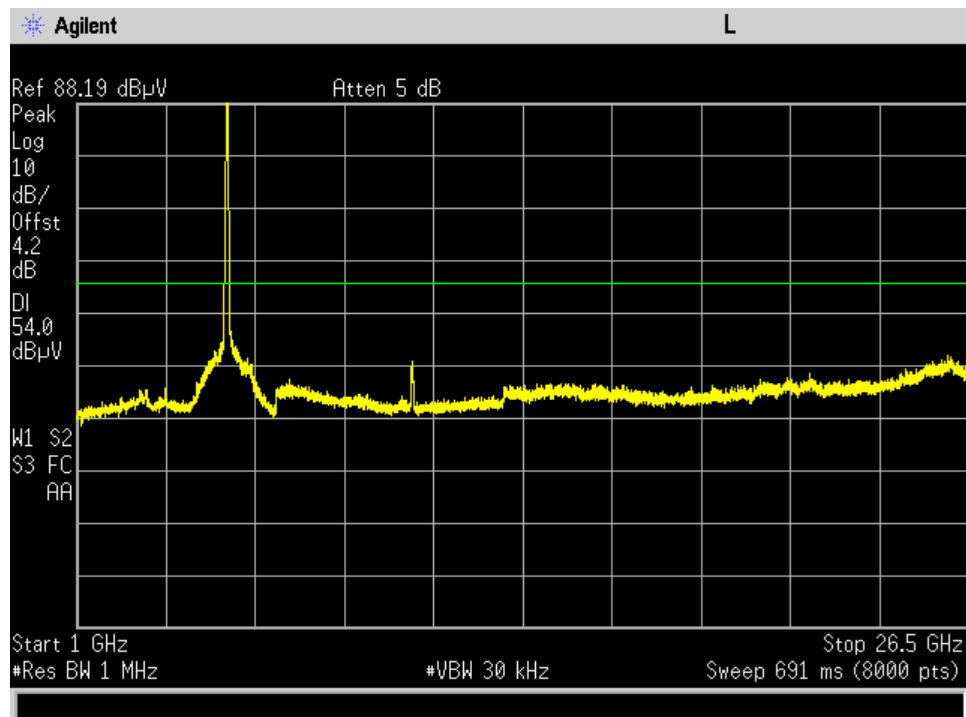




mid ch port 1 50MHz BW 1-26.5GHz

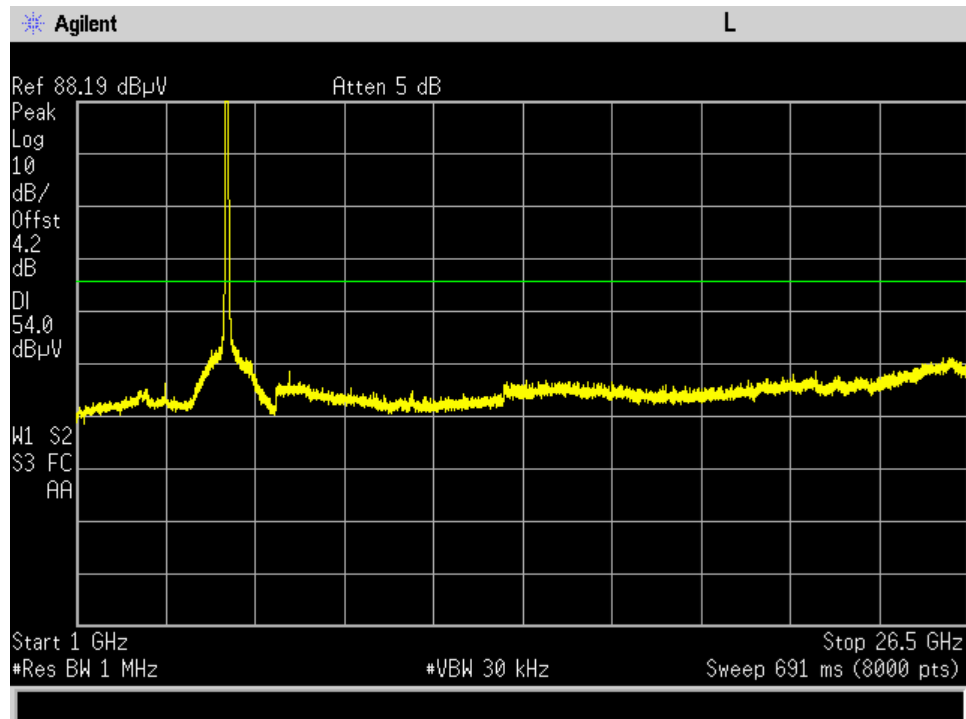


mid ch port 1 60MHz BW 1-26.5GHz

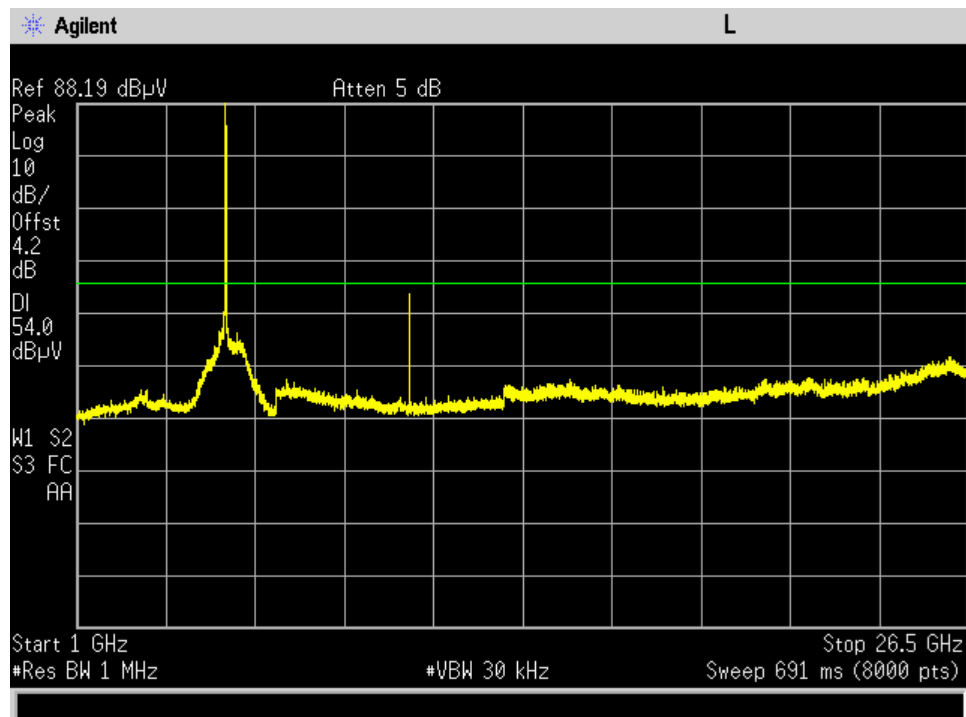




mid ch port 1 80MHz BW 1-26.5GHz

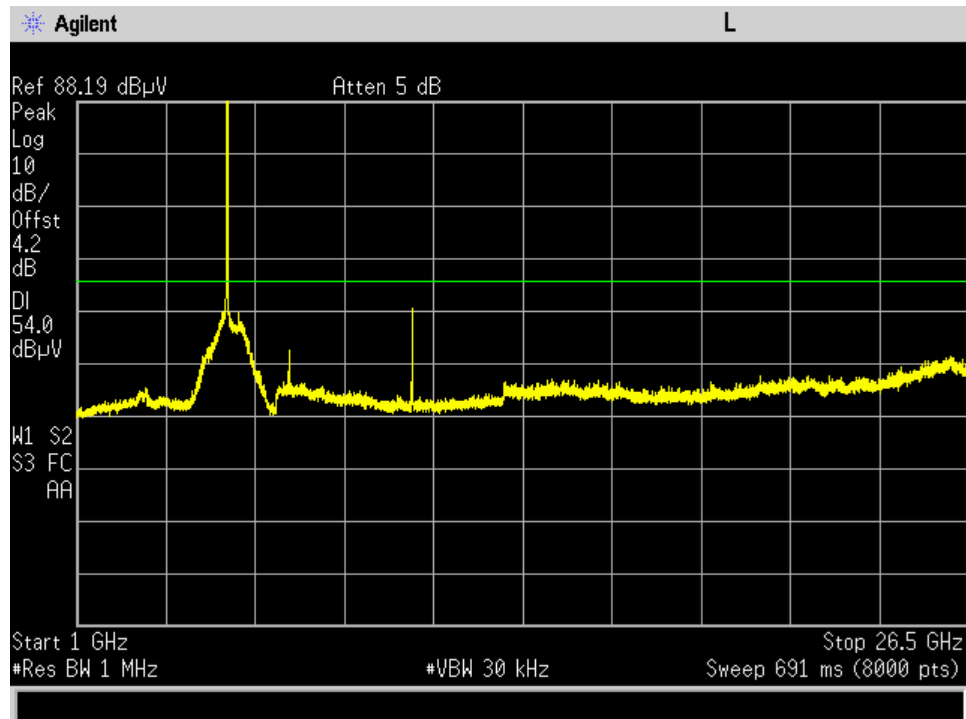


mid ch port 2 10MHz BW 1-26.5GHz

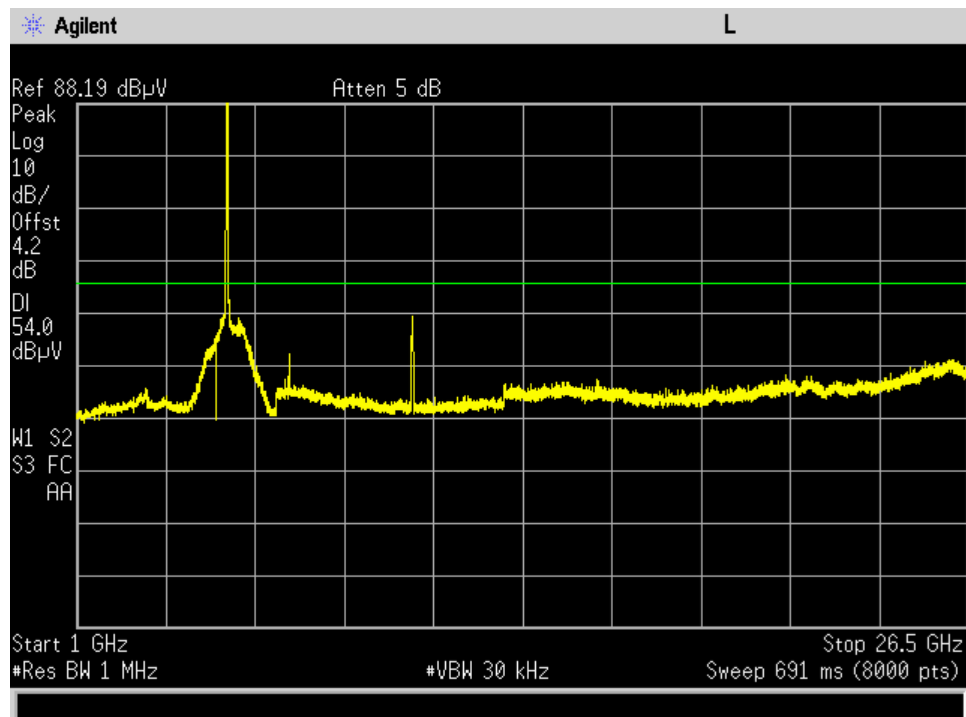




mid ch port 2 20MHz BW 1-26.5GHz

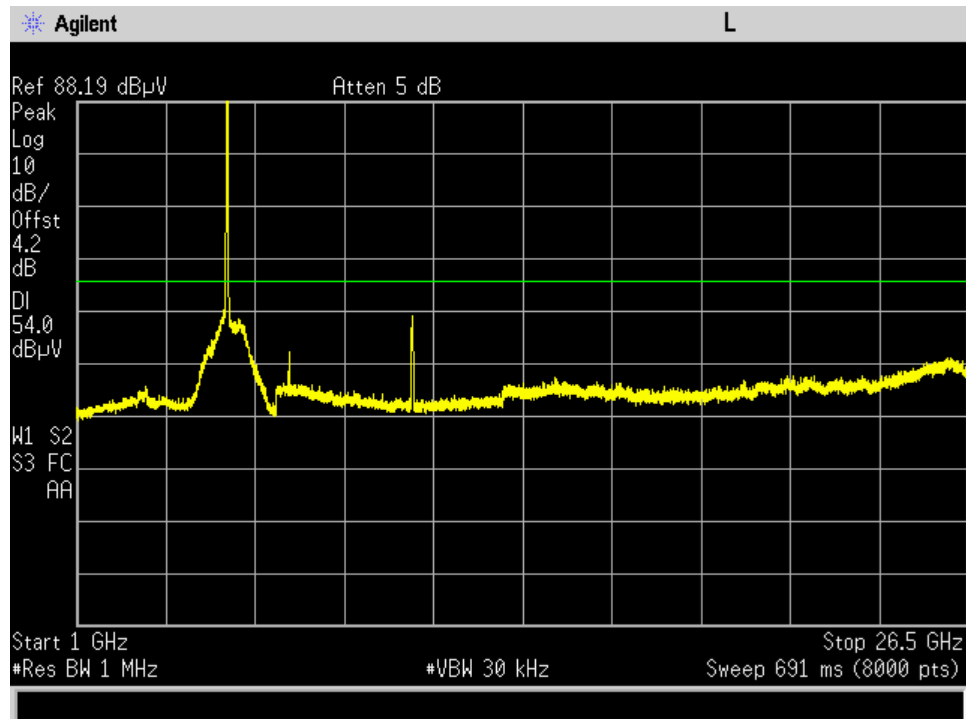


mid ch port 2 30MHz BW 1-26.5GHz

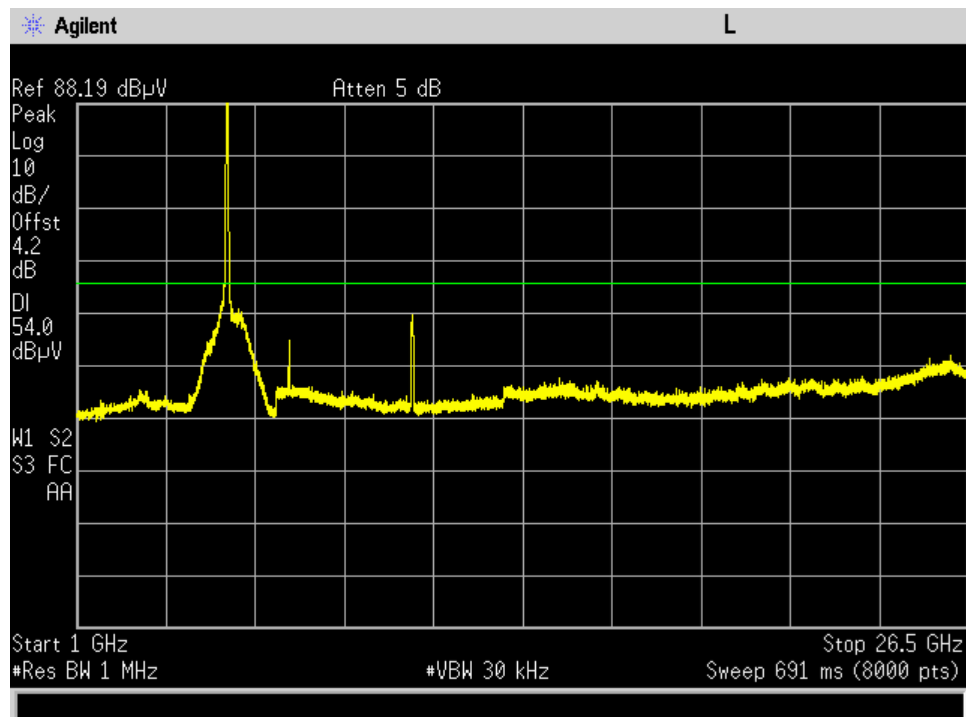




mid ch port 2 40MHz BW 1-26.5GHz

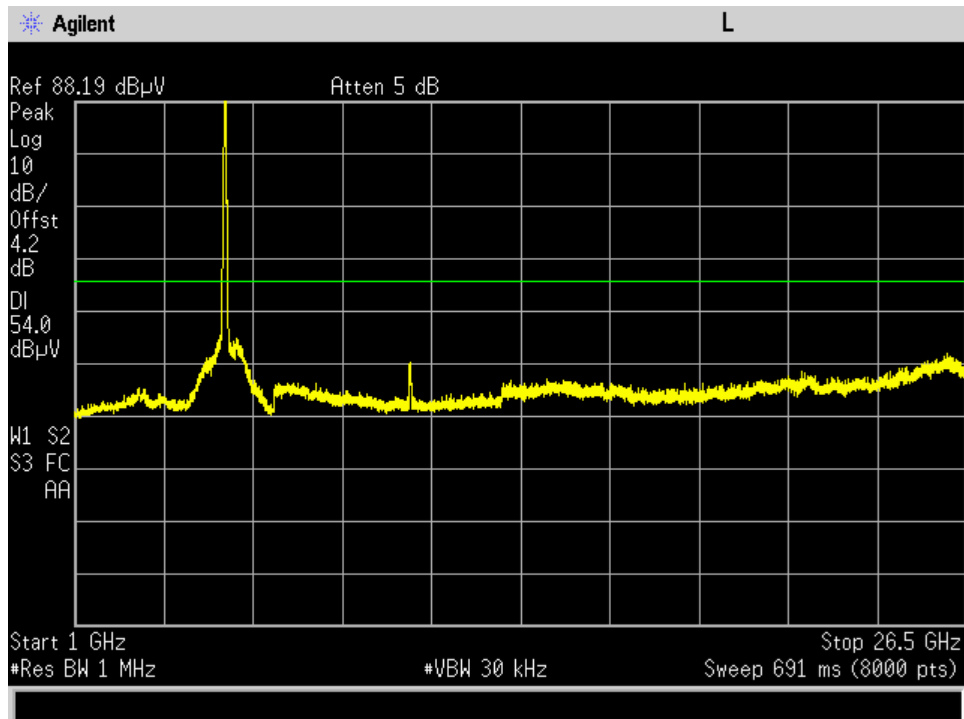


mid ch port 2 50MHz BW 1-26.5GHz

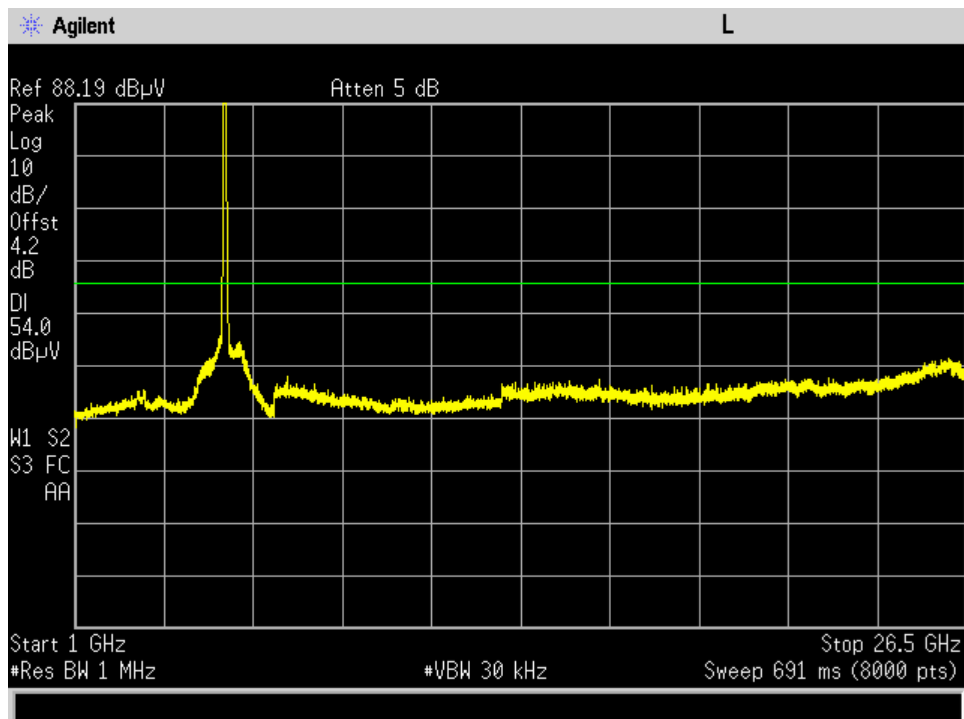




mid ch port 2 60MHz BW 1-26.5GHz



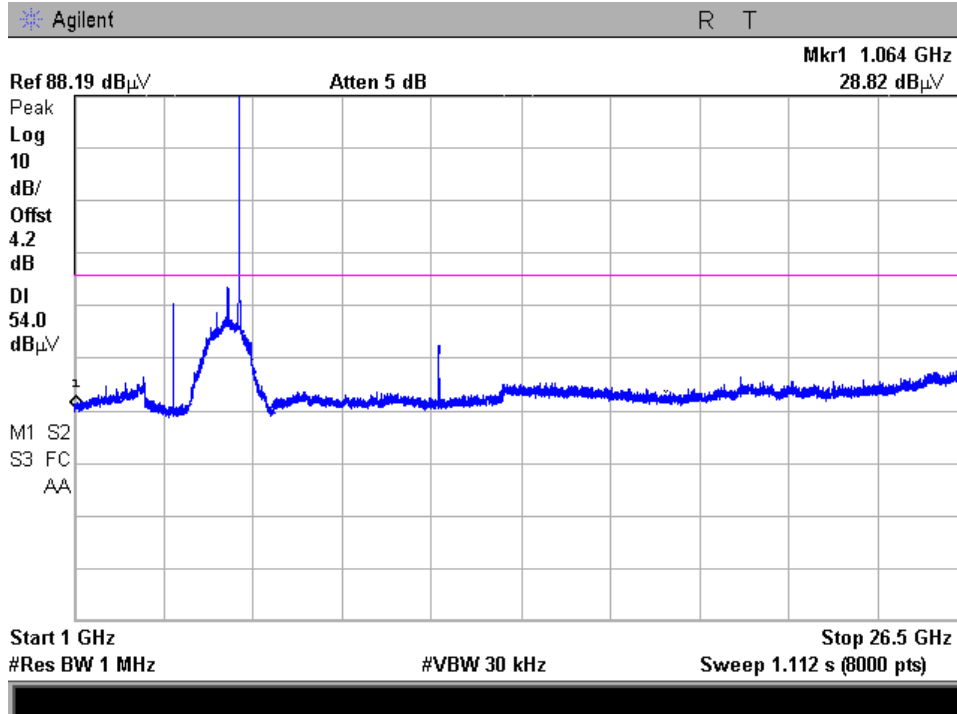
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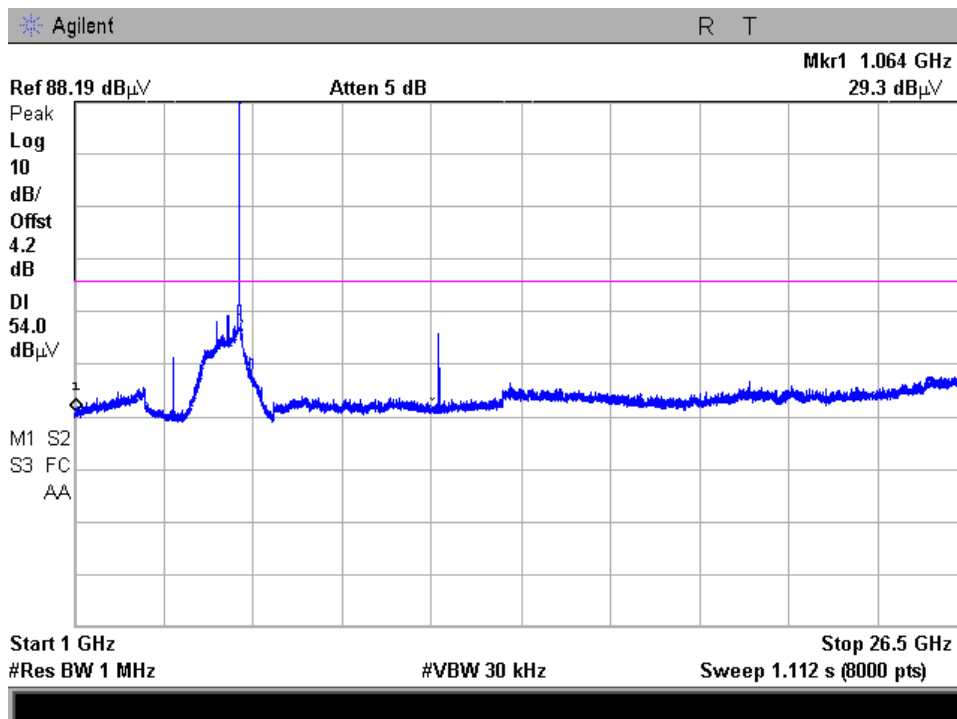


UNII-2C

10MHz High Ch 1-26.5GHz Port J8

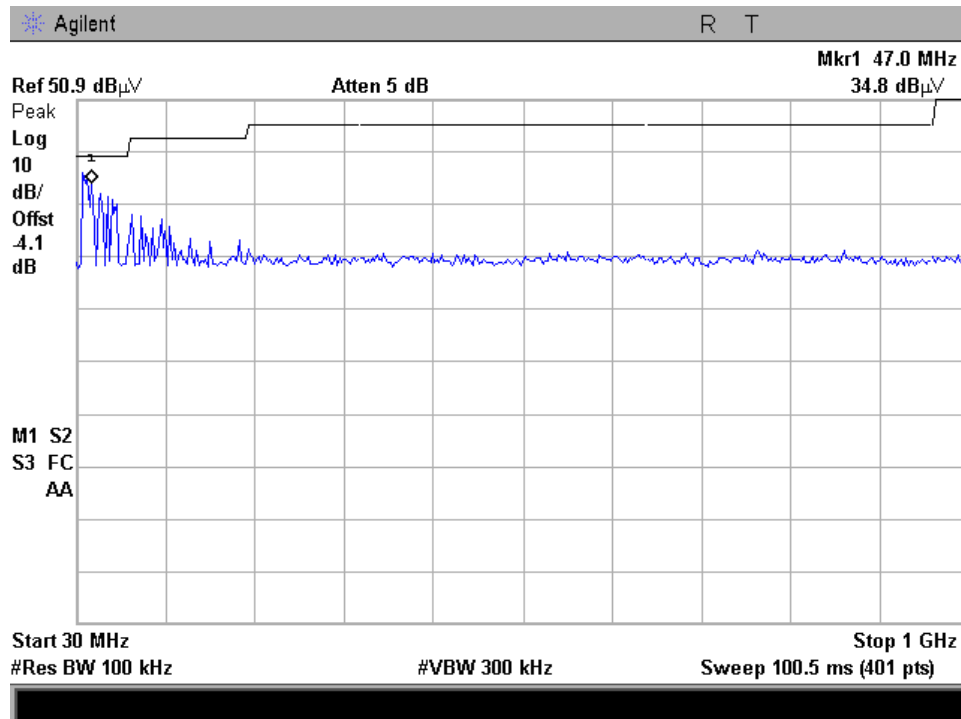


10MHz High Ch 1-26.5GHz Port J7

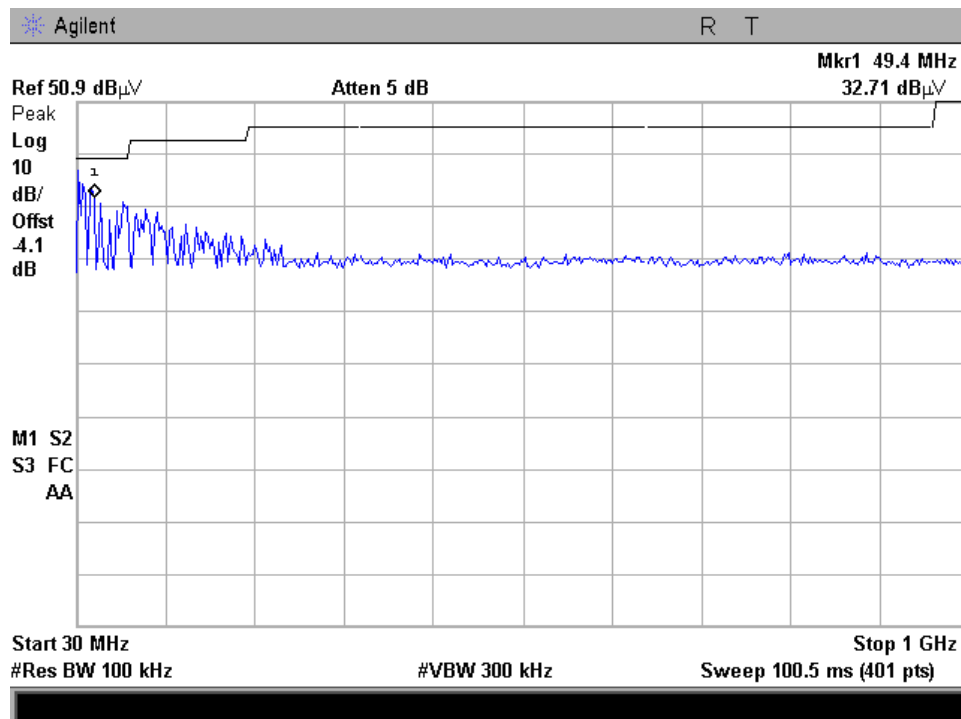




10MHz High Ch 30-1000MHz Port J8

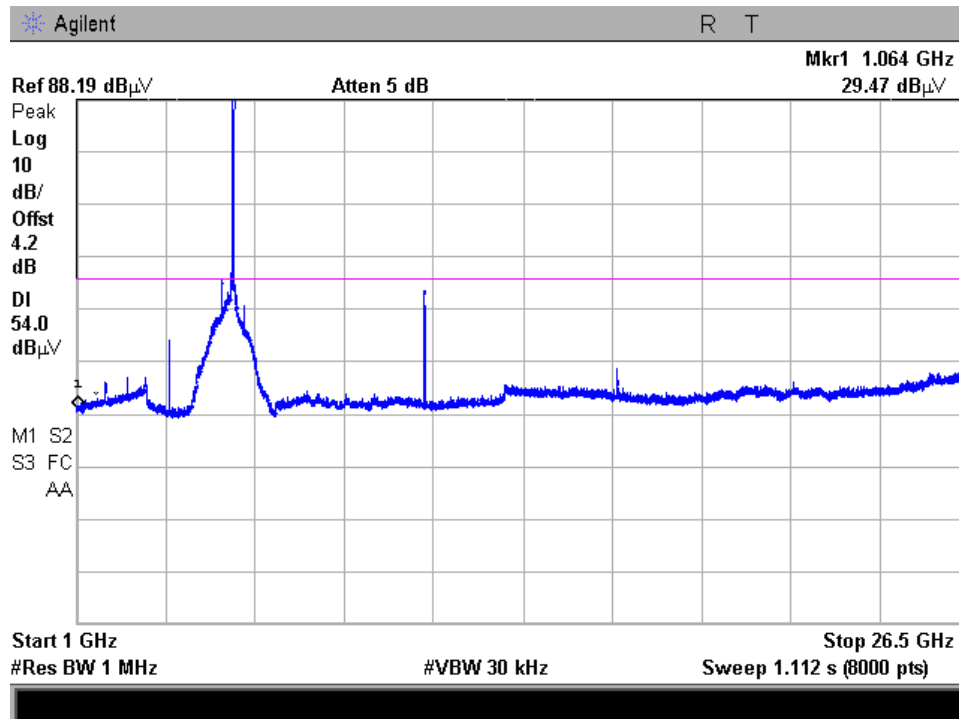


10MHz High Ch 30-1000MHz Port J7

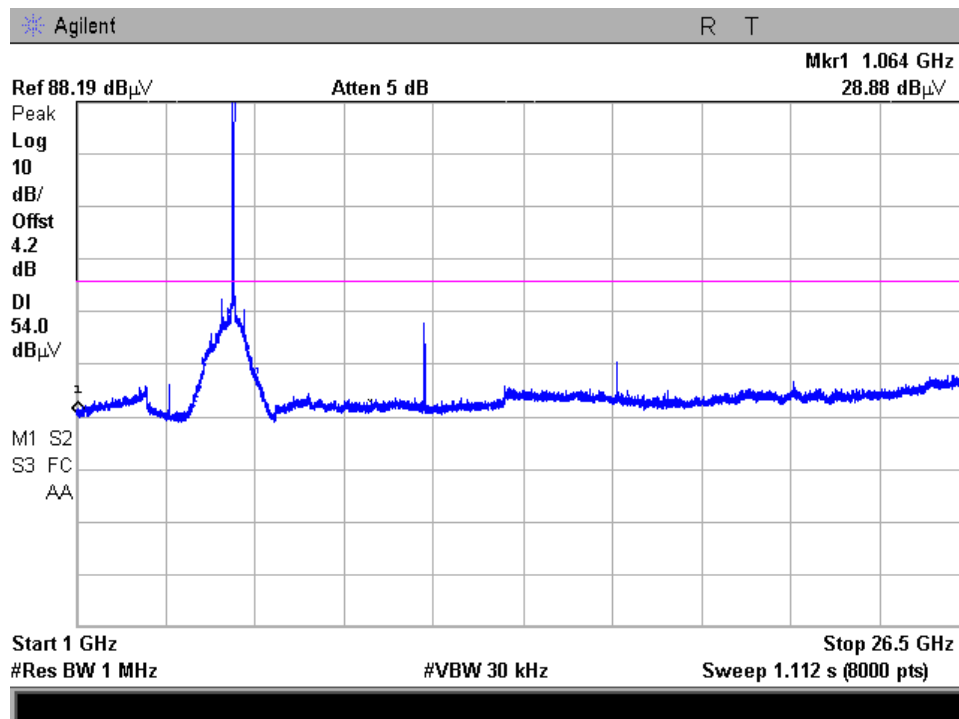




10MHz Low Ch 1-26.5GHz Port J8

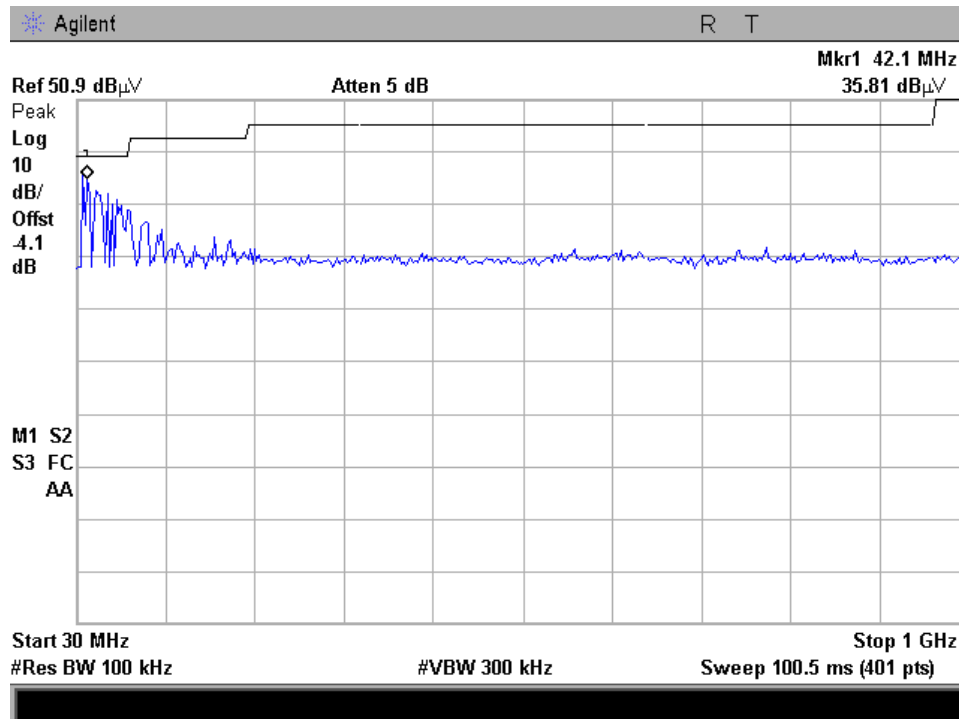


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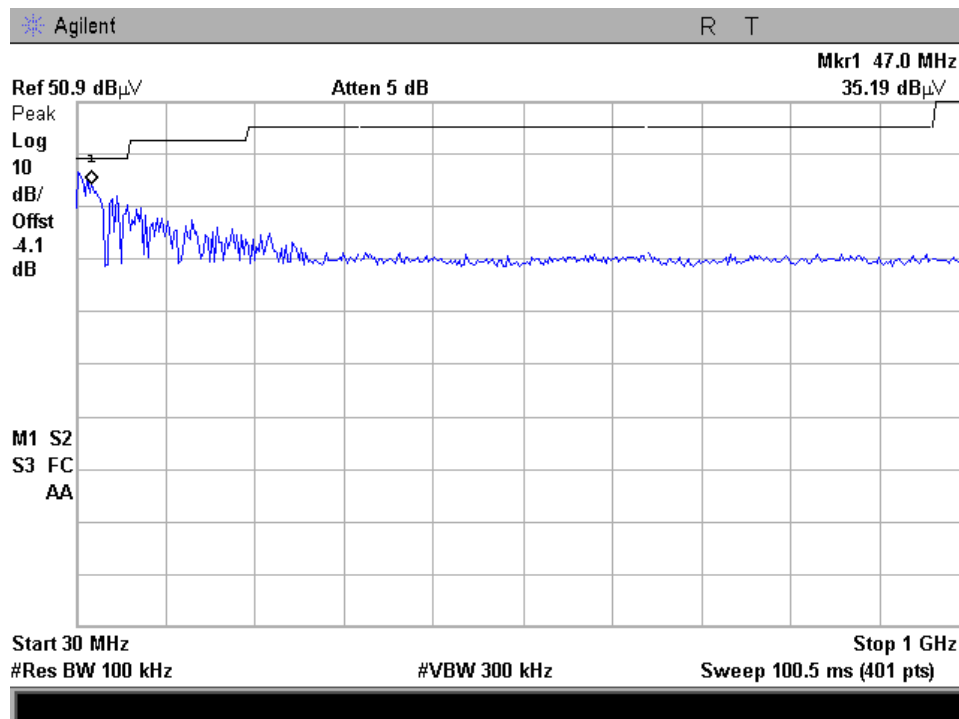




10MHz Low Ch 30-1000MHz Port J8

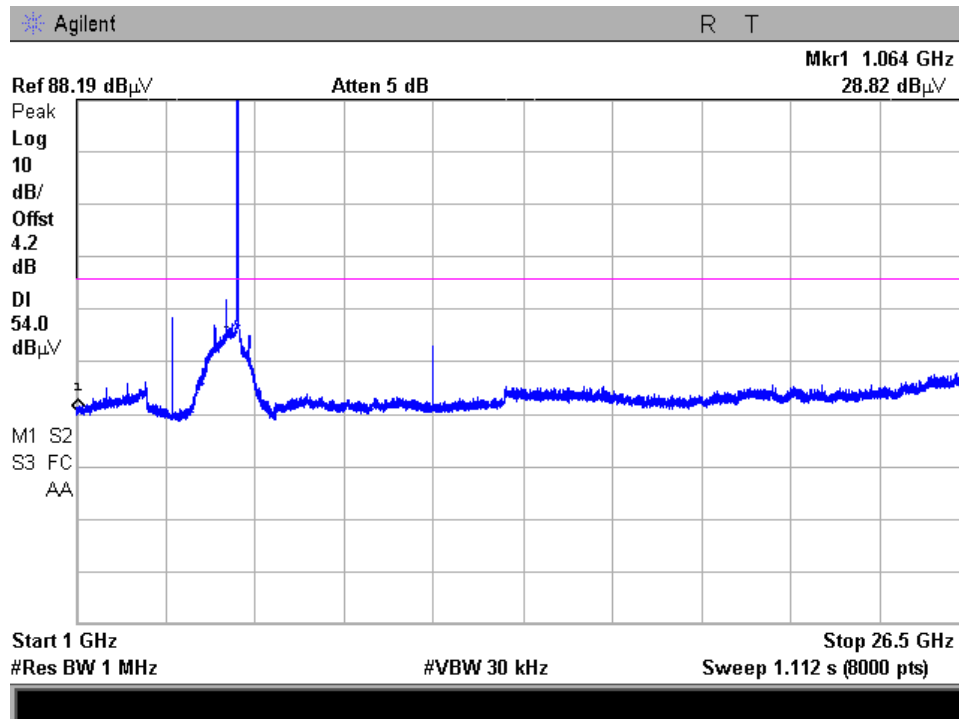


10MHz Low Ch 30-1000MHz Port J7

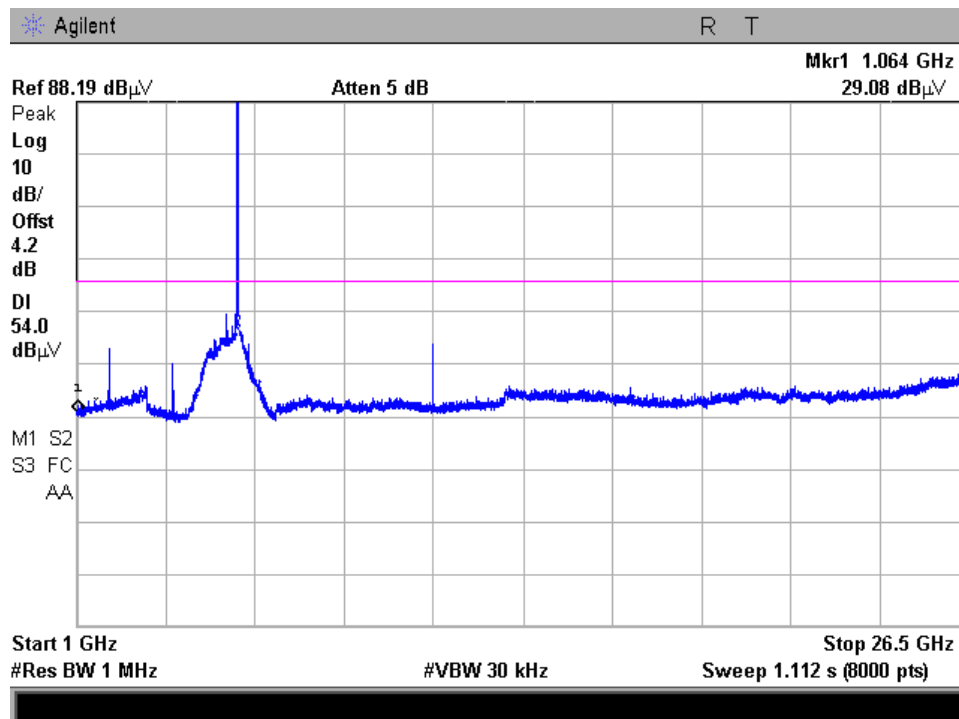




10MHz Mid Ch 1-26.5GHz Port J8

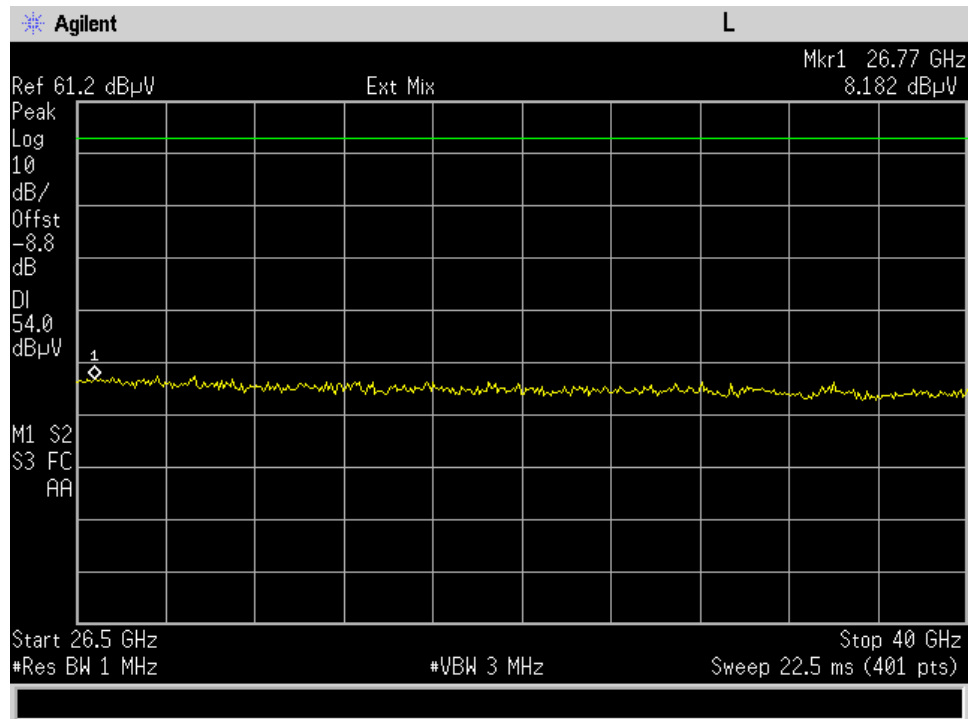


10MHz Mid Ch 1-26.5GHz Port J7

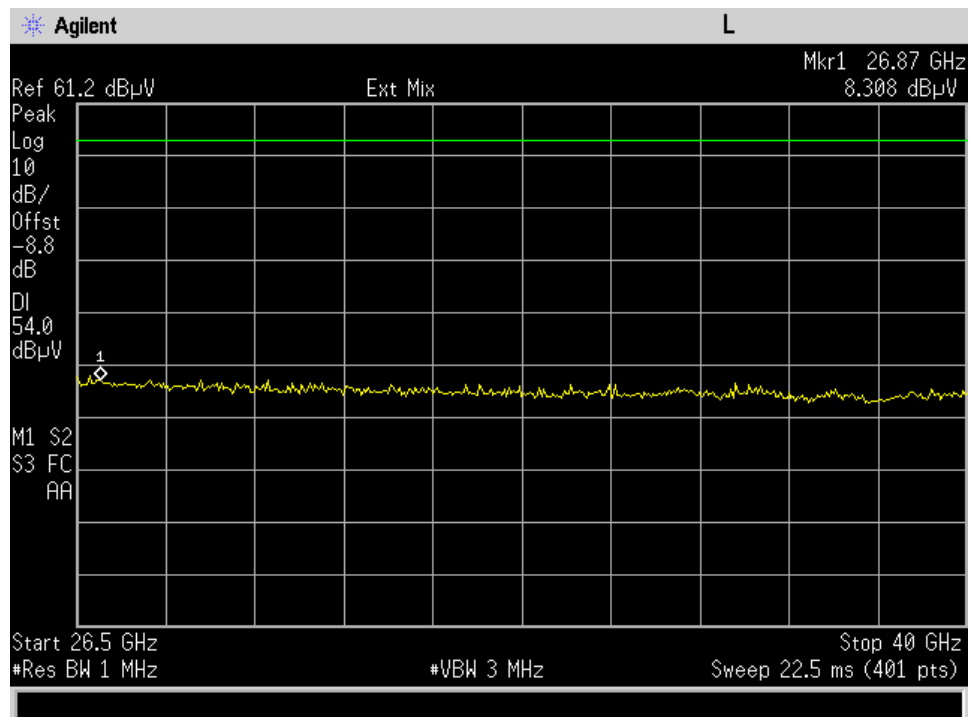




10MHz Mid Ch 26.5-40GHz Port J8

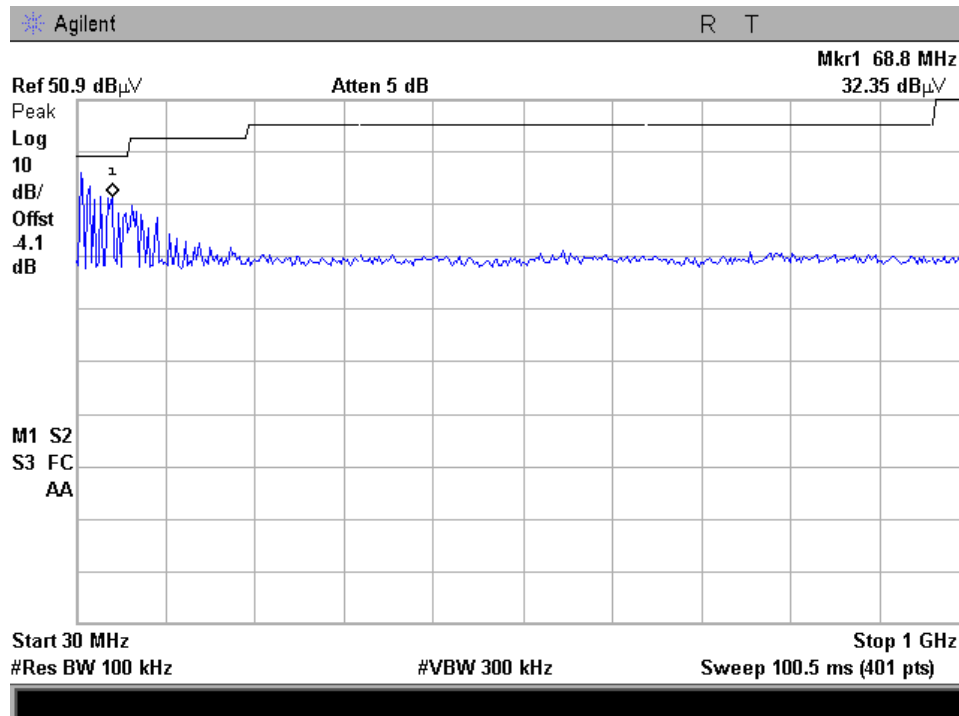


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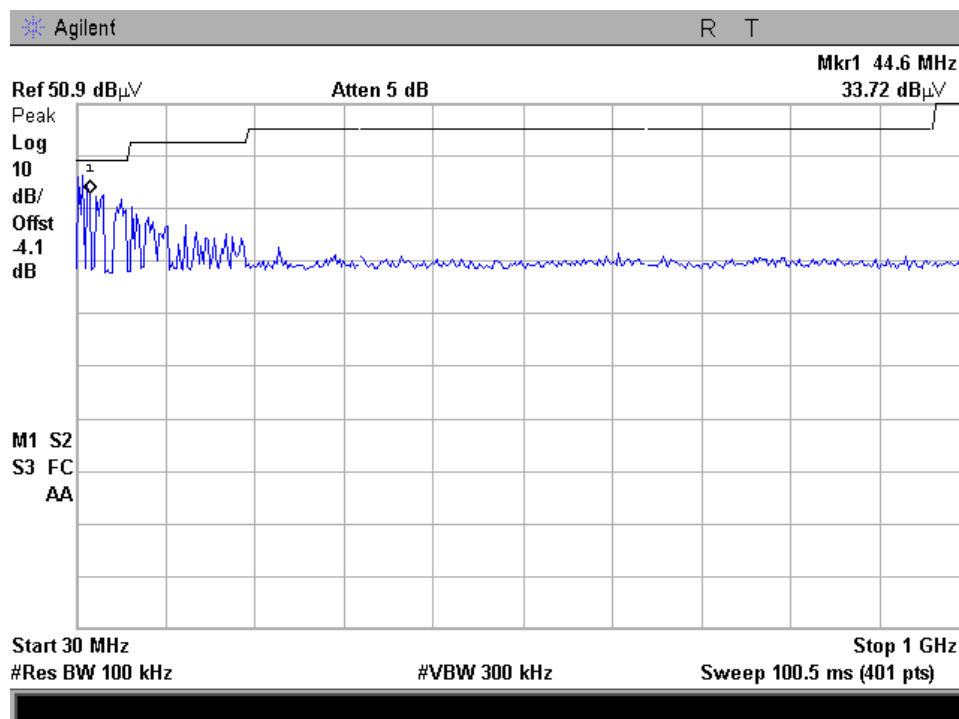




10MHz Mid Ch 30-1000MHz Port J8

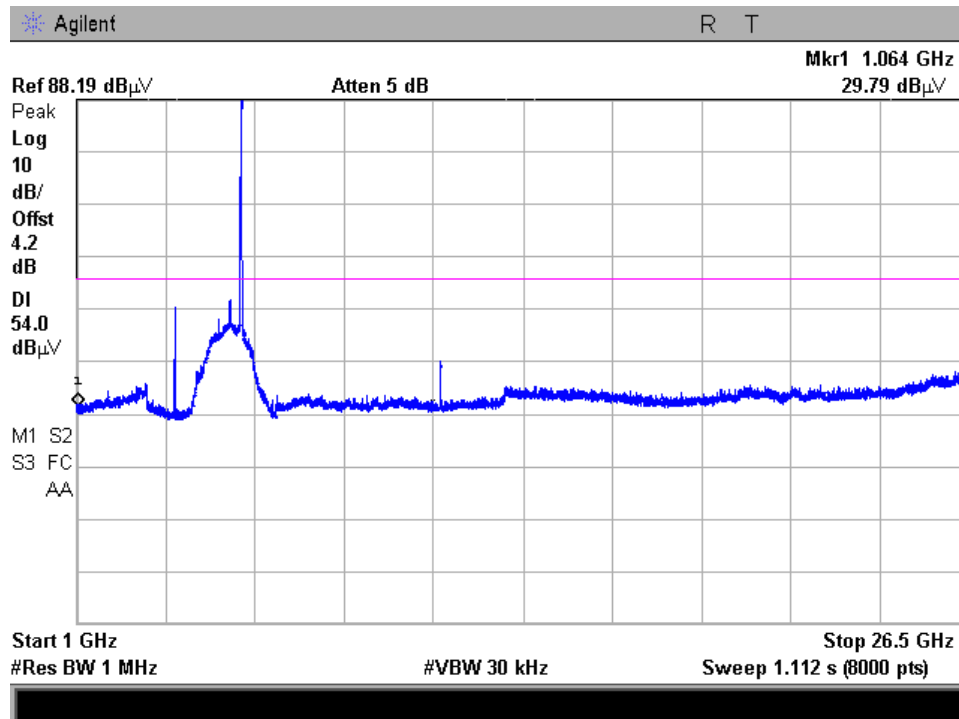


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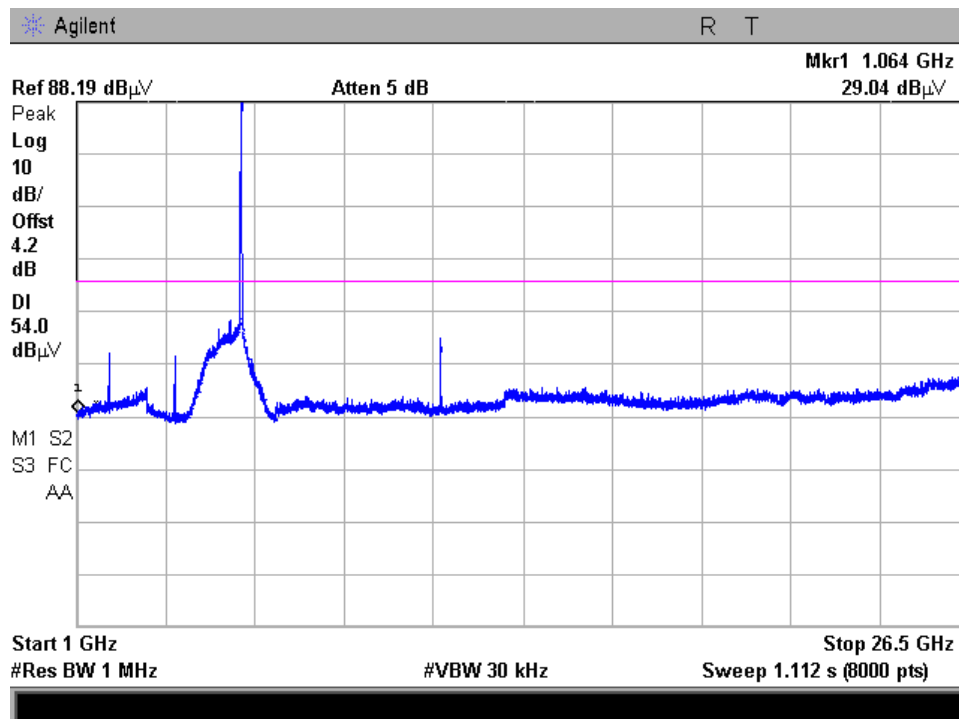




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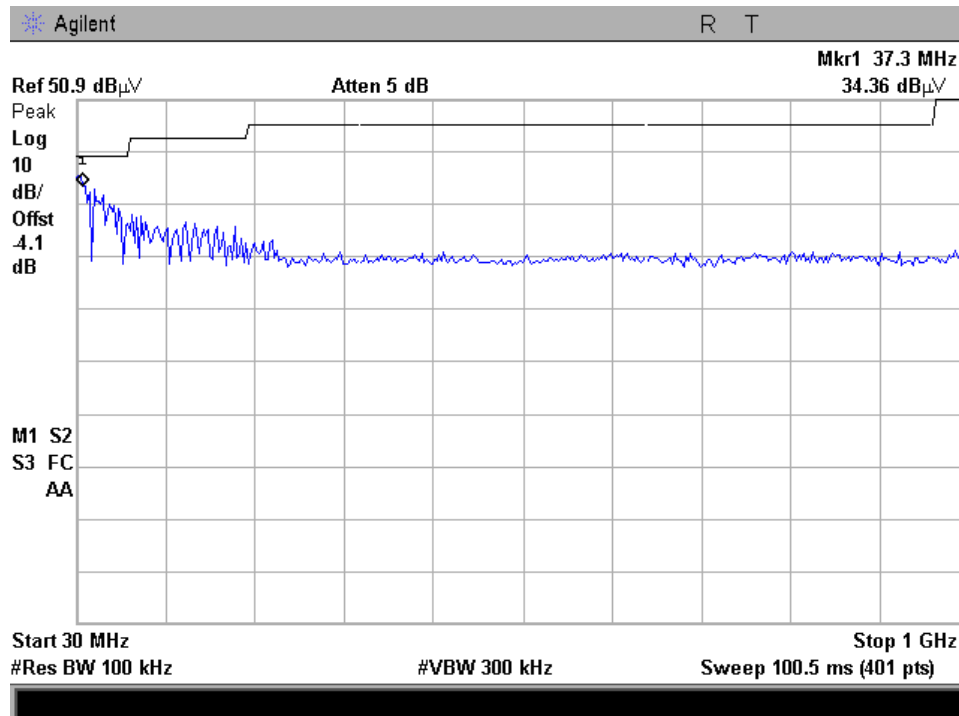


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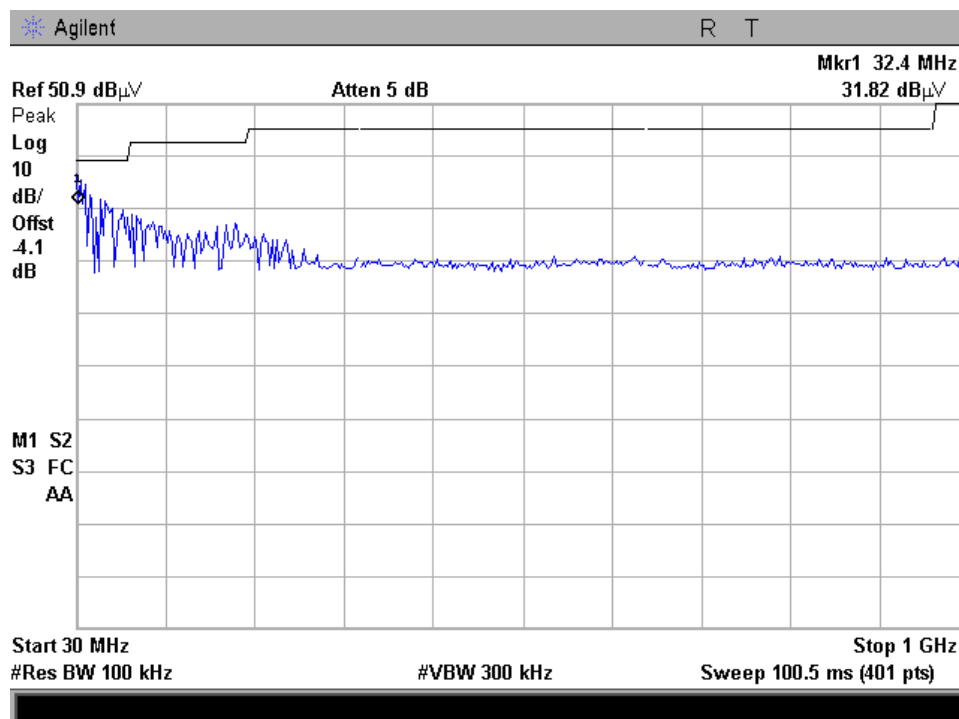




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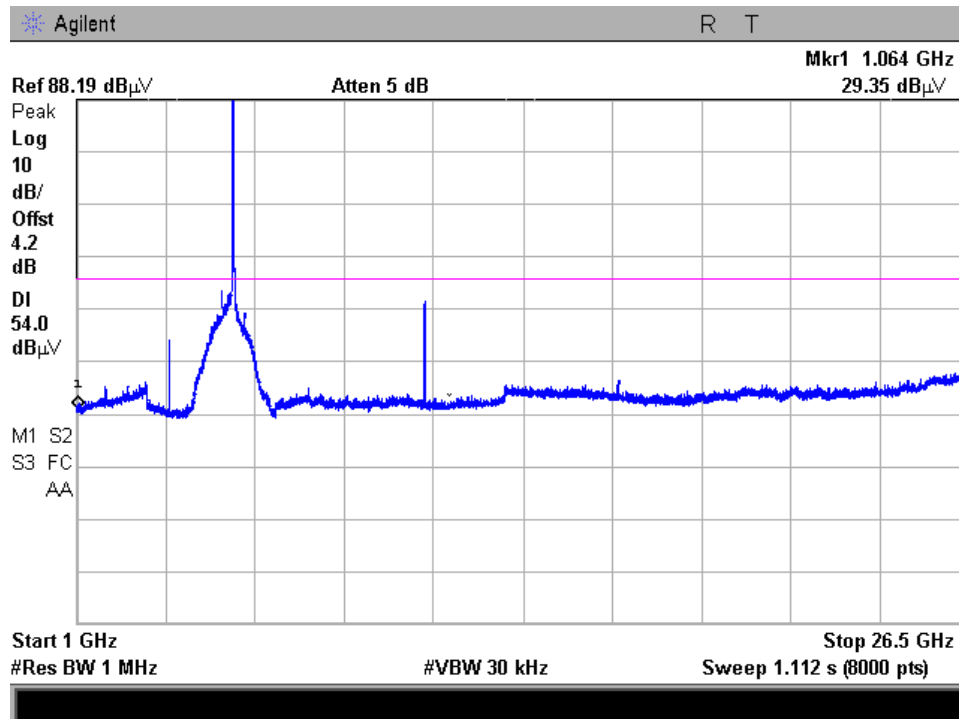


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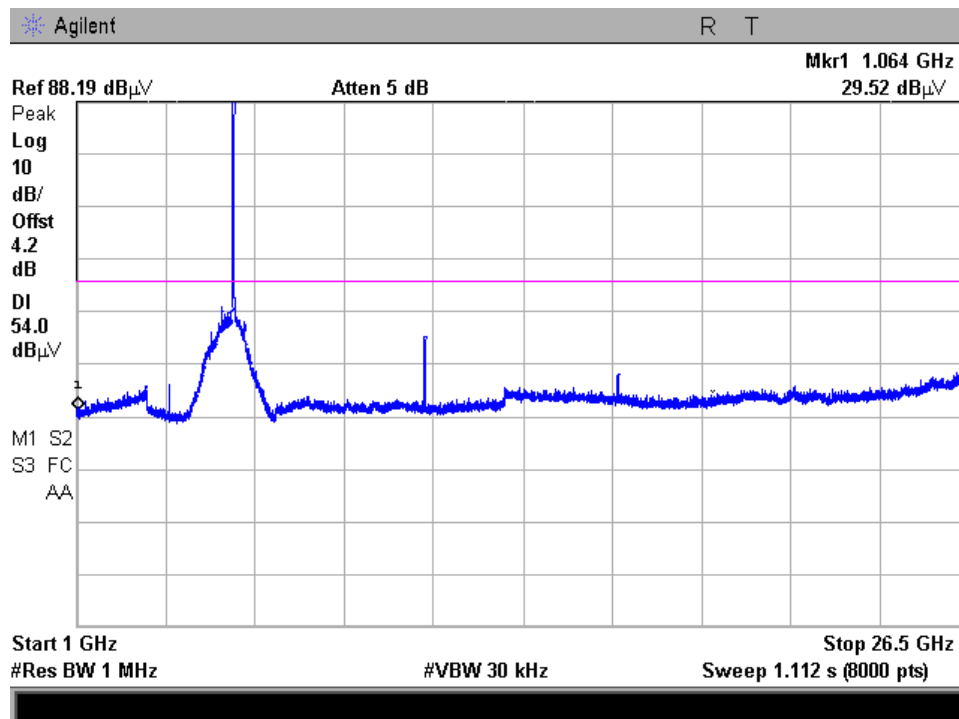




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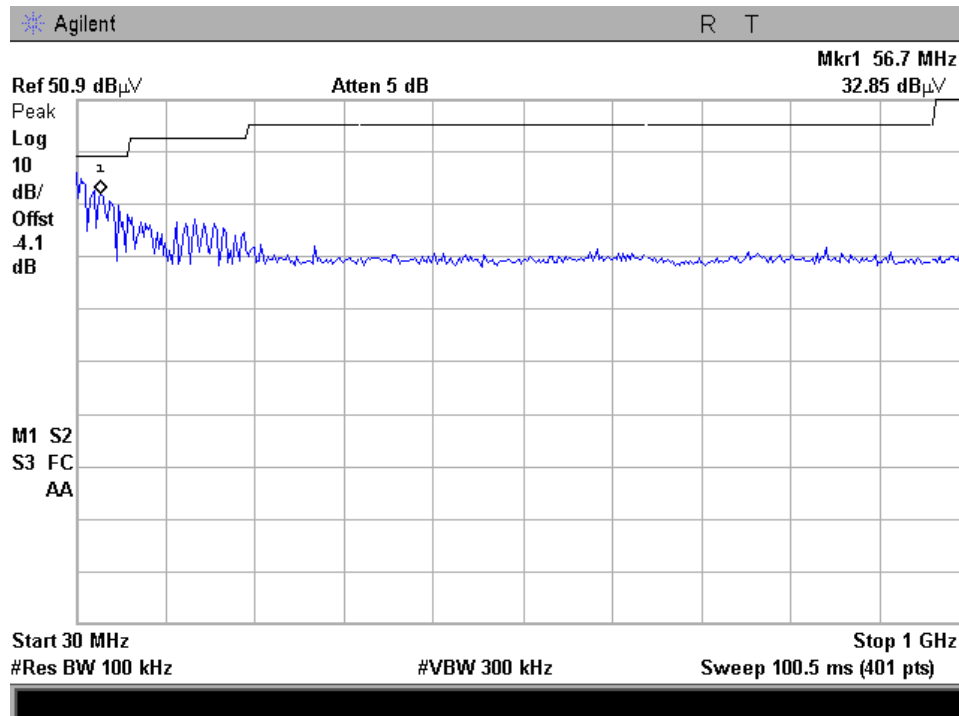


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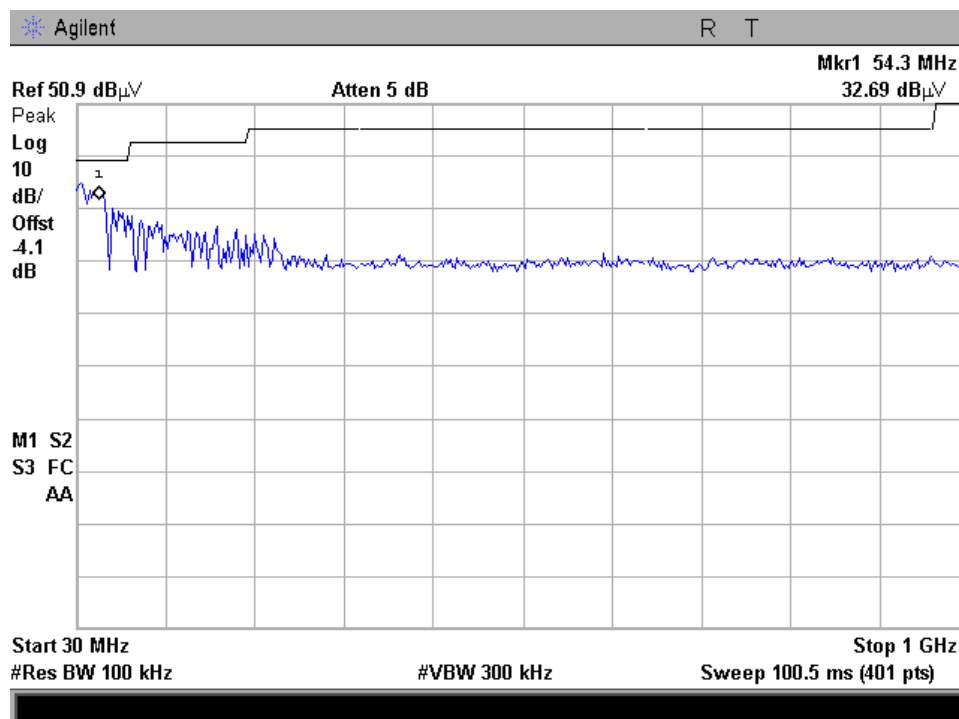




20MHz Low Ch 30-1000MHz Port J8

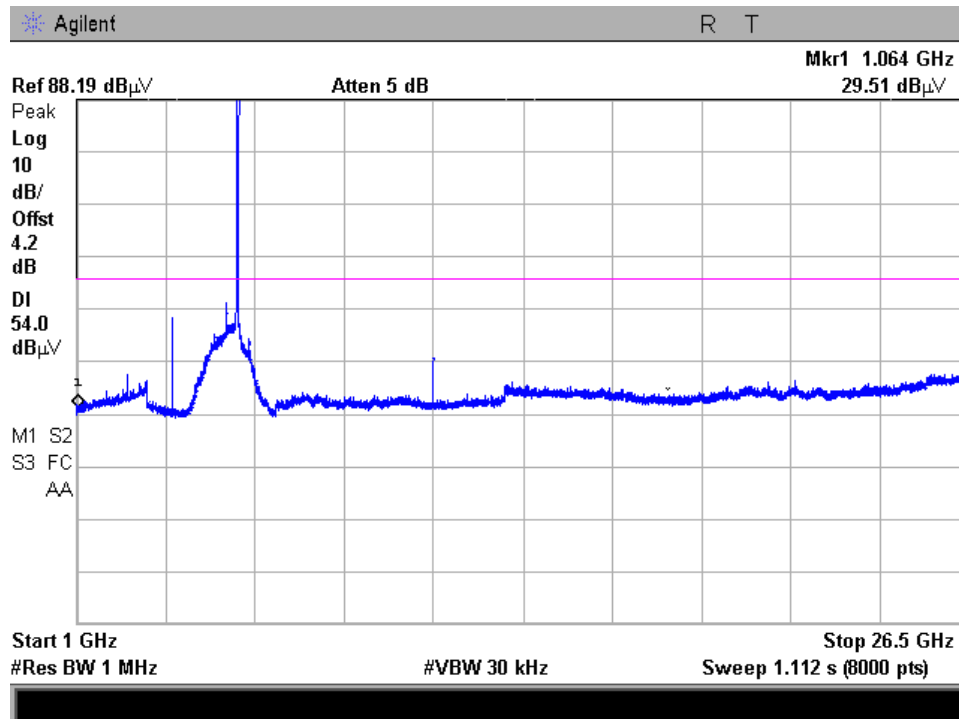


20MHz Low Ch 30-1000MHz Port J7

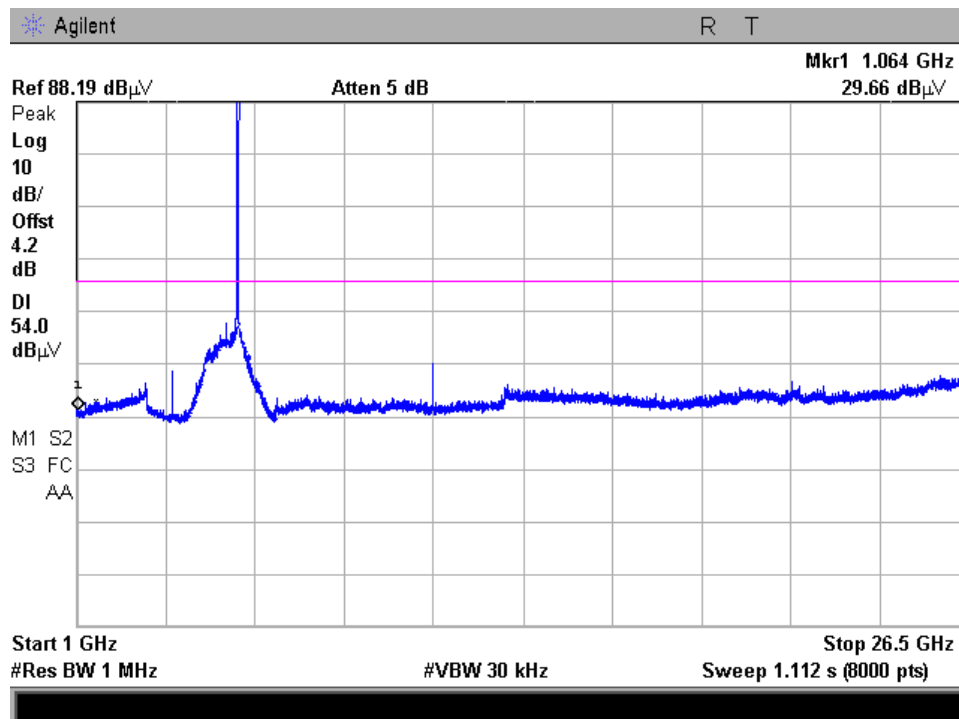




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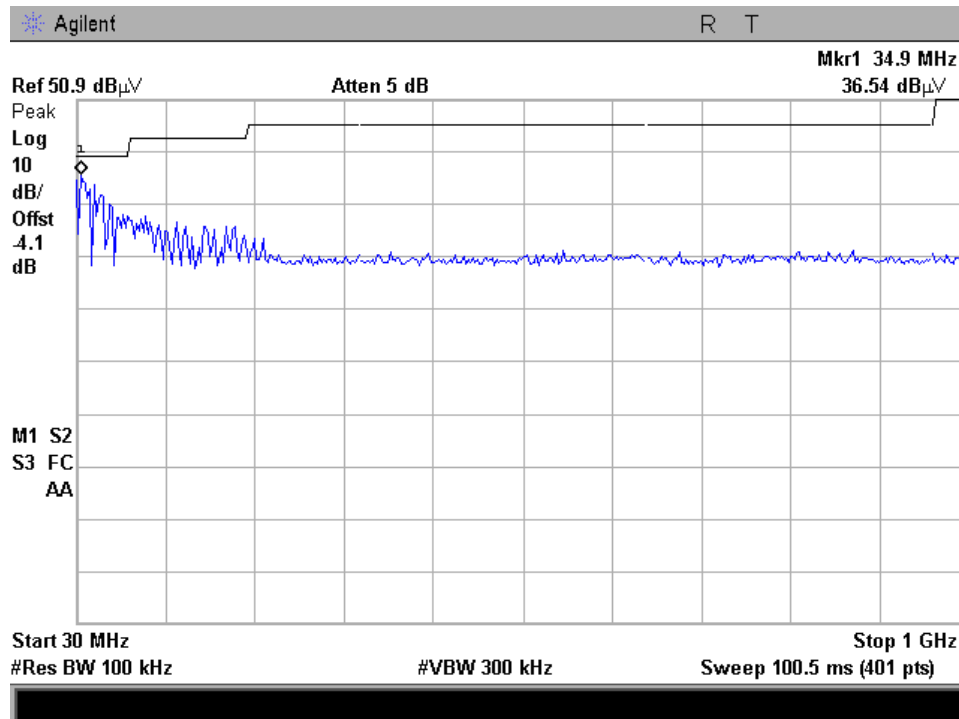


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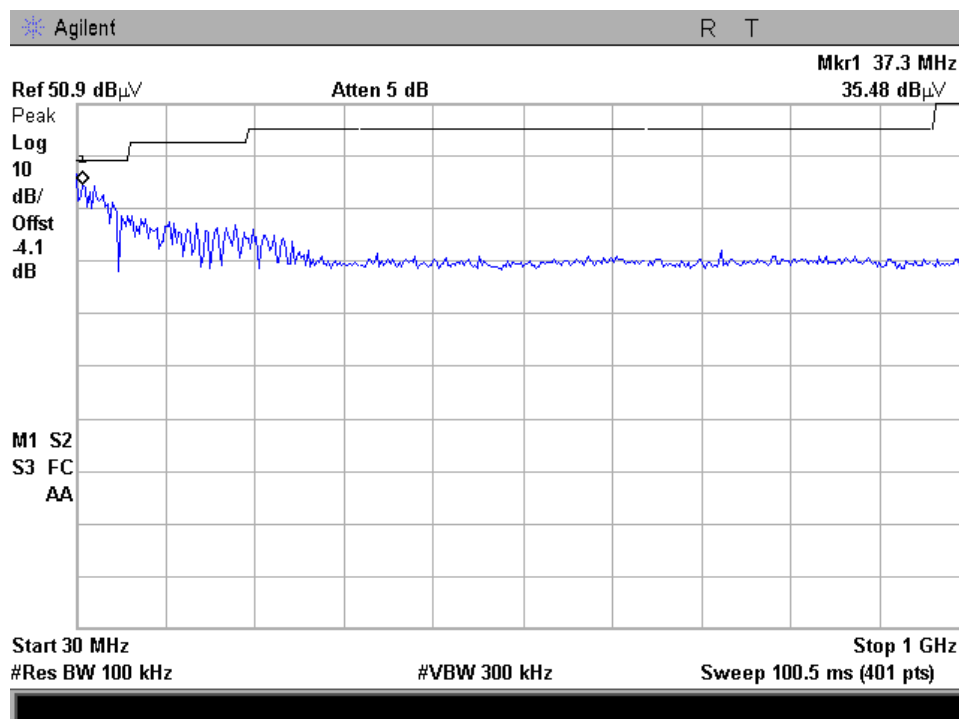




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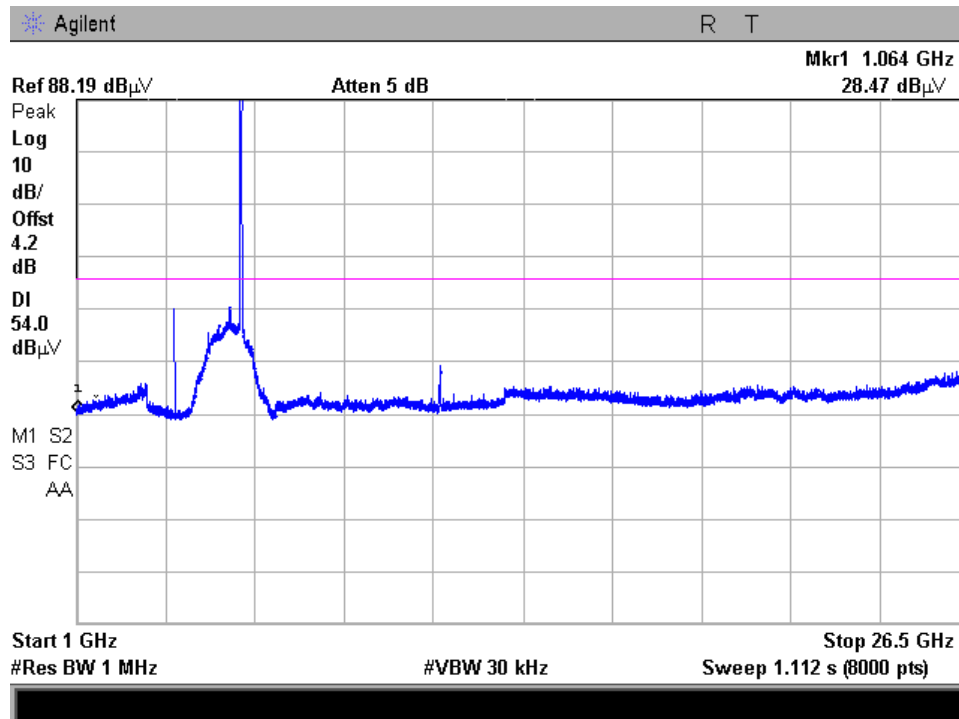


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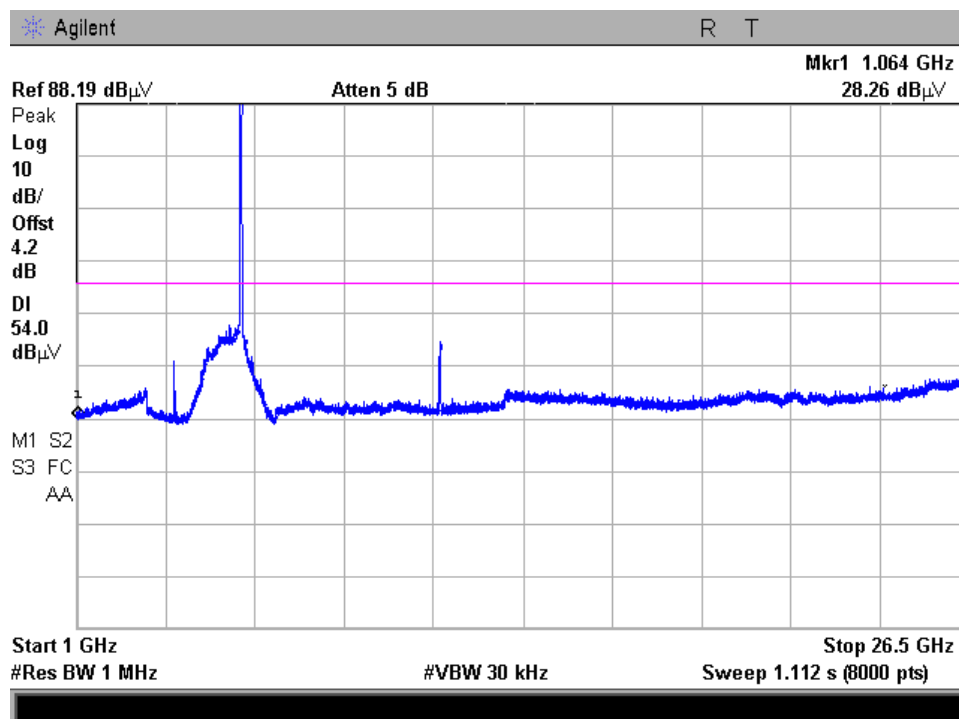




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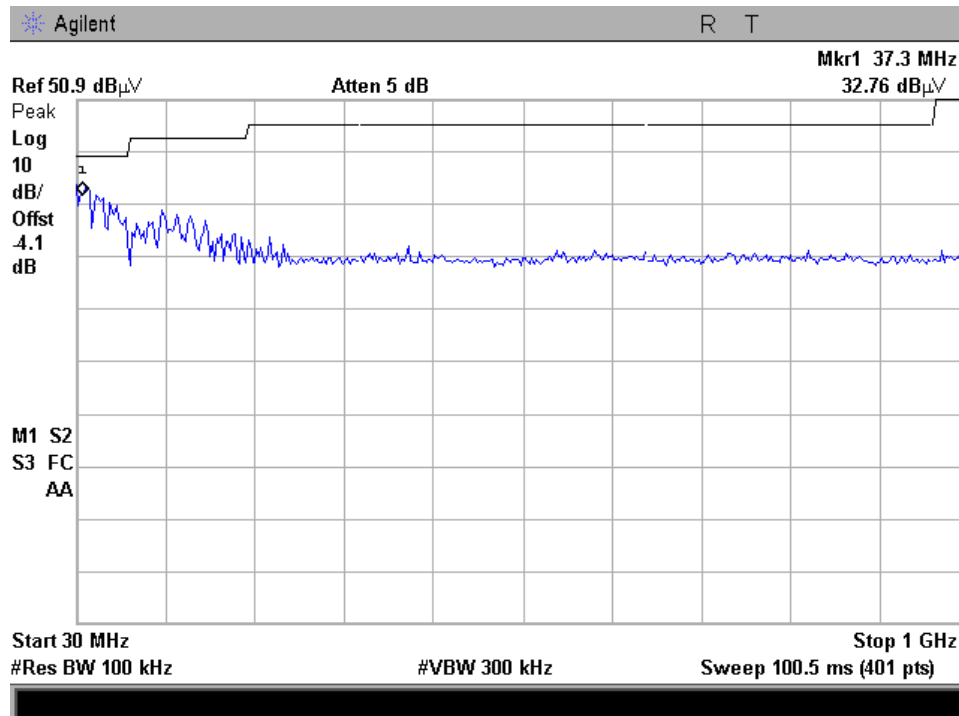


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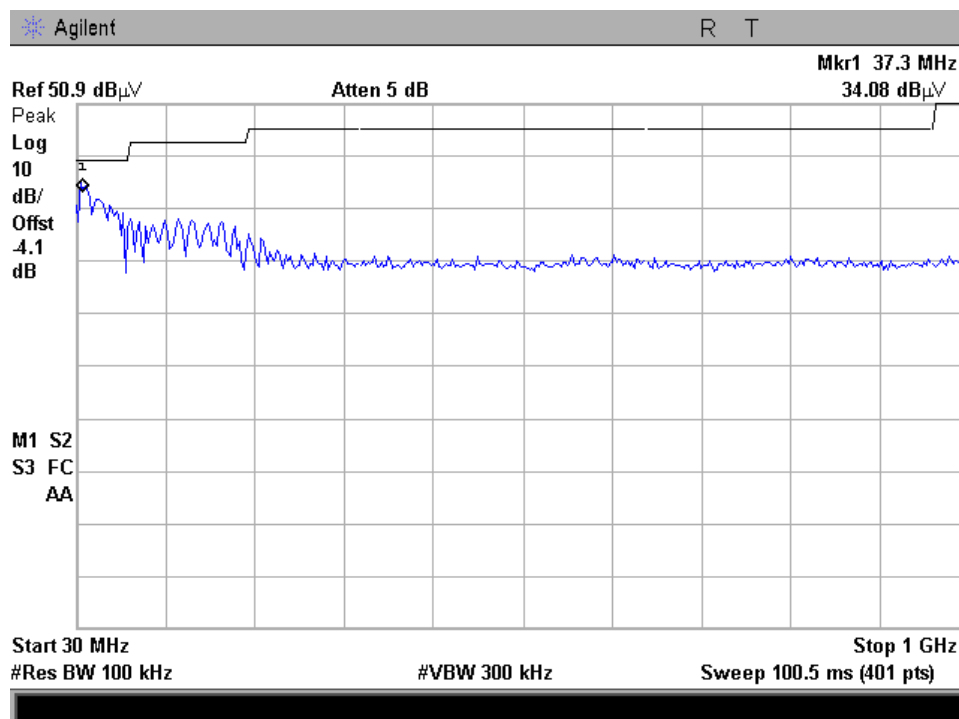




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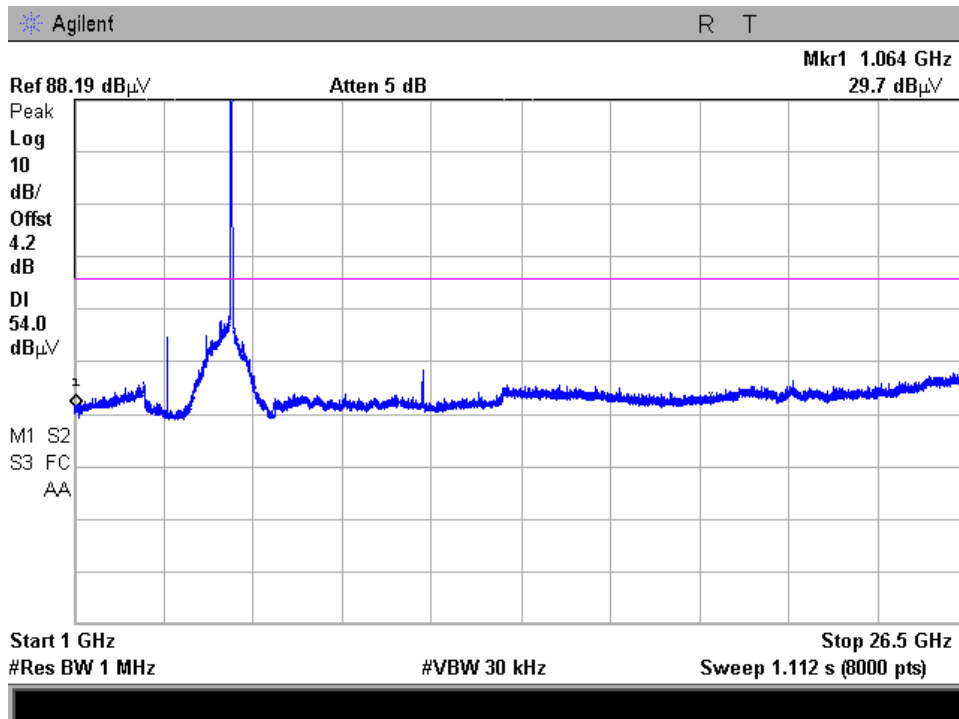


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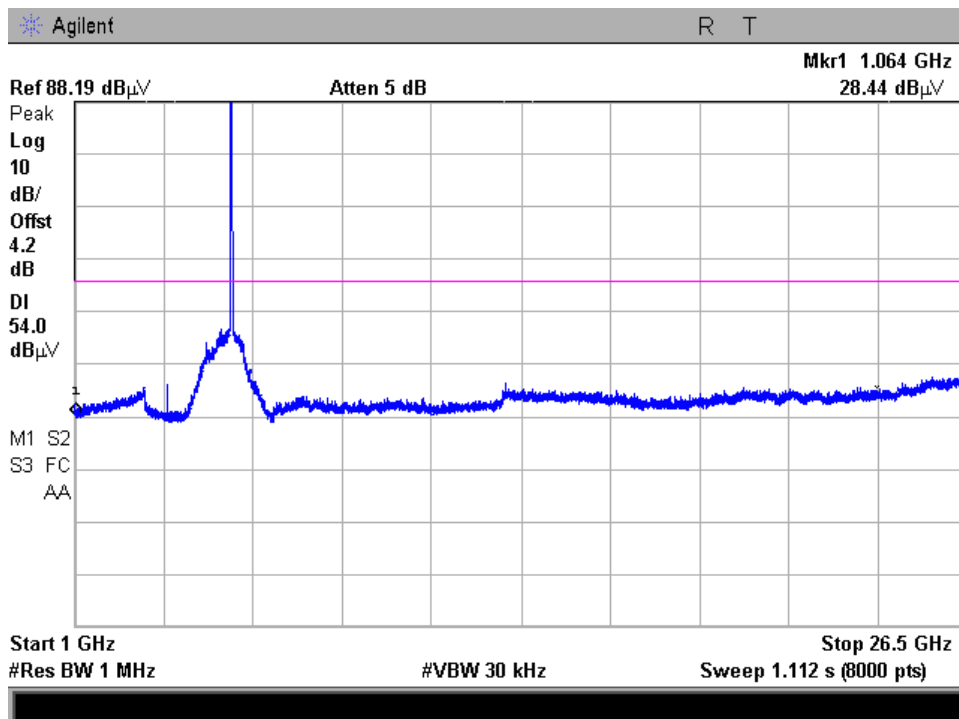




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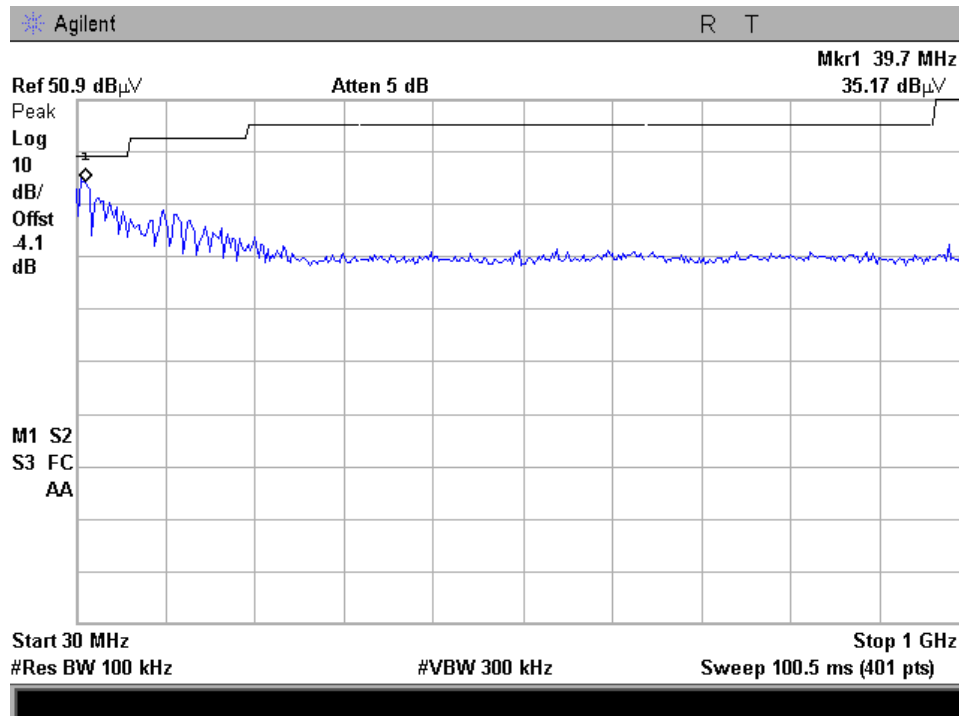


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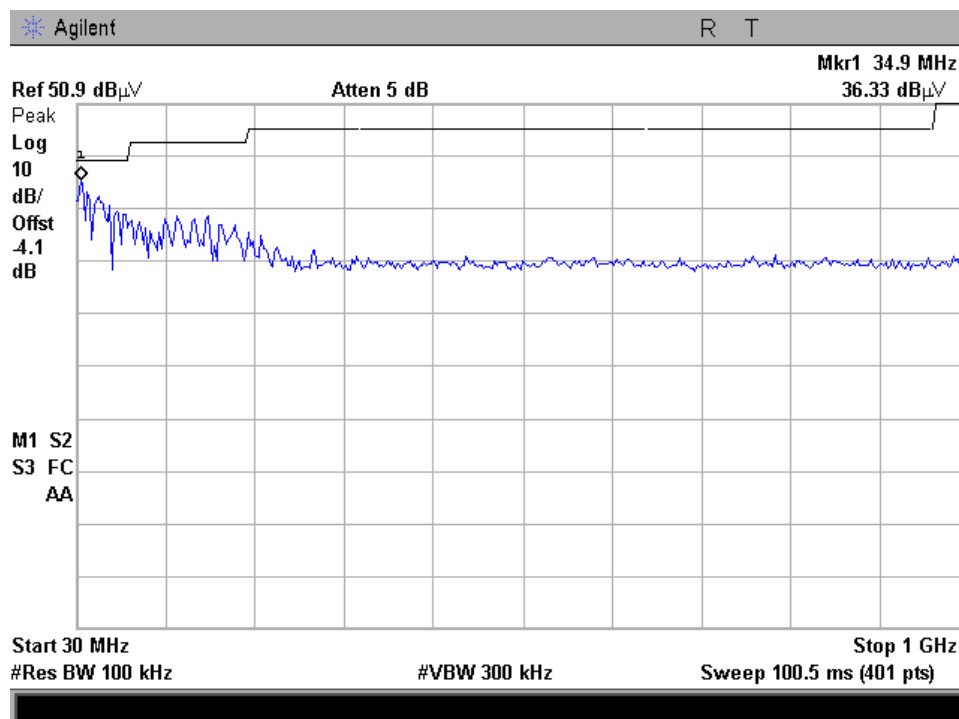




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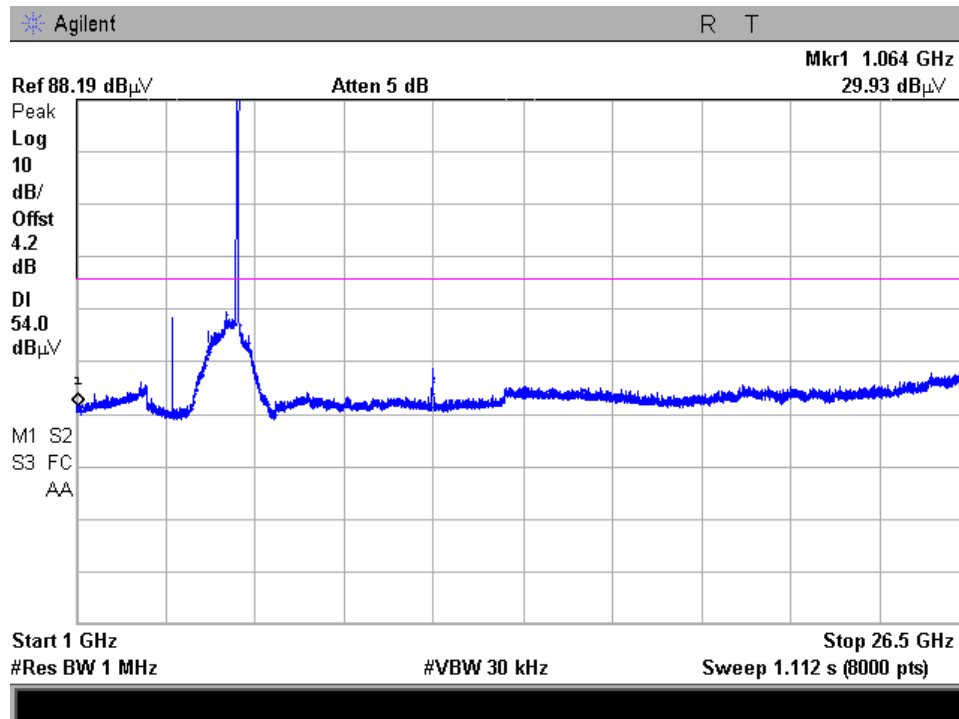


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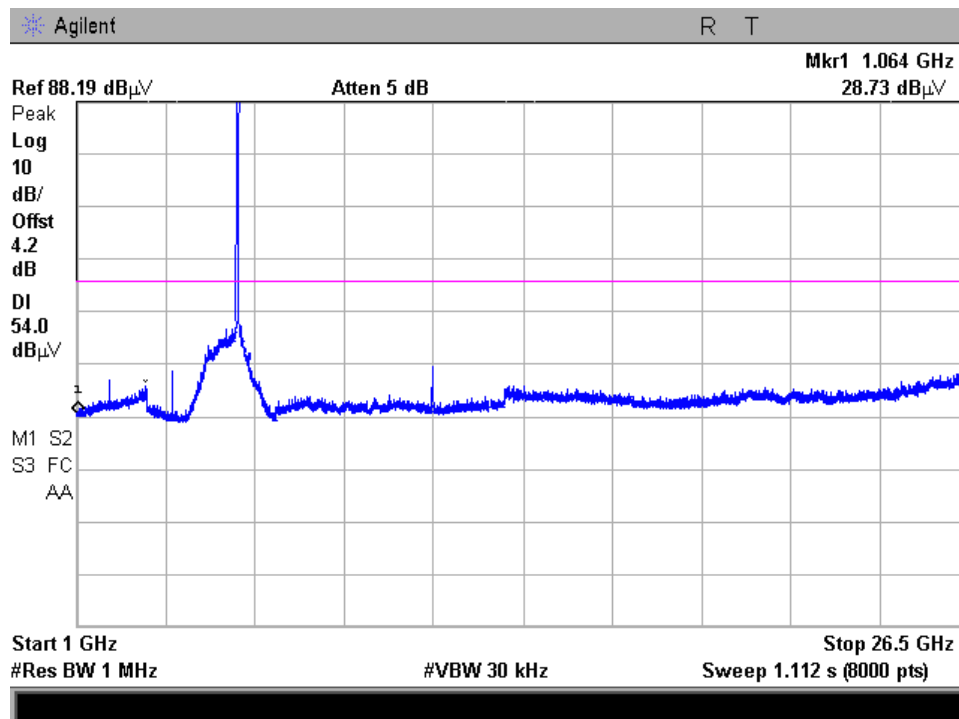




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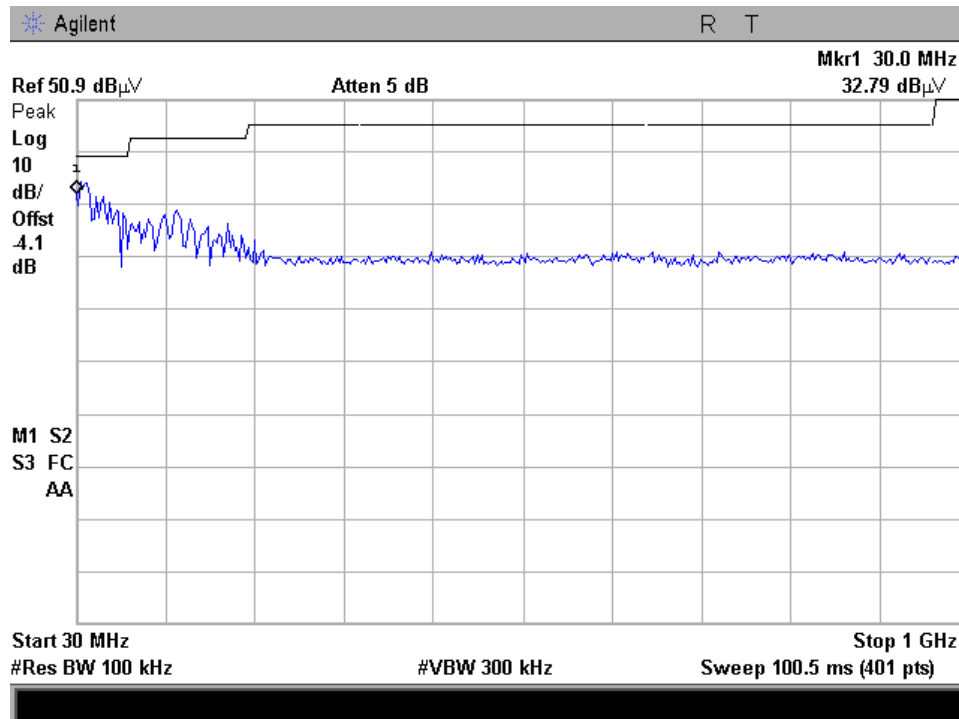


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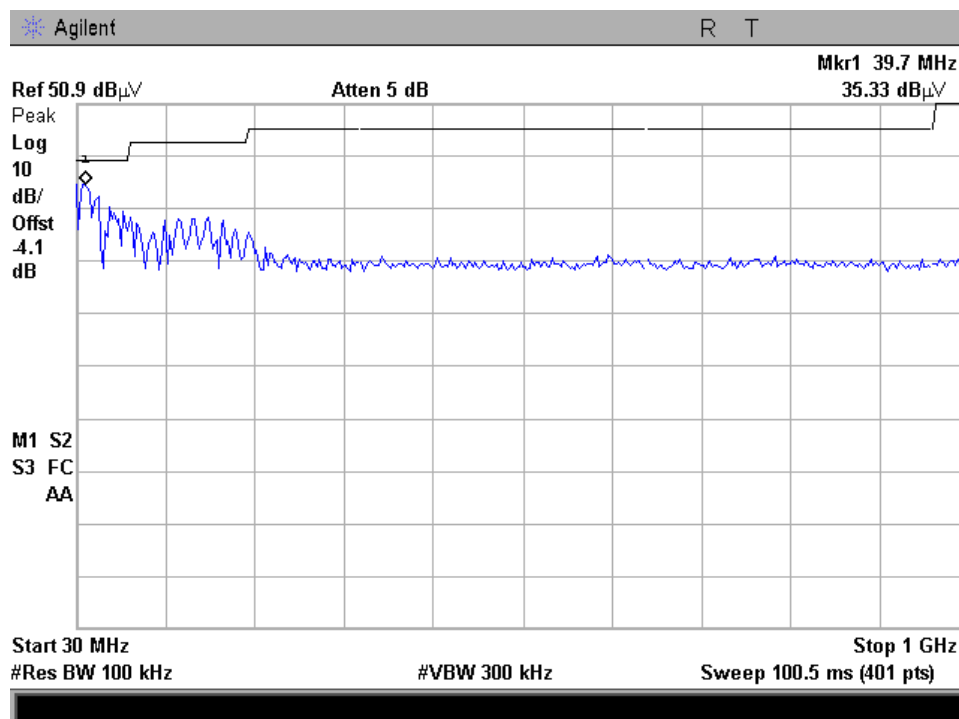




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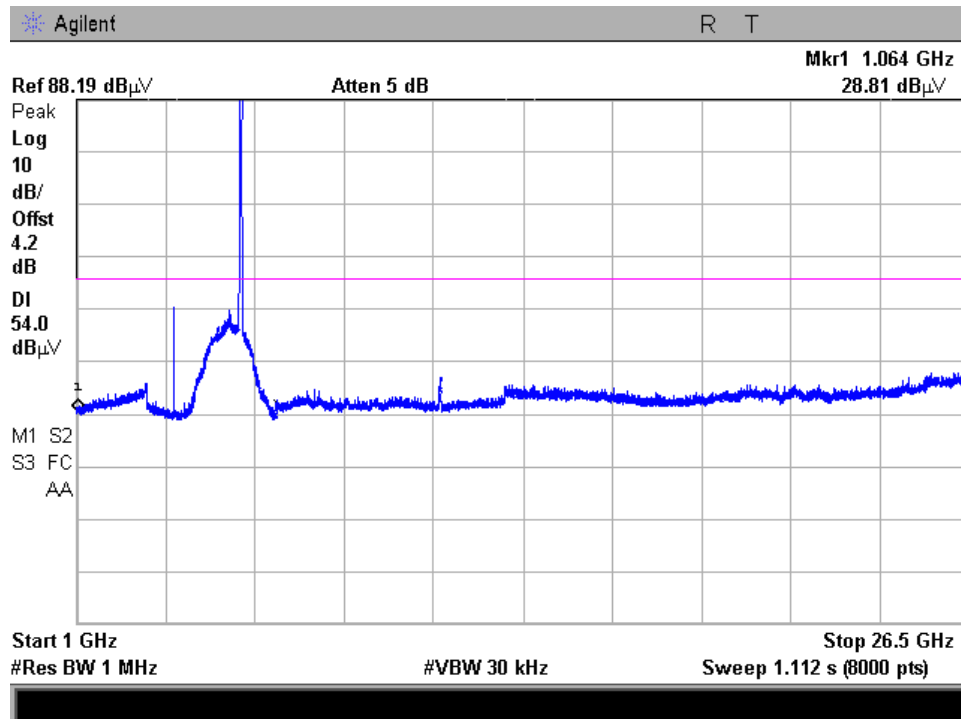


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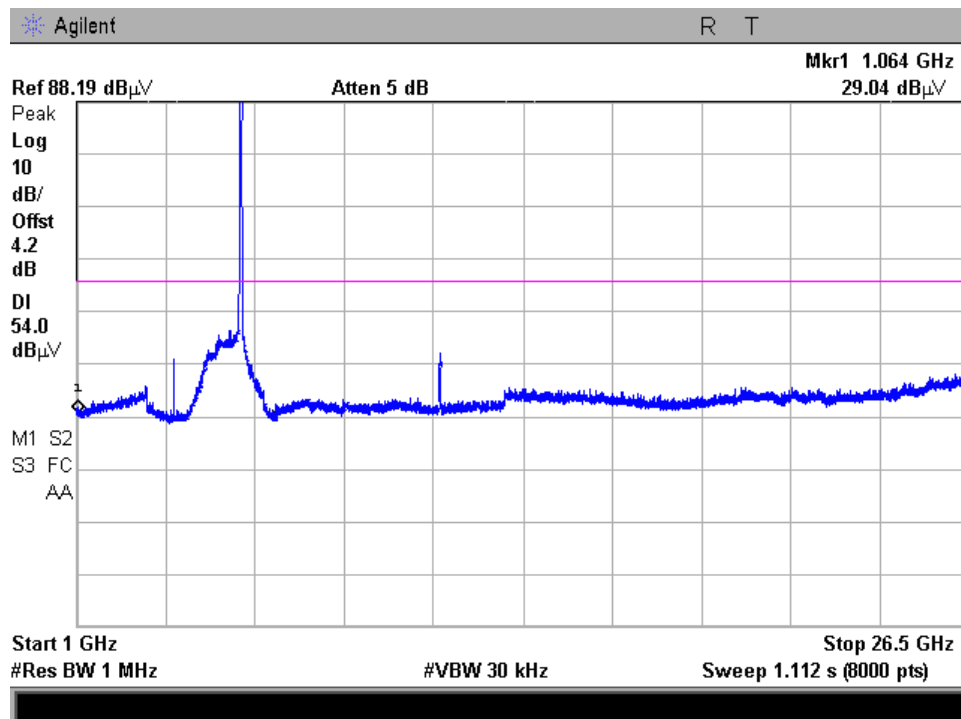




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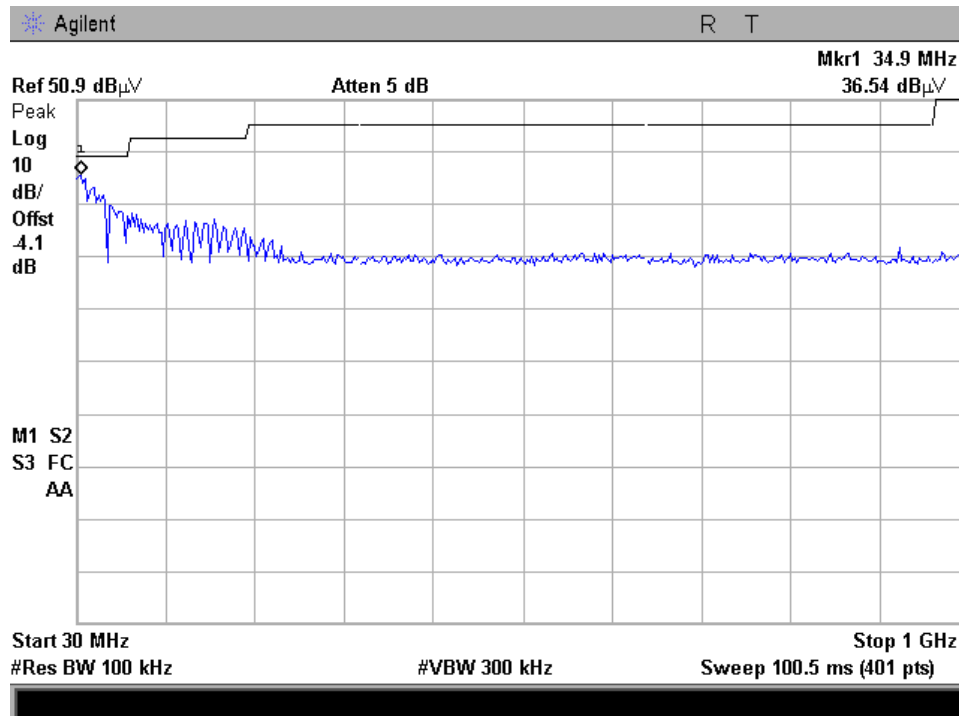


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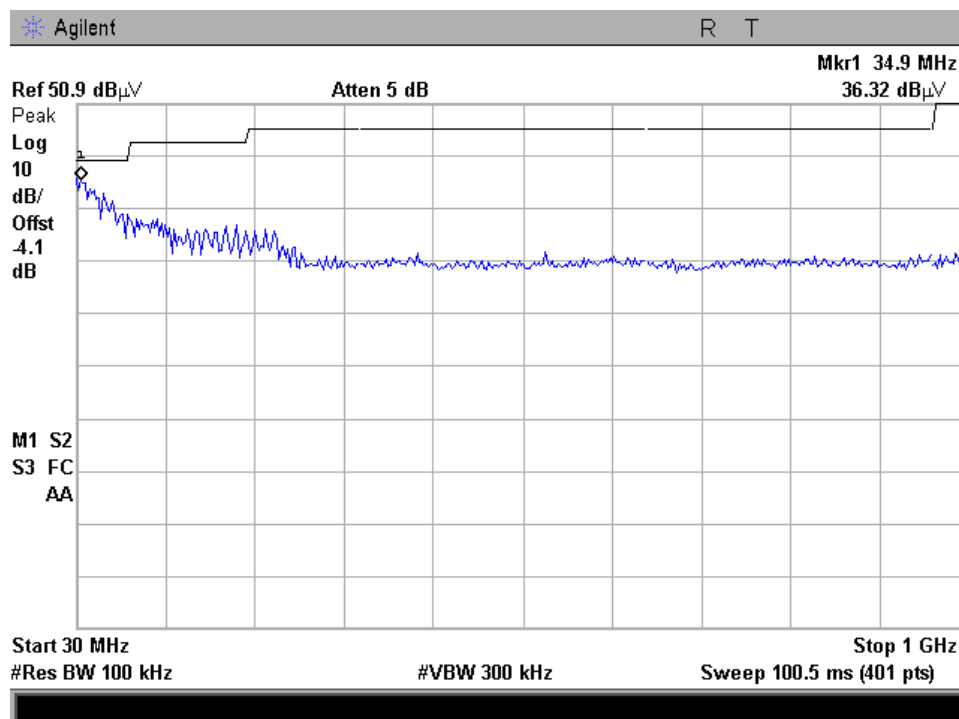




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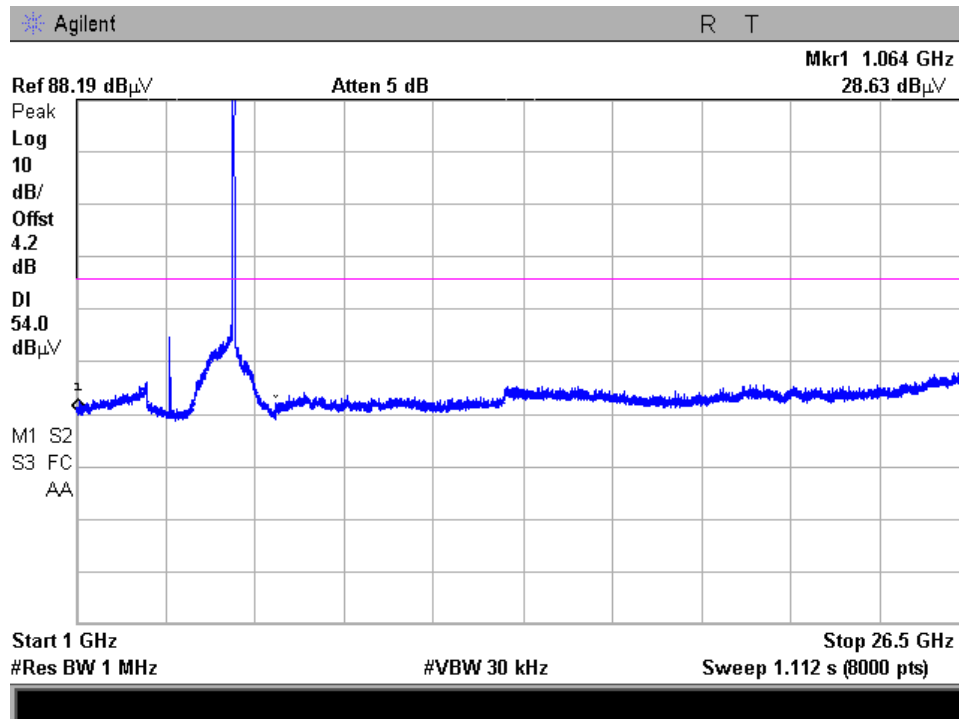


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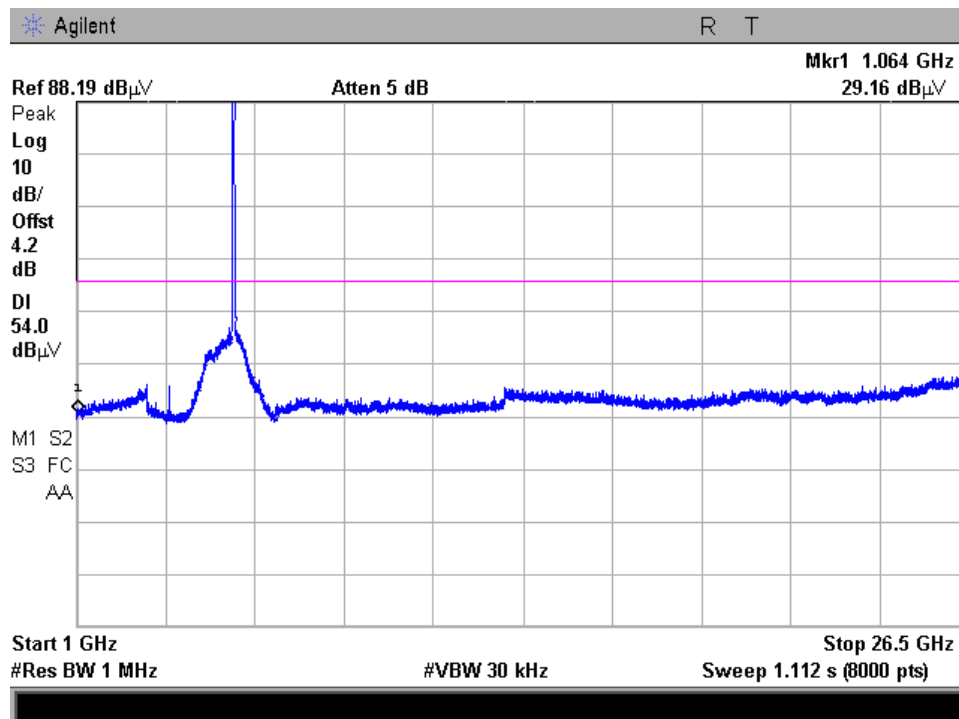




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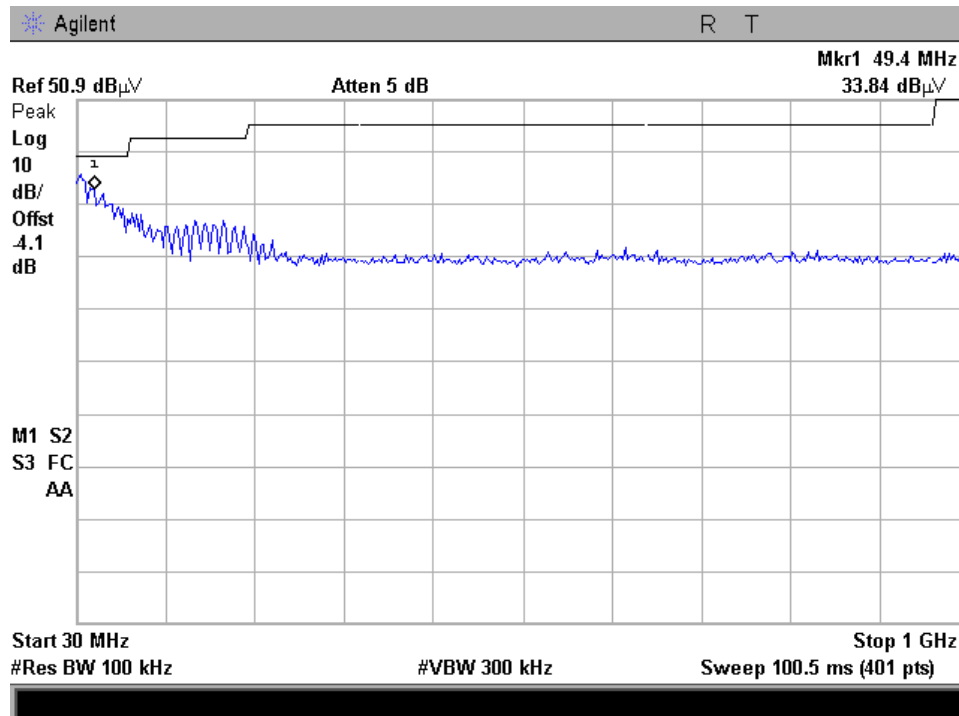


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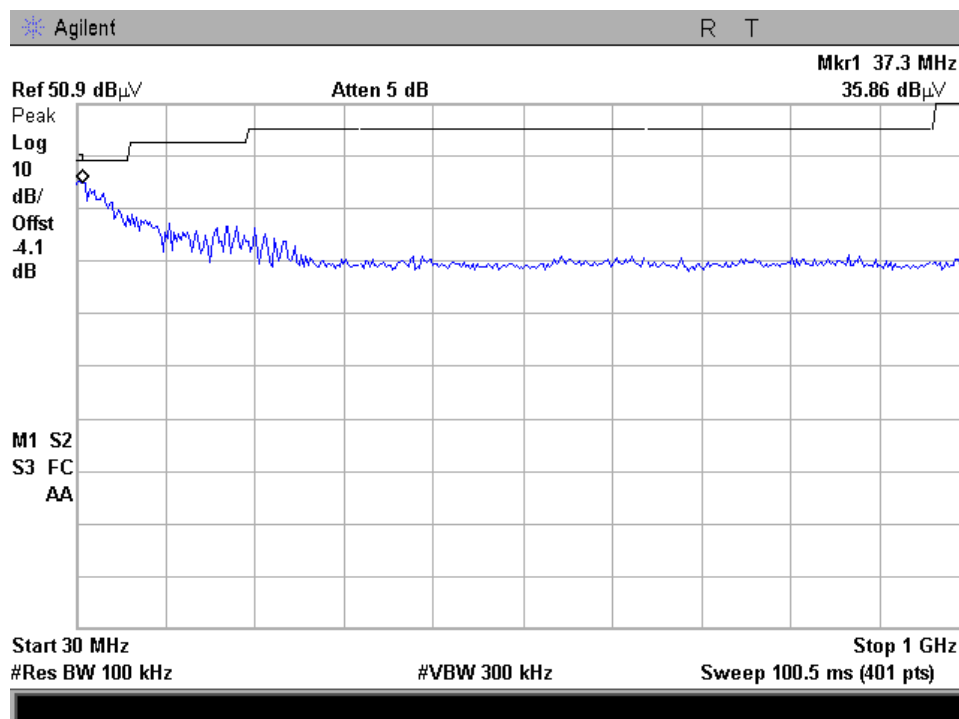




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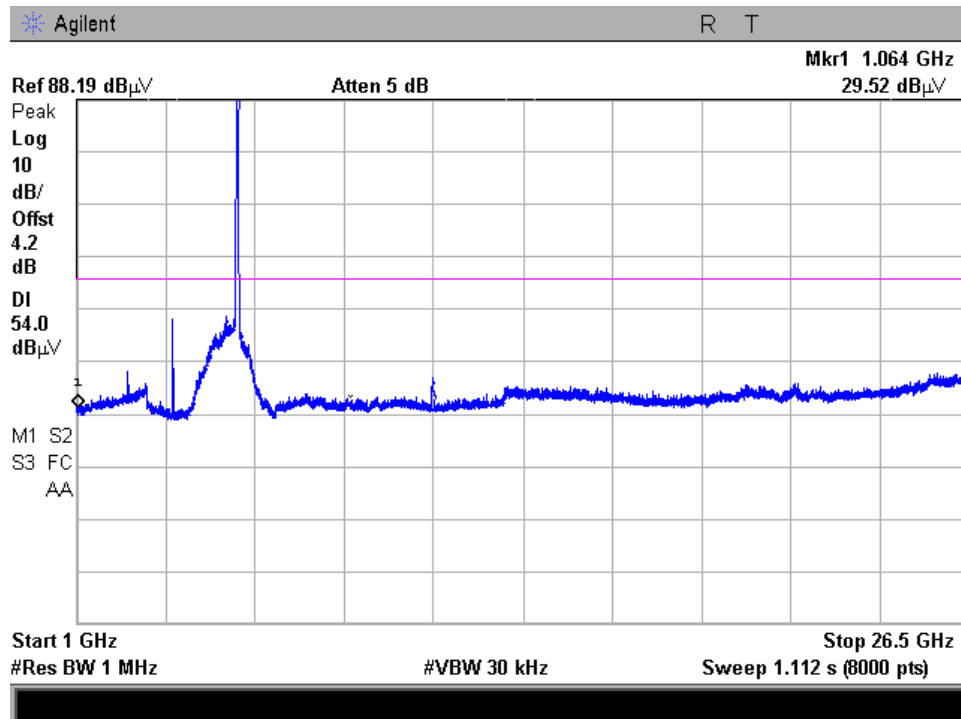


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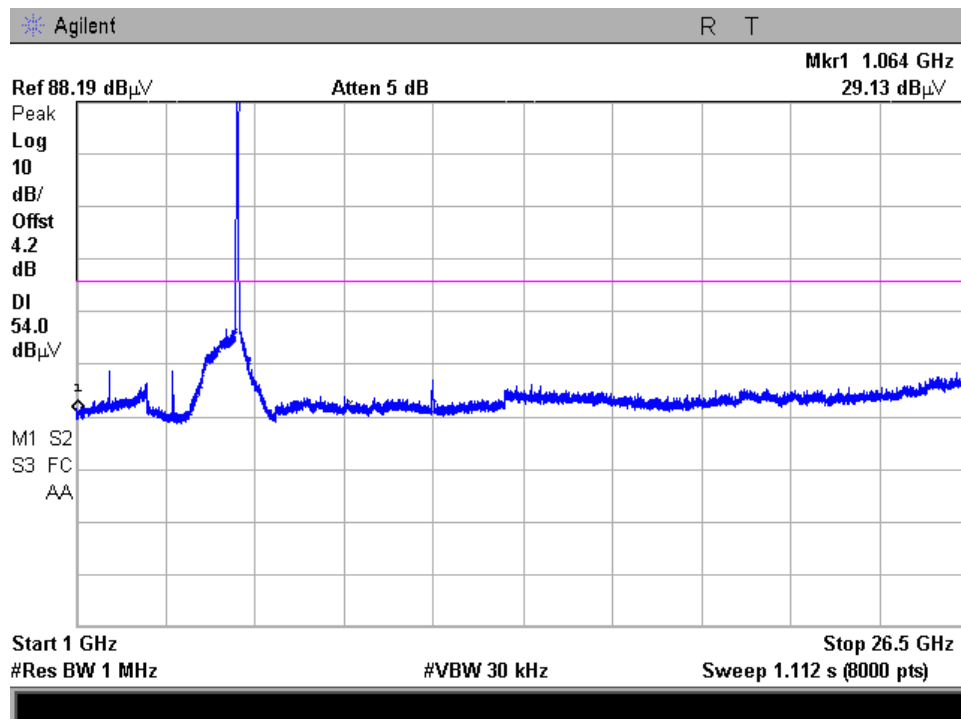




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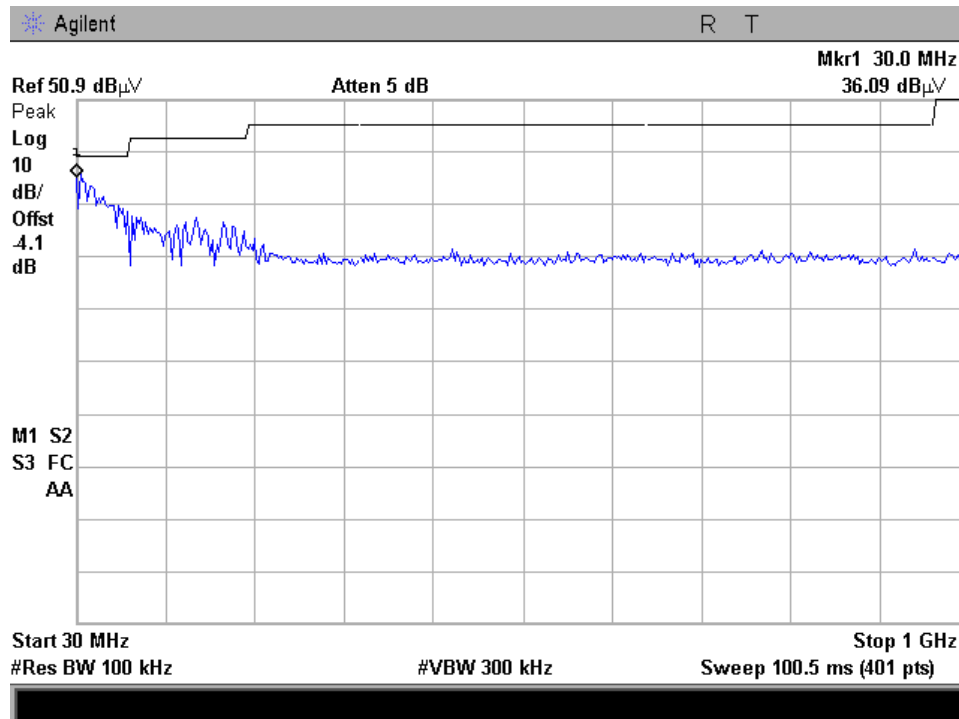


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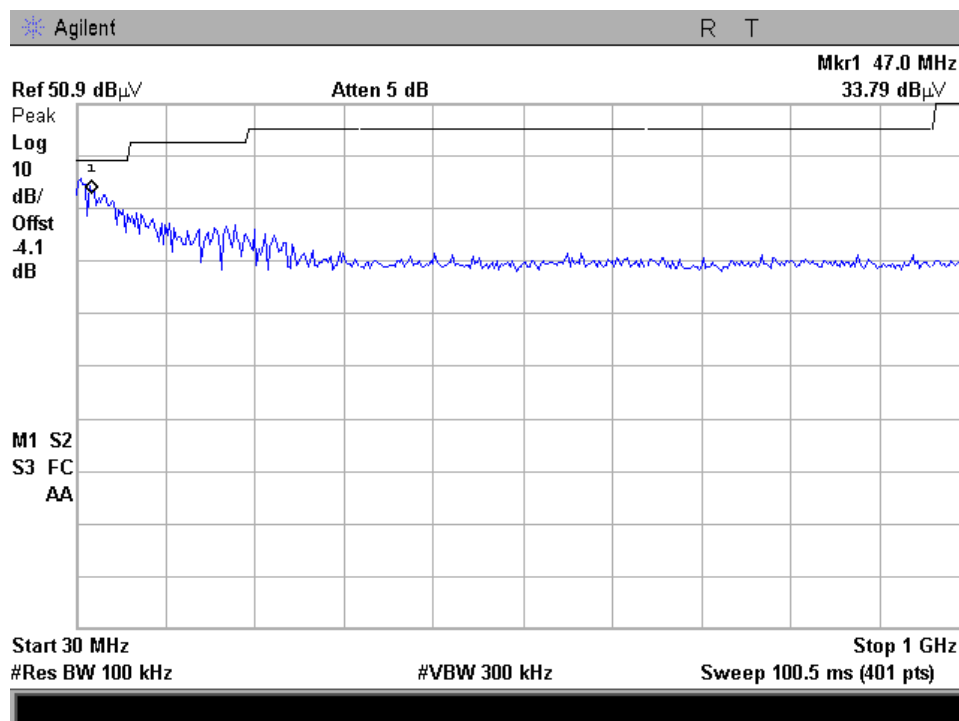




40MHz Mid Ch 30-1000MHz Port J8

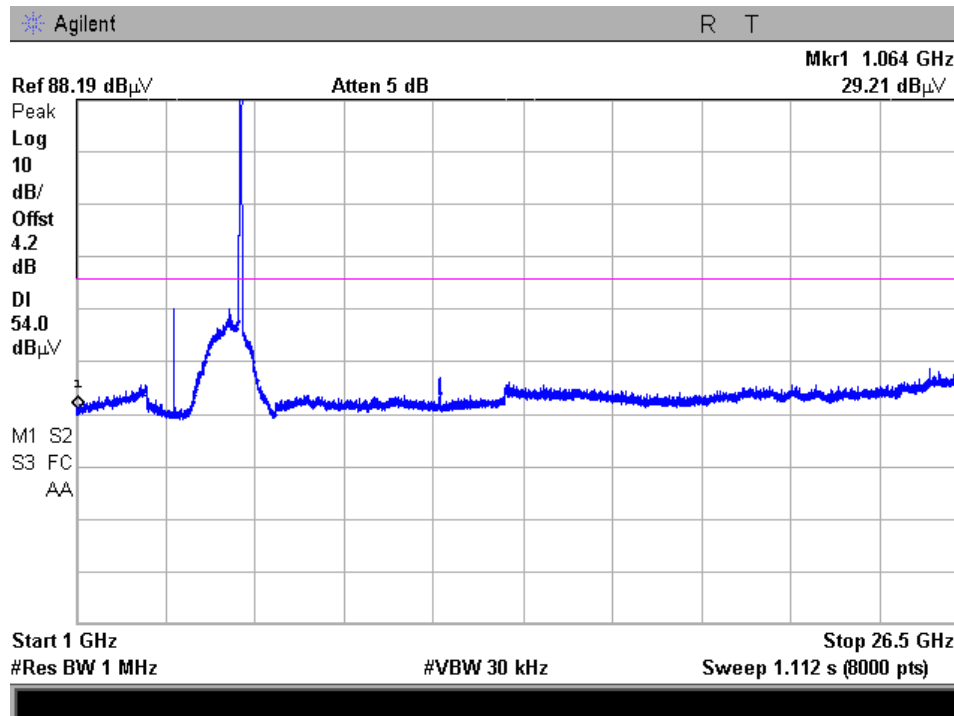


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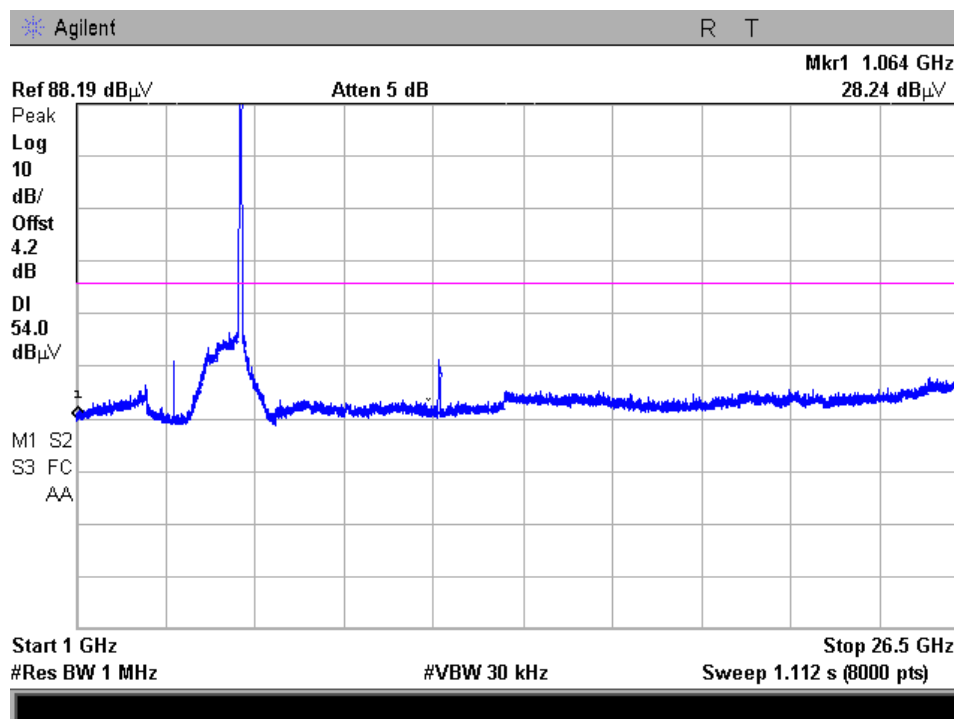




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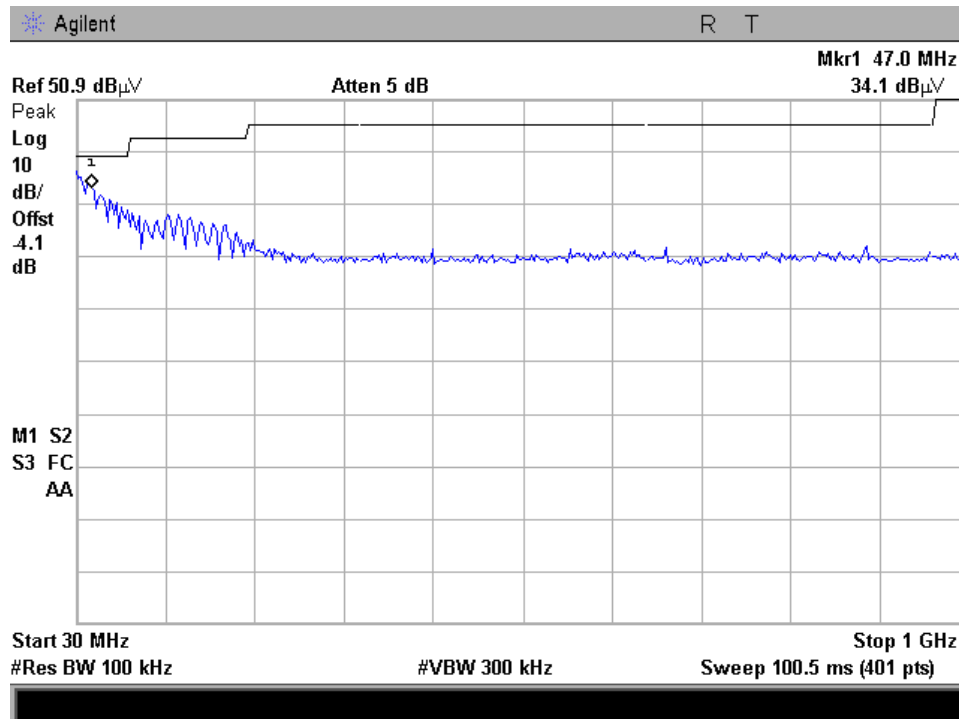


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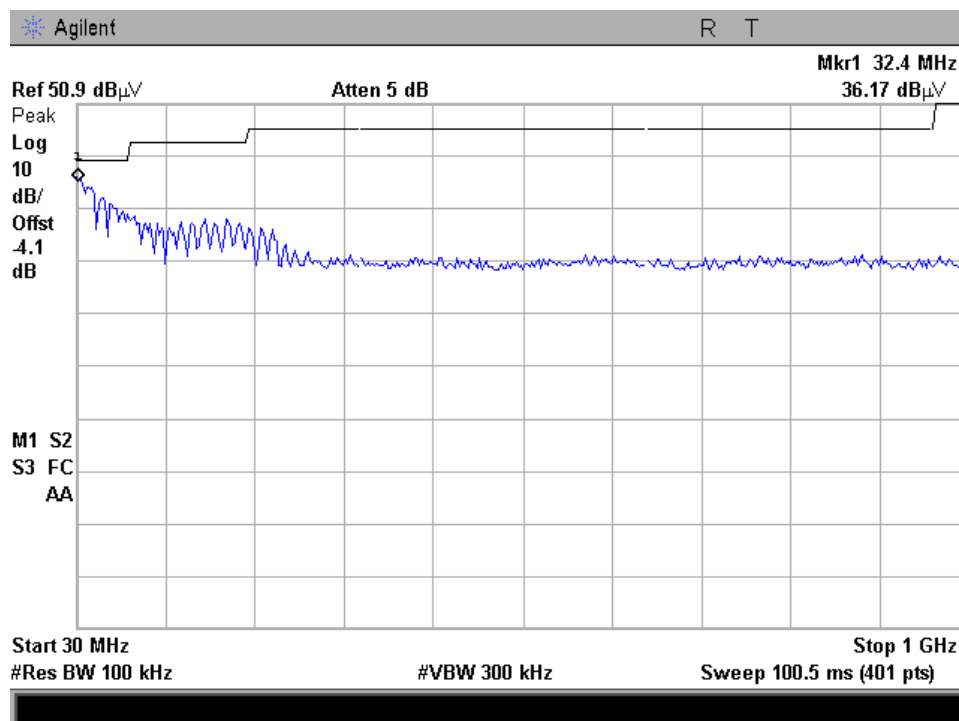




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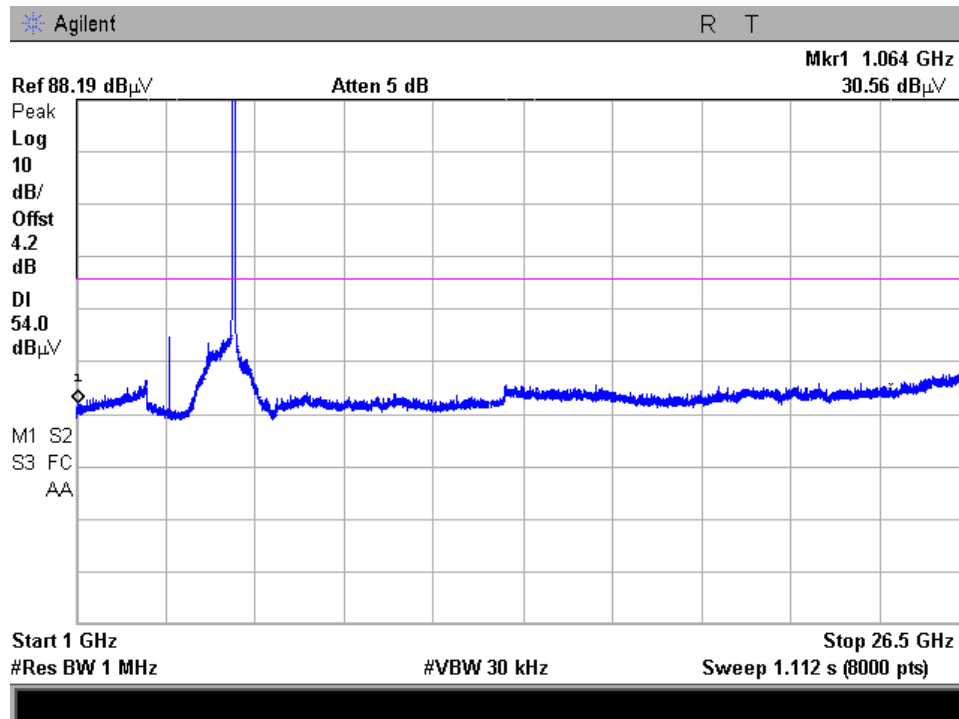


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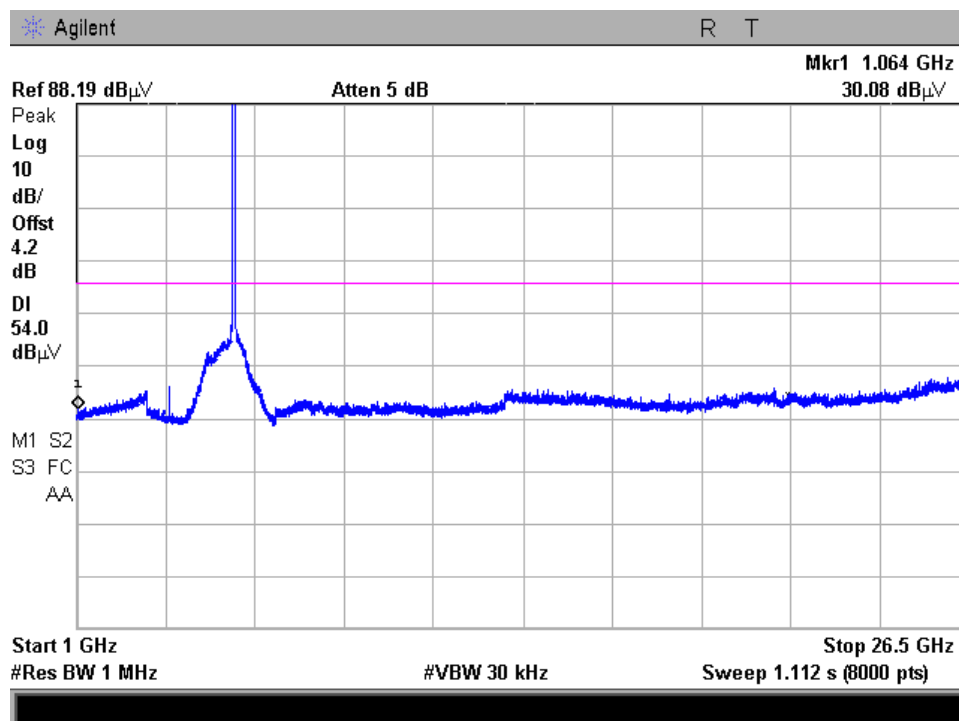




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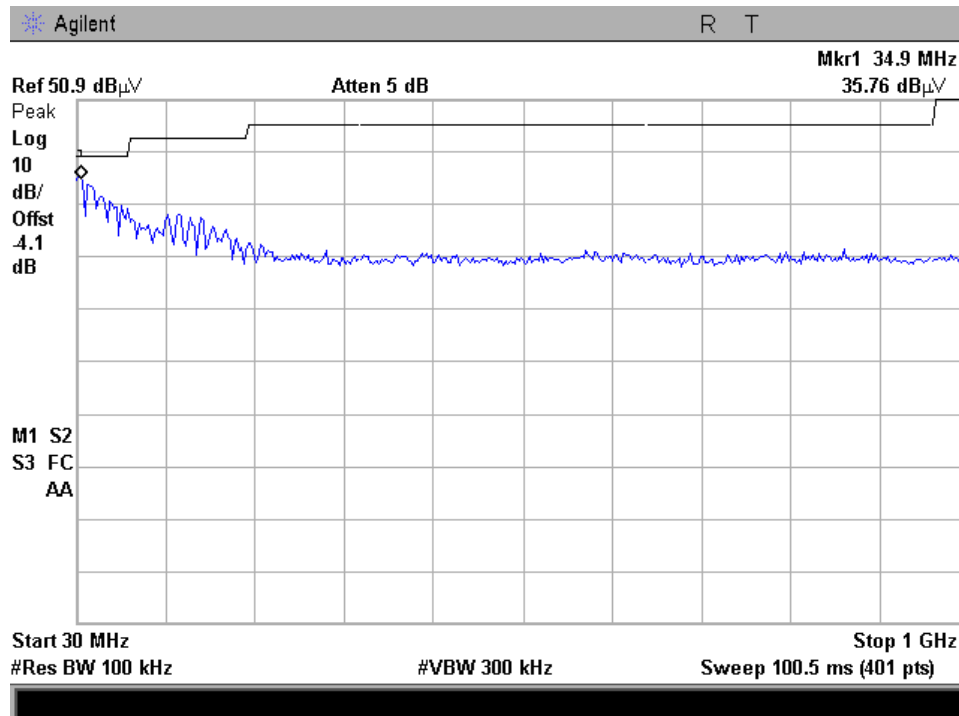


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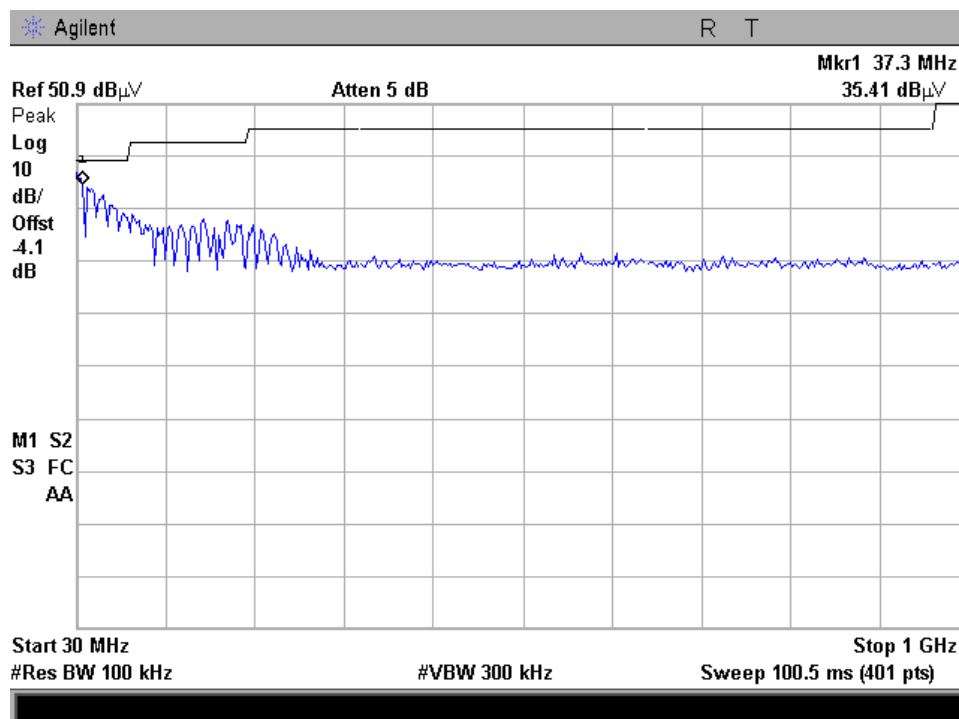




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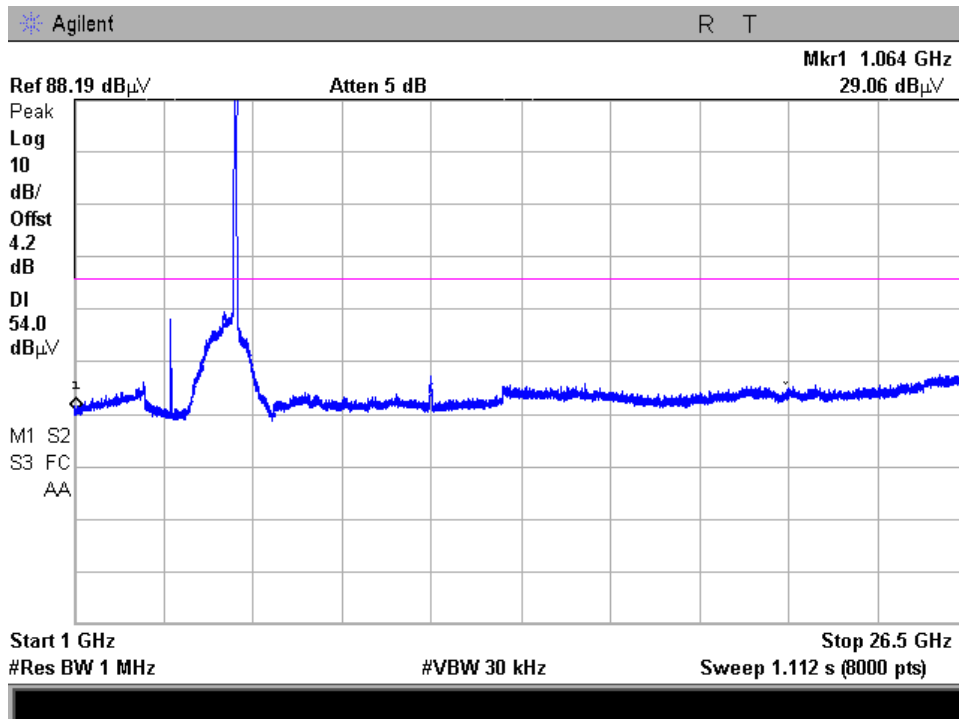


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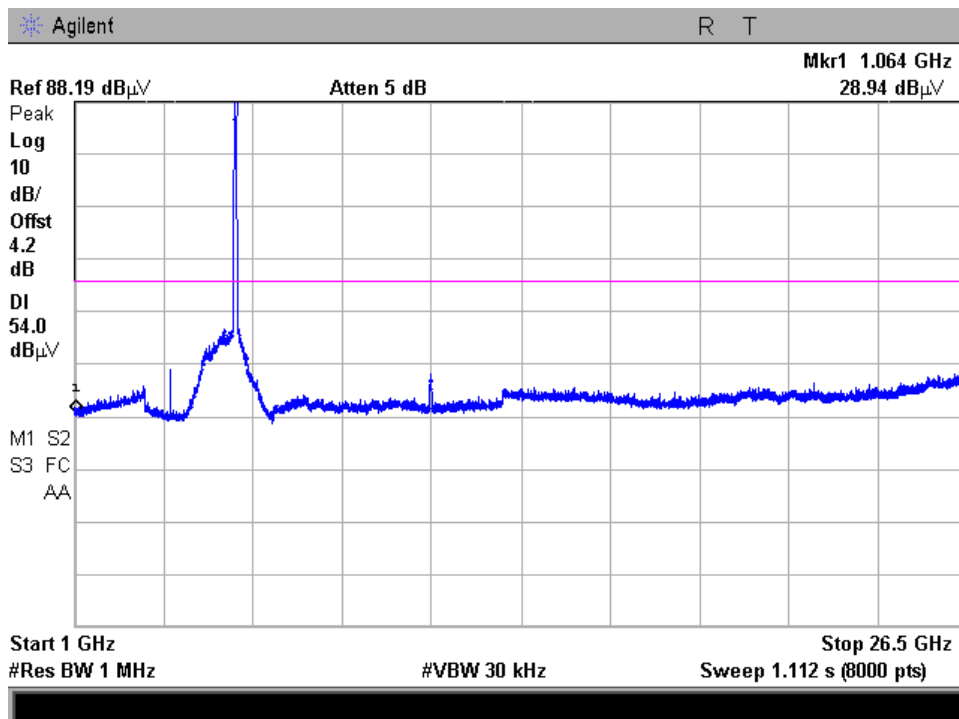




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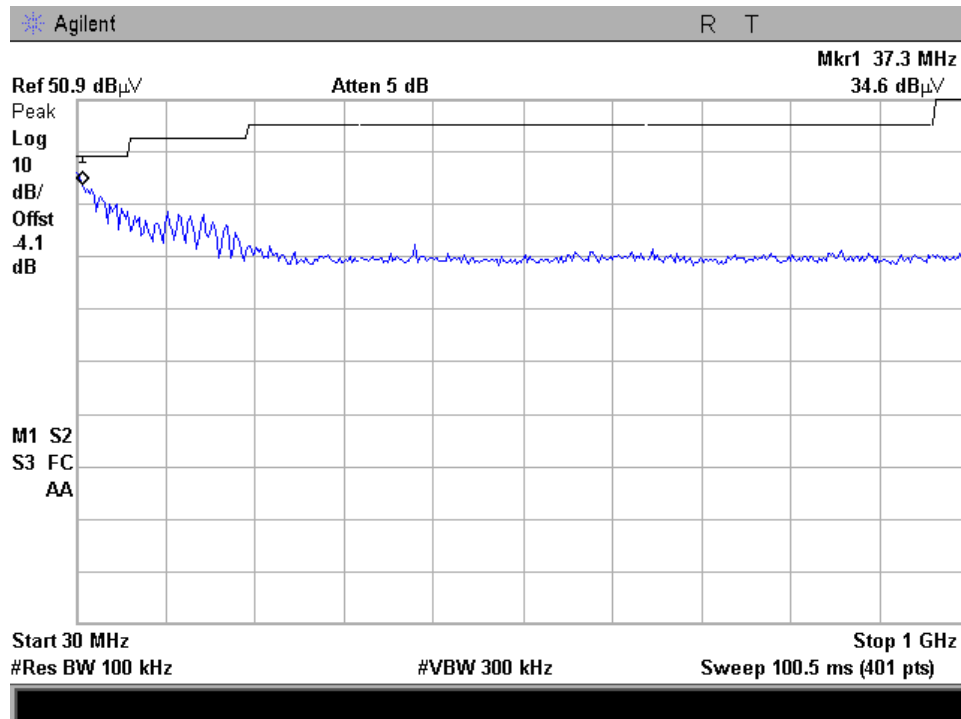


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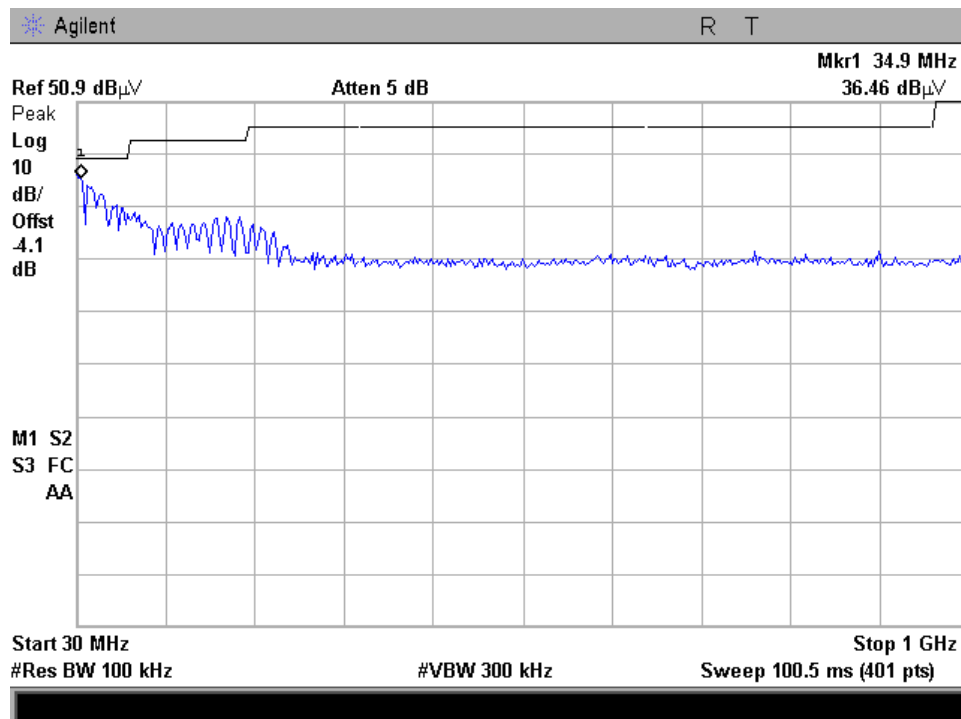




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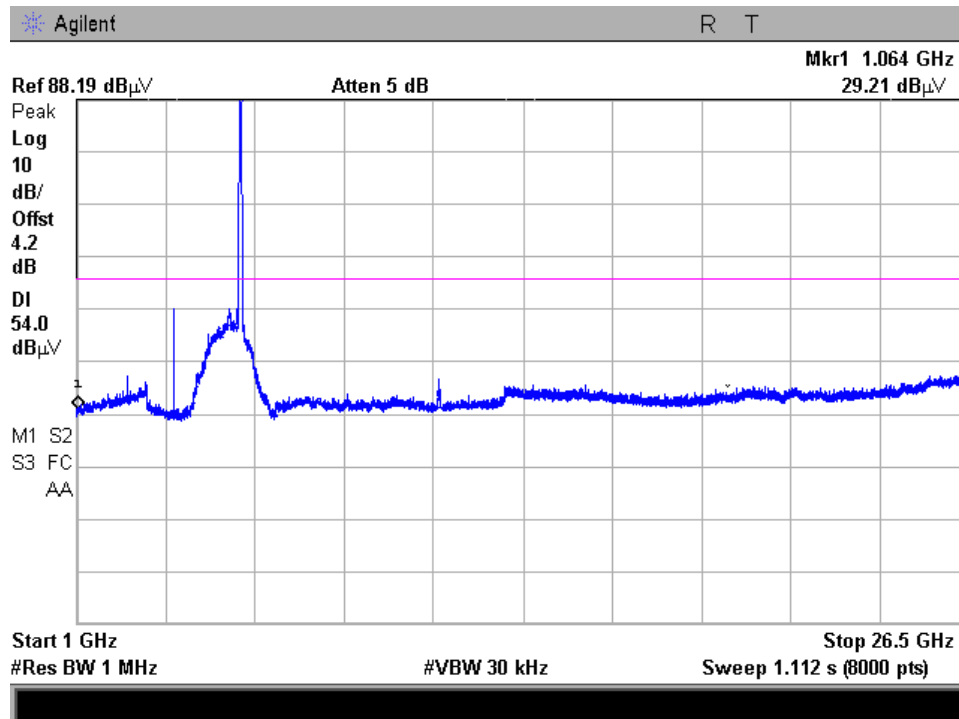


50MHz Mid Ch 30-1000MHz Port J7

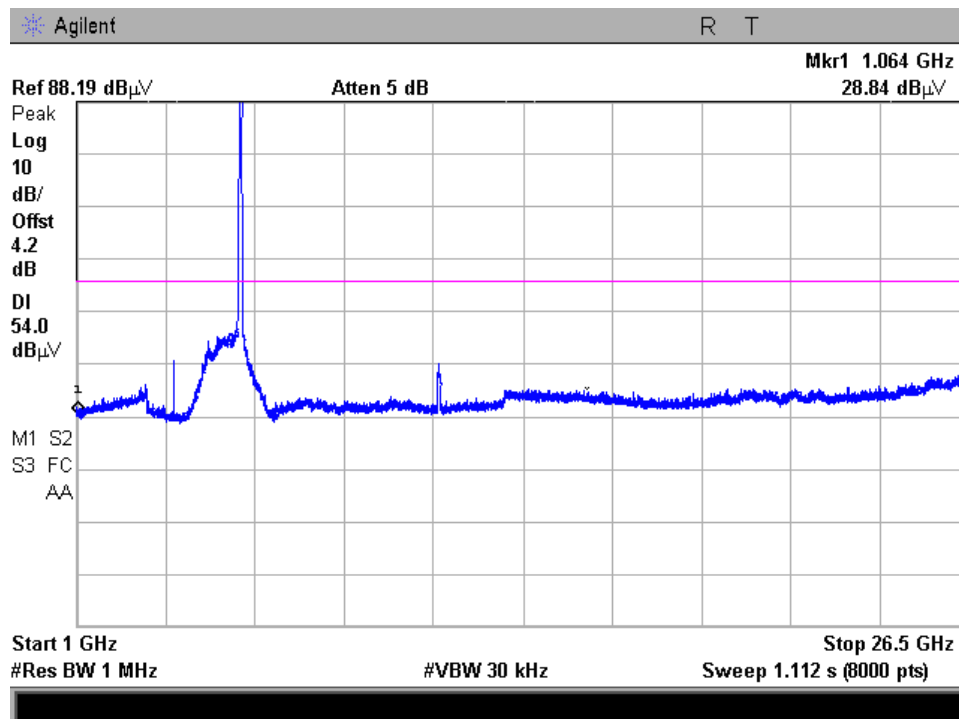




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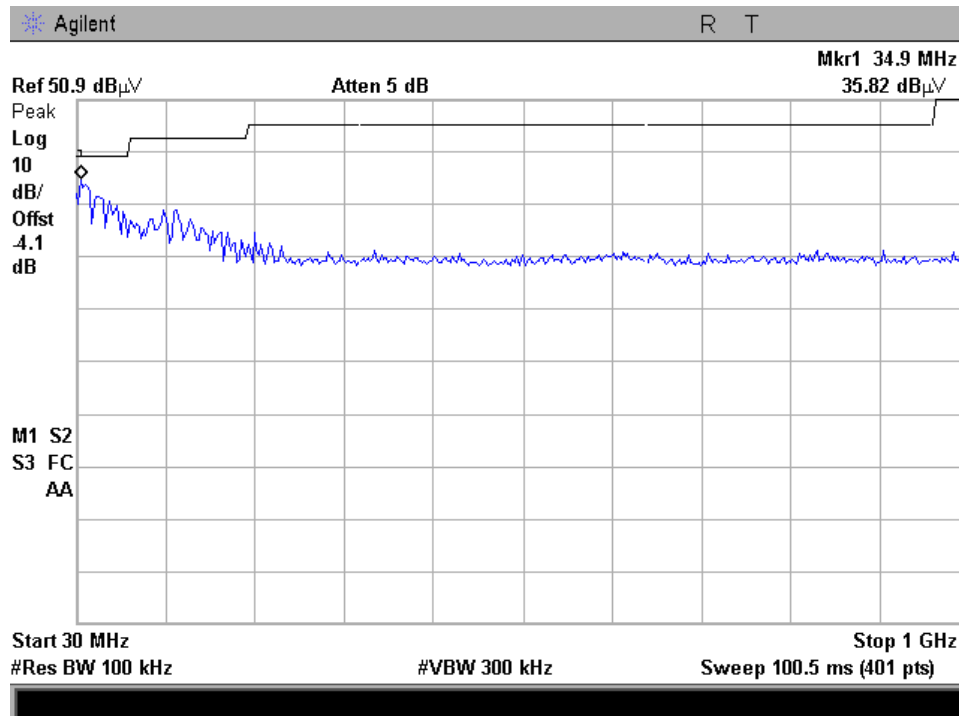


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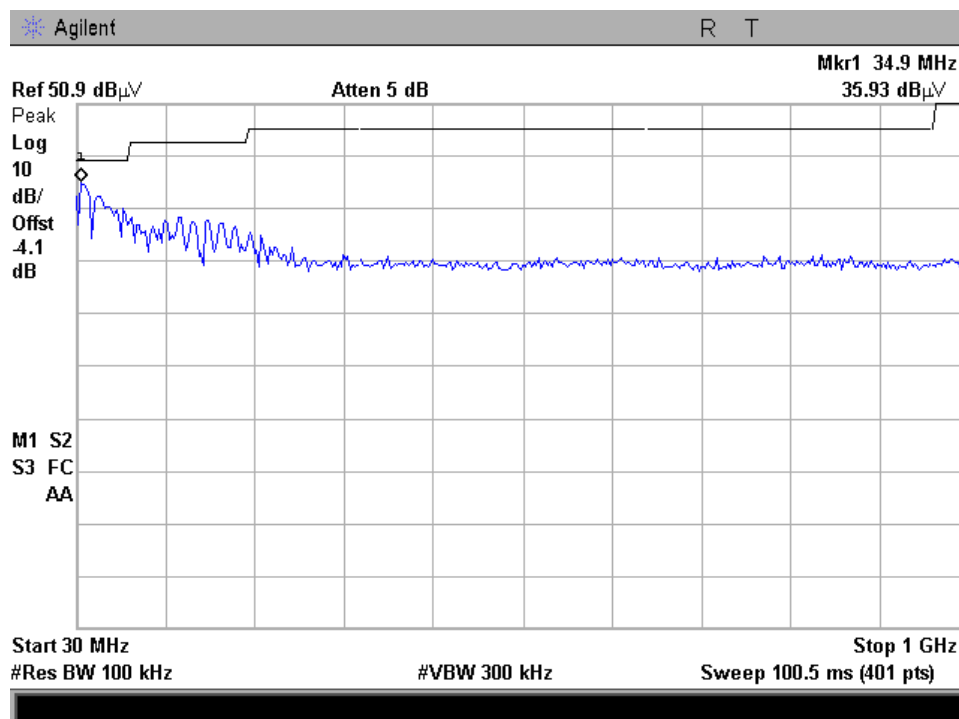




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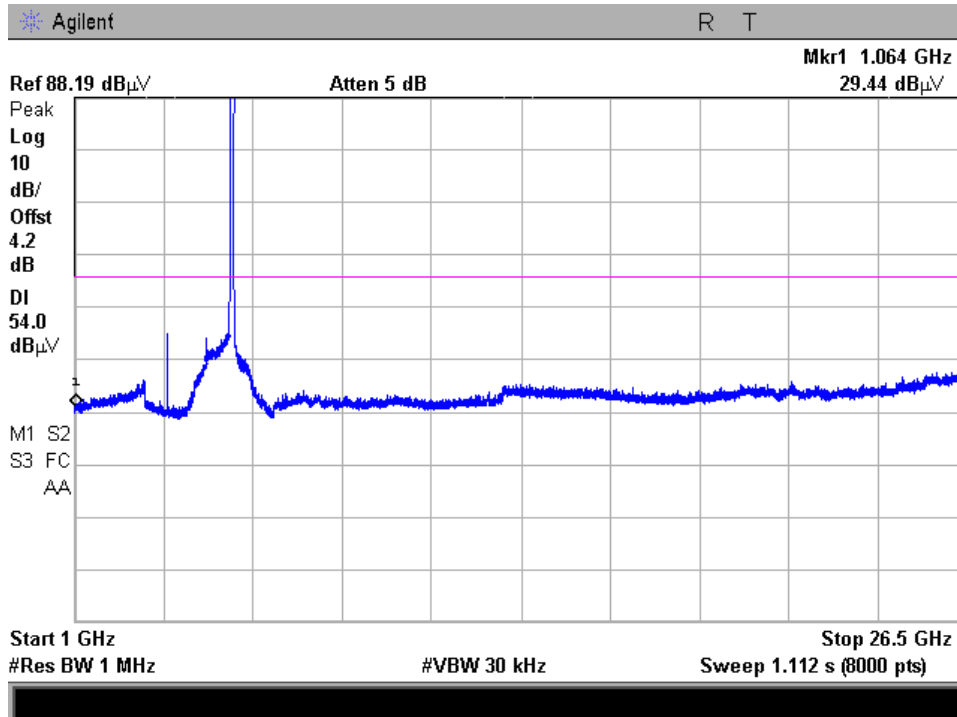


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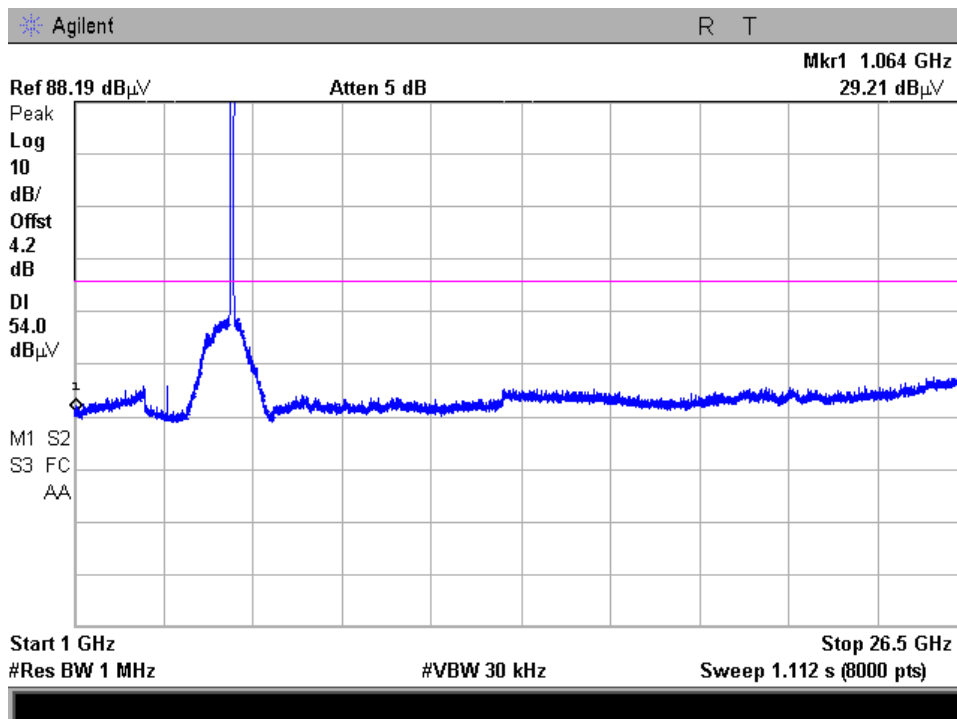




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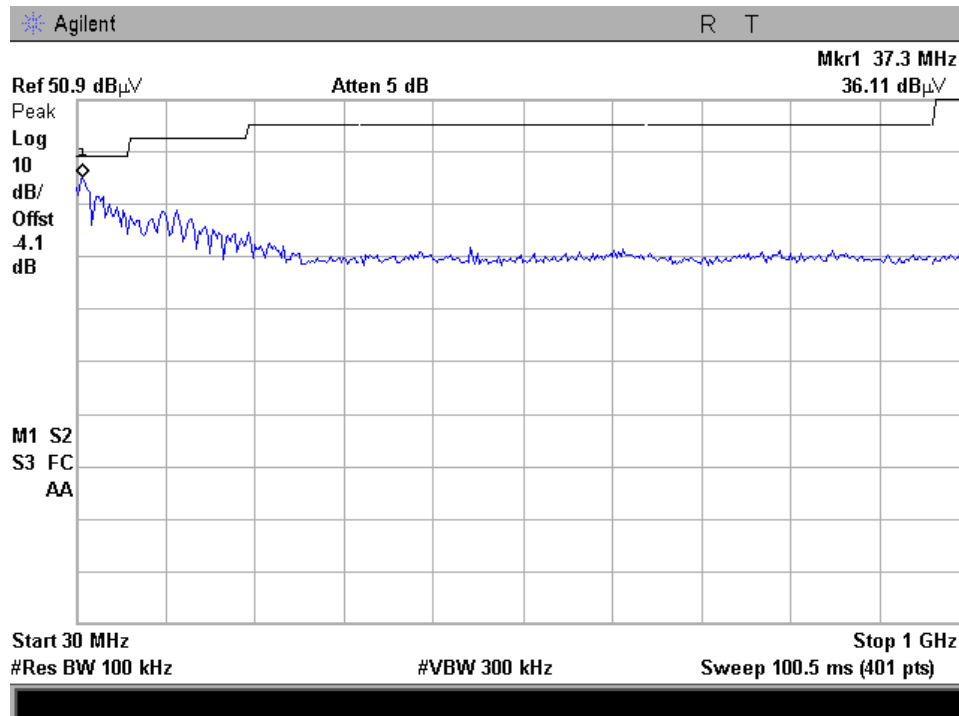


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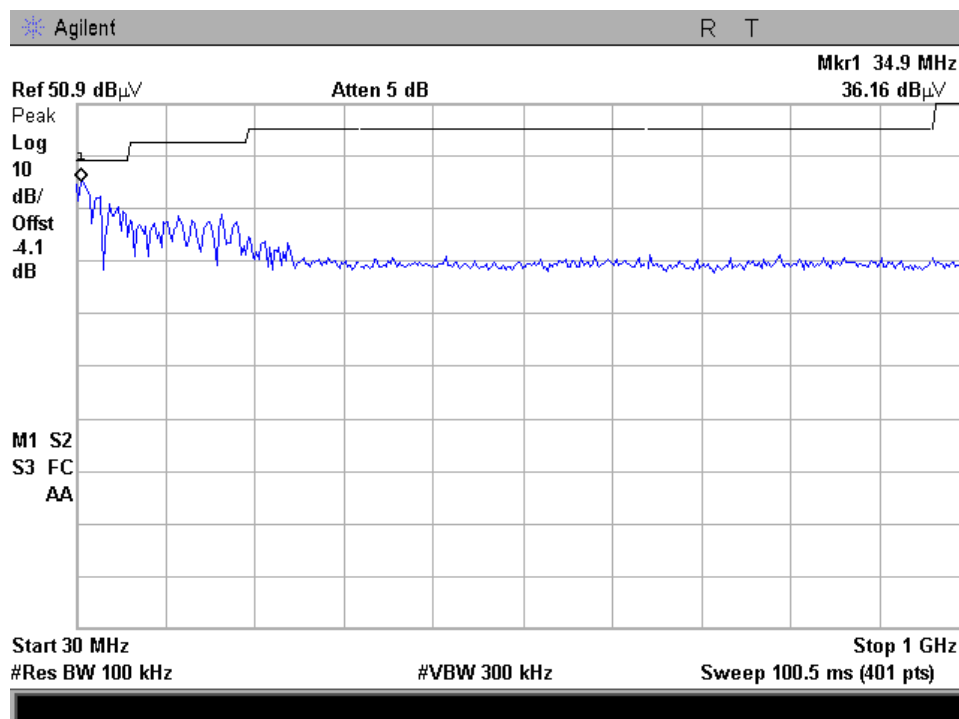




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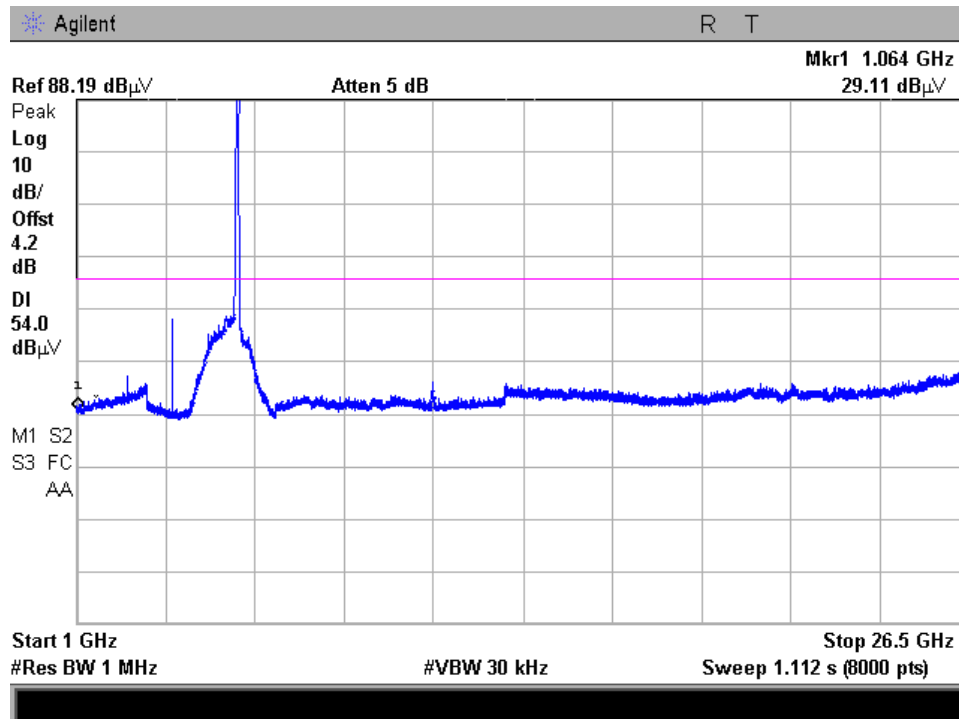


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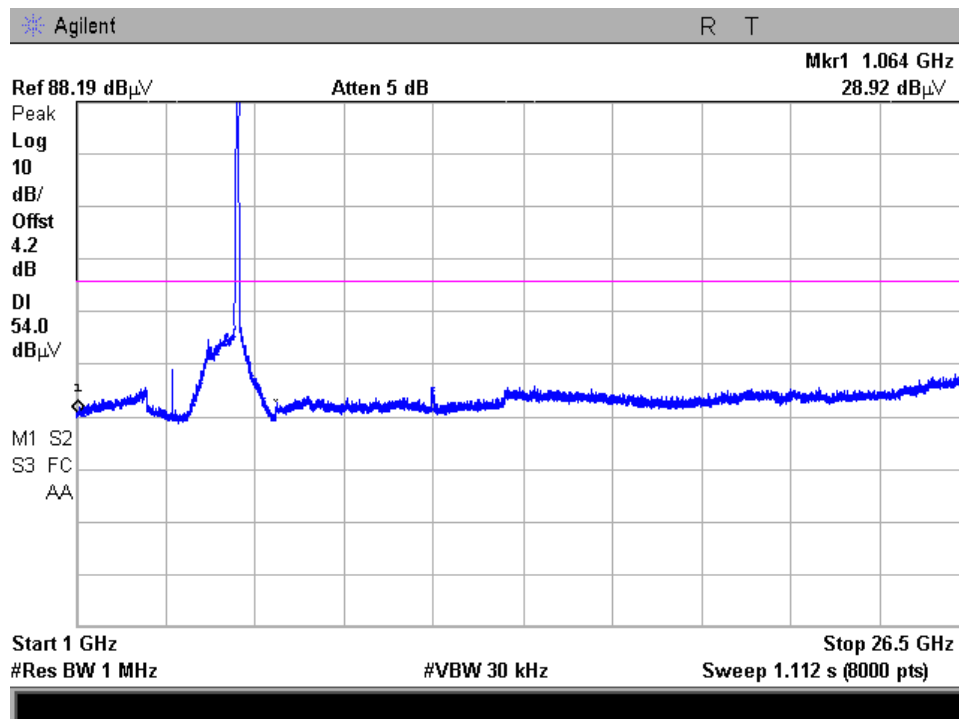




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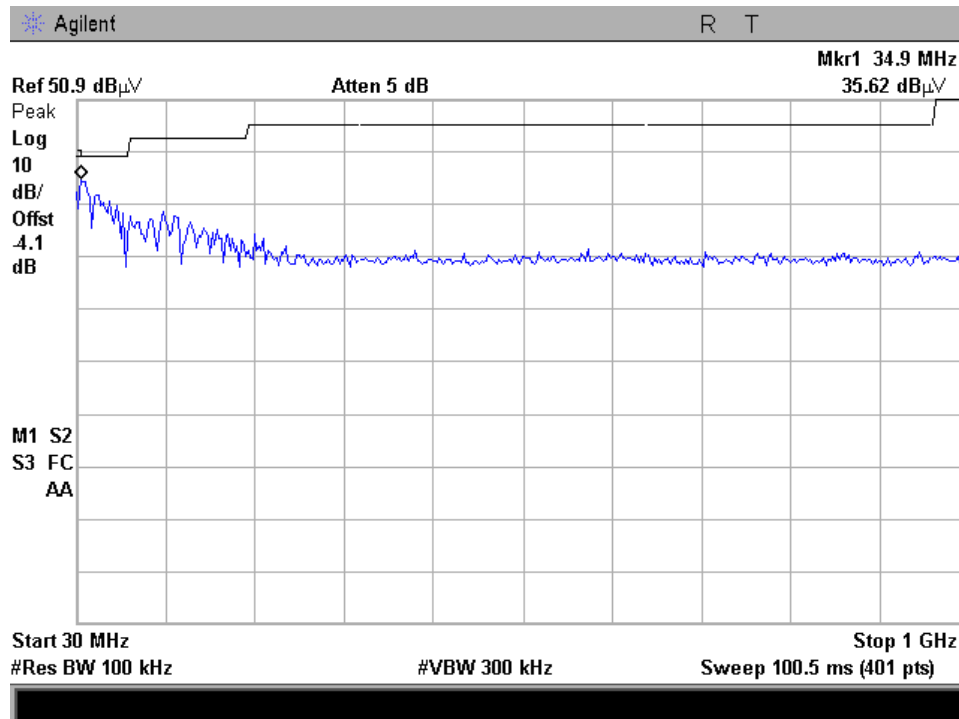


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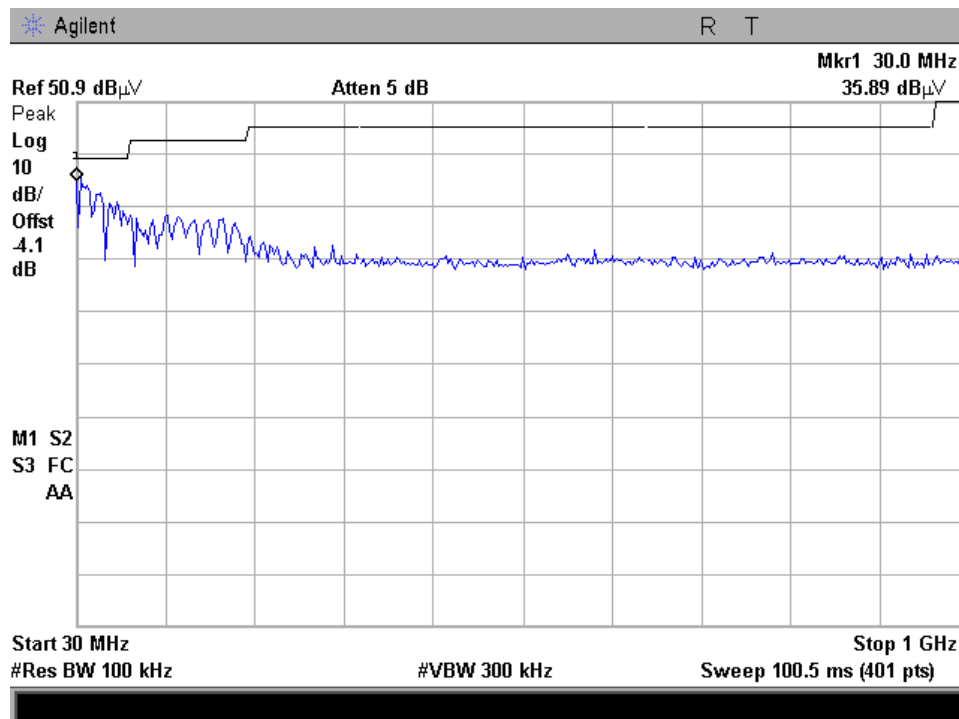




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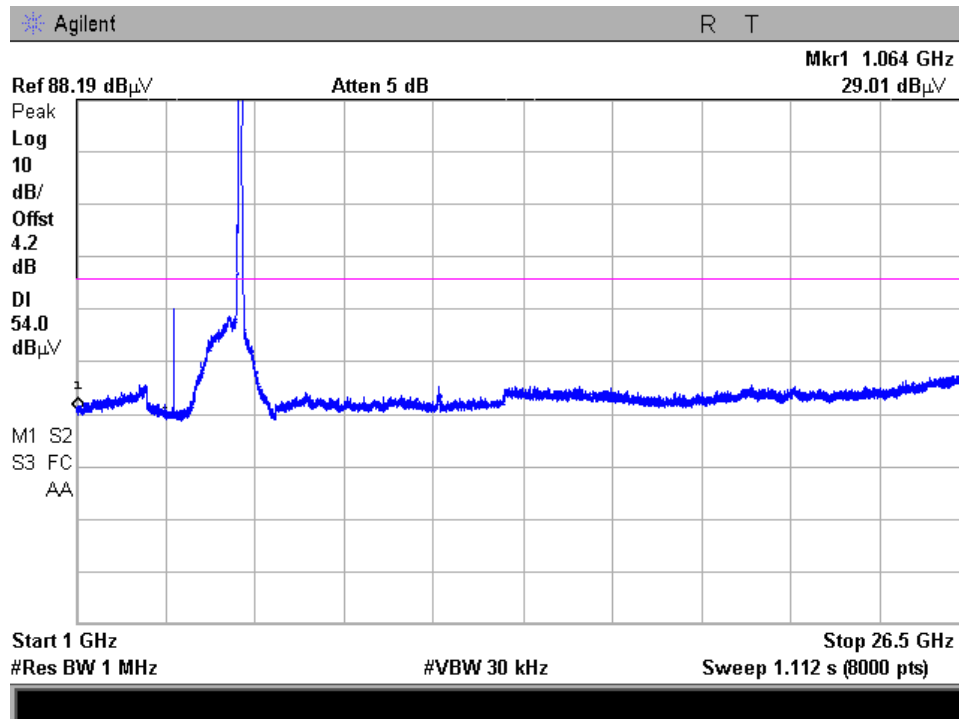


60MHz Mid Ch 30-1000MHz Port J7

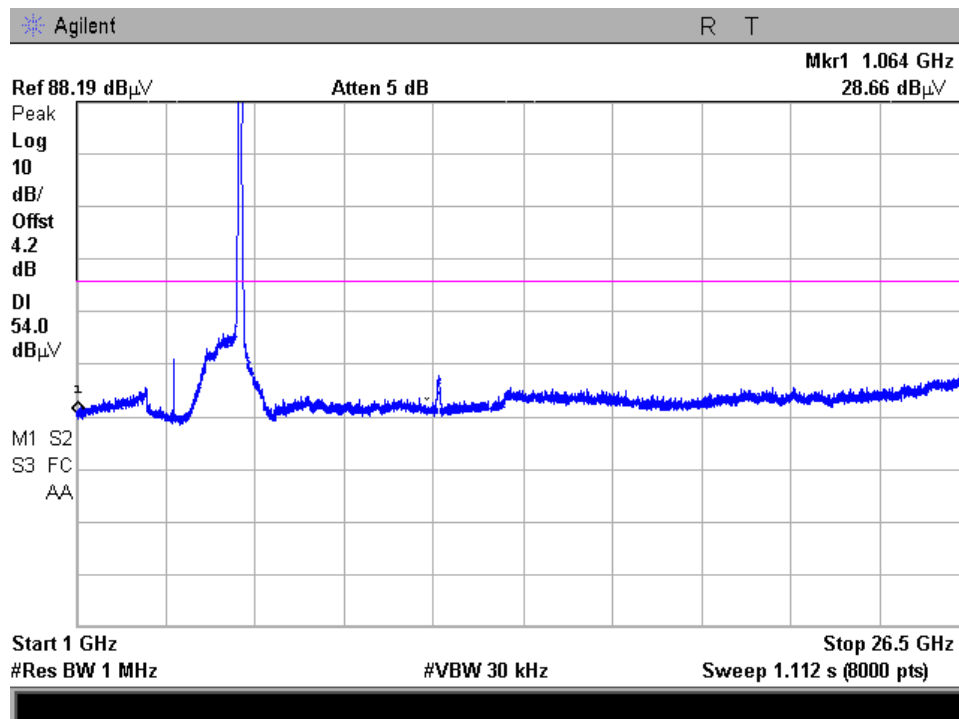




80MHz High Ch 1-26.5GHz Port J8

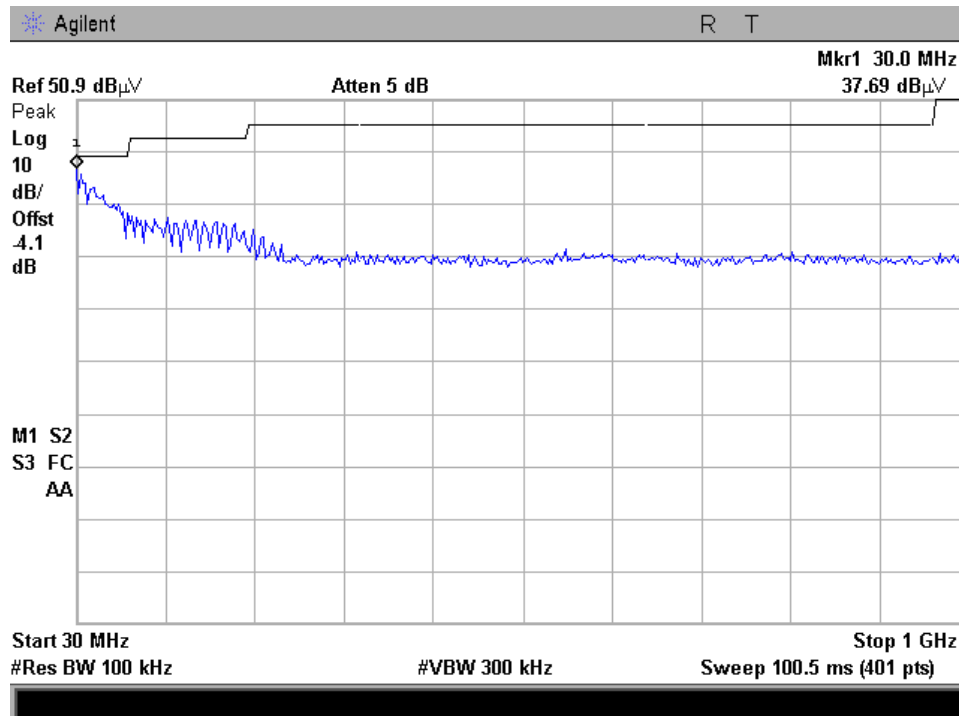


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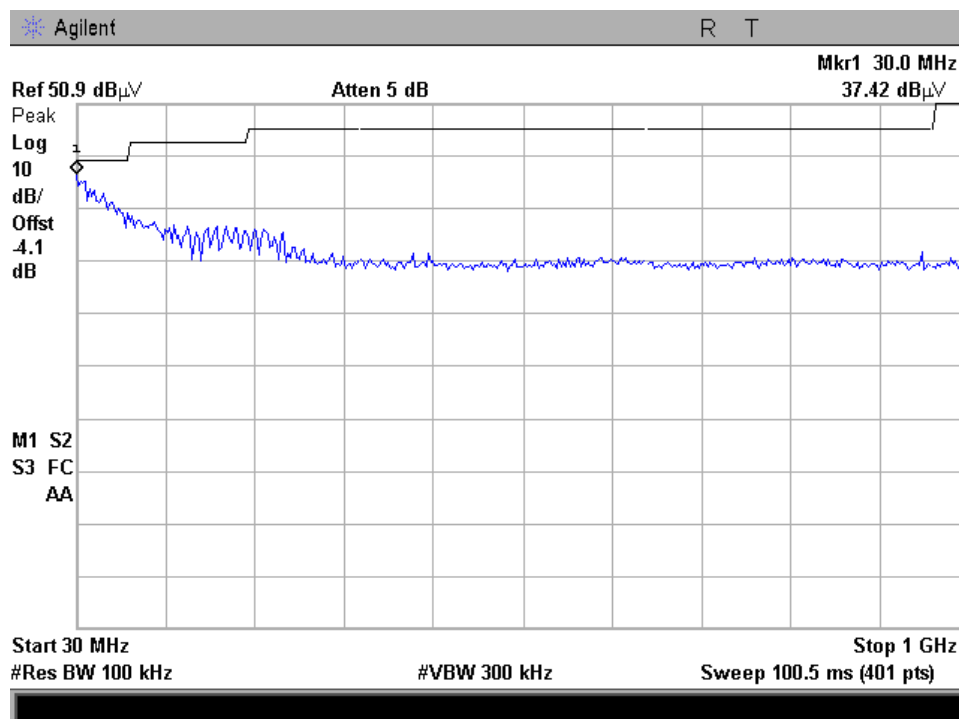




80MHz High Ch 30-1000MHz Port J8

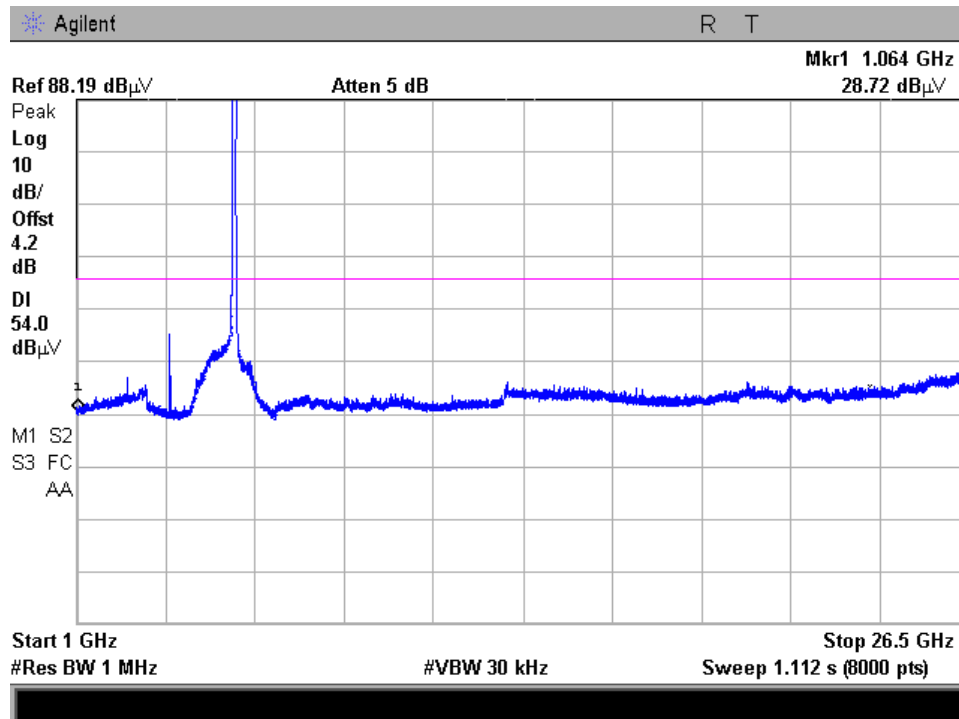


80MHz High Ch 30-1000MHz Port J7

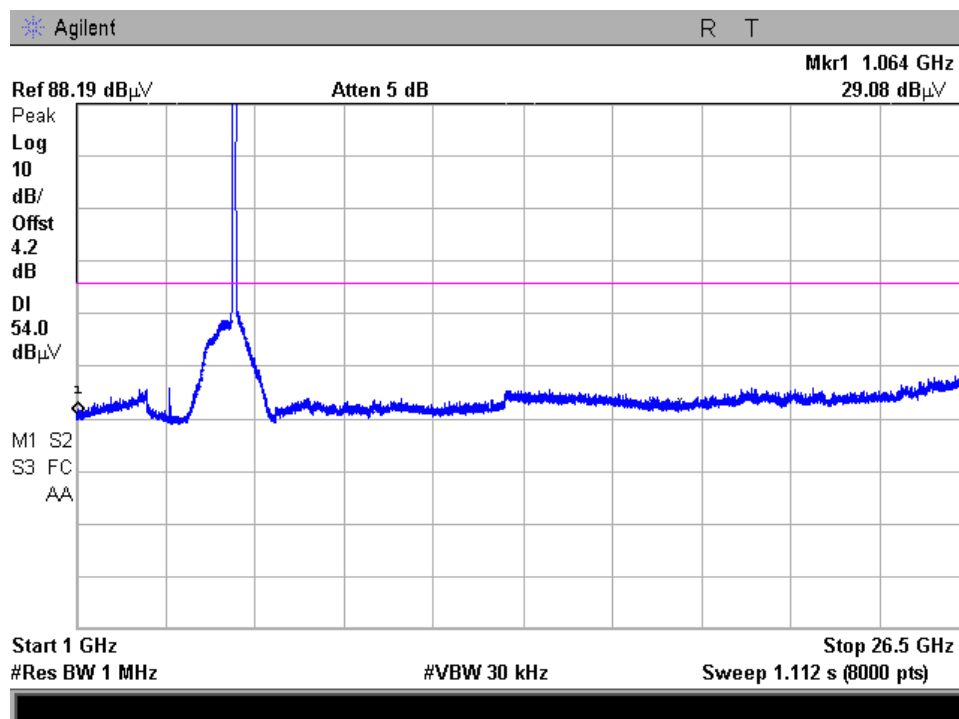




80MHz Low Ch 1-26.5GHz Port J8

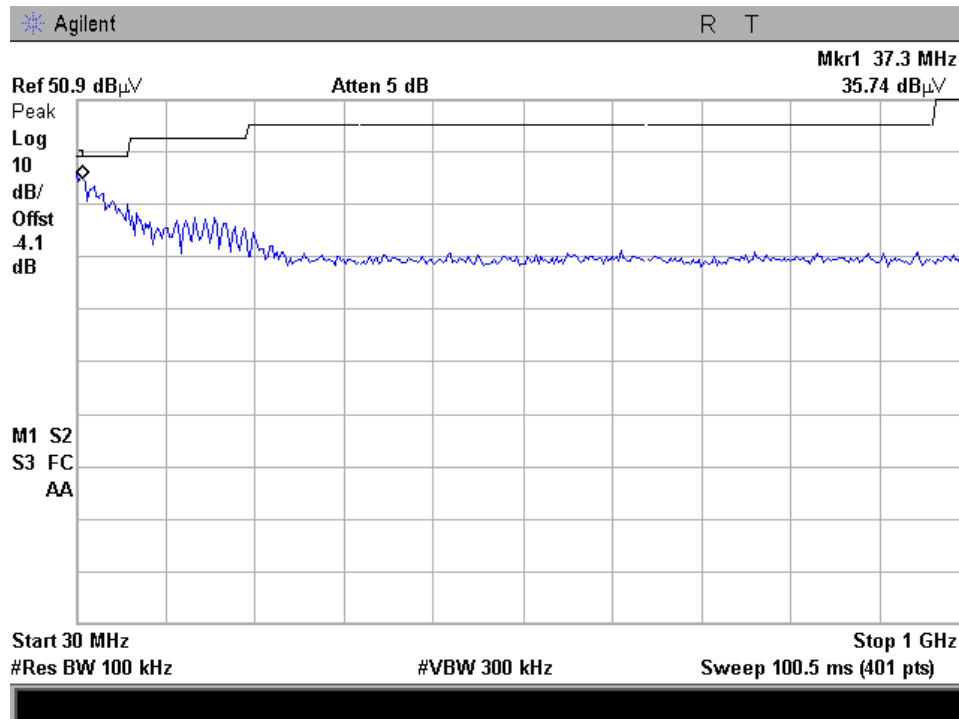


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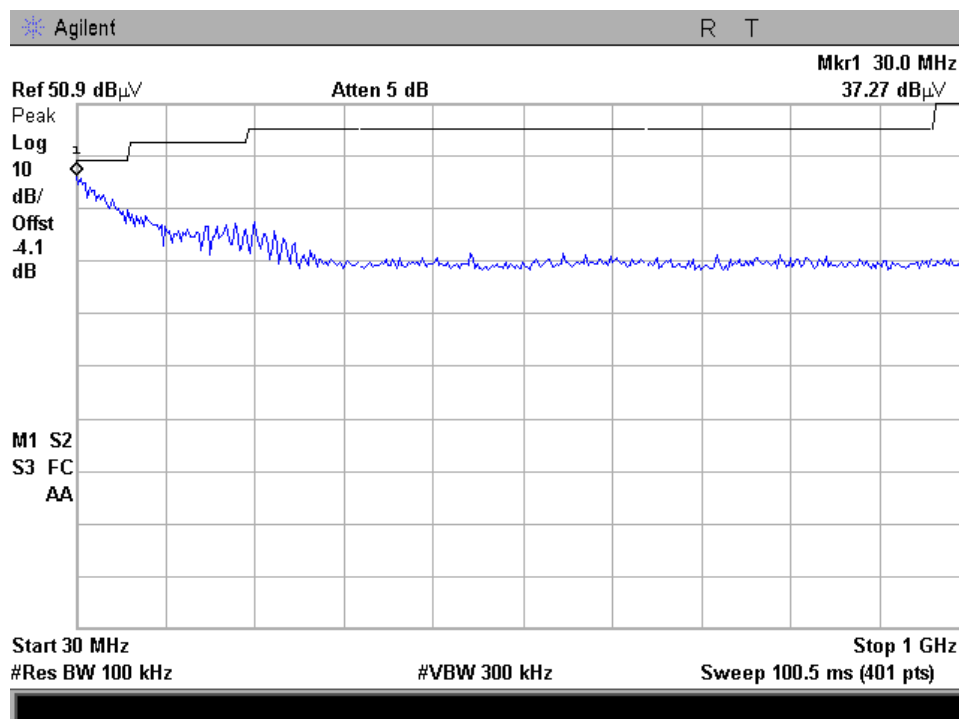




80MHz Low Ch 30-1000MHz Port J8

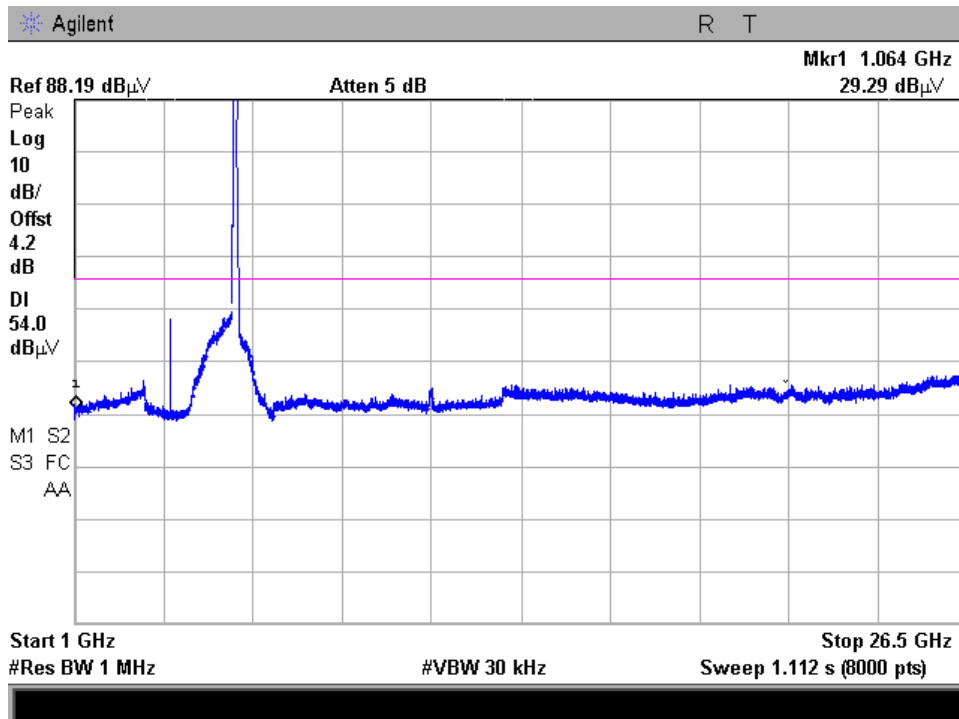


80MHz Low Ch 30-1000MHz Port J7

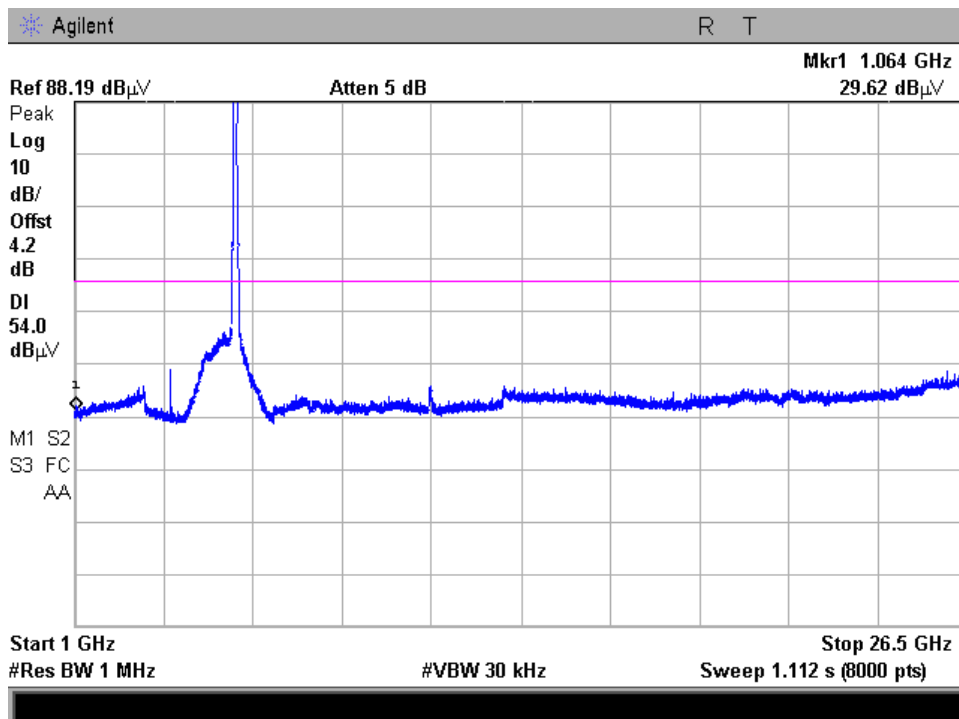




80MHz Mid Ch 1-26.5GHz Port J8

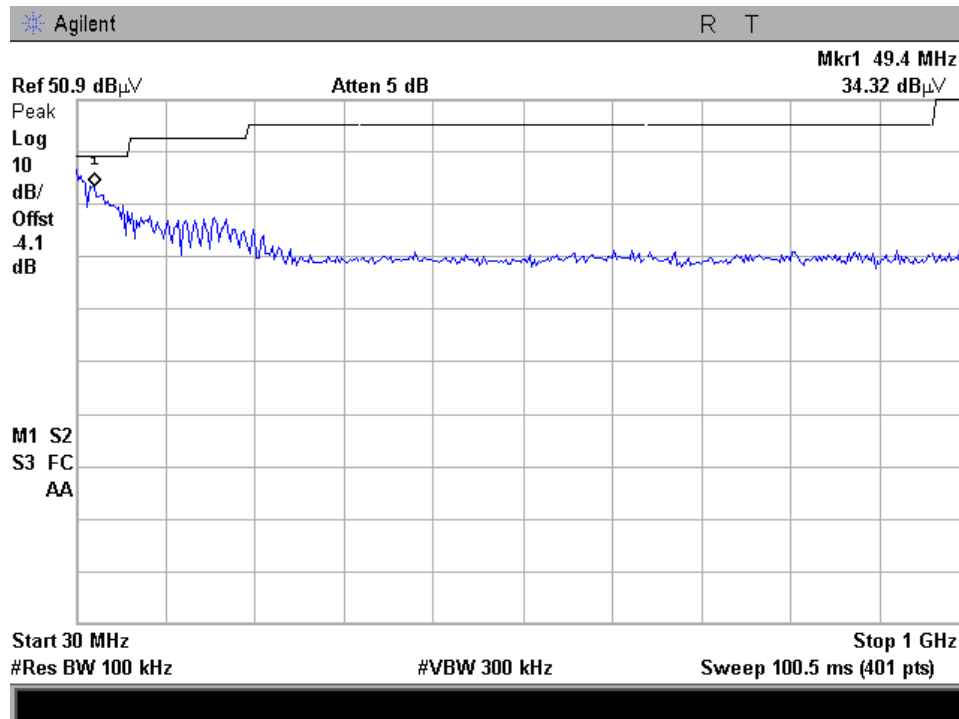


80MHz Mid Ch 1-26.5GHz Port J7





80MHz Mid Ch 30-1000MHz Port J8



80MHz Mid Ch 30-1000MHz Port J7

