

Annex A

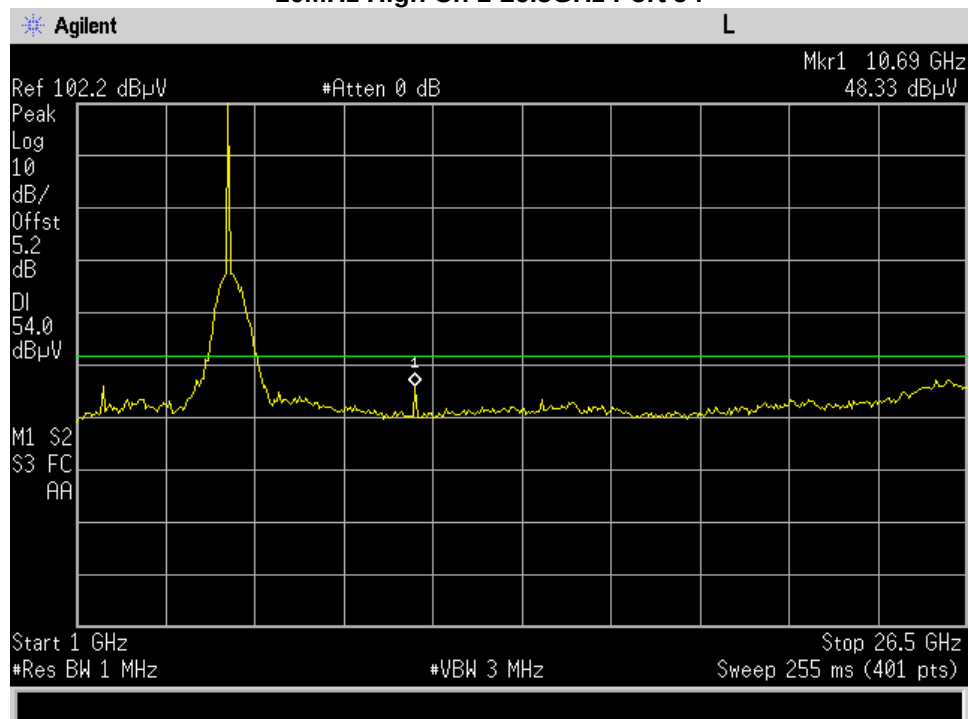
Undesirable Emissions Conducted

UNII-2A and UNII-2C

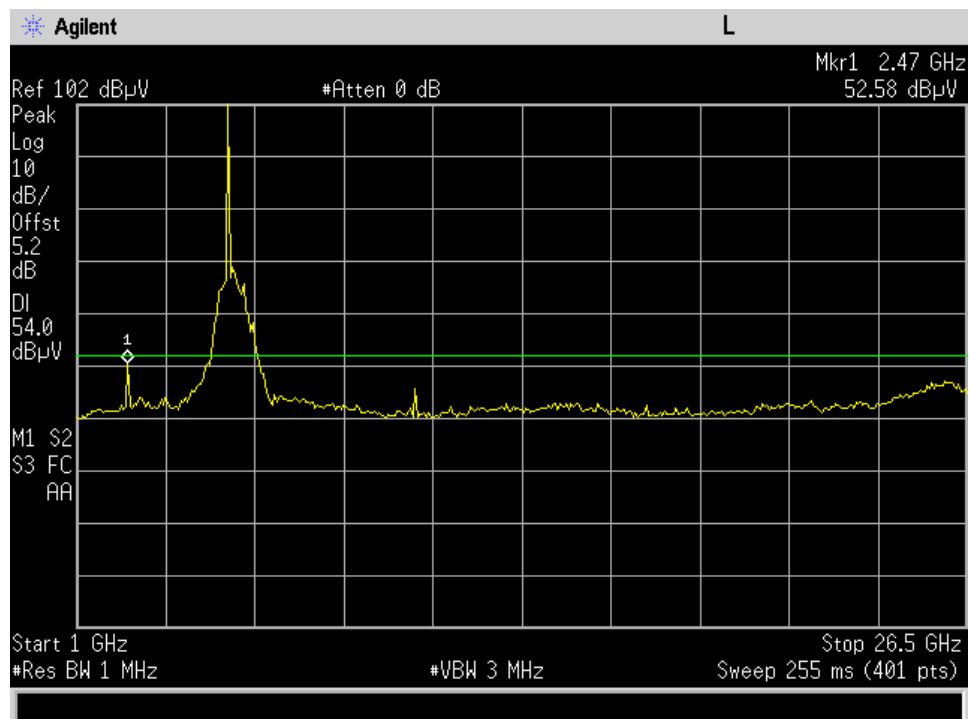


Conducted Measurements in lieu of Radiated

UNII-2A 10MHz High Ch 1-26.5GHz Port J4

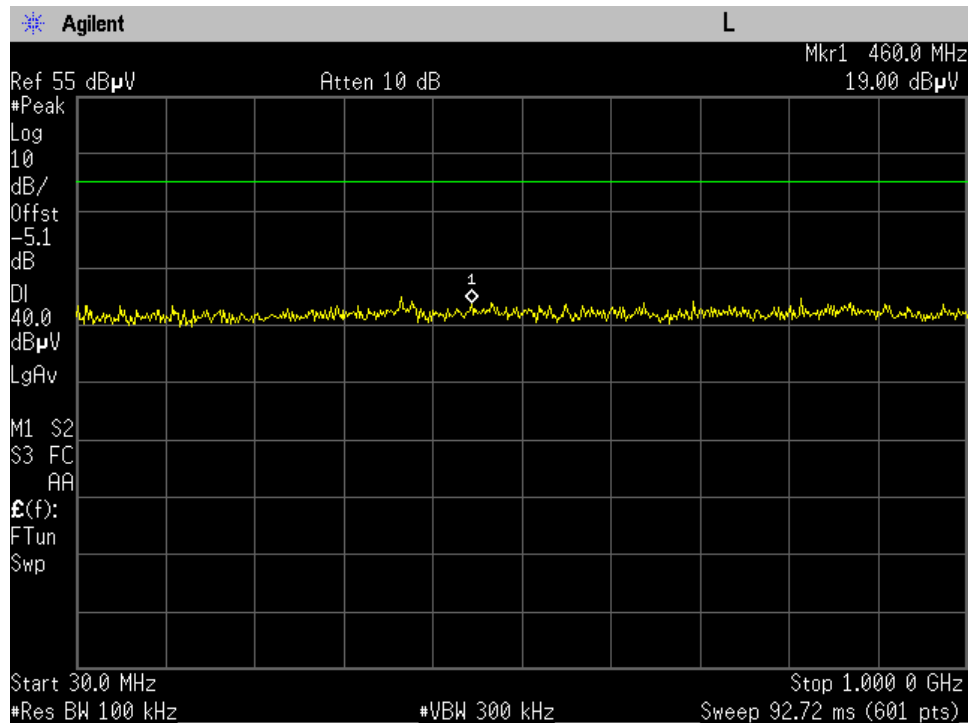


10MHz High Ch 1-26.5GHz Port J7

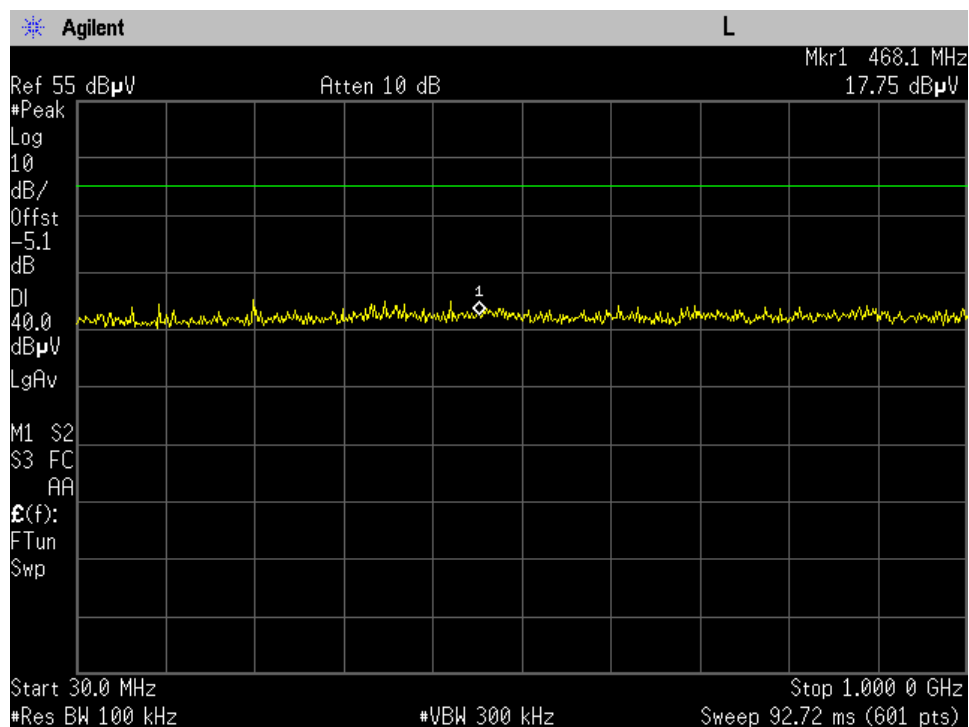




10MHz High Ch 30 - 1000MHz Port J4

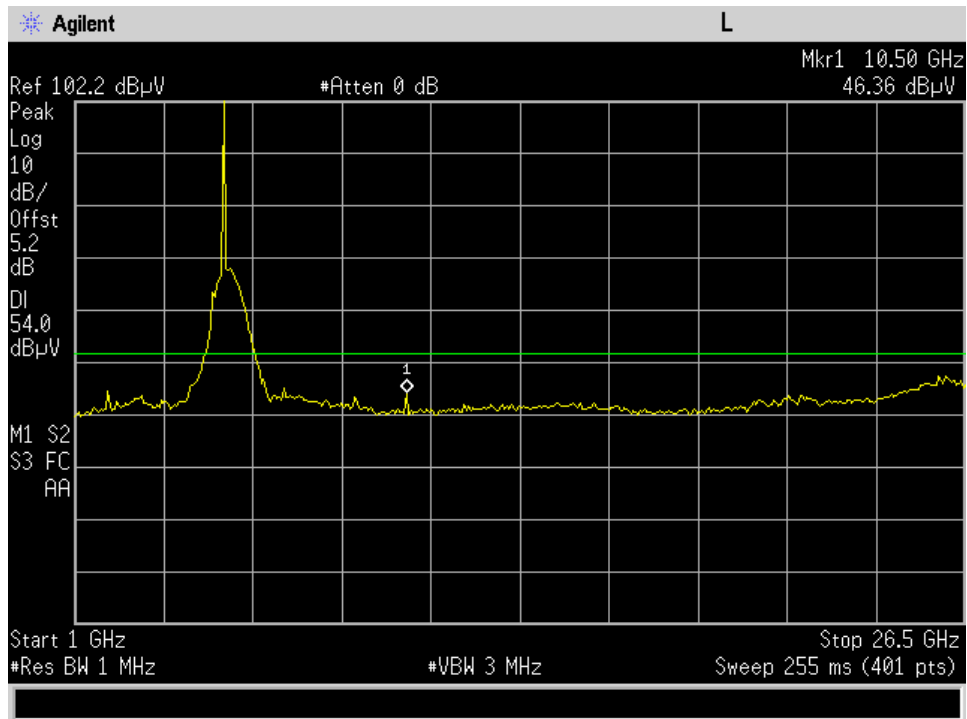


10MHz High Ch 30 - 1000MHz Port J7

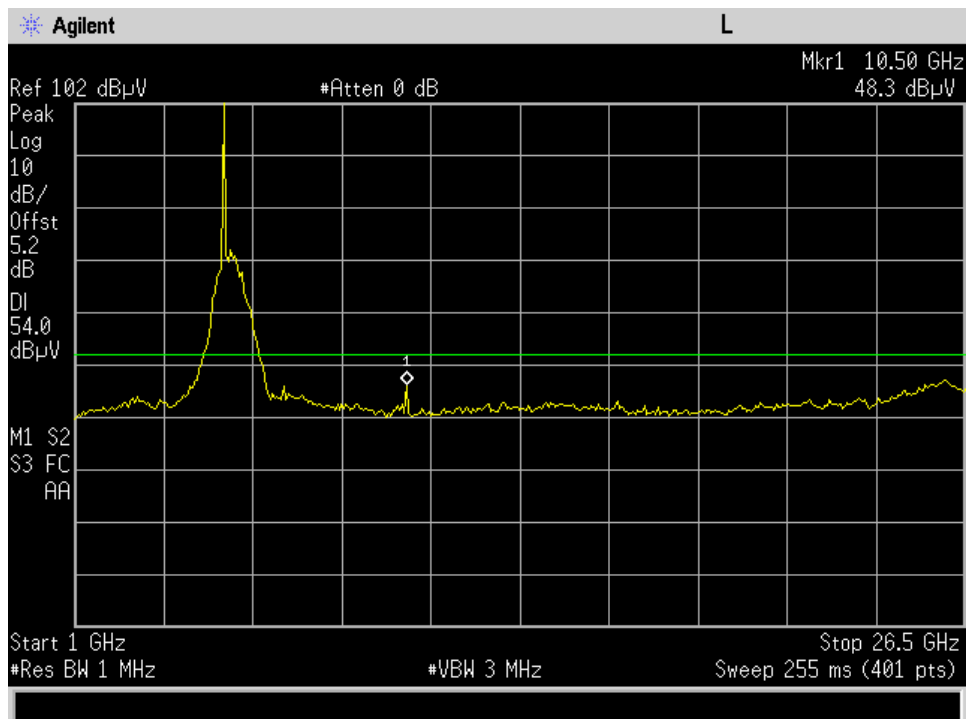




10MHz Low Ch 1-26.5GHz Port J4

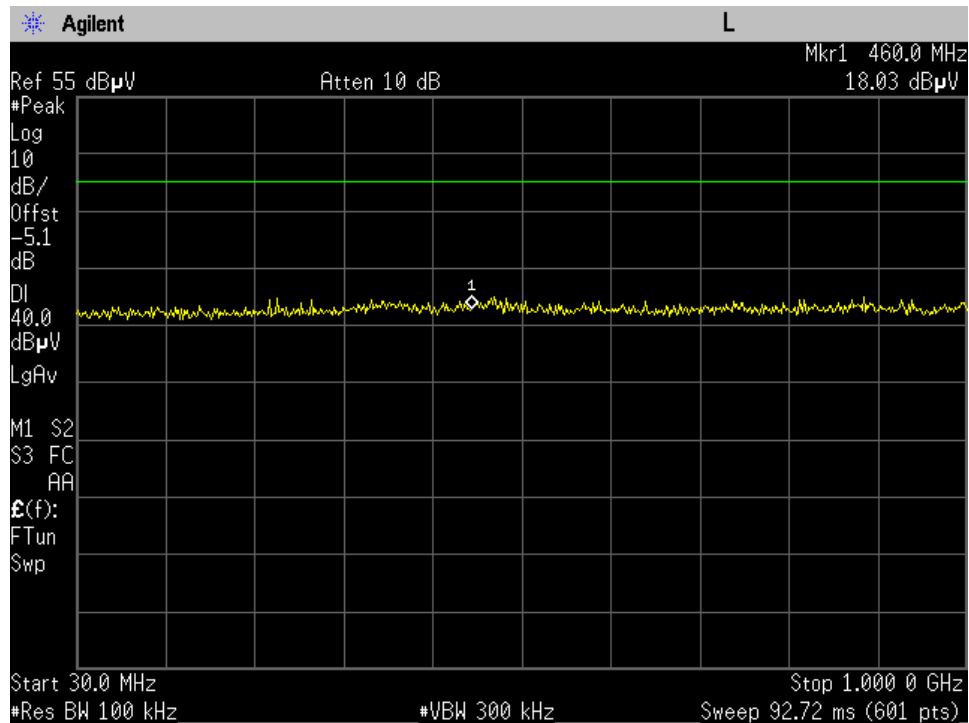


10MHz Low Ch 1-26.5GHz Port J7

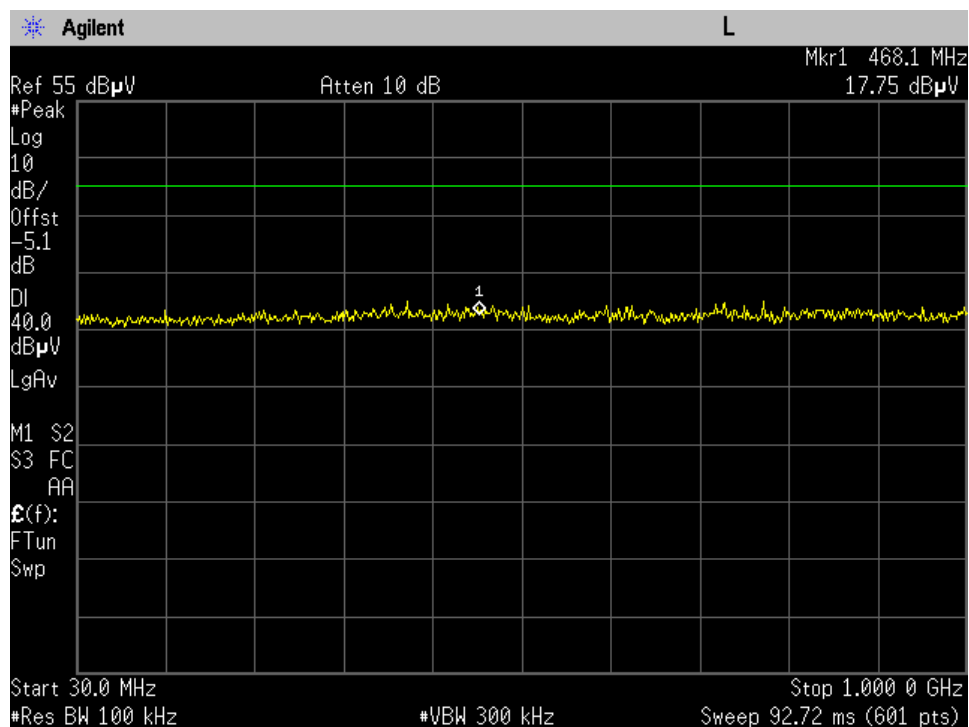




10MHz Low Ch 30 - 1000MHz Port J4

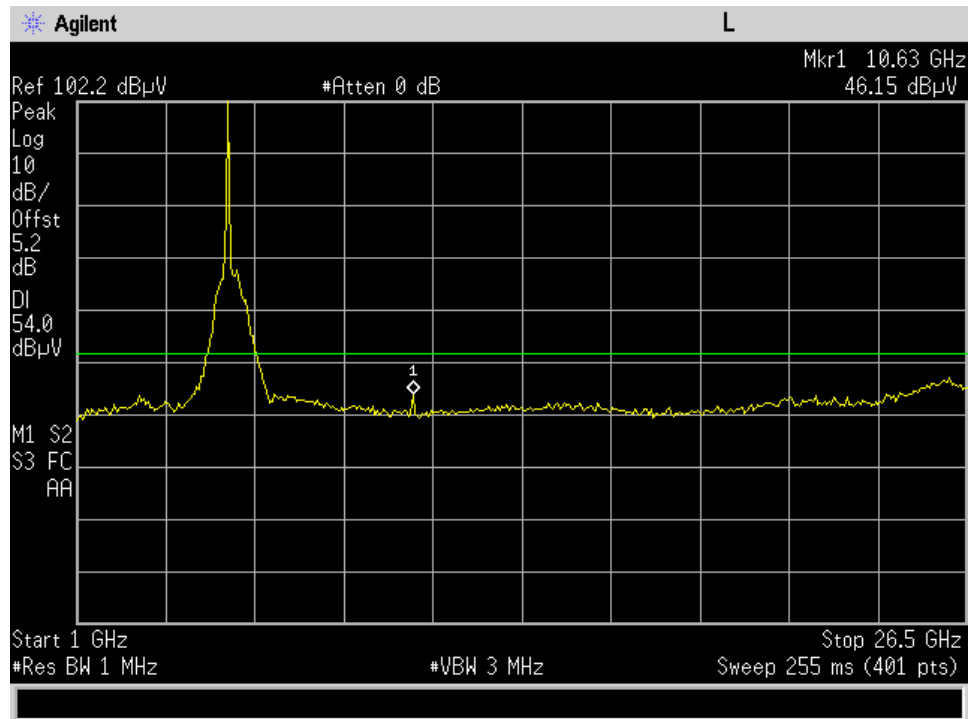


10MHz Low Ch 30 - 1000MHz Port J7

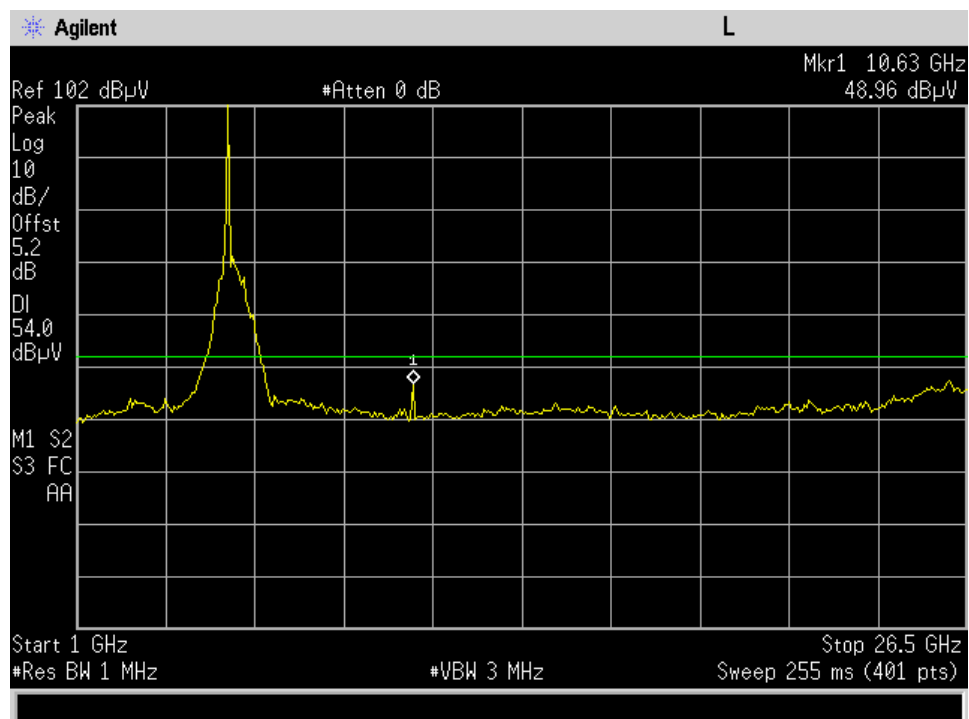




10MHz Mid Ch 1-26.5GHz Port J4

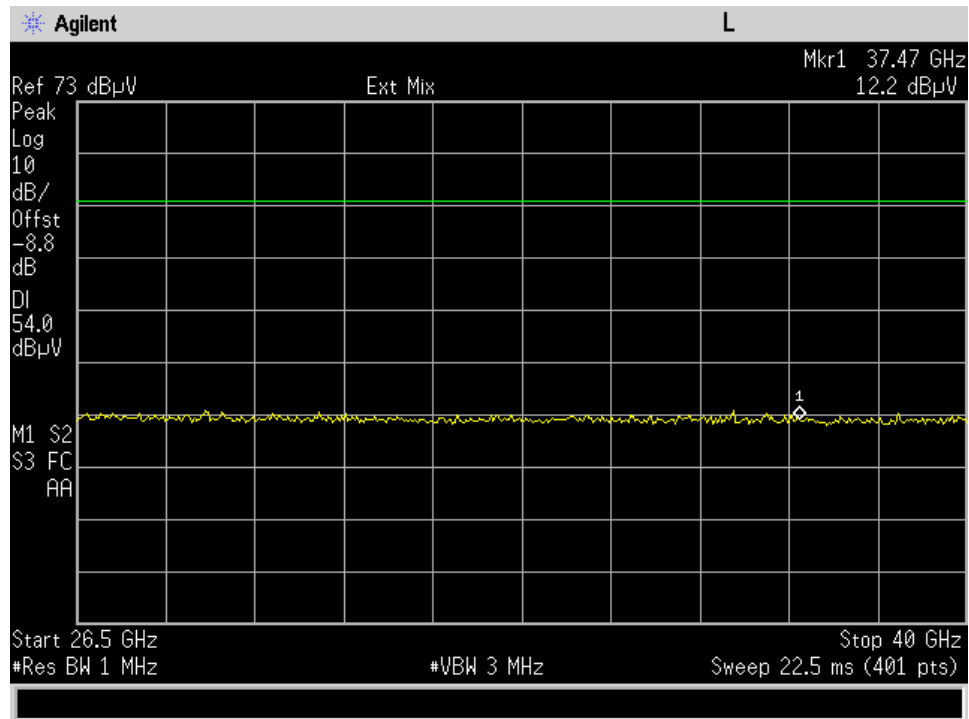


10MHz Mid Ch 1-26.5GHz Port J7

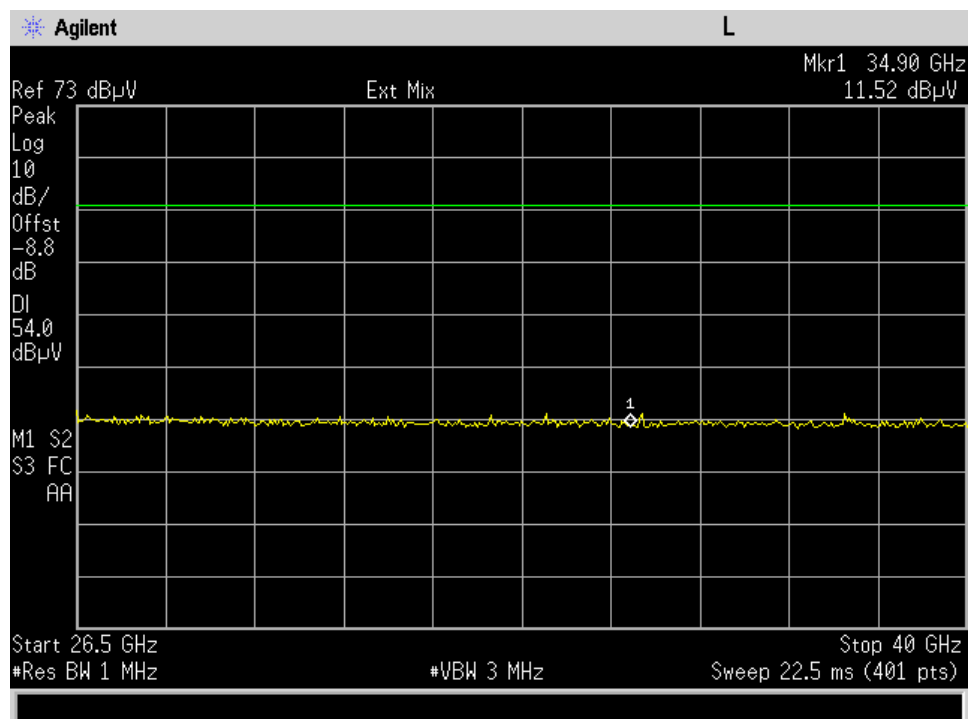




10MHz Mid Ch 26.5-40GHz Port J4

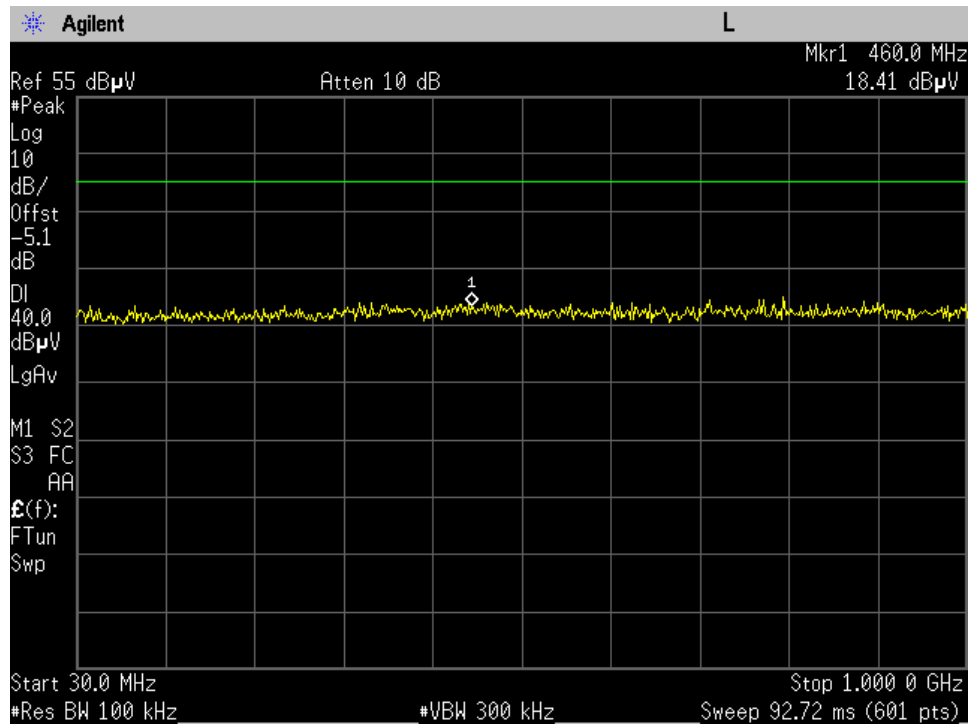


10MHz Mid Ch 26.5-40GHz Port J7

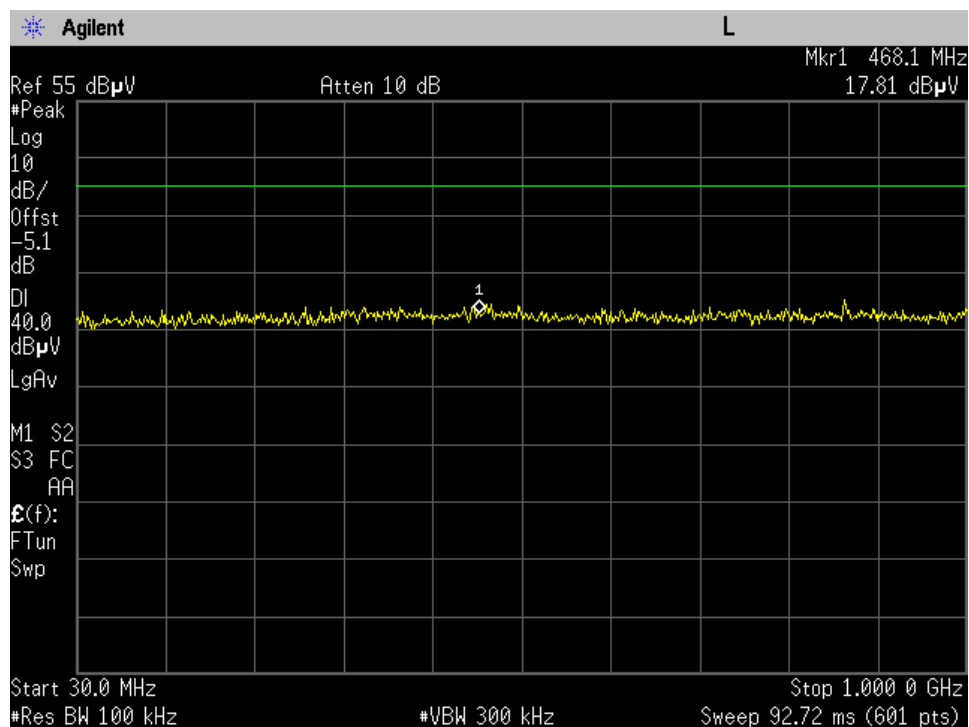




10MHz Mid Ch 30 - 1000MHz Port J4

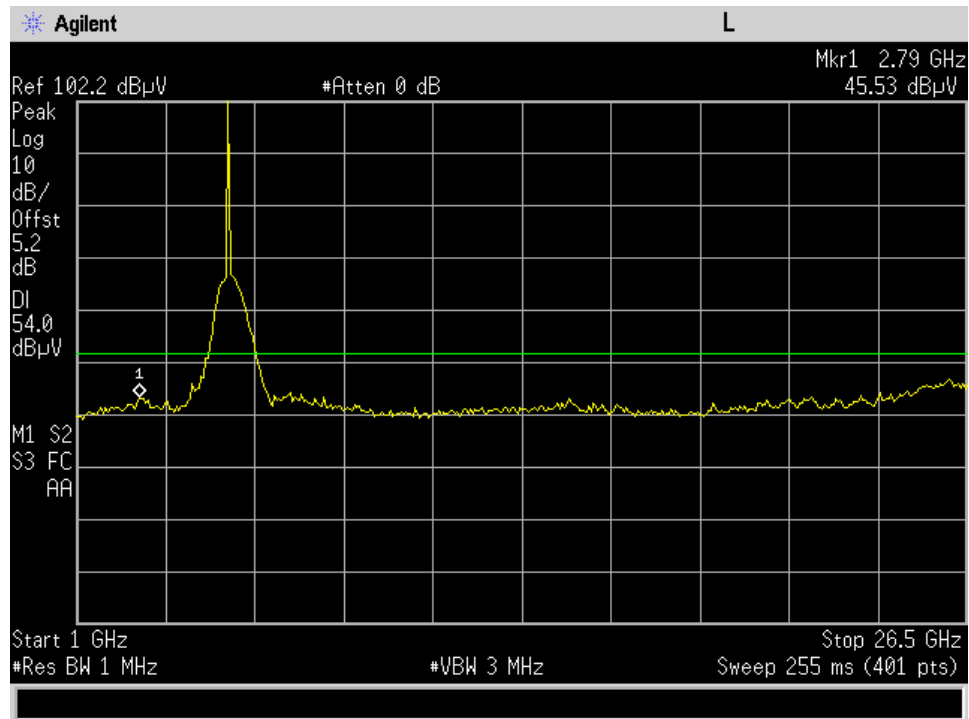


10MHz Mid Ch 30 - 1000MHz Port J7

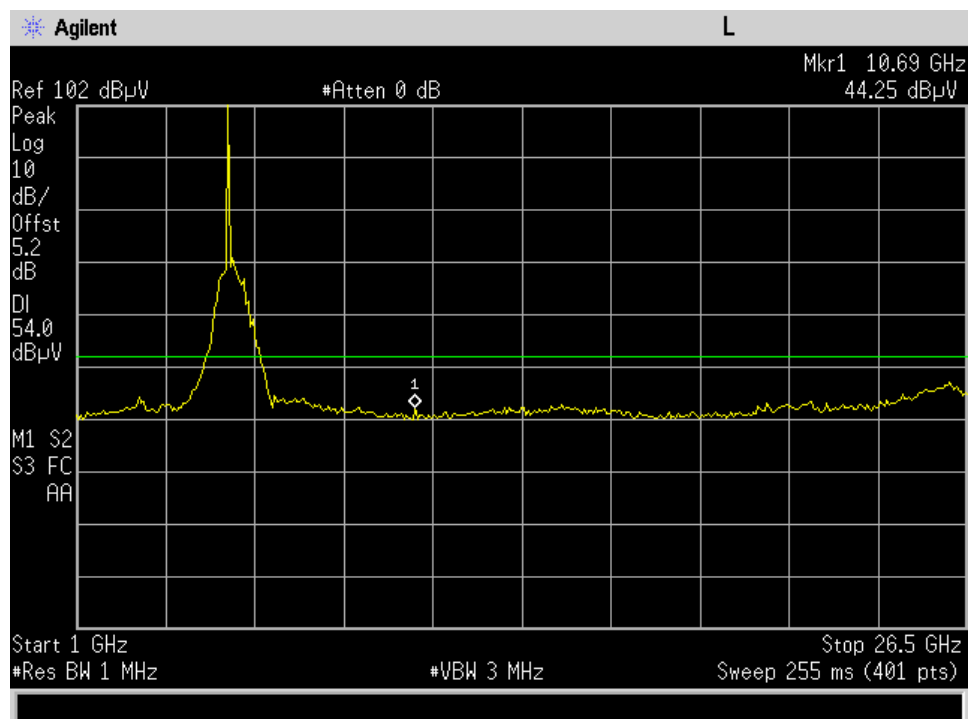




20MHz High Ch 1-26.5GHz Port J4

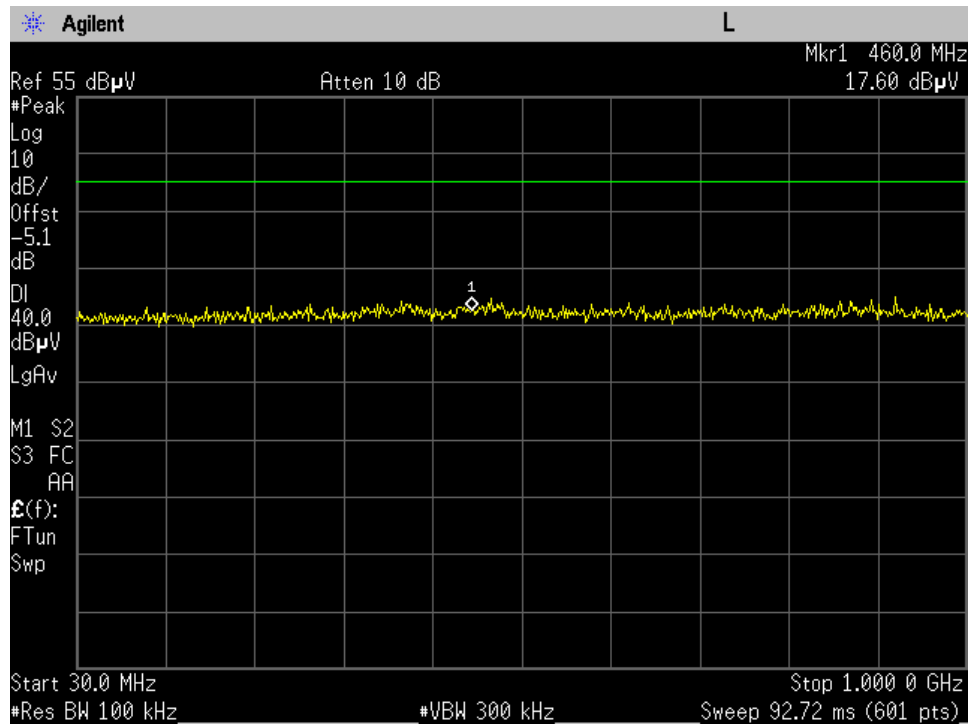


20MHz High Ch 1-26.5GHz Port J7

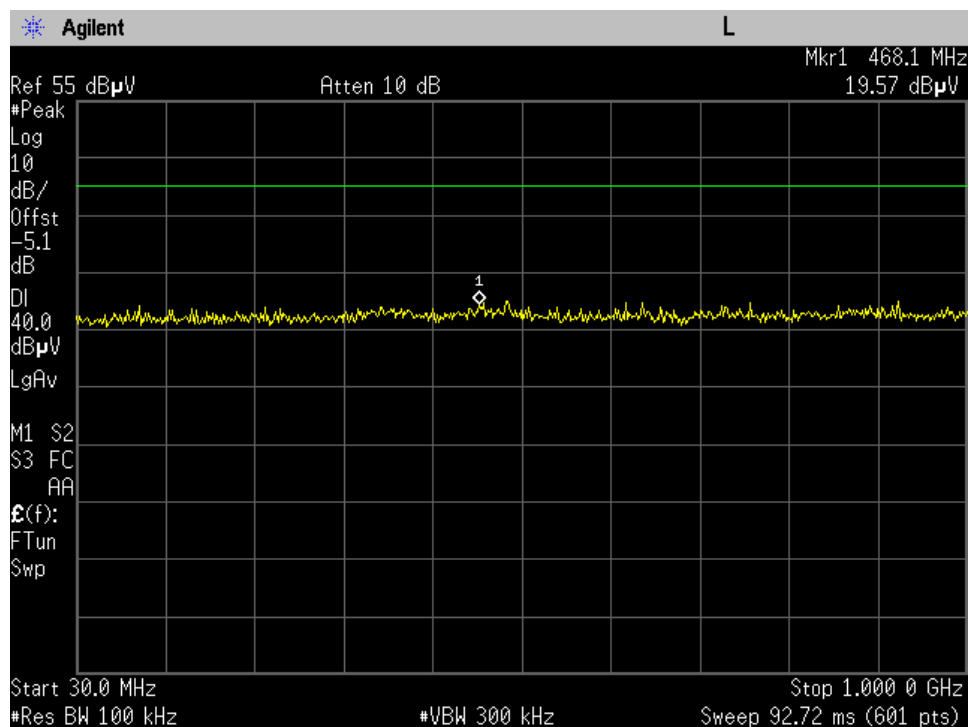




20MHz High Ch 30 - 1000MHz Port J4

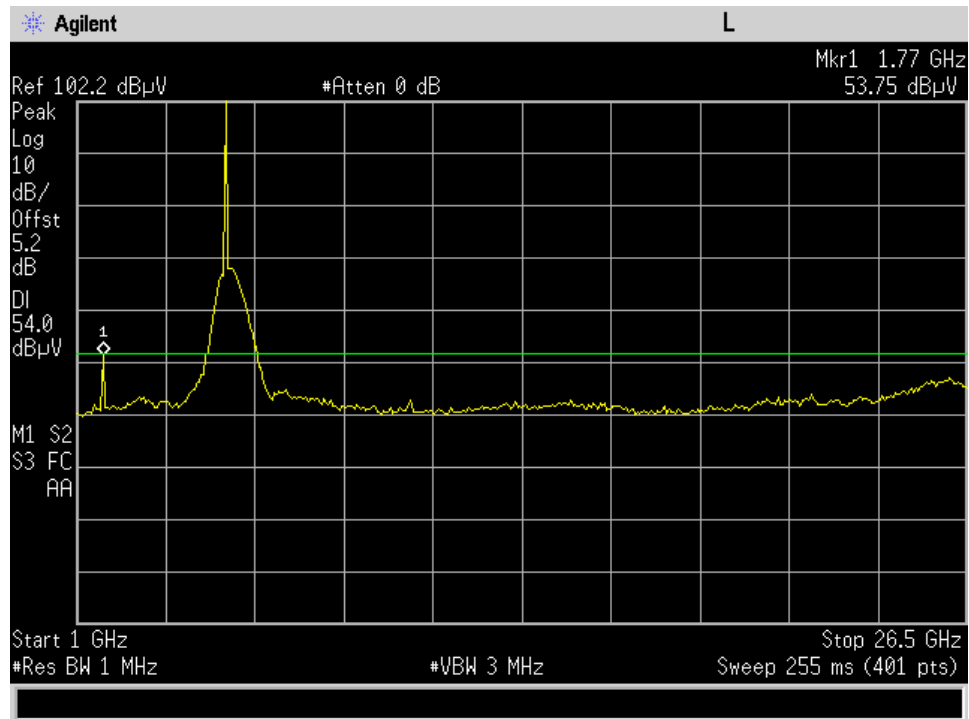


20MHz High Ch 30 - 1000MHz Port J7

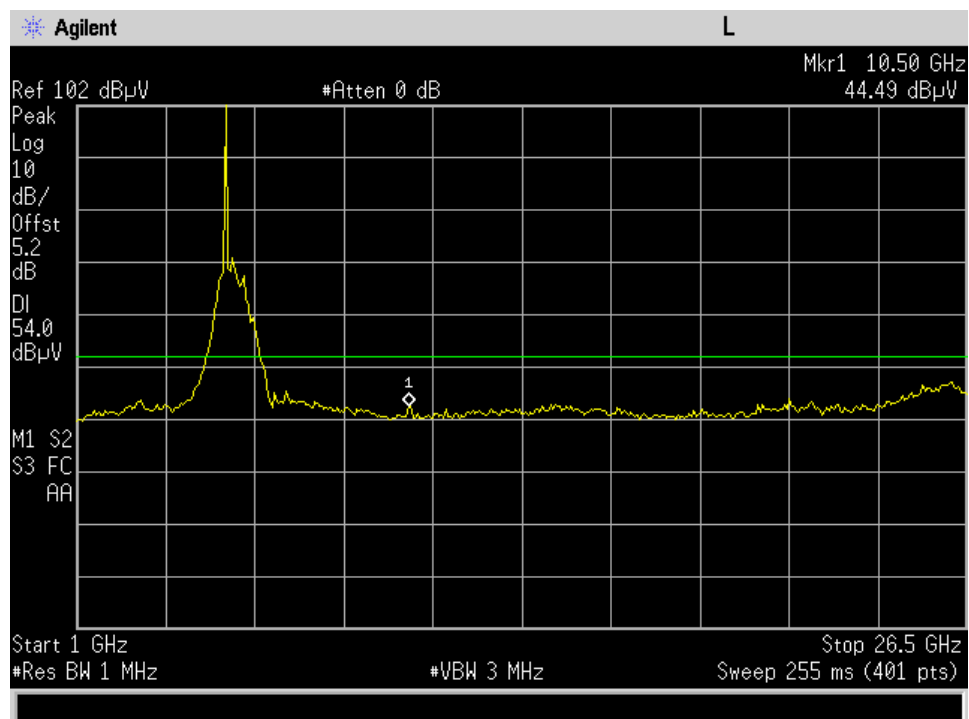




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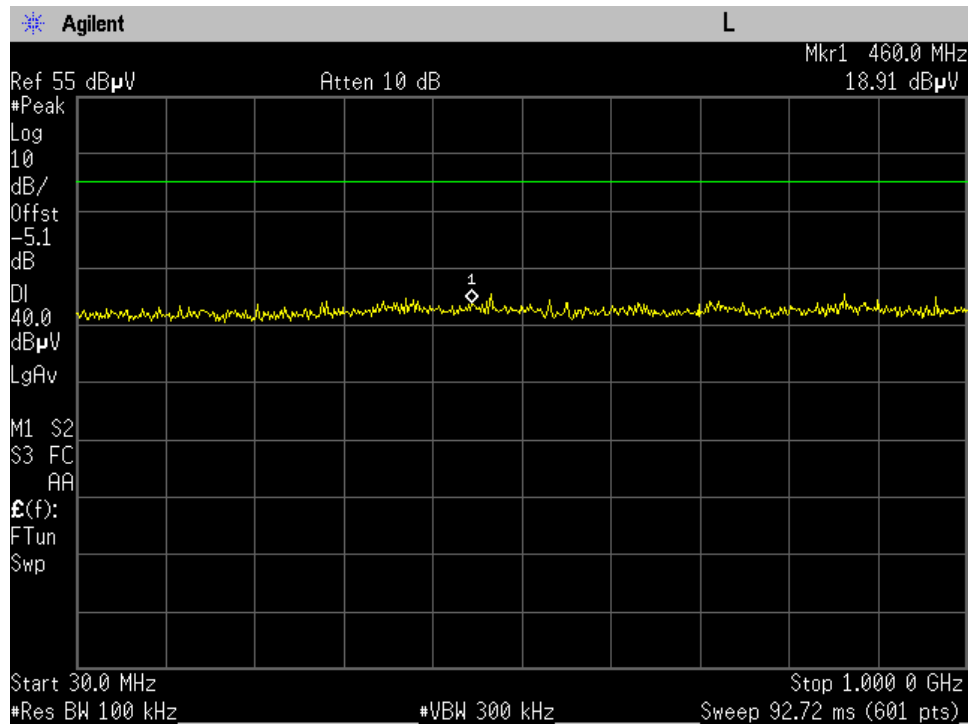


20MHz Low Ch 1-26.5GHz Port J7

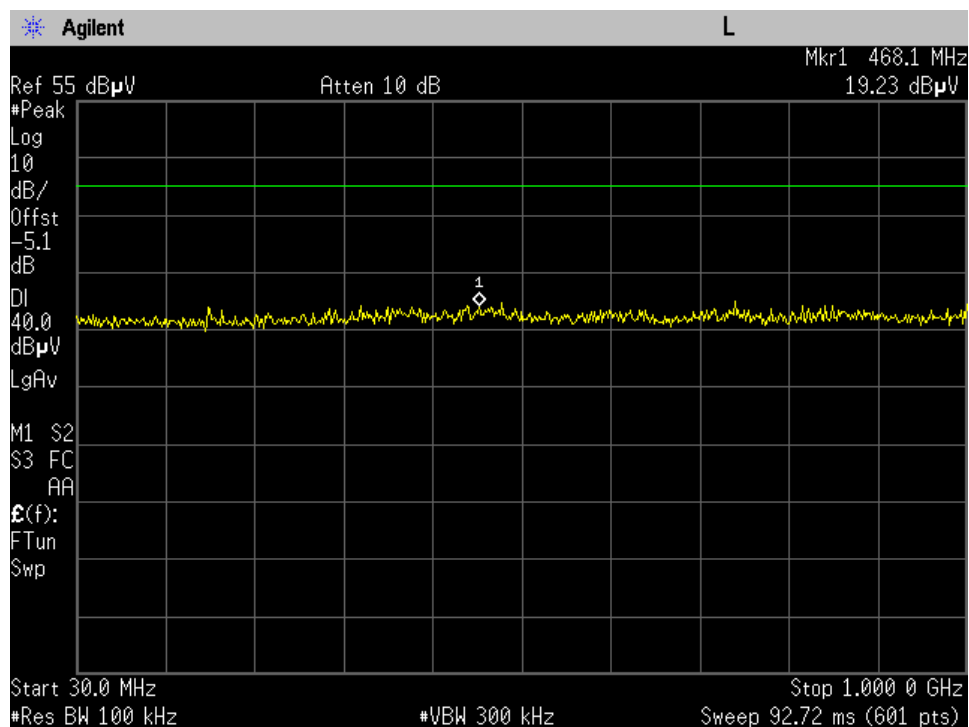




20MHz Low Ch 30 - 1000MHz Port J4

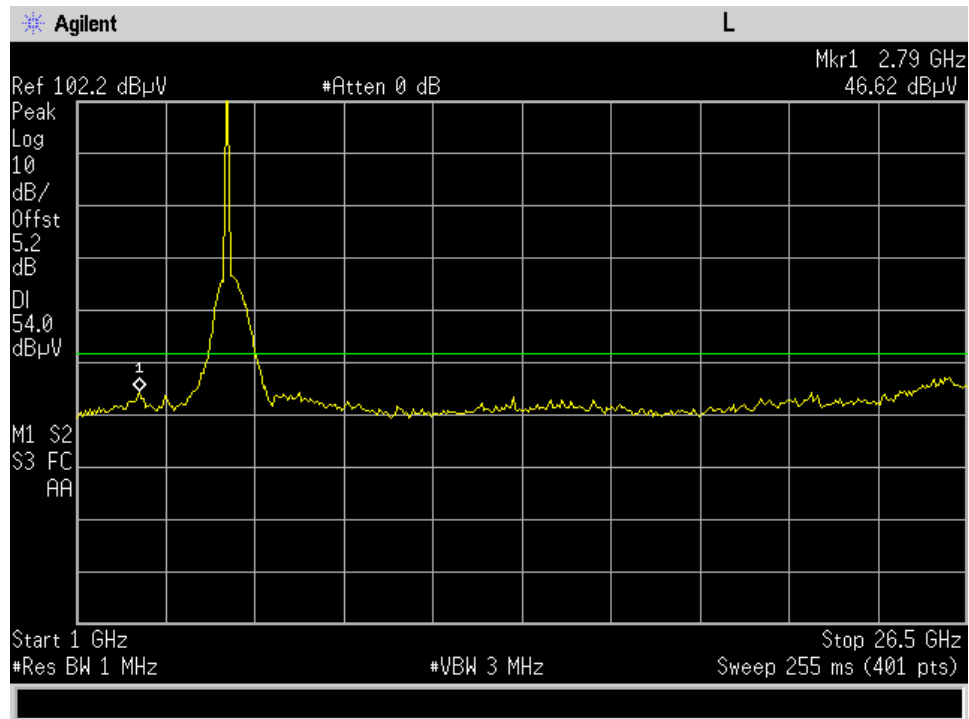


20MHz Low Ch 30 - 1000MHz Port J7

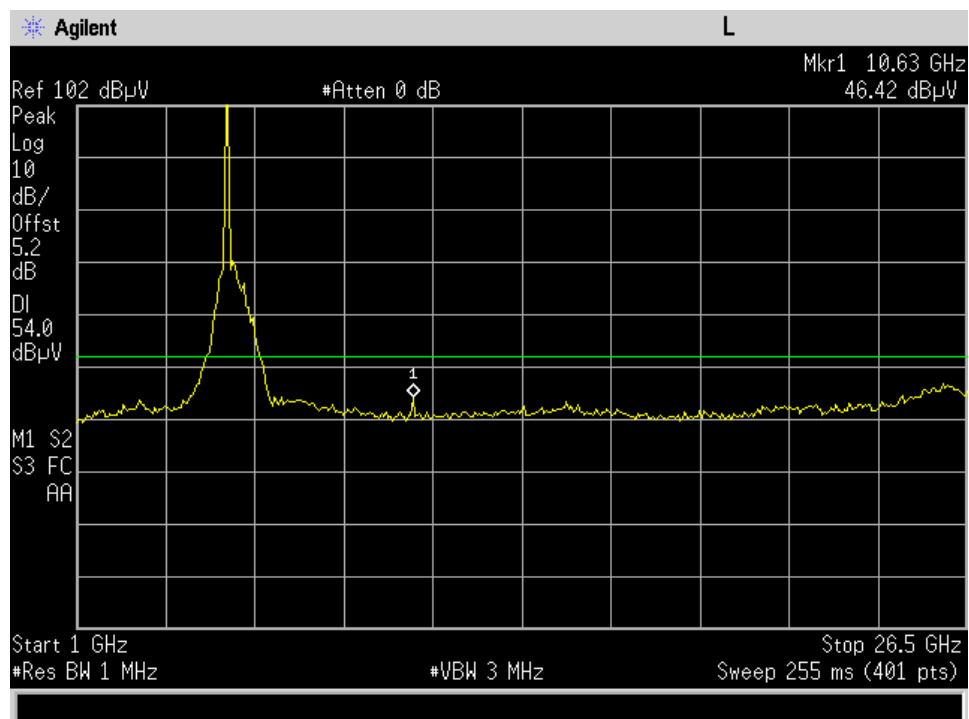




20MHz Mid Ch 1-26.5GHz Port J4

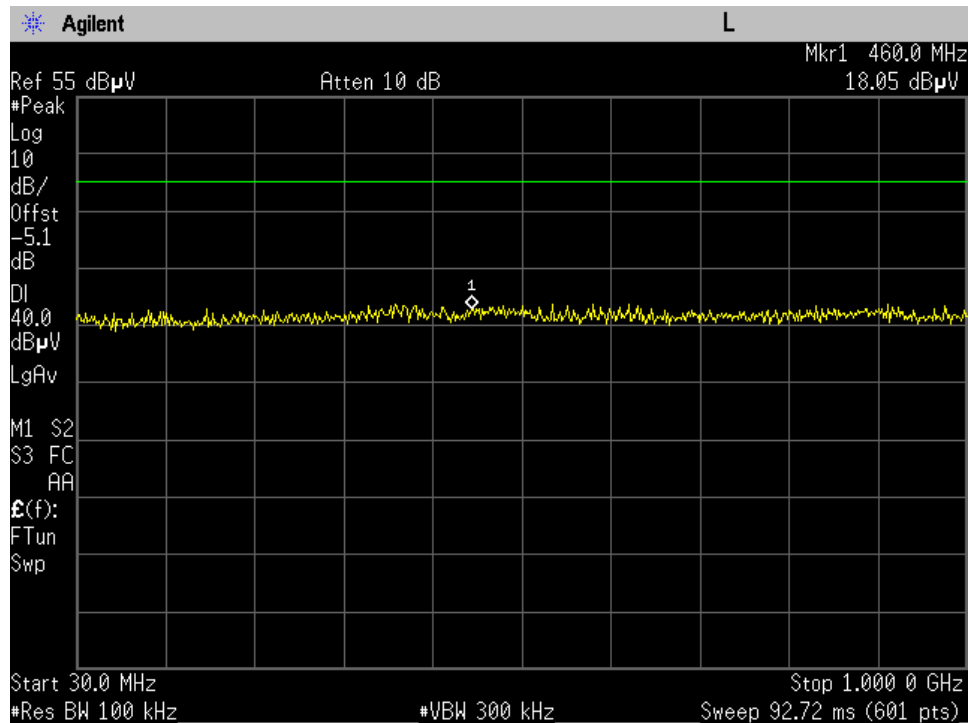


20MHz Mid Ch 1-26.5GHz Port J7

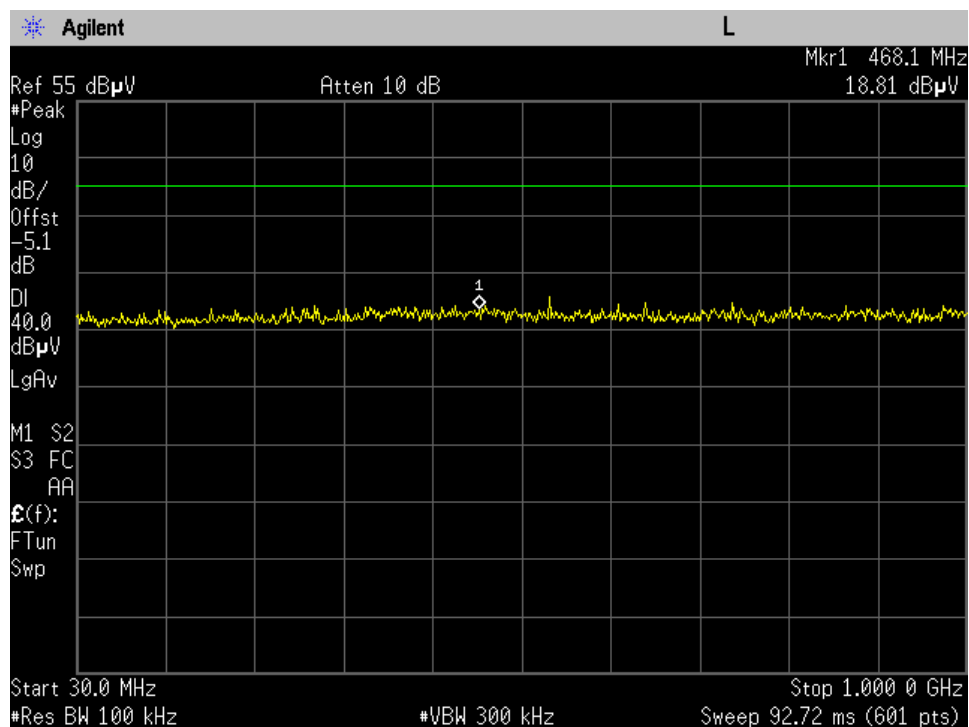




20MHz Mid Ch 30 - 1000MHz Port J4

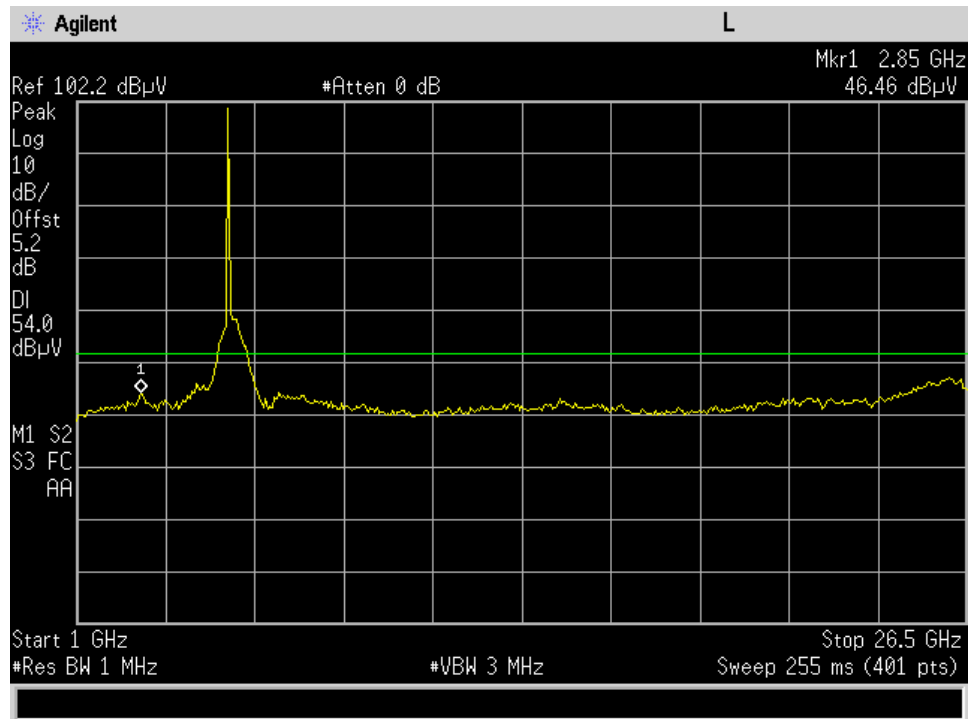


20MHz Mid Ch 30 - 1000MHz Port J7

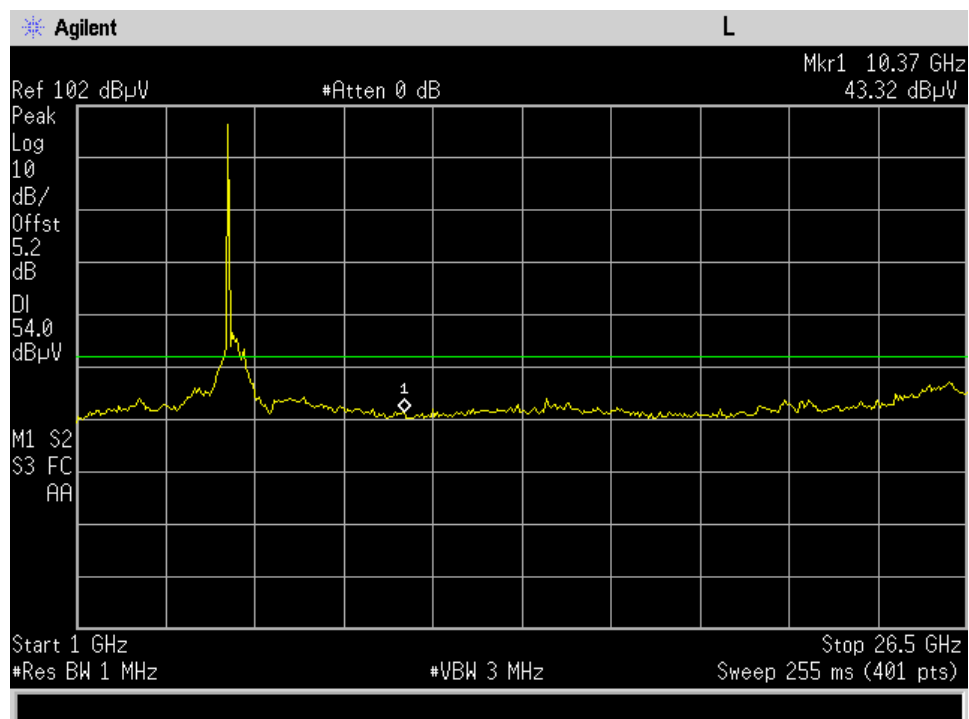




30MHz High Ch 1-26.5GHz Port J4

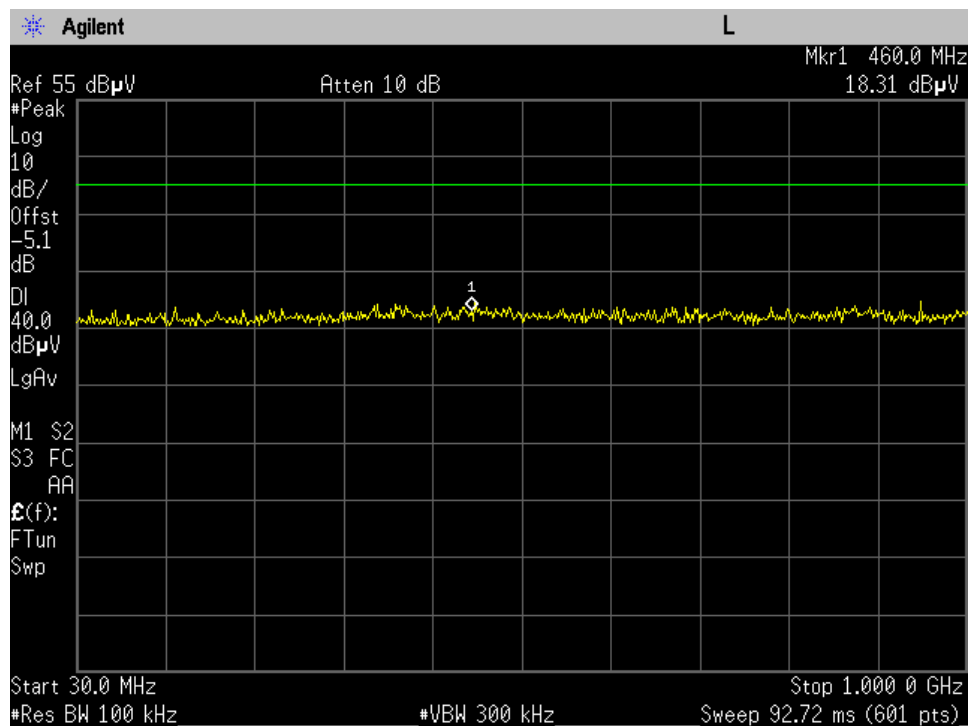


30MHz High Ch 1-26.5GHz Port J7

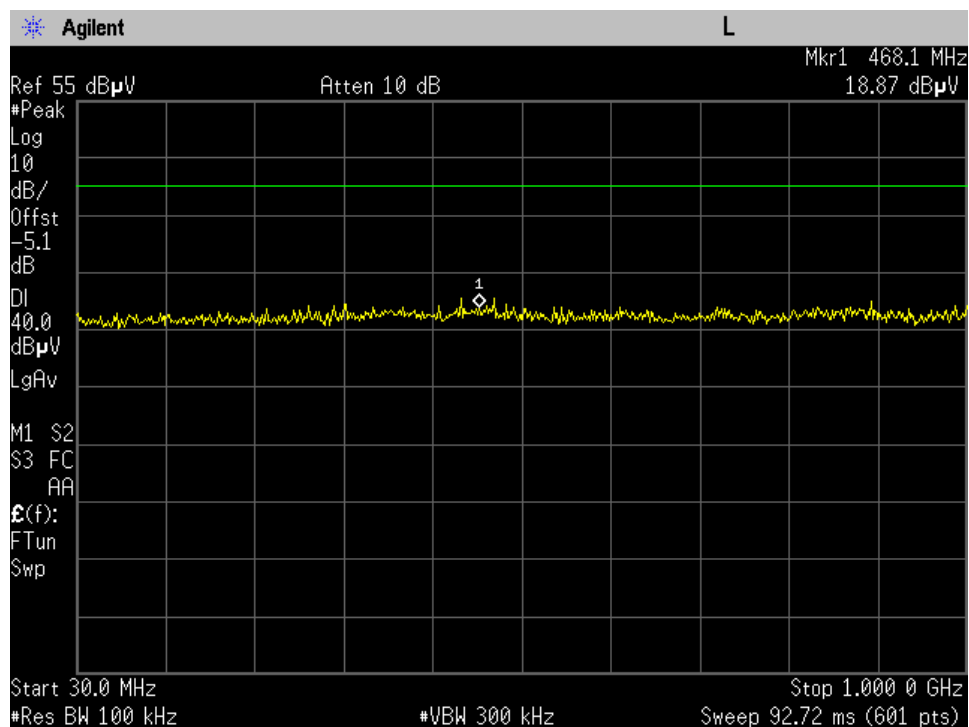




30MHz High Ch 30 - 1000MHz Port J4

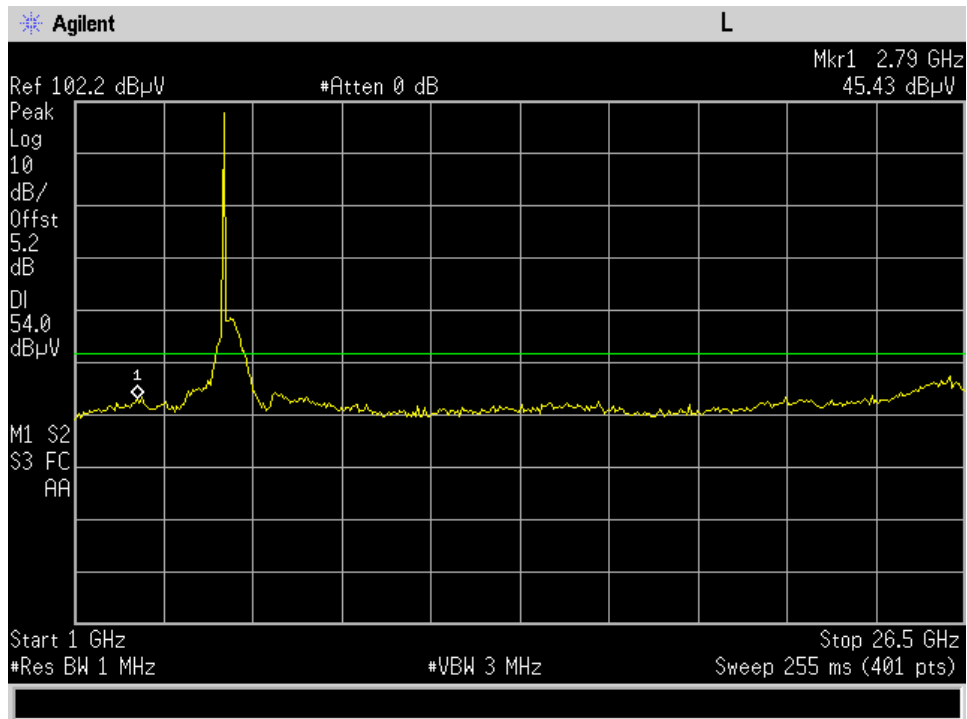


30MHz High Ch 30 - 1000MHz Port J7

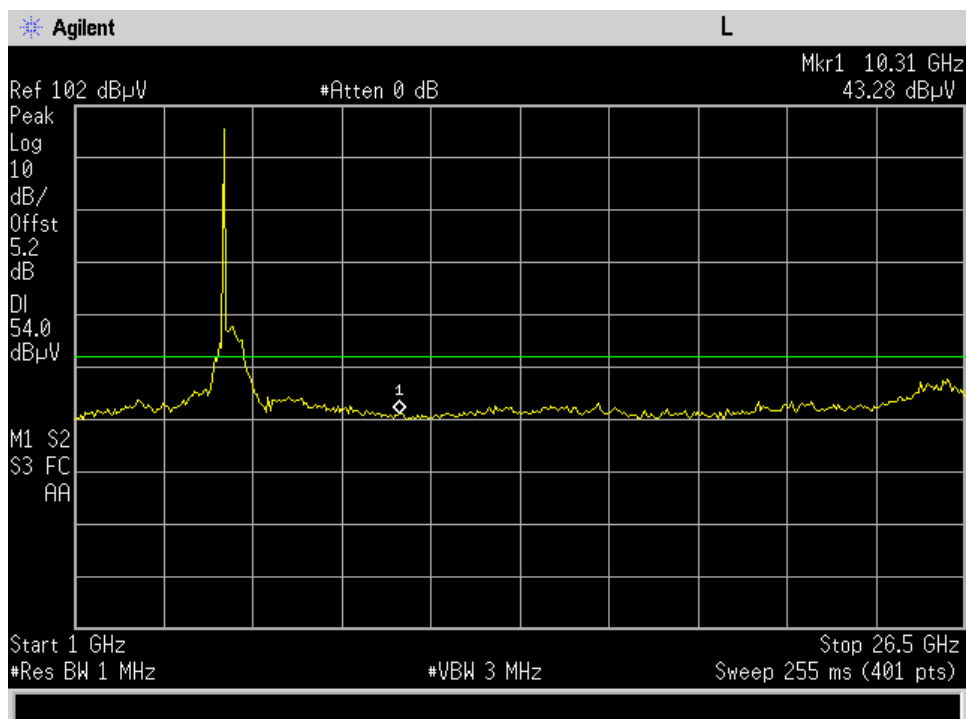




30MHz Low Ch 1-26.5GHz Port J4

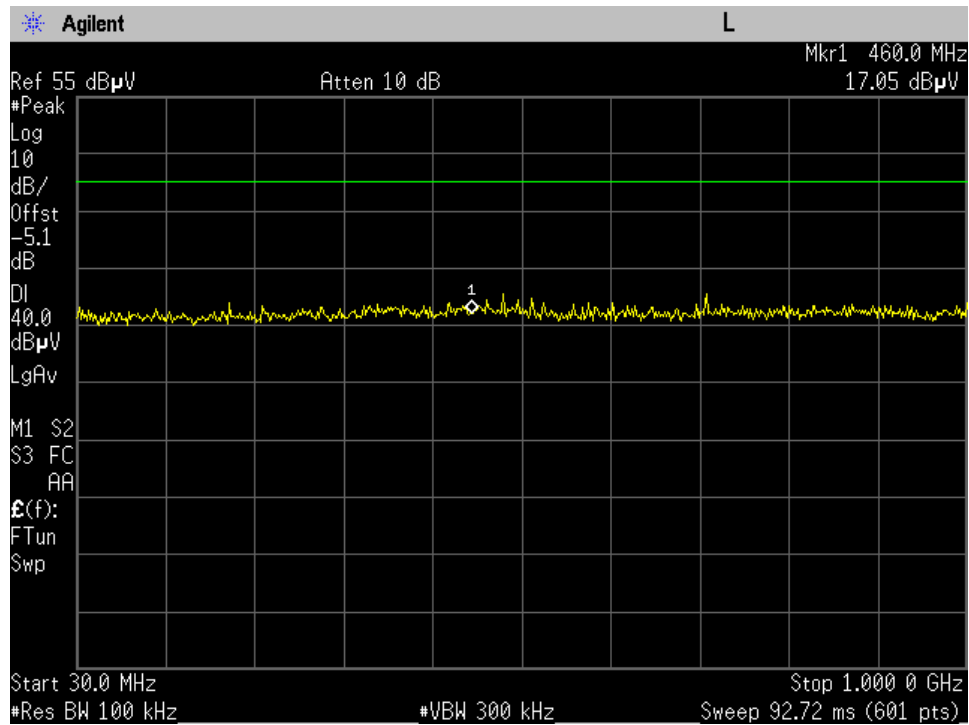


30MHz Low Ch 1-26.5GHz Port J7

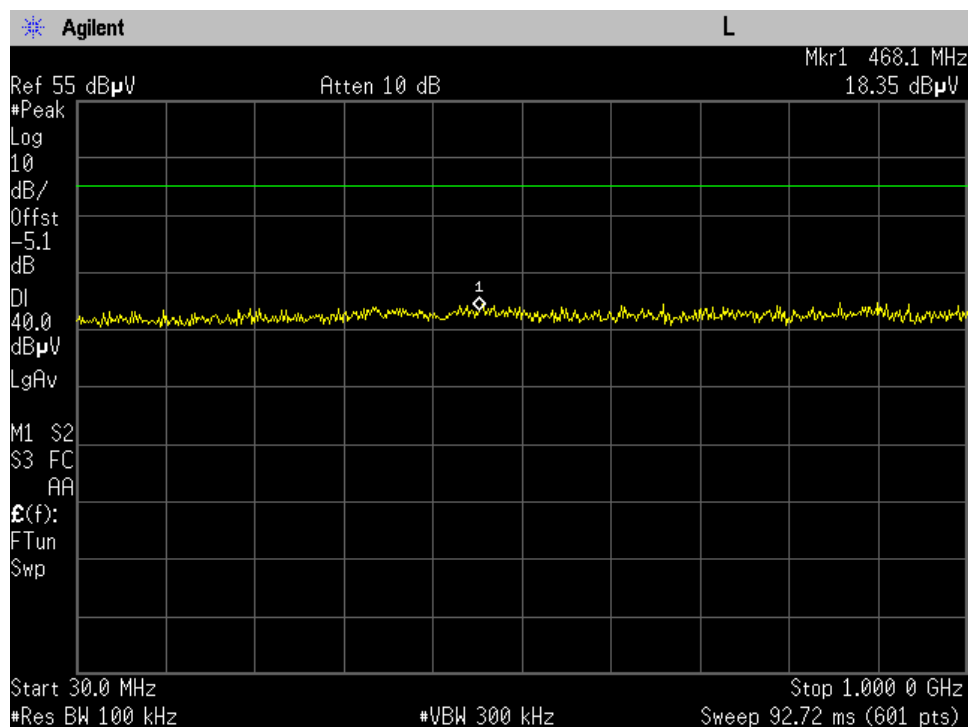




30MHz Low Ch 30 - 1000MHz Port J4

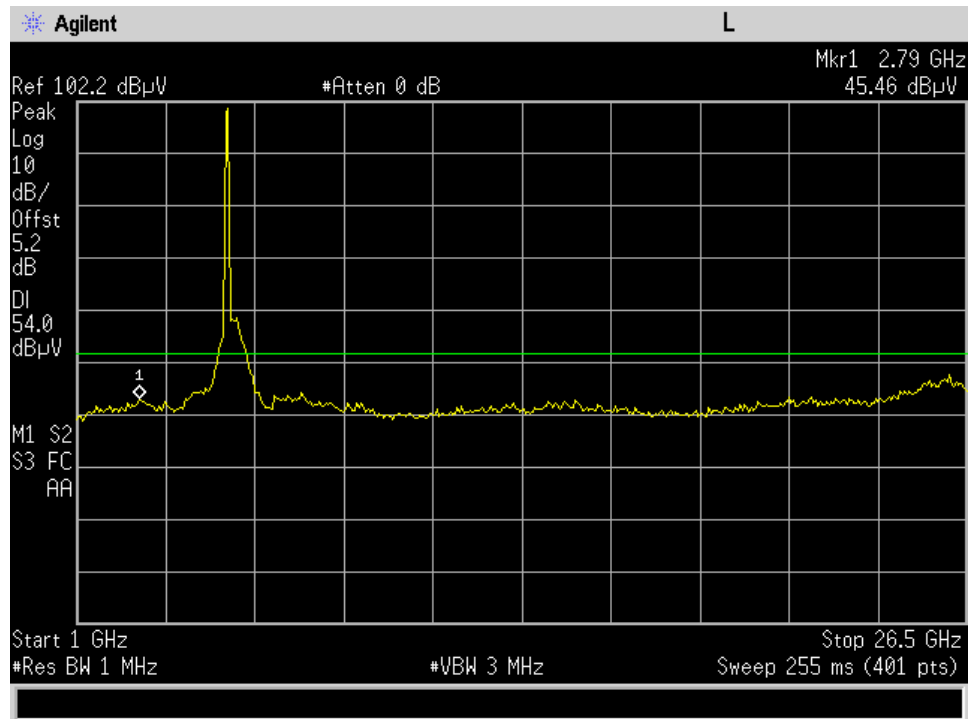


30MHz Low Ch 30 - 1000MHz Port J7

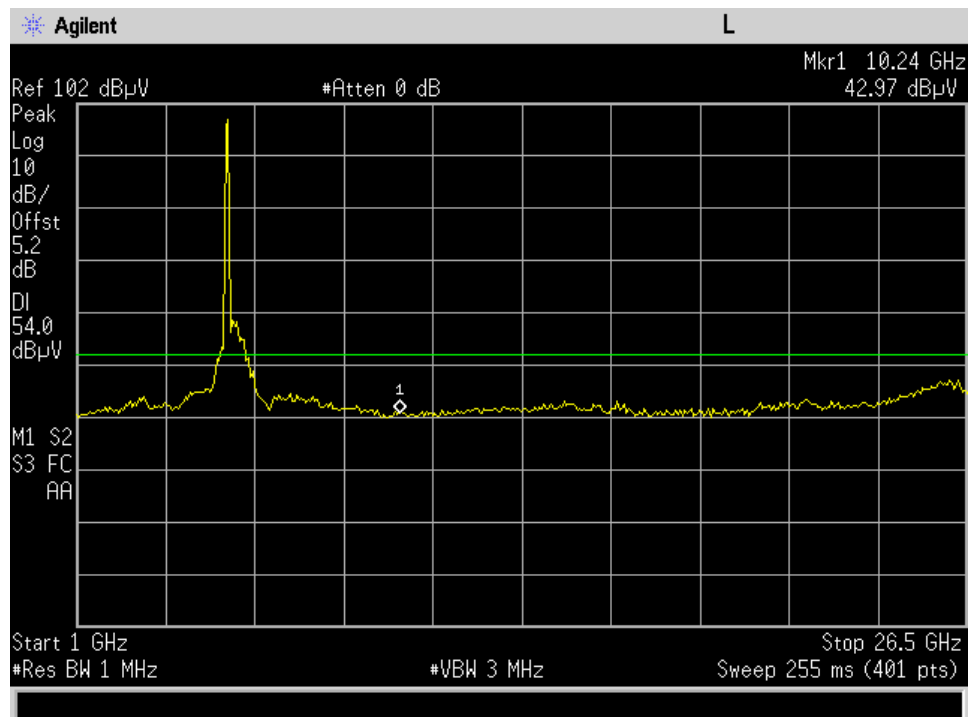




30MHz Mid Ch 1-26.5GHz Port J4

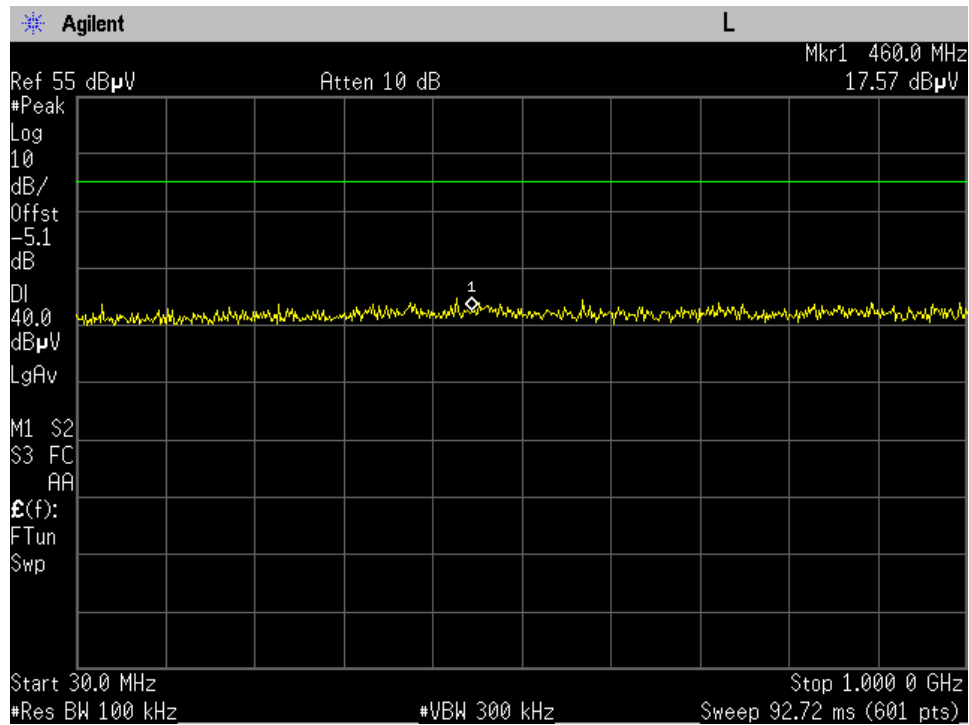


30MHz Mid Ch 1-26.5GHz Port J7

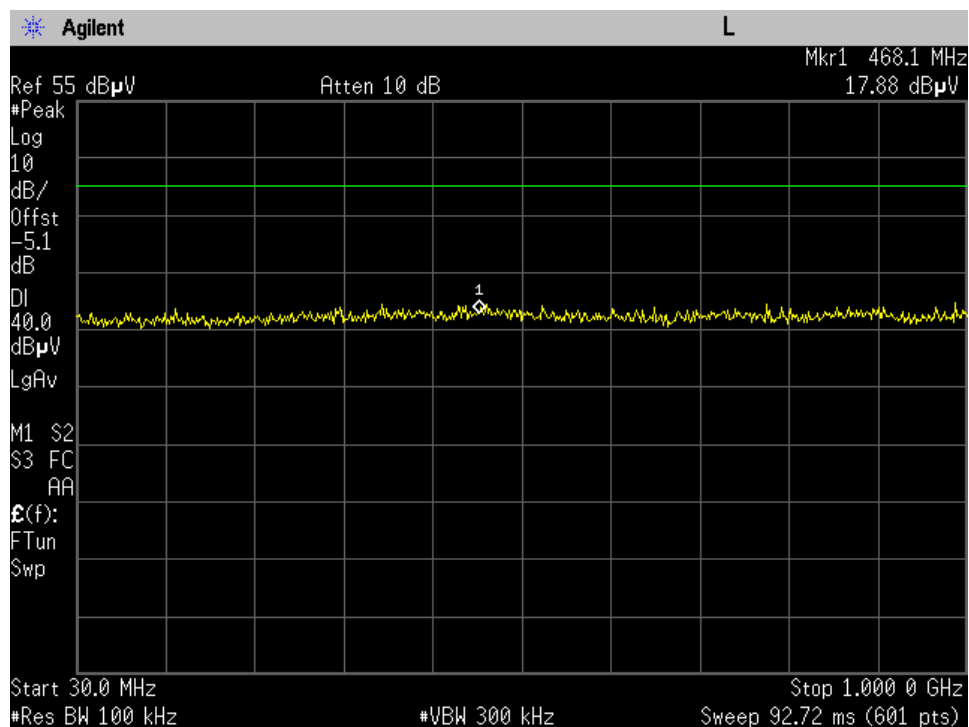




30MHz Mid Ch 30 - 1000MHz Port J4

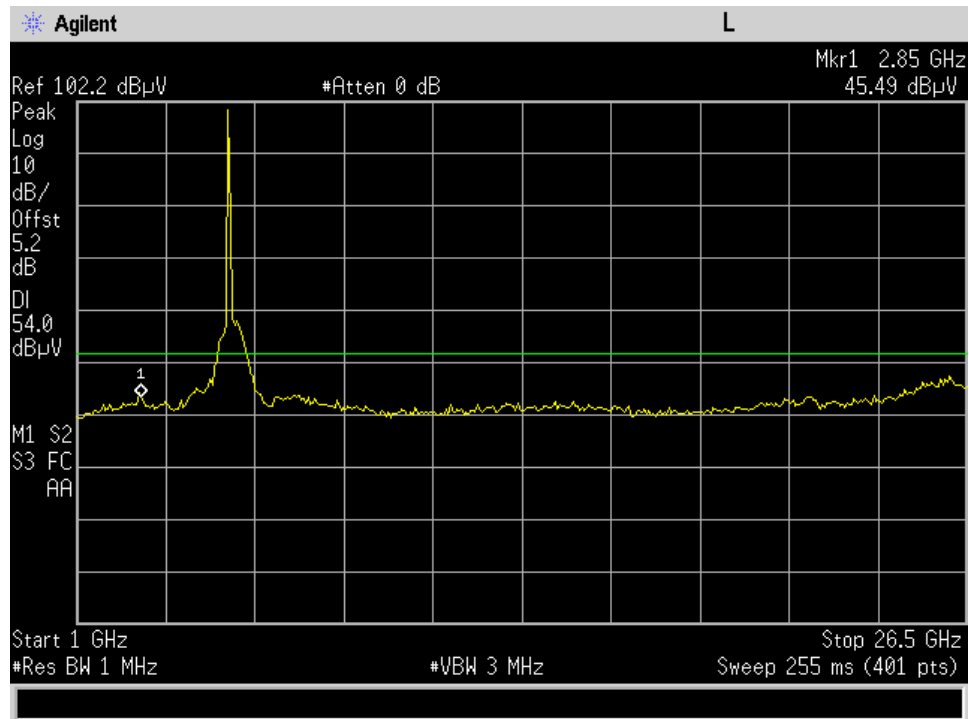


30MHz Mid Ch 30 - 1000MHz Port J7

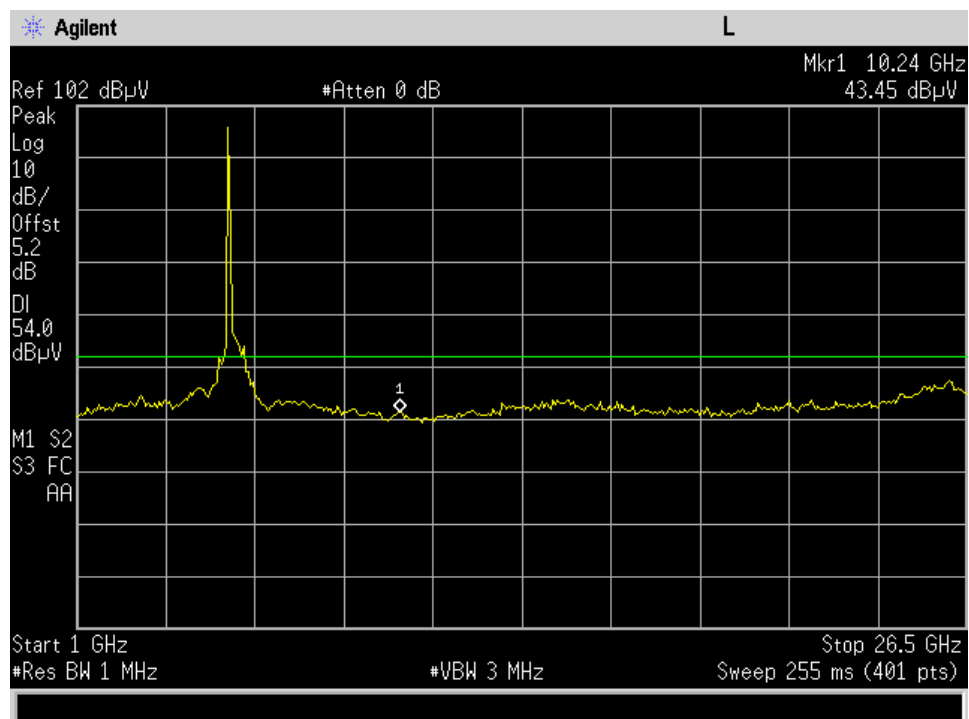




40MHz High Ch 1-26.5GHz Port J4

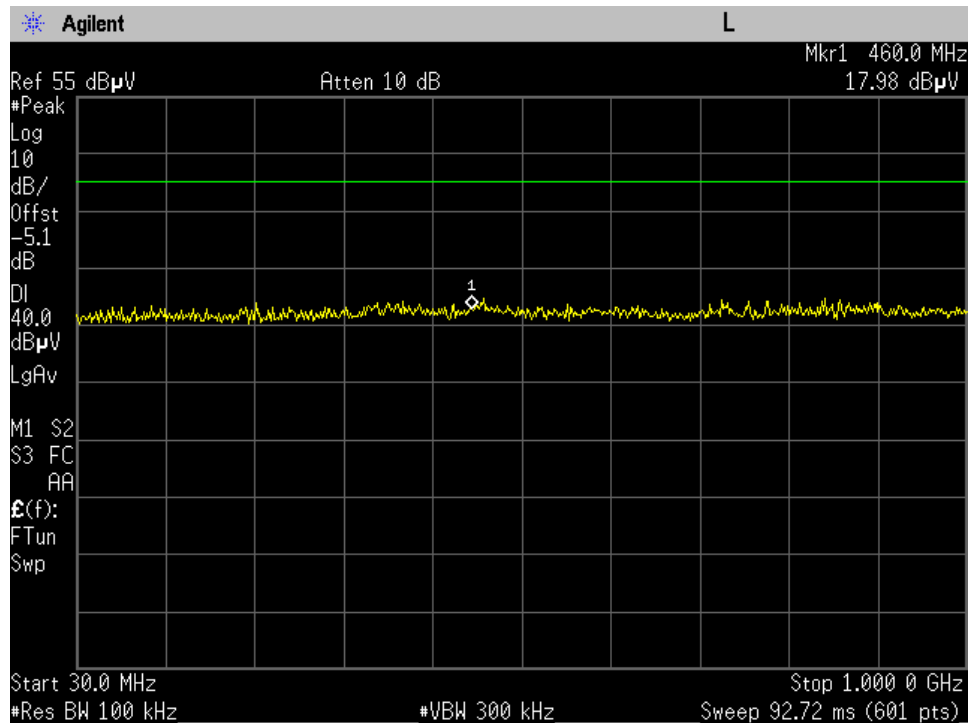


40MHz High Ch 1-26.5GHz Port J7

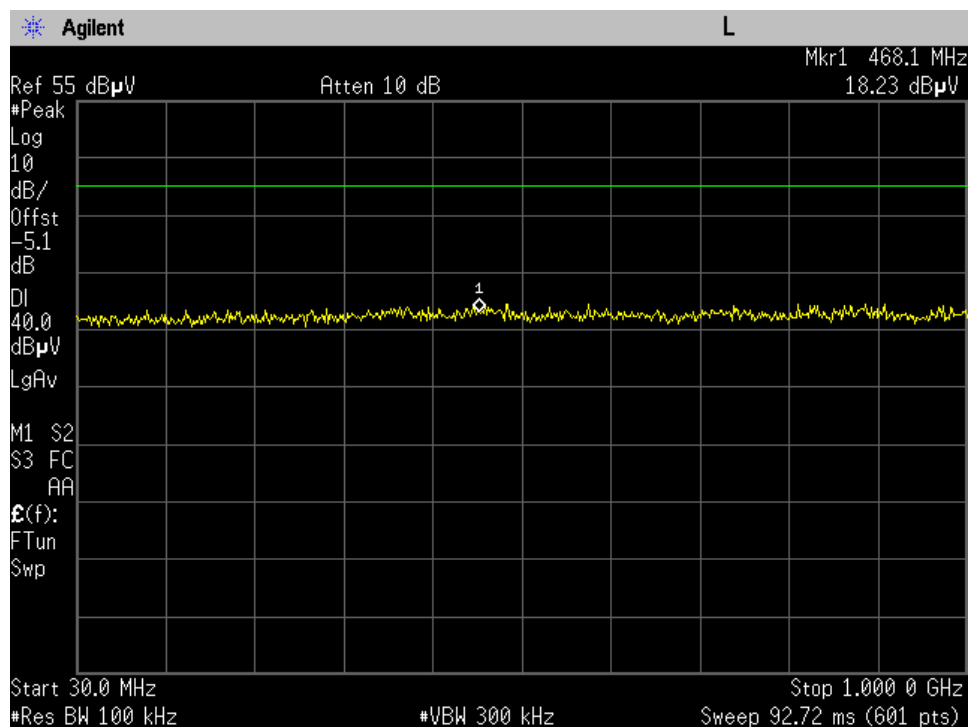




40MHz High Ch 30 - 1000MHz Port J4

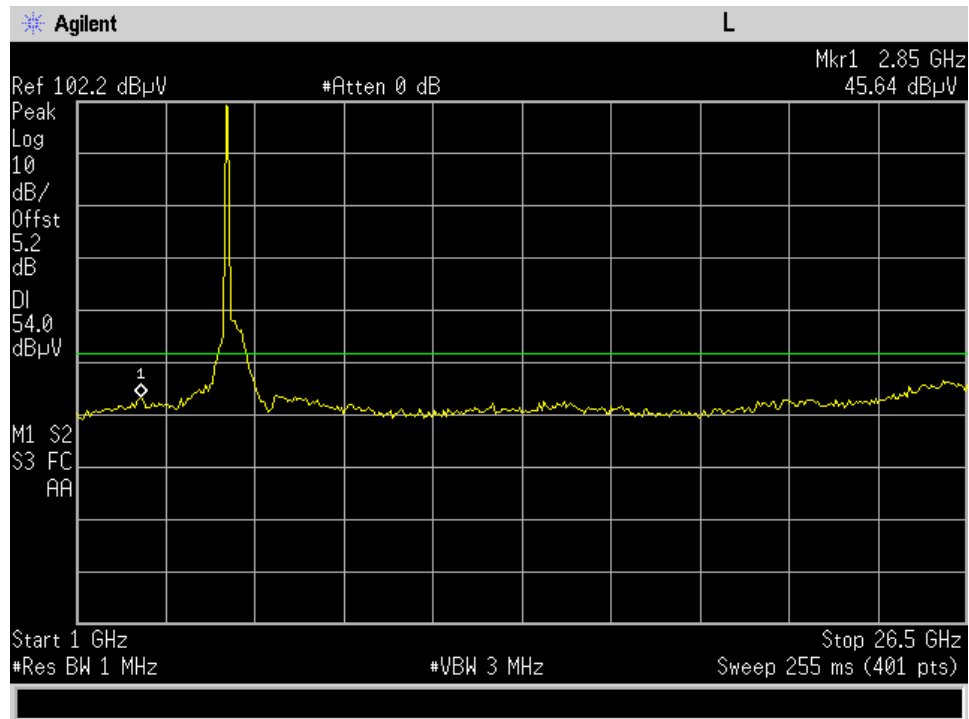


40MHz High Ch 30 - 1000MHz Port J7

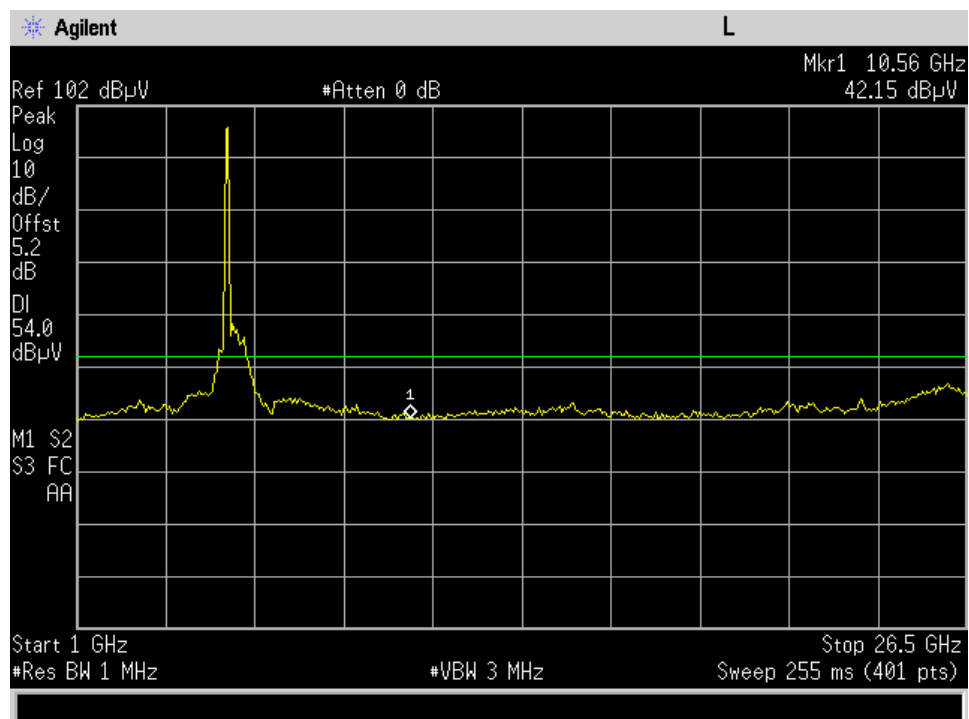




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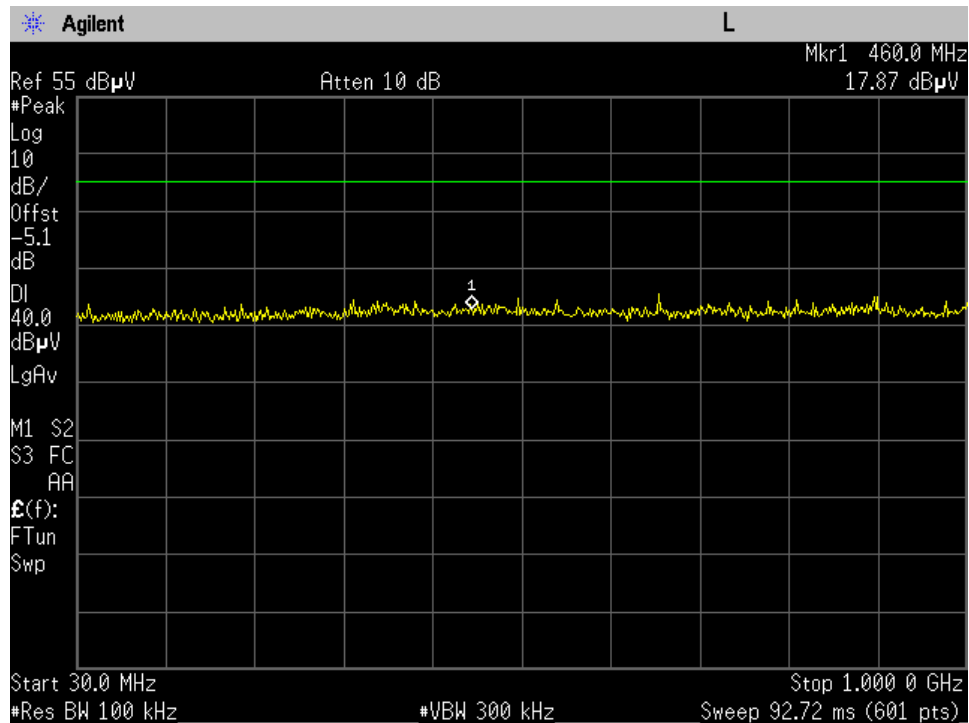


40MHz Low Ch 1-26.5GHz Port J7

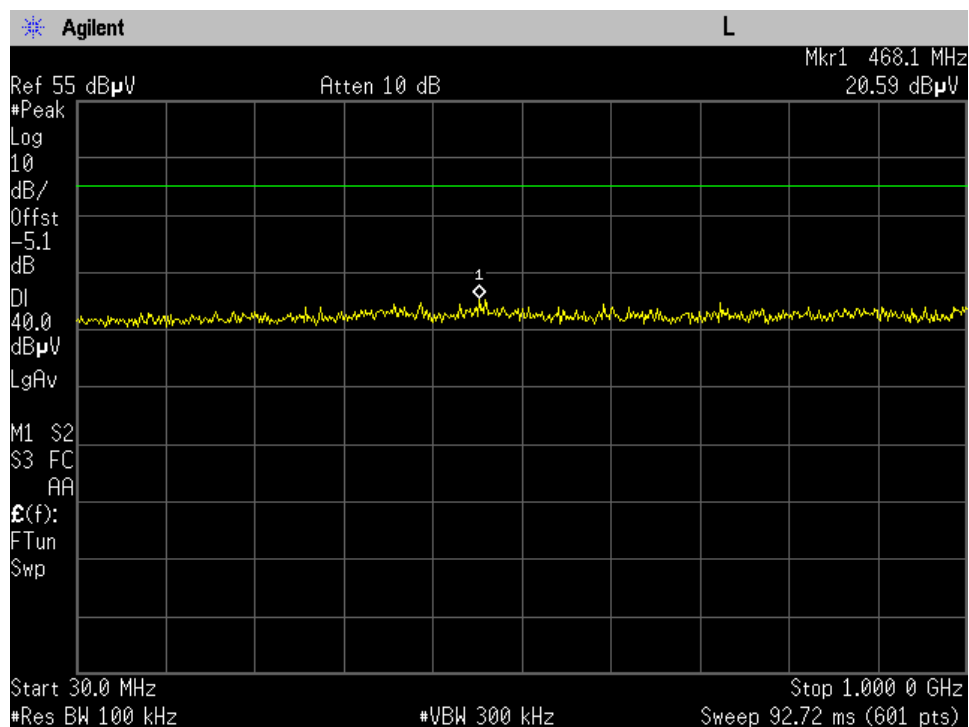




40MHz Low Ch 30 - 1000MHz Port J4

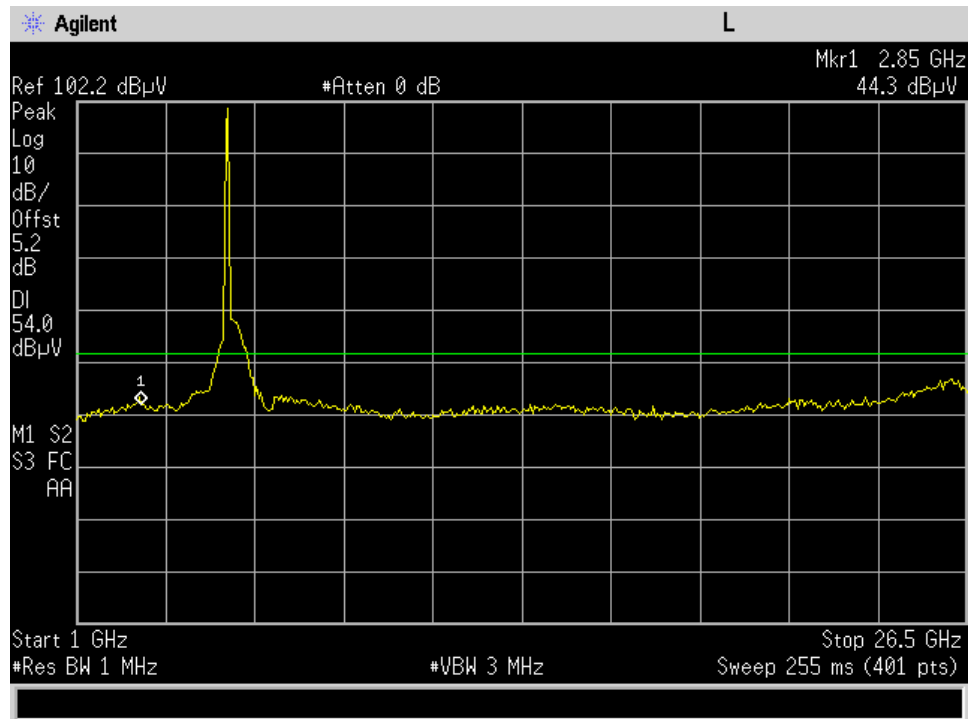


40MHz Low Ch 30 - 1000MHz Port J7

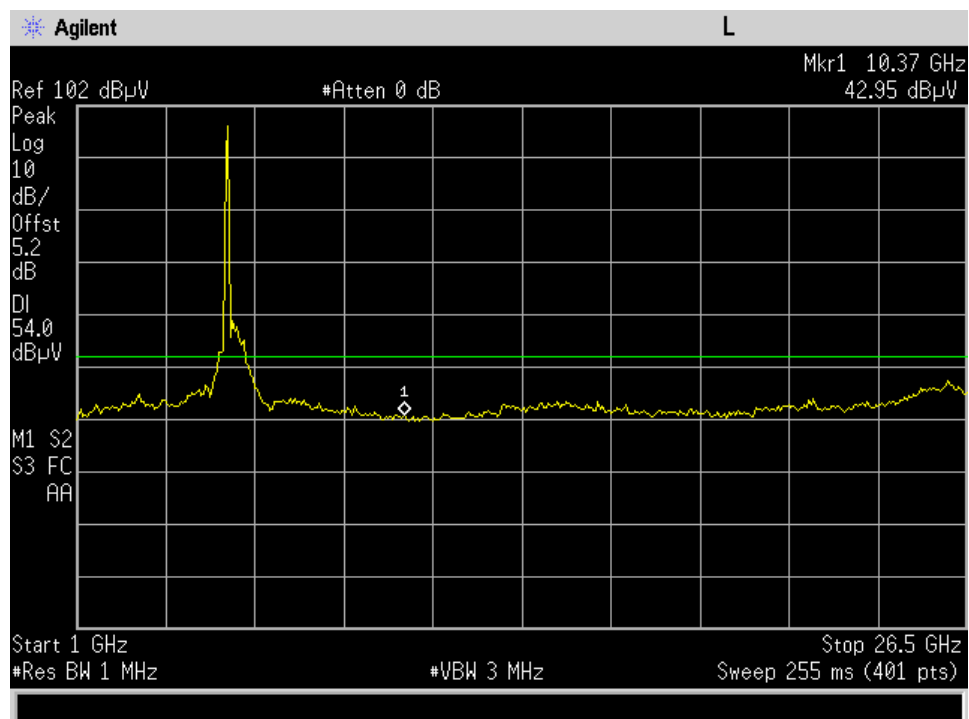




40MHz Mid Ch 1-26.5GHz Port J4

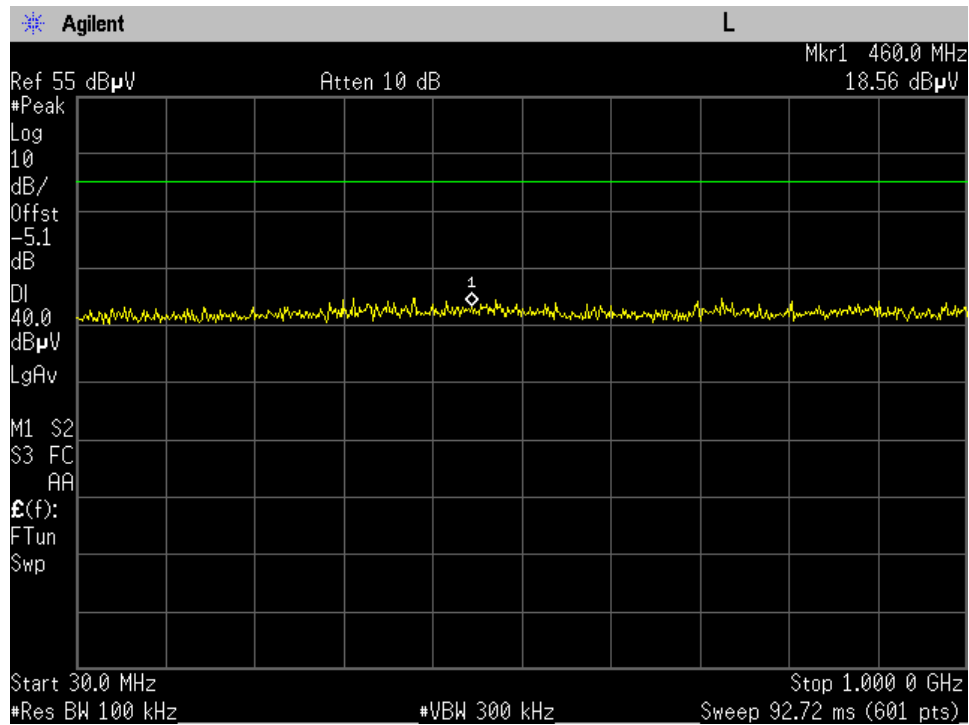


40MHz Mid Ch 1-26.5GHz Port J7

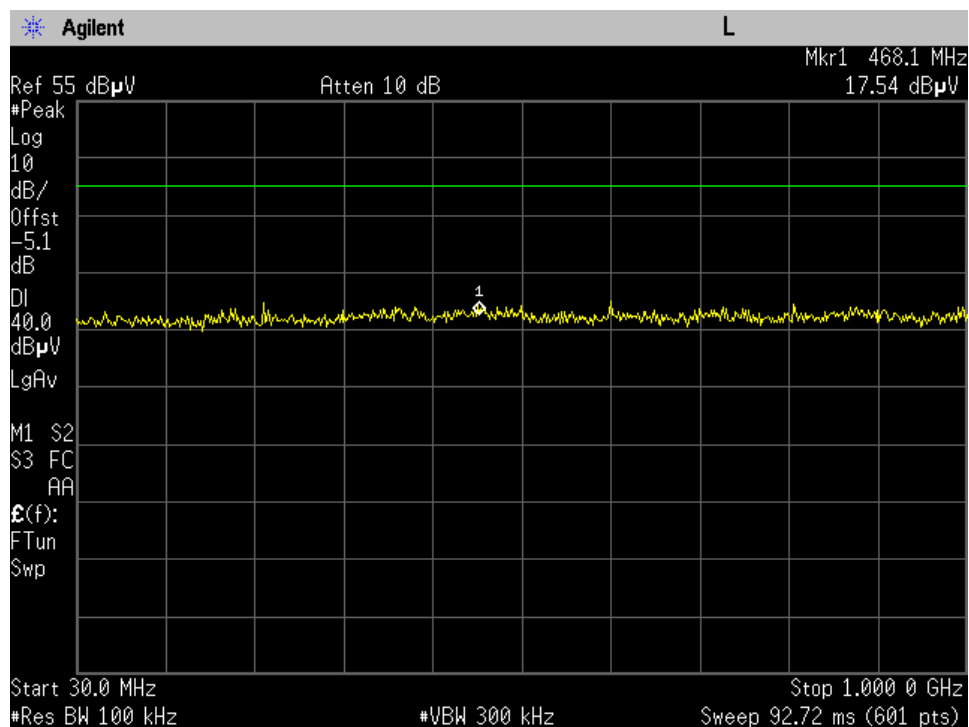




40MHz Mid Ch 30 - 1000MHz Port J4

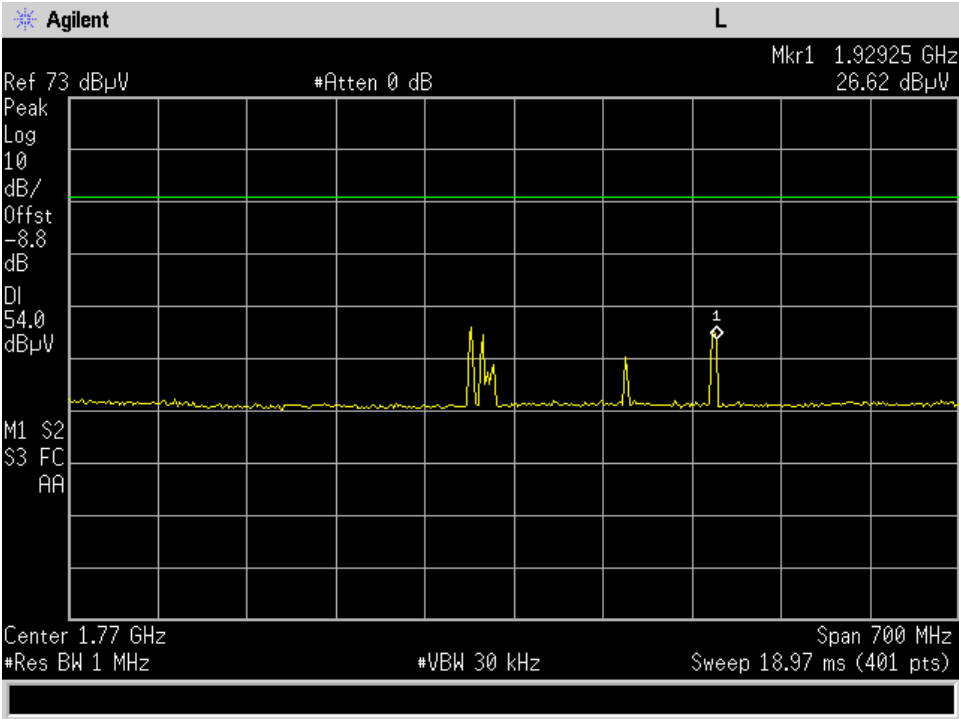


40MHz Mid Ch 30 - 1000MHz Port J7





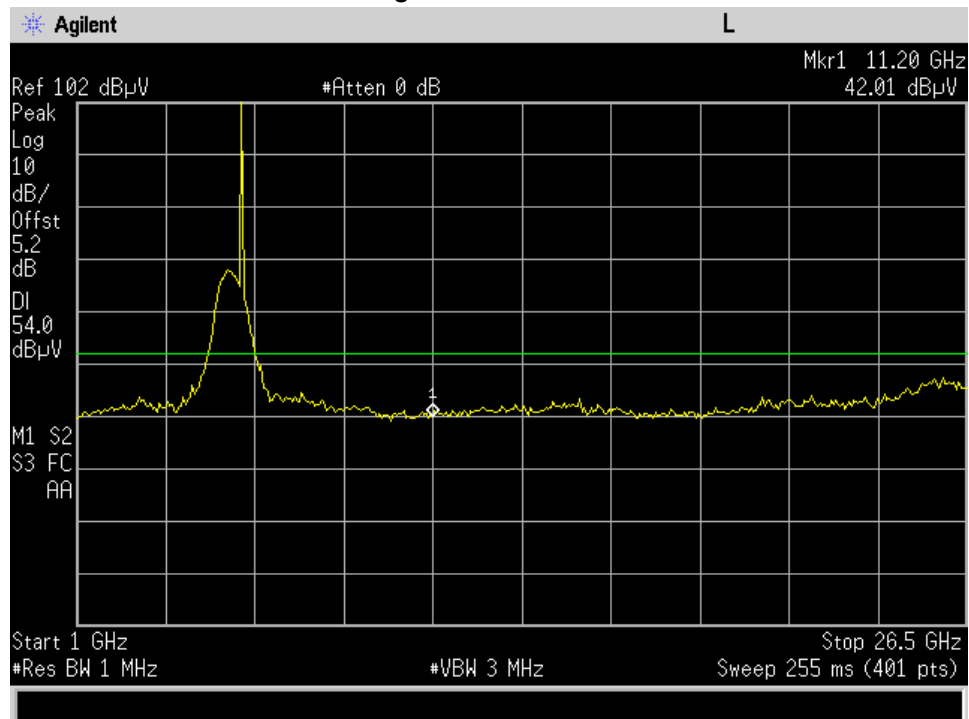
Spur Low Ch 1.93GHz Port J4



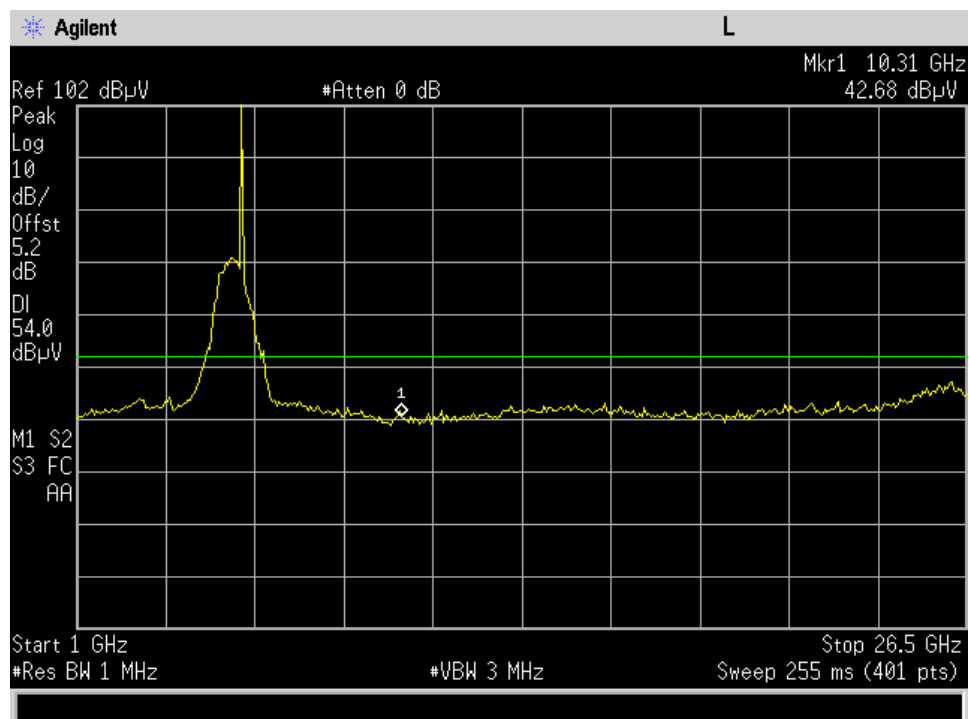


UNII-2C

10MHz High Ch 1-26.5GHz Port J4

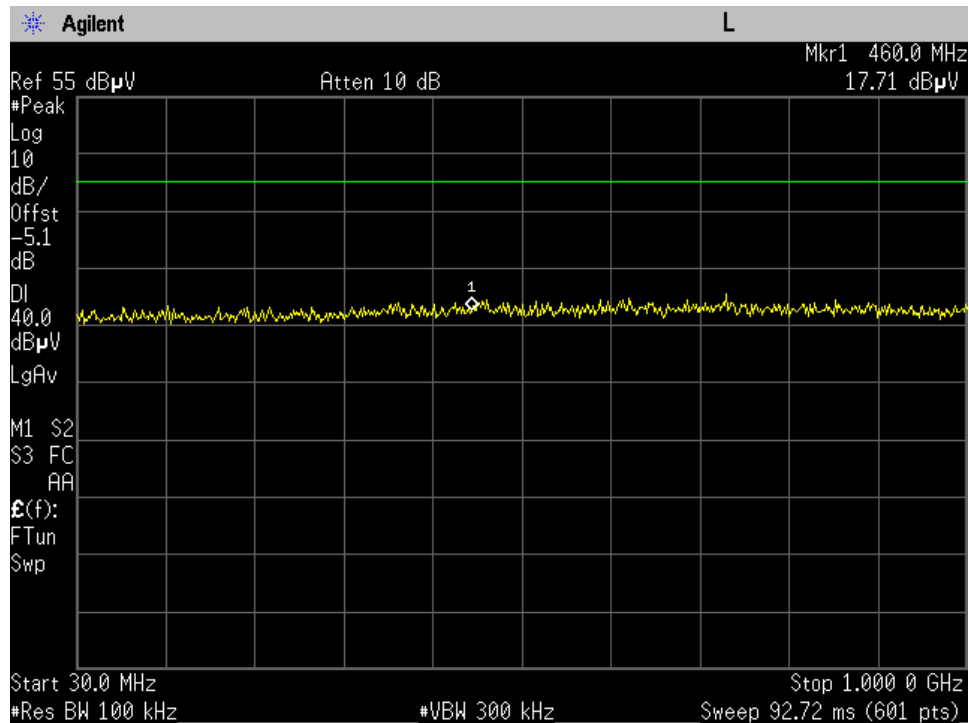


10MHz High Ch 1-26.5GHz Port J7

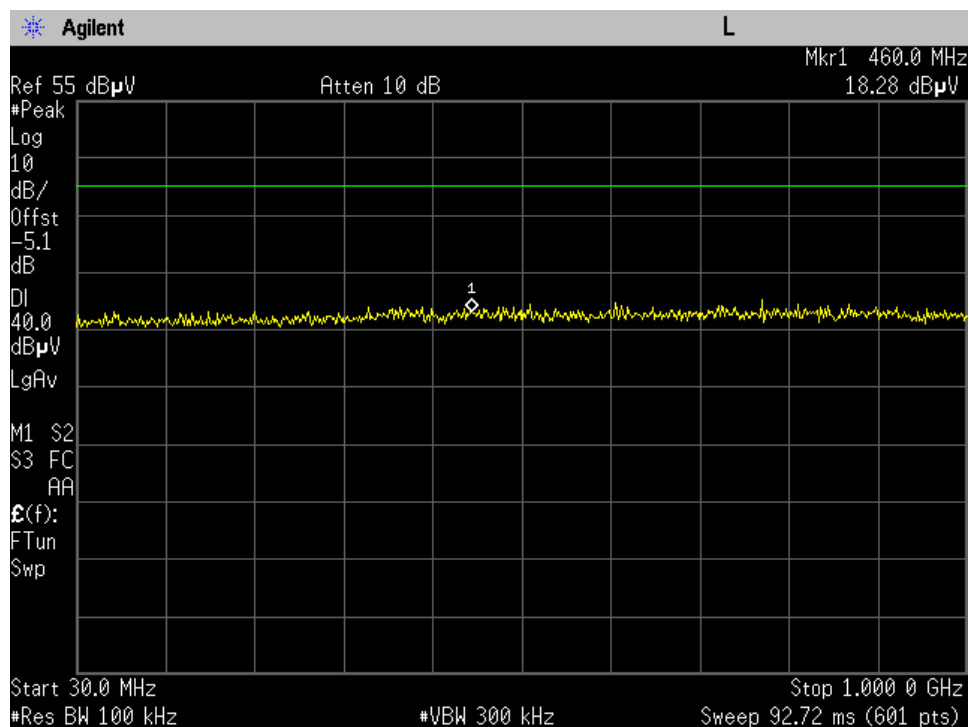




10MHz High Ch 30 - 1000MHz Port J4

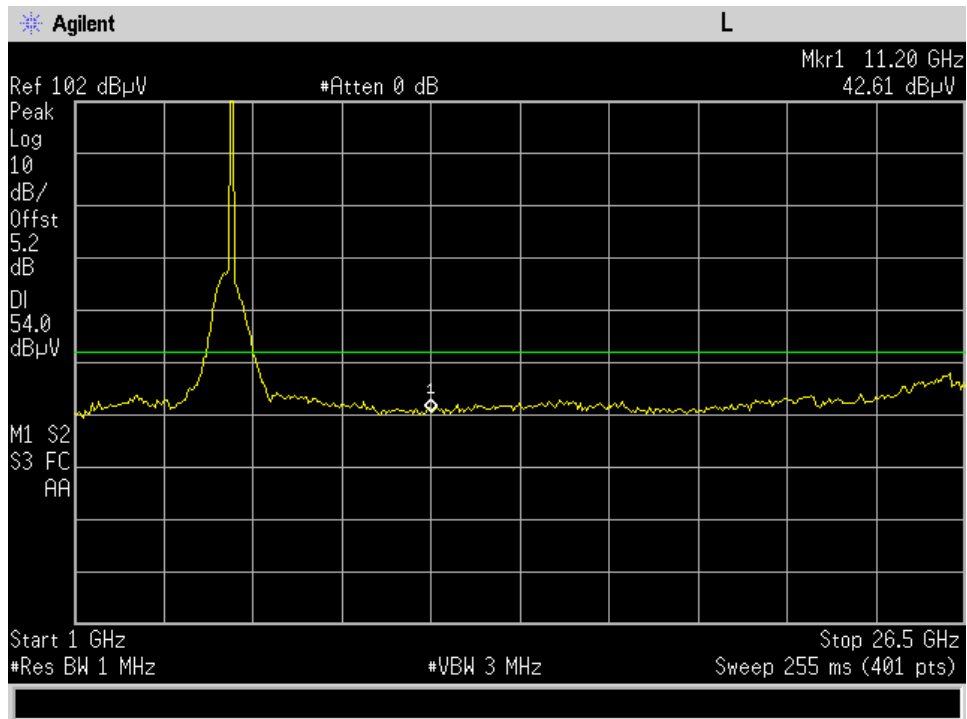


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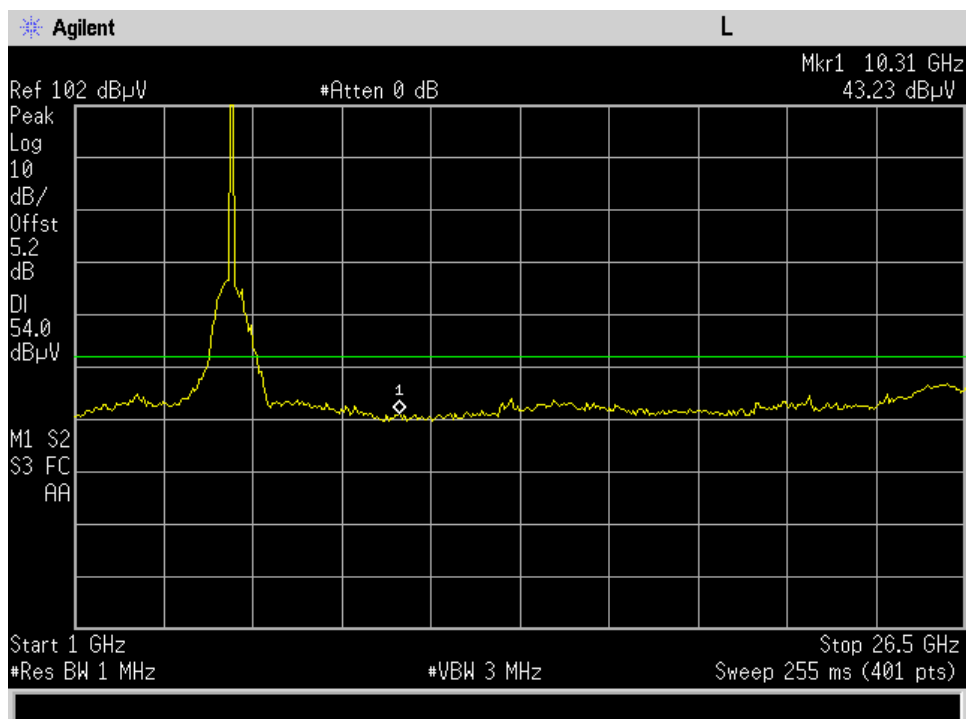




10MHz Low Ch 1-26.5GHz Port J4

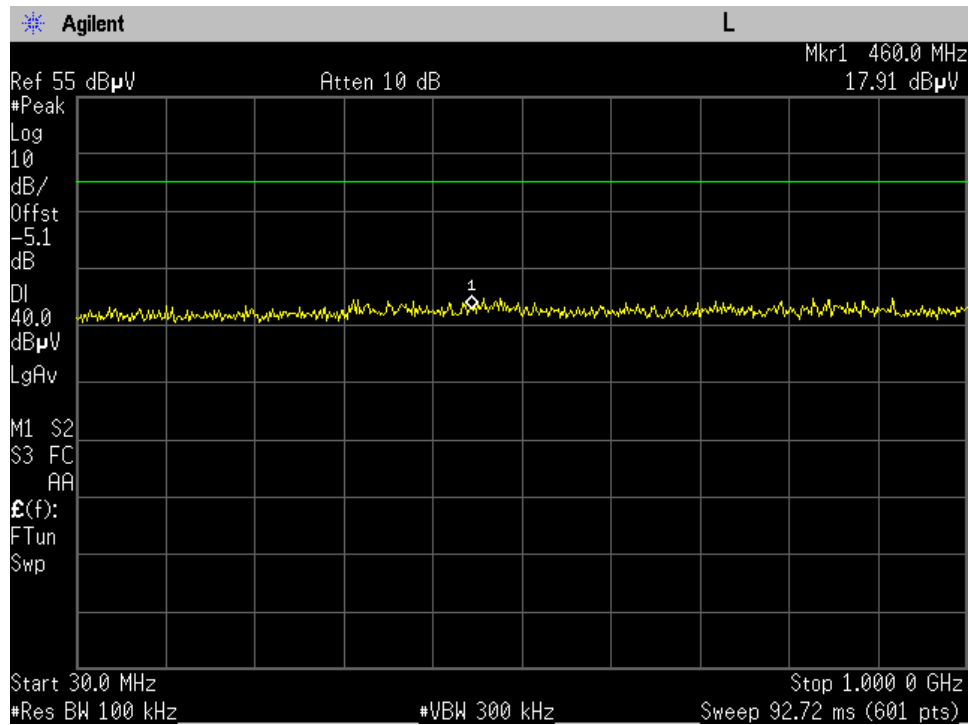


10MHz Low Ch 1-26.5GHz Port J7

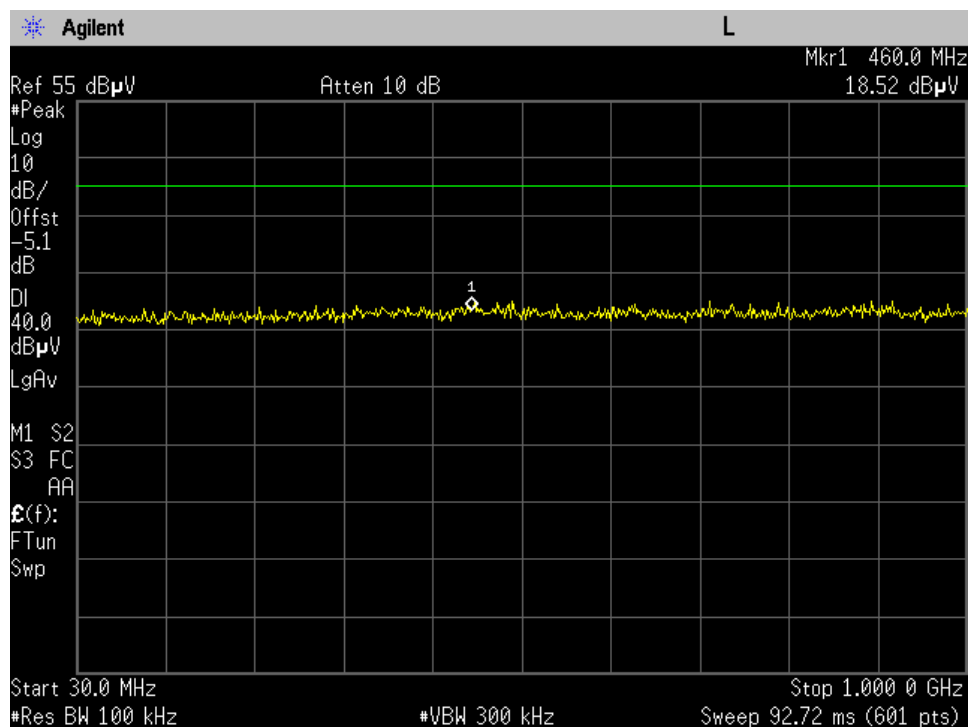




10MHz Low Ch 30 - 1000MHz Port J4

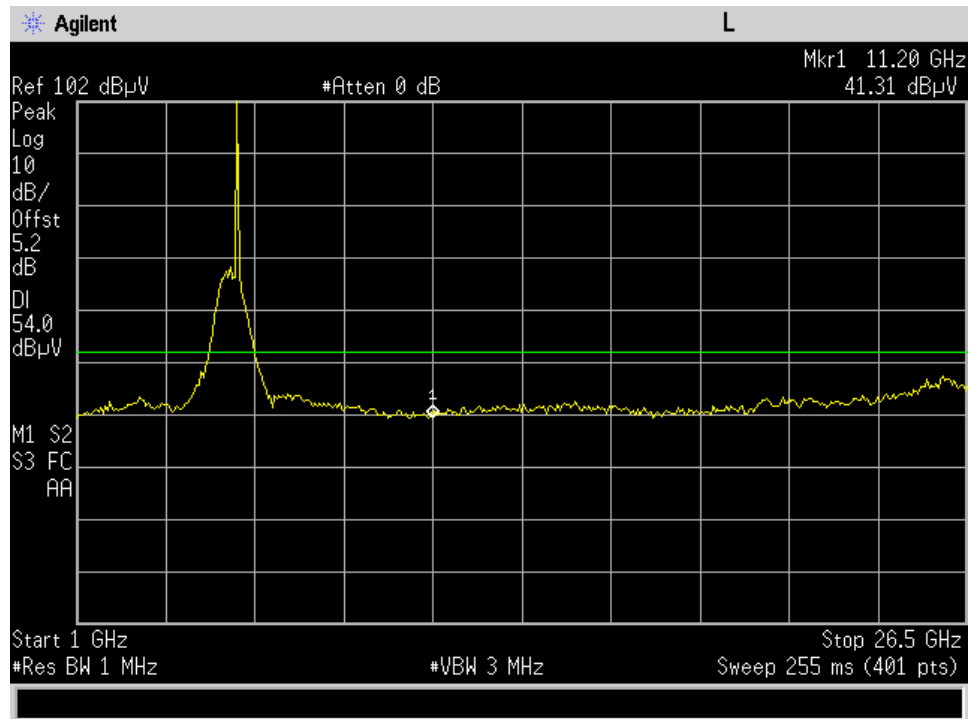


10MHz Low Ch 30 - 1000MHz Port J7

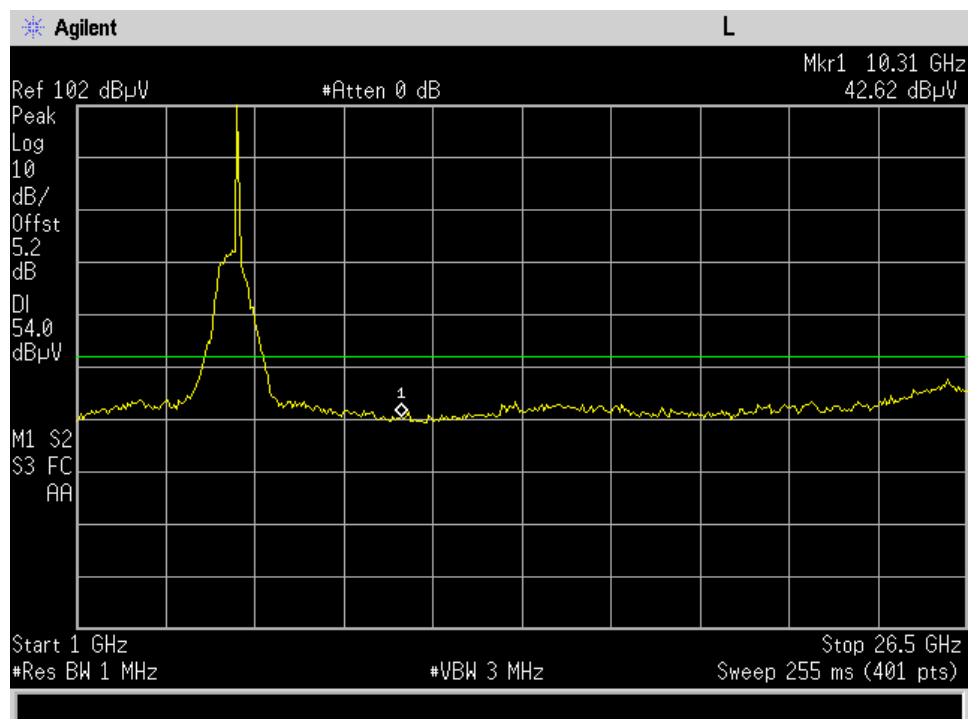




10MHz Mid Ch 1-26.5GHz Port J4

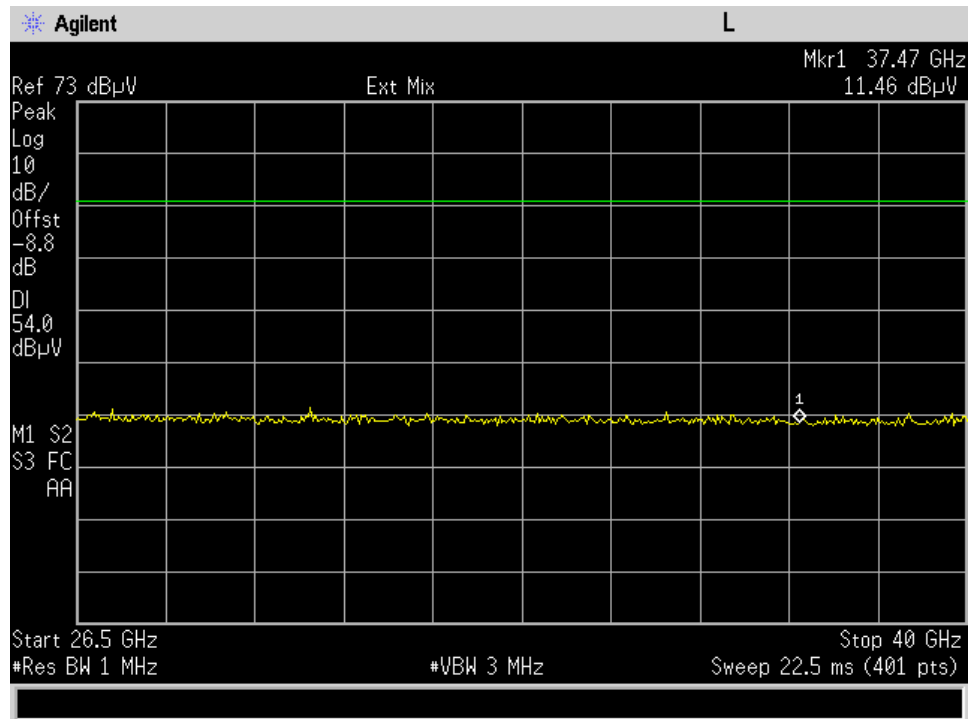


10MHz Mid Ch 1-26.5GHz Port J7

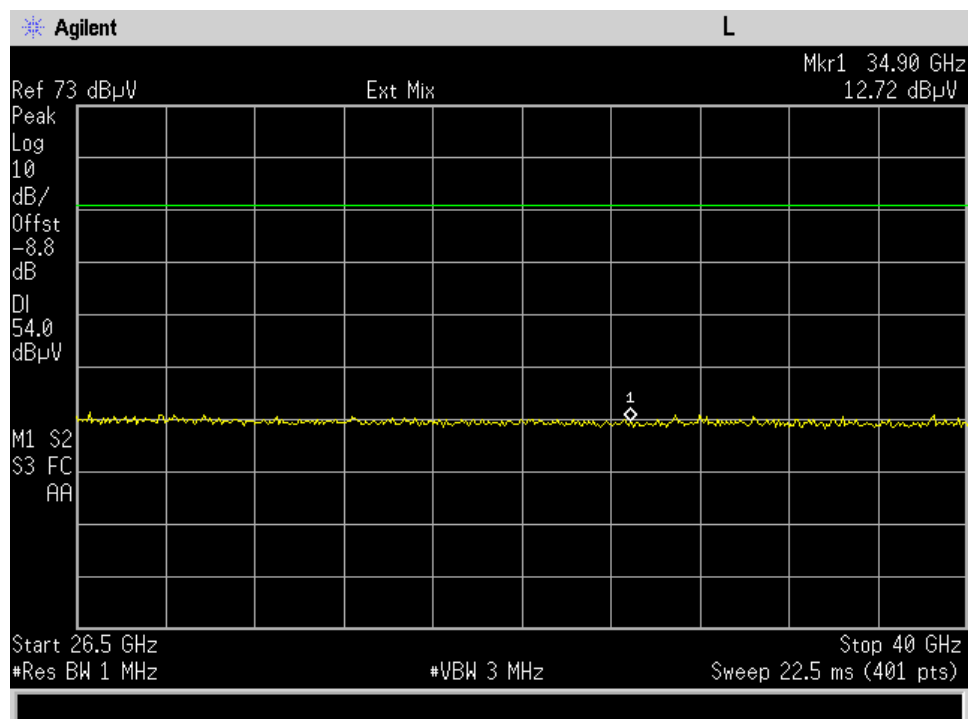




10MHz Mid Ch 26.5-40GHz Port J4

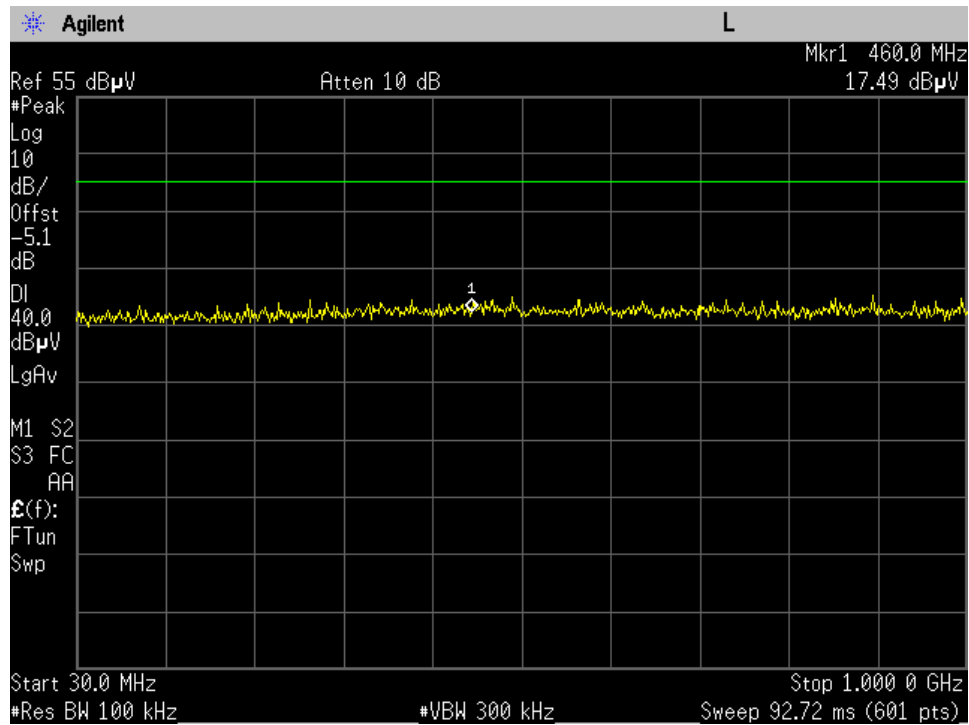


10MHz Mid Ch 26.5-40GHz Port J7

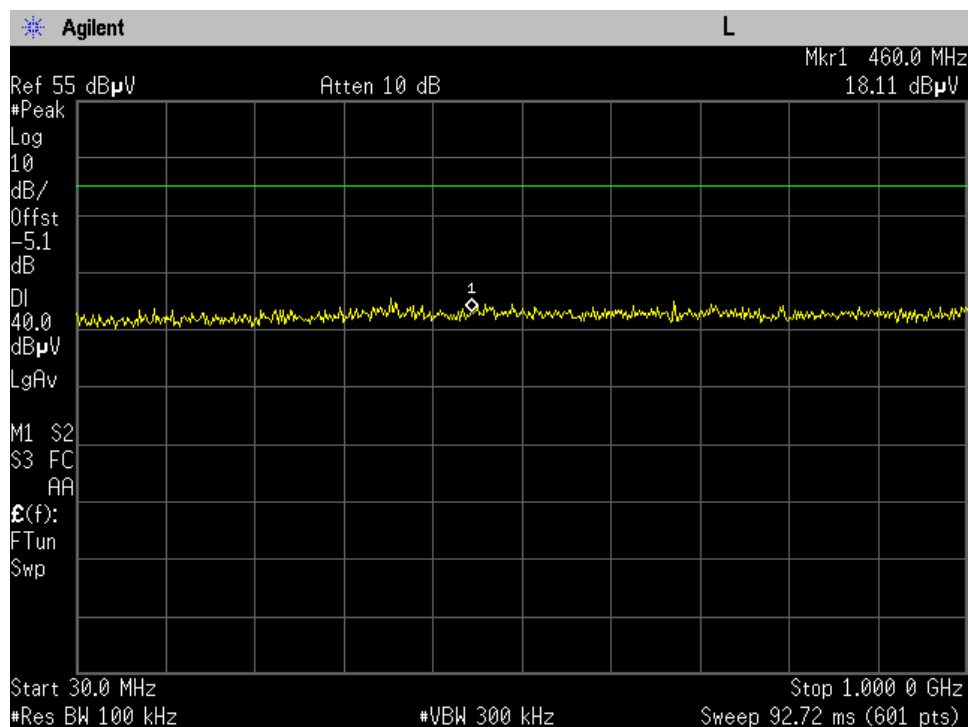




10MHz Mid Ch 30 - 1000MHz Port J4

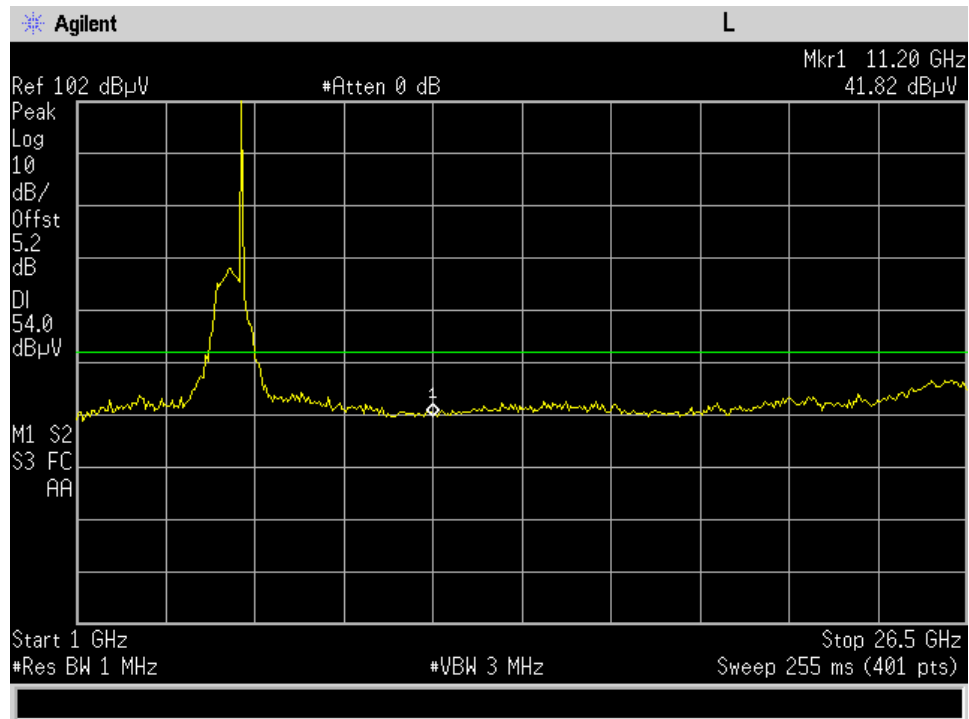


10MHz Mid Ch 30 - 1000MHz Port J7

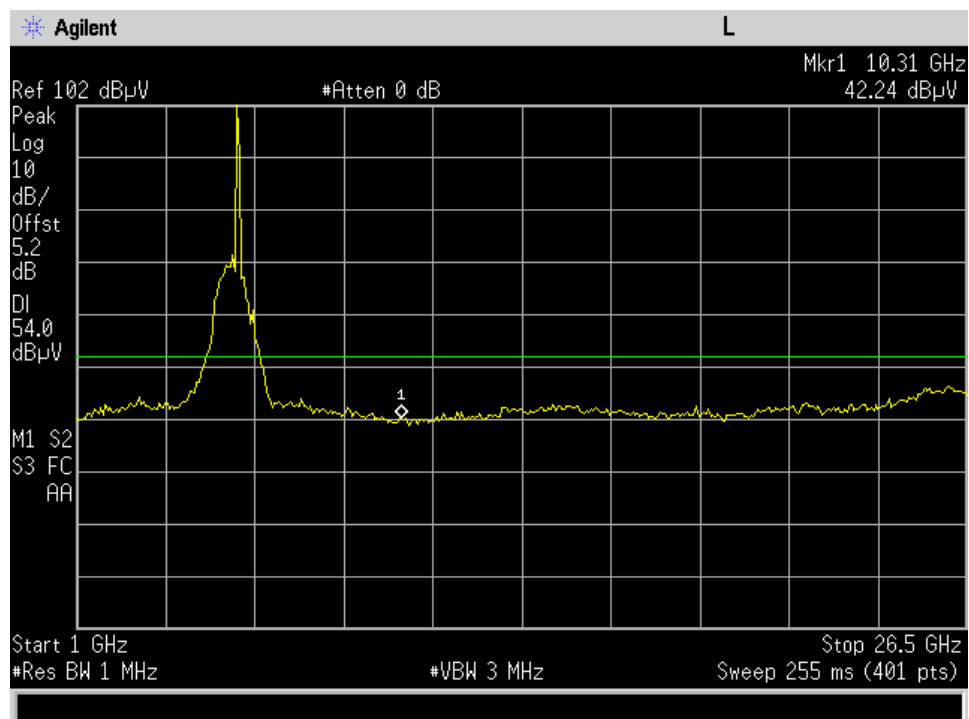




20MHz High Ch 1-26.5GHz Port J4

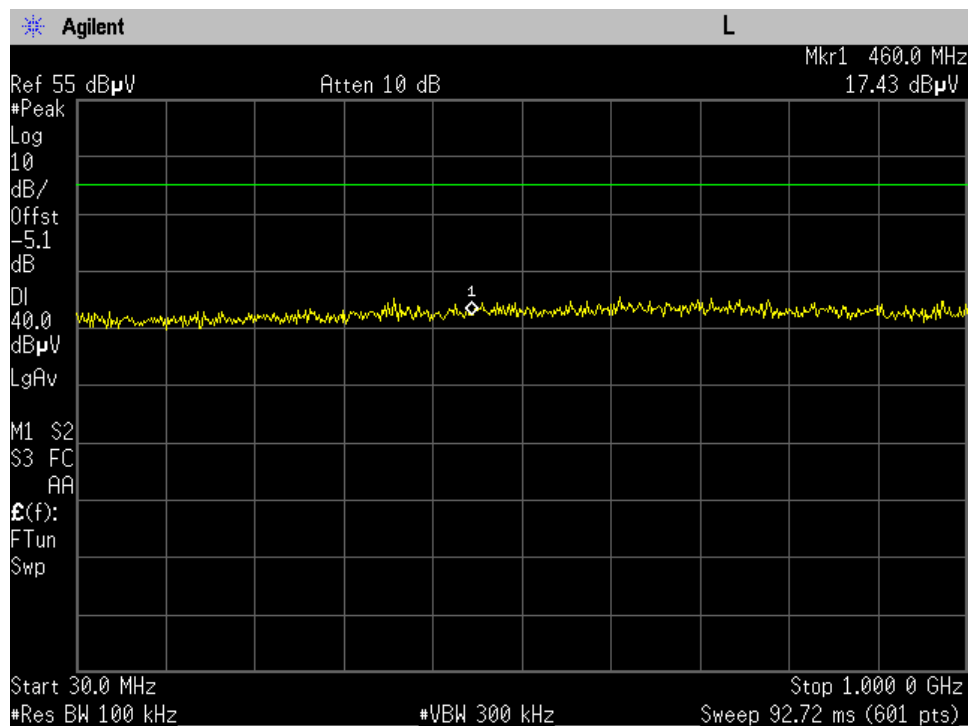


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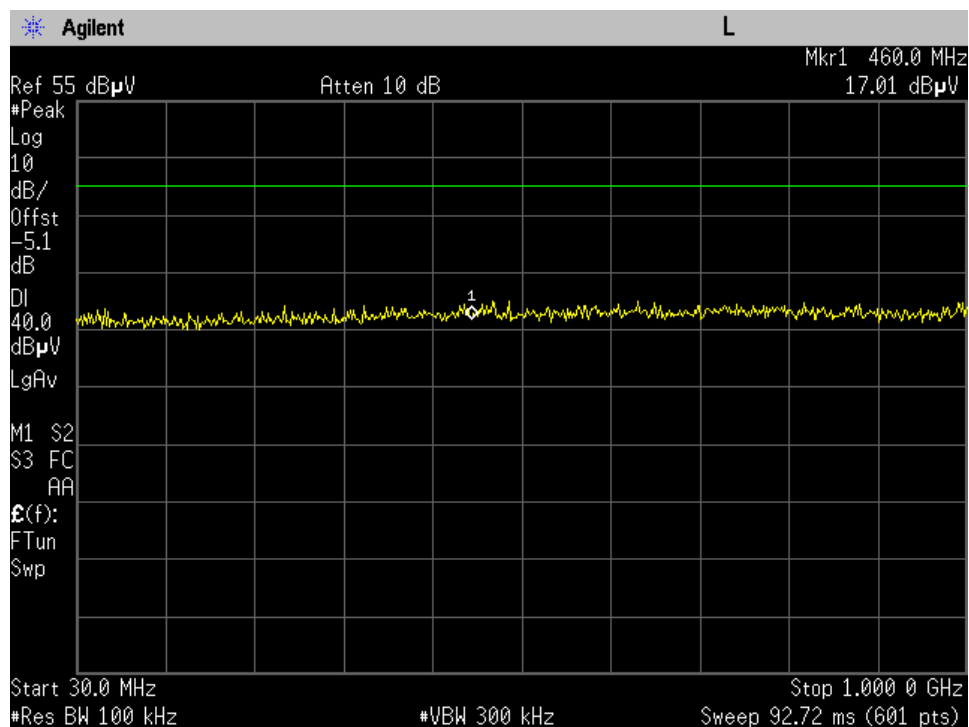




20MHz High Ch 30 - 1000MHz Port J4

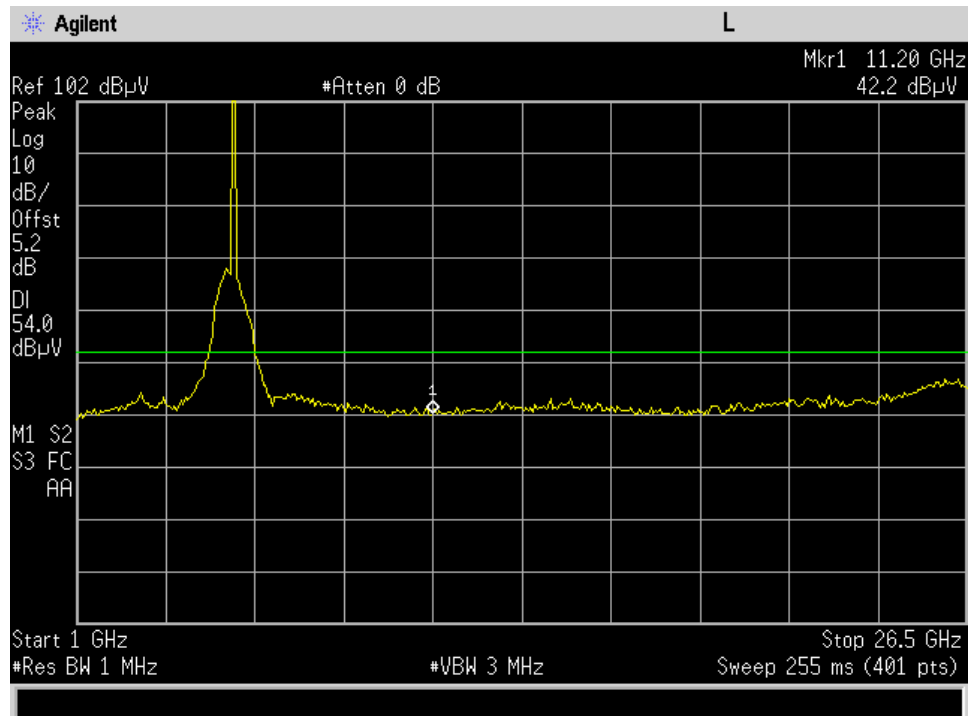


20MHz High Ch 30 - 1000MHz Port J7

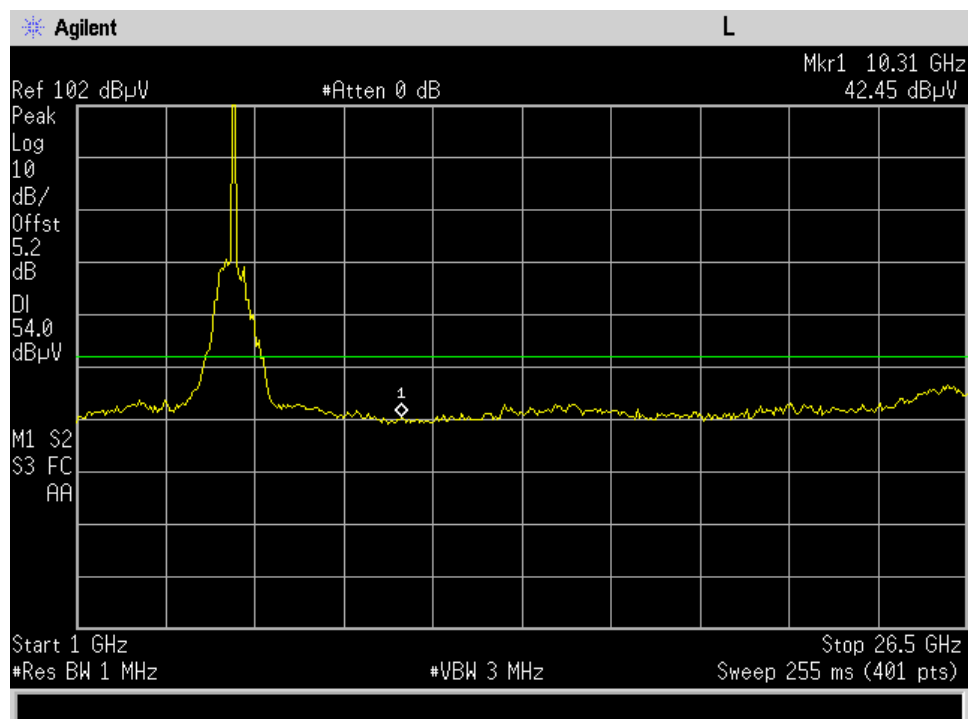




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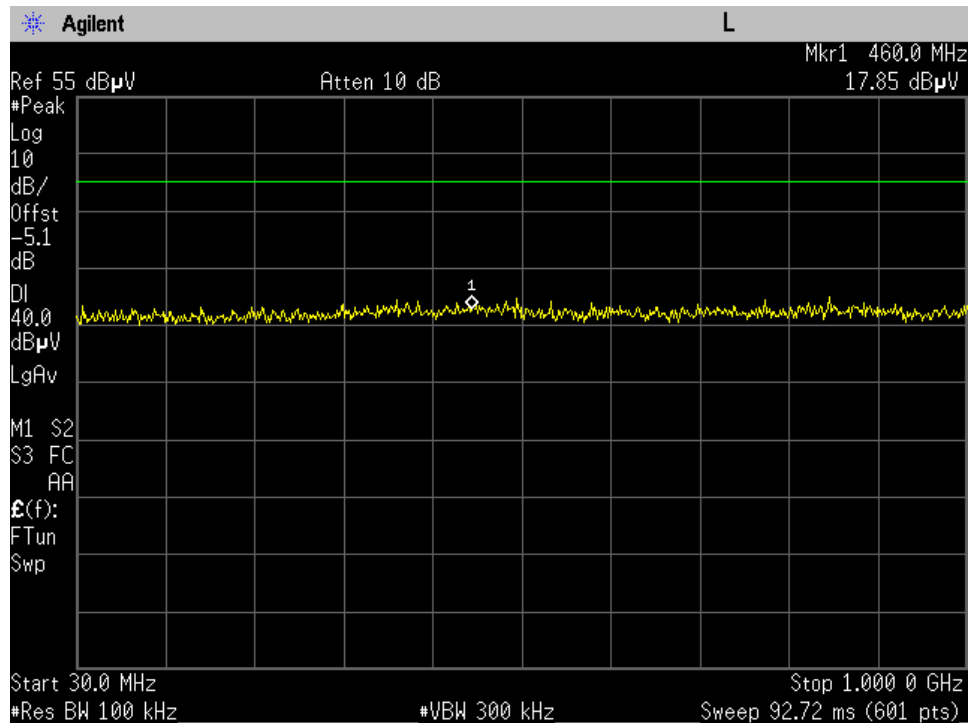


20MHz Low Ch 1-26.5GHz Port J7

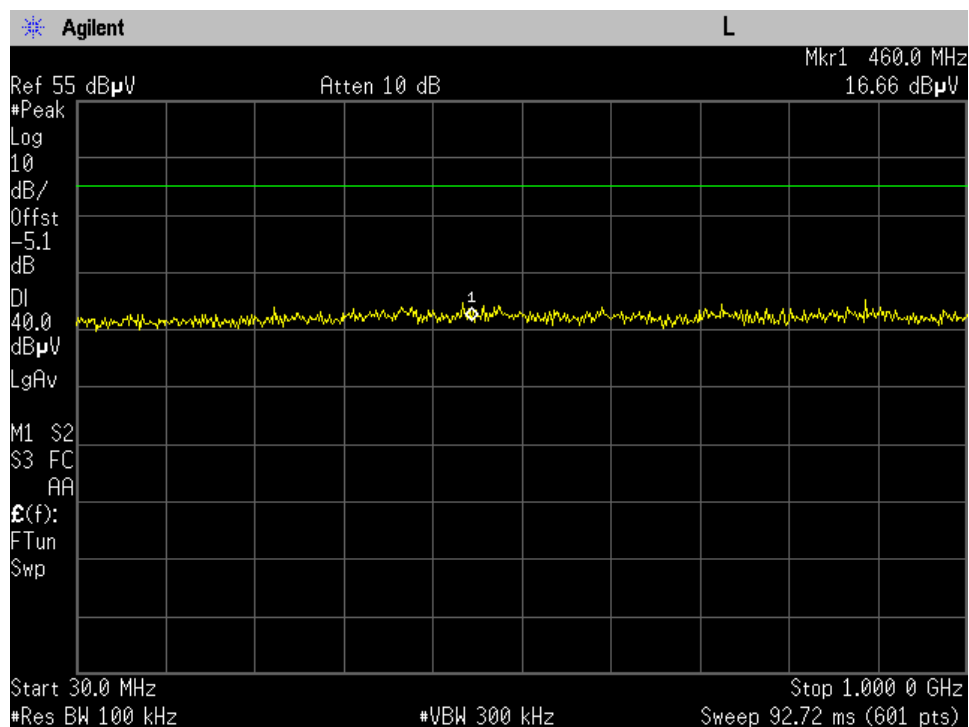




20MHz Low Ch 30 - 1000MHz Port J4

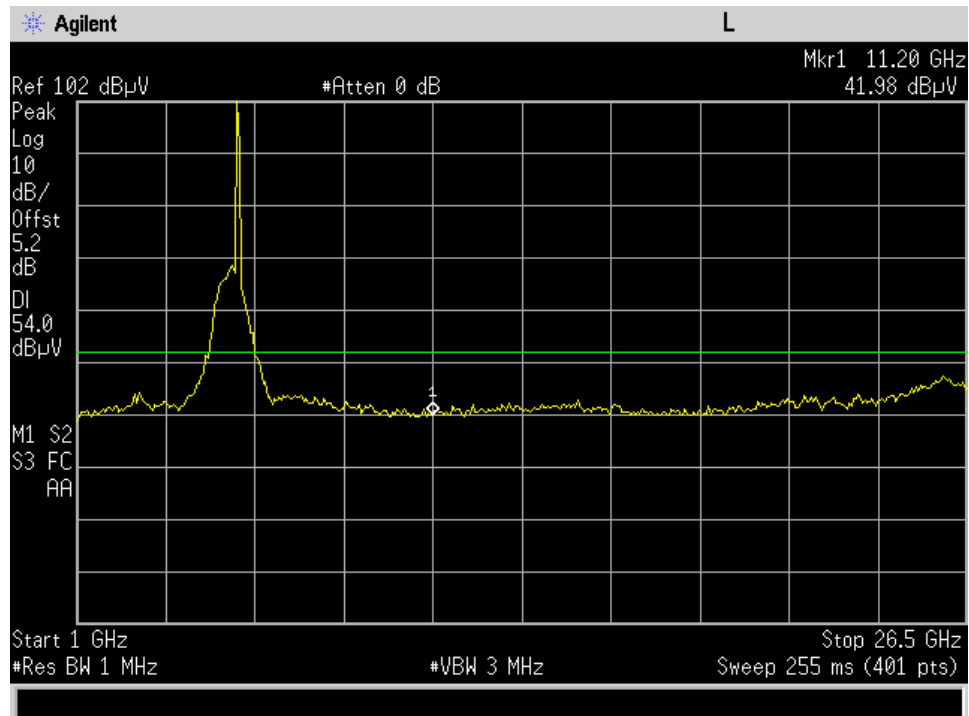


20MHz Low Ch 30 - 1000MHz Port J7

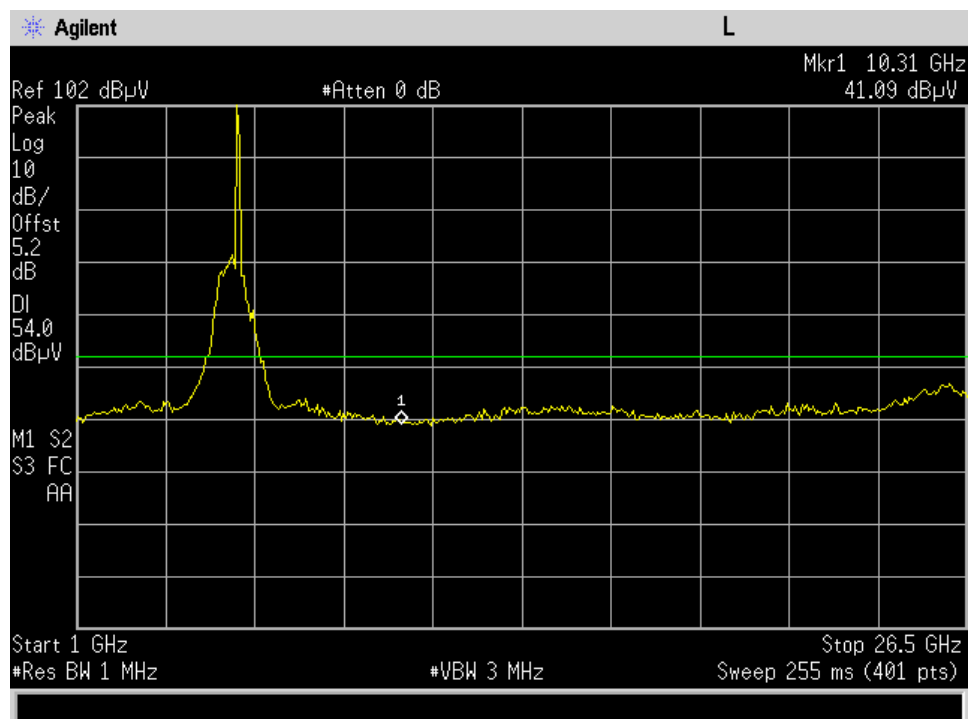




20MHz Mid Ch 1-26.5GHz Port J4

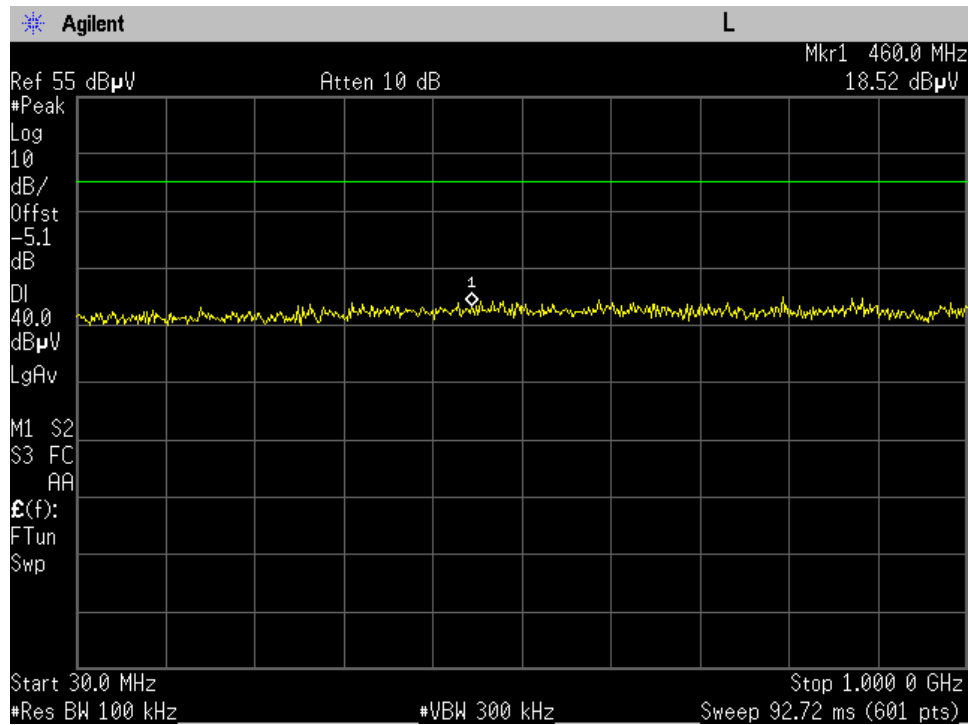


20MHz Mid Ch 1-26.5GHz Port J7

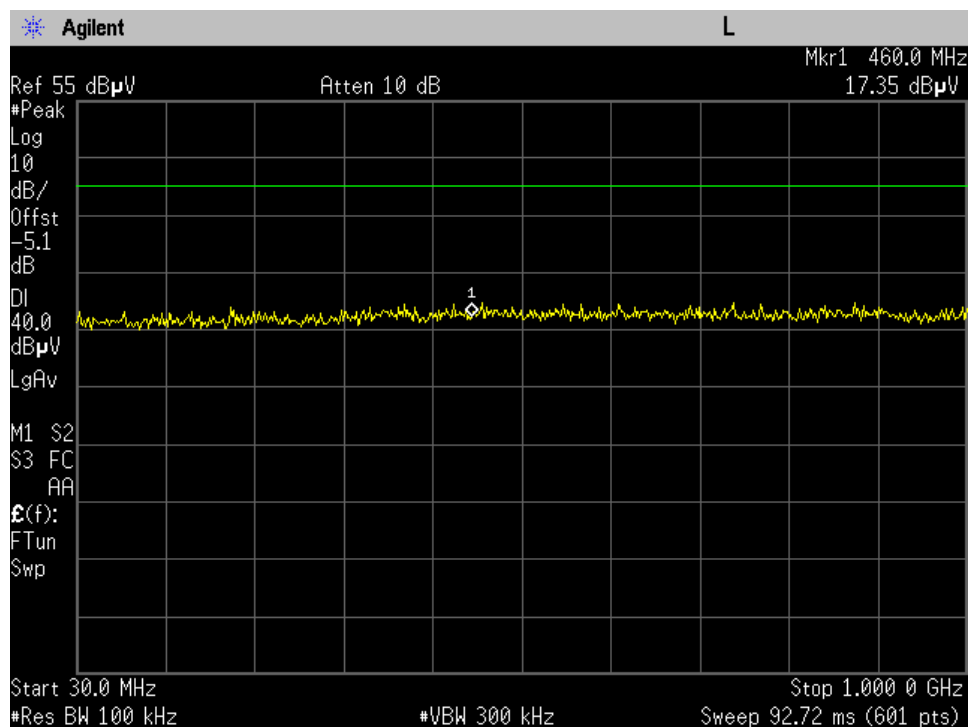




20MHz Mid Ch 30 - 1000MHz Port J4

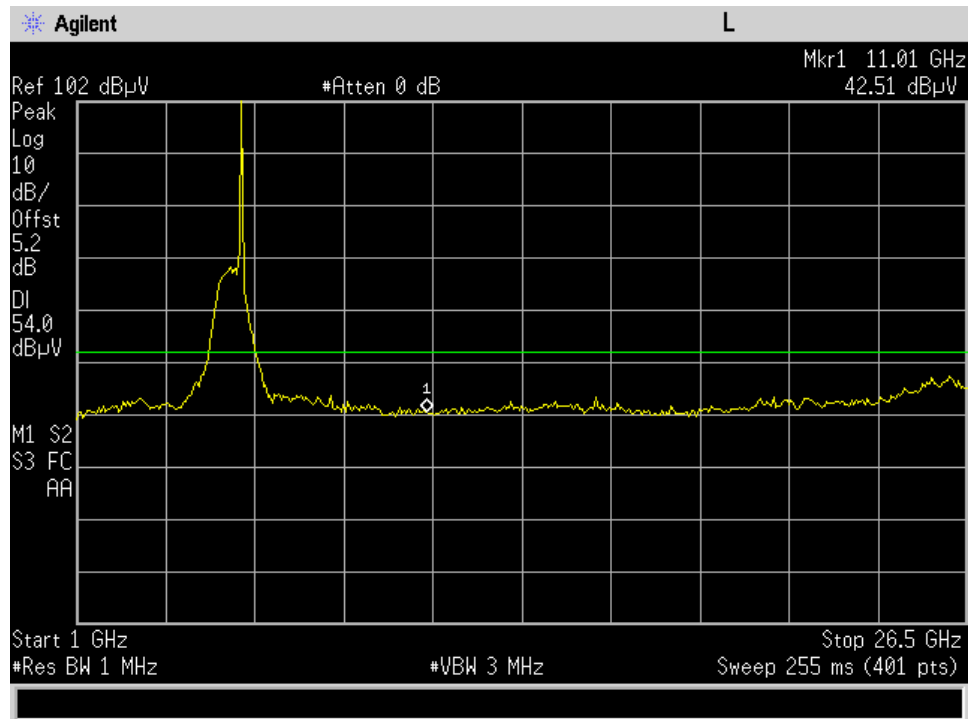


20MHz Mid Ch 30 - 1000MHz Port J7

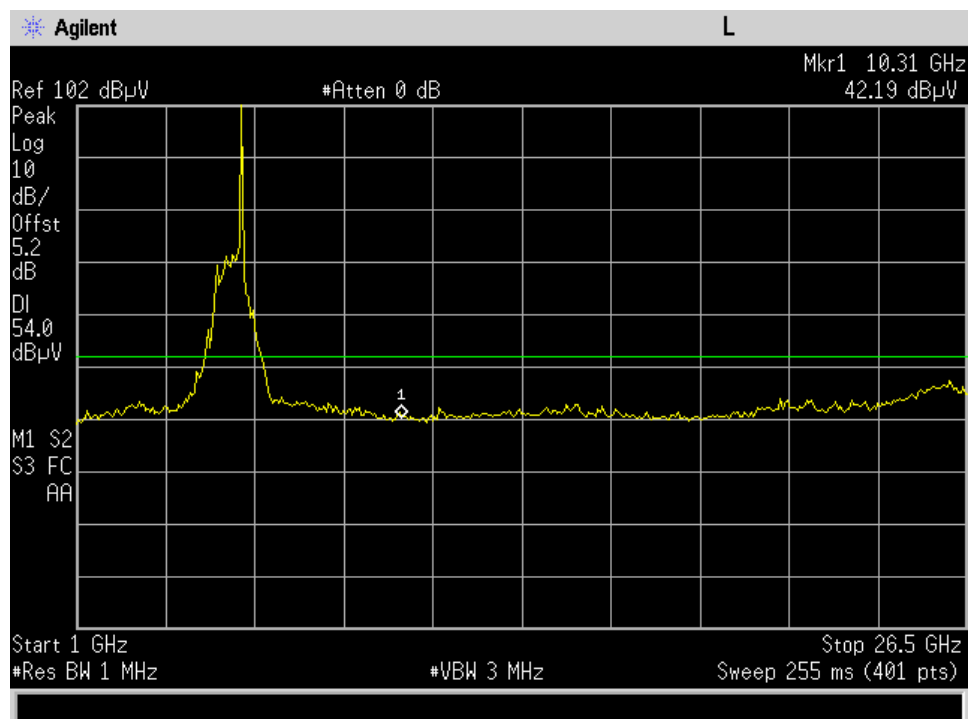




30MHz High Ch 1-26.5GHz Port J4

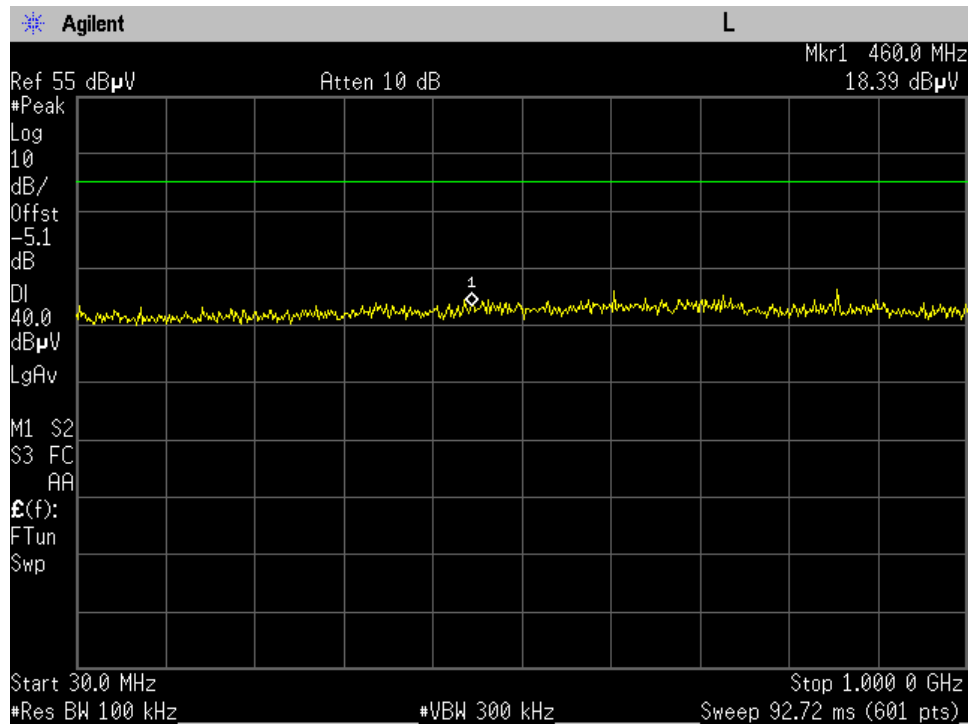


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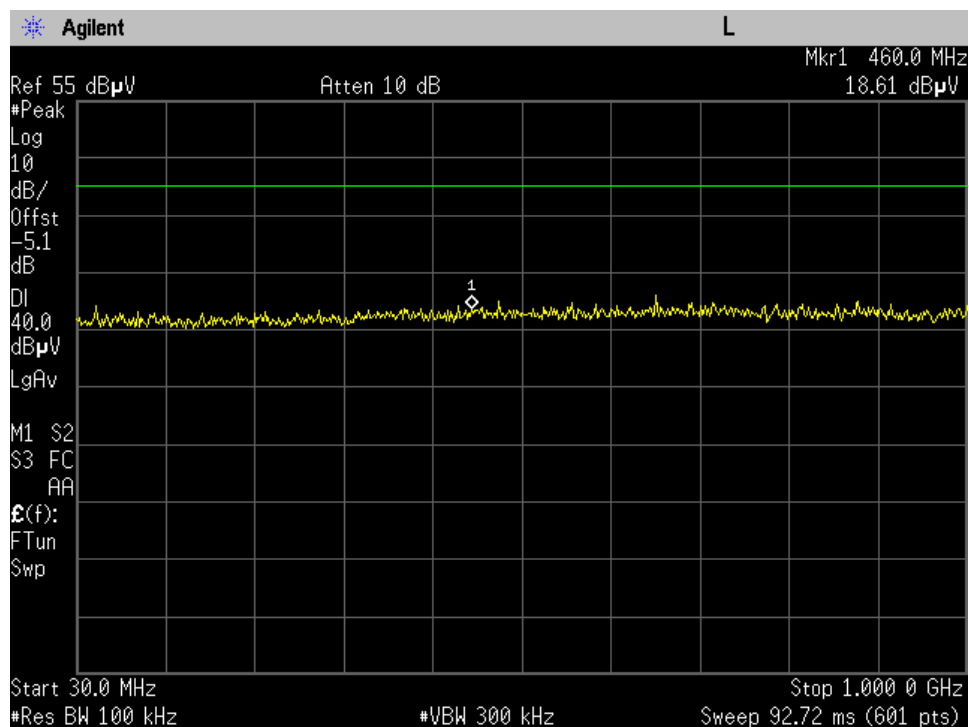




30MHz High Ch 30 - 1000MHz Port J4

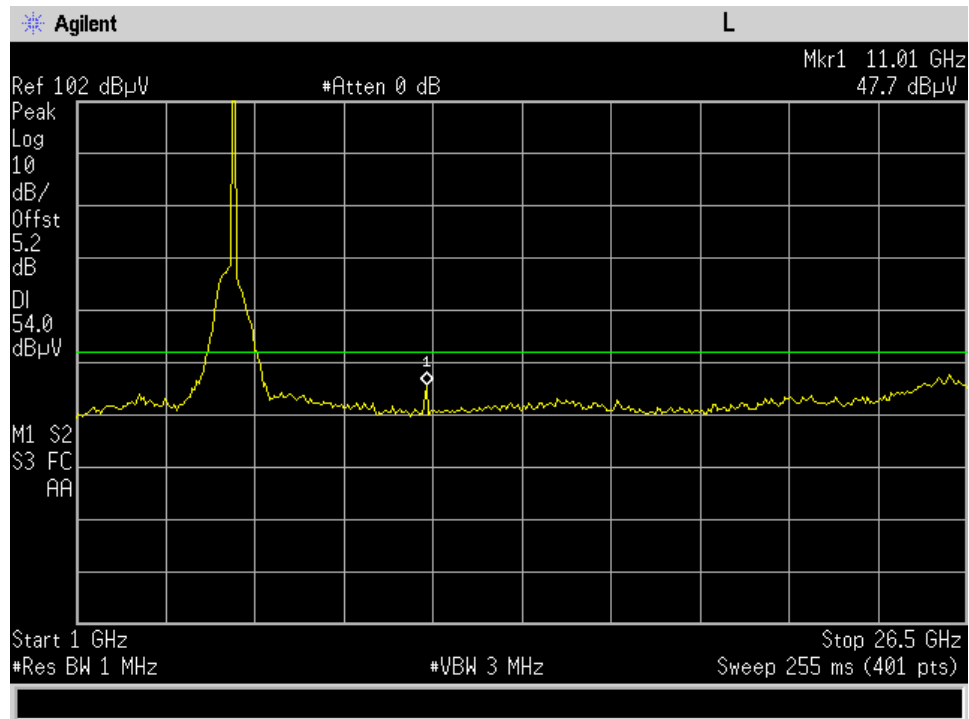


30MHz High Ch 30 - 1000MHz Port J7

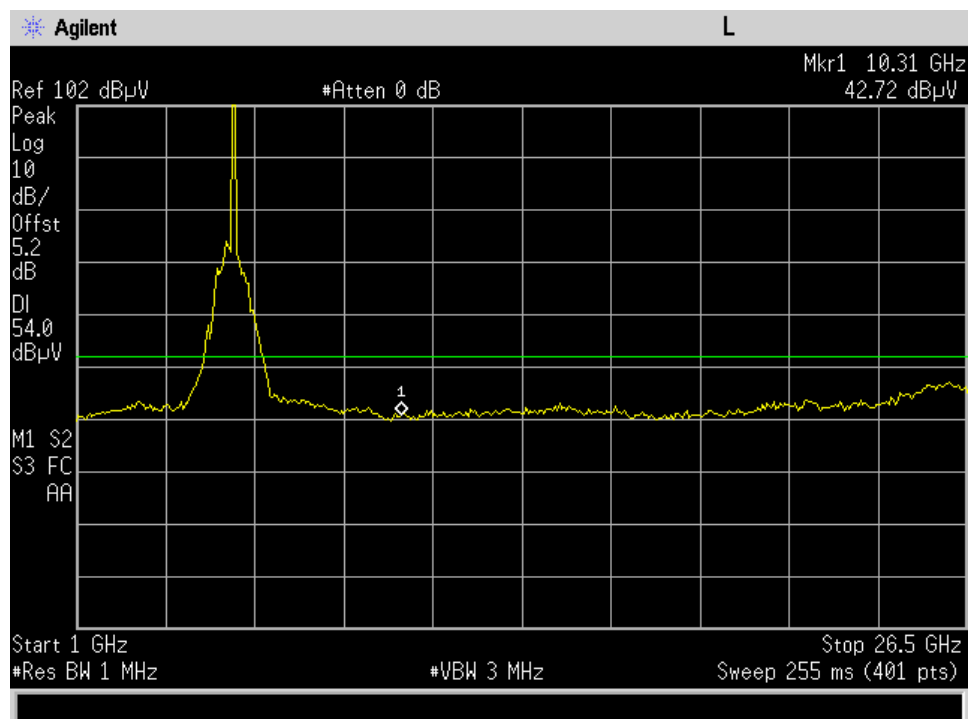




30MHz Low Ch 1-26.5GHz Port J4

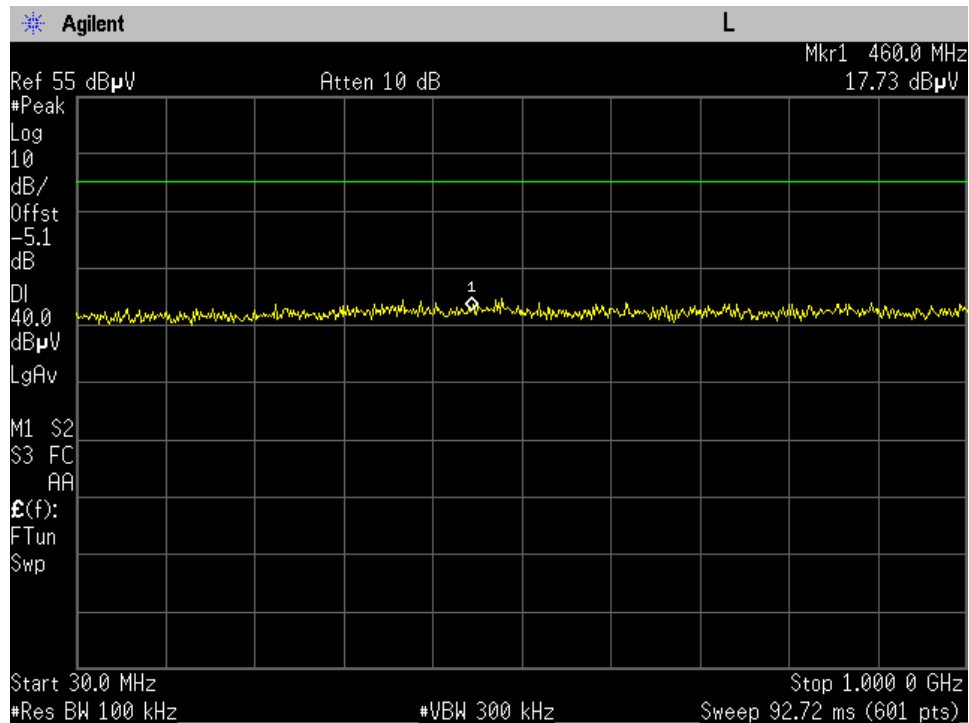


30MHz Low Ch 1-26.5GHz Port J7

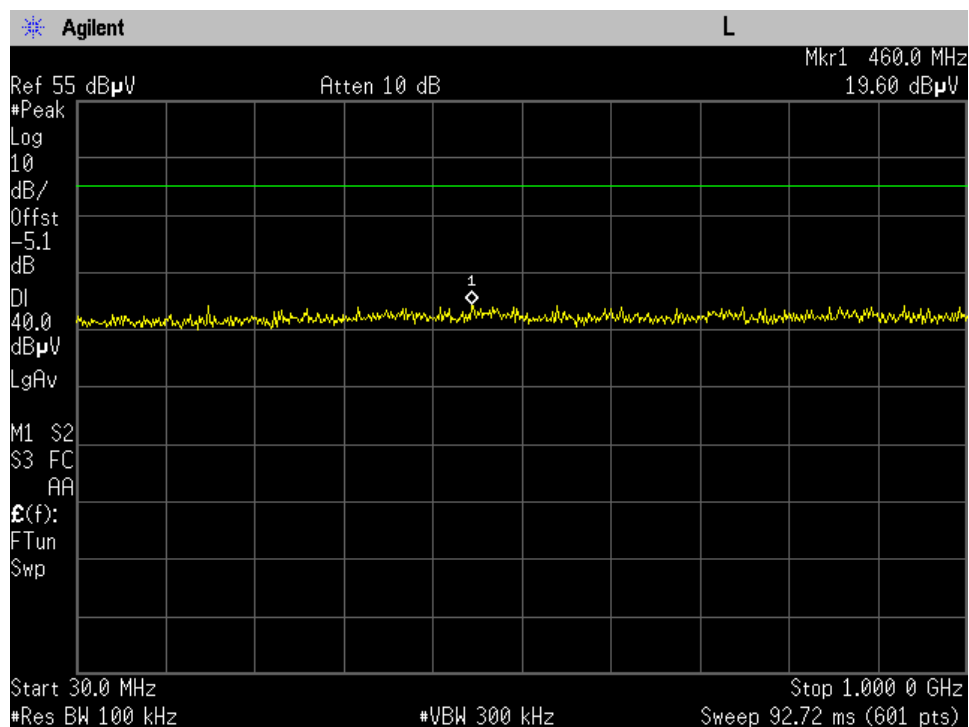




30MHz Low Ch 30 - 1000MHz Port J4

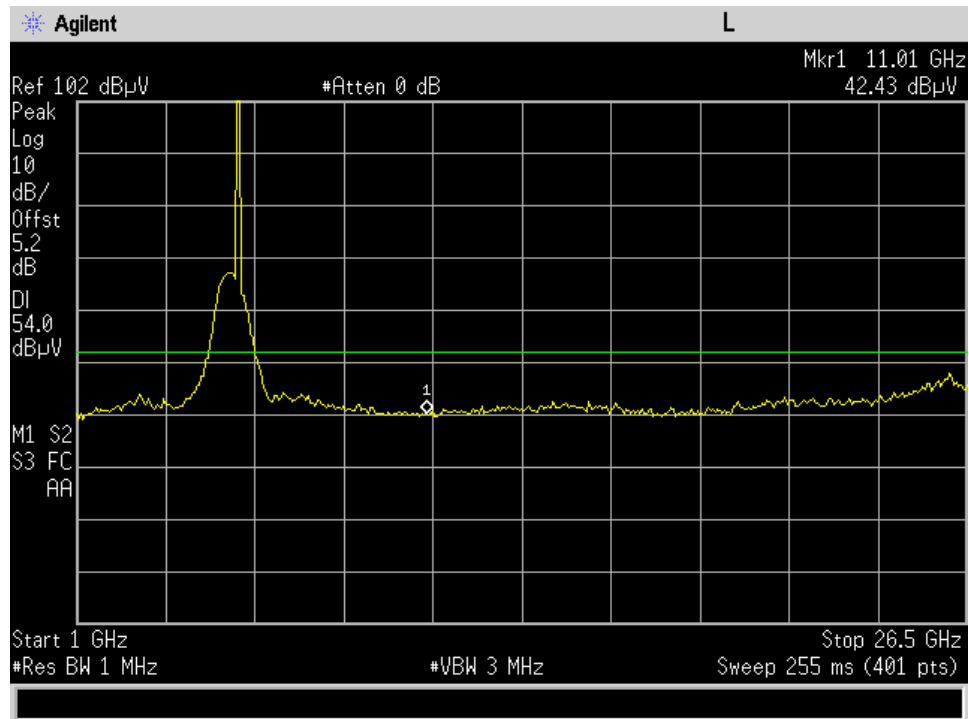


30MHz Low Ch 30 - 1000MHz Port J7

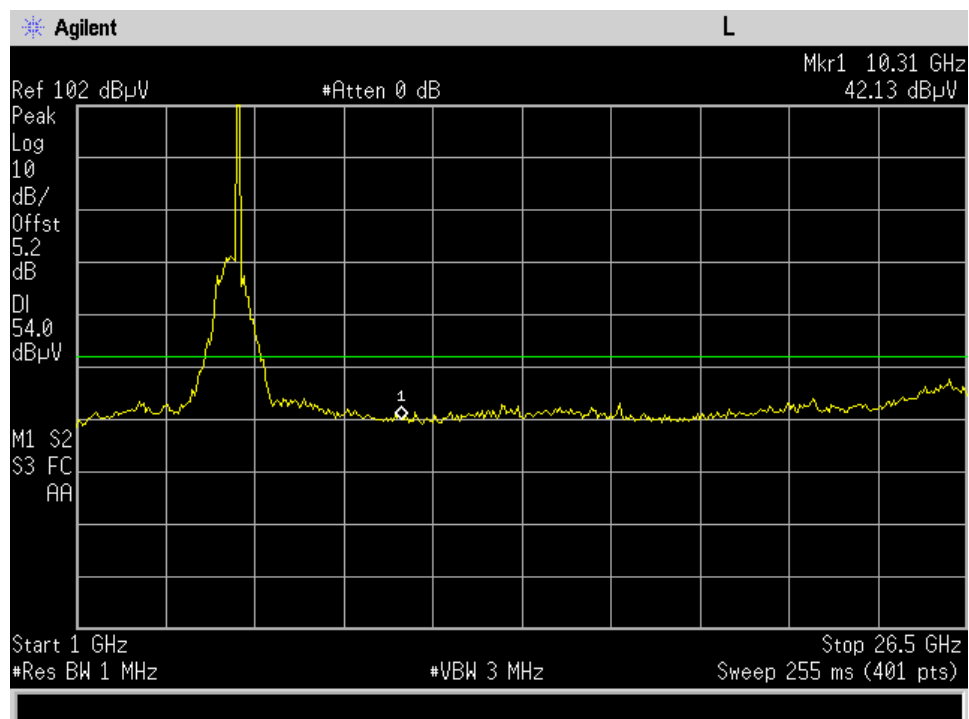




30MHz Mid Ch 1-26.5GHz Port J4

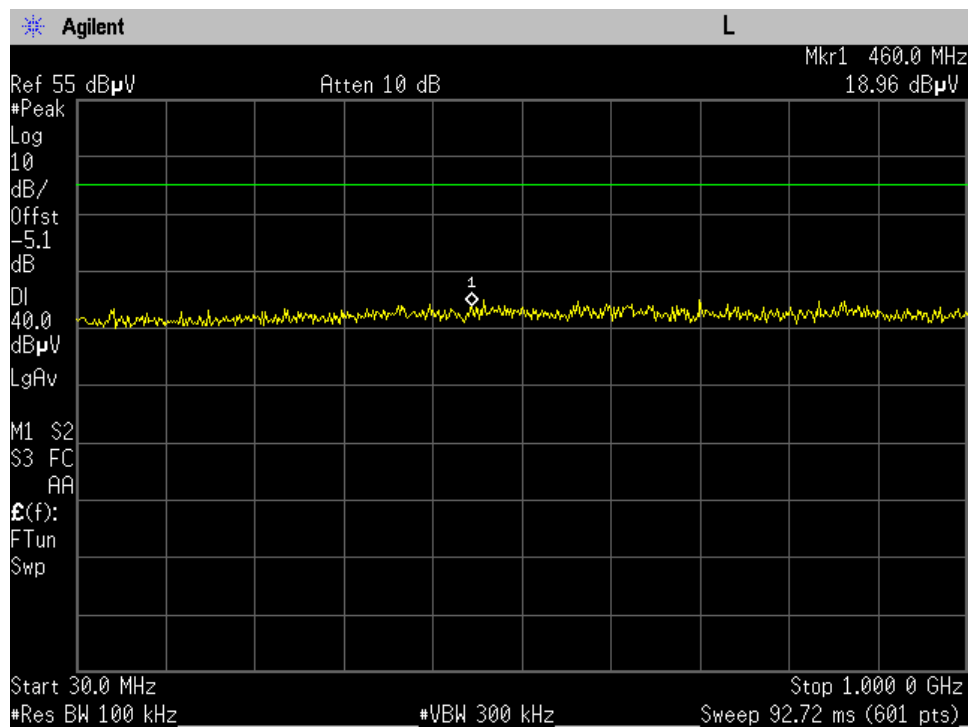


30MHz Mid Ch 1-26.5GHz Port J7

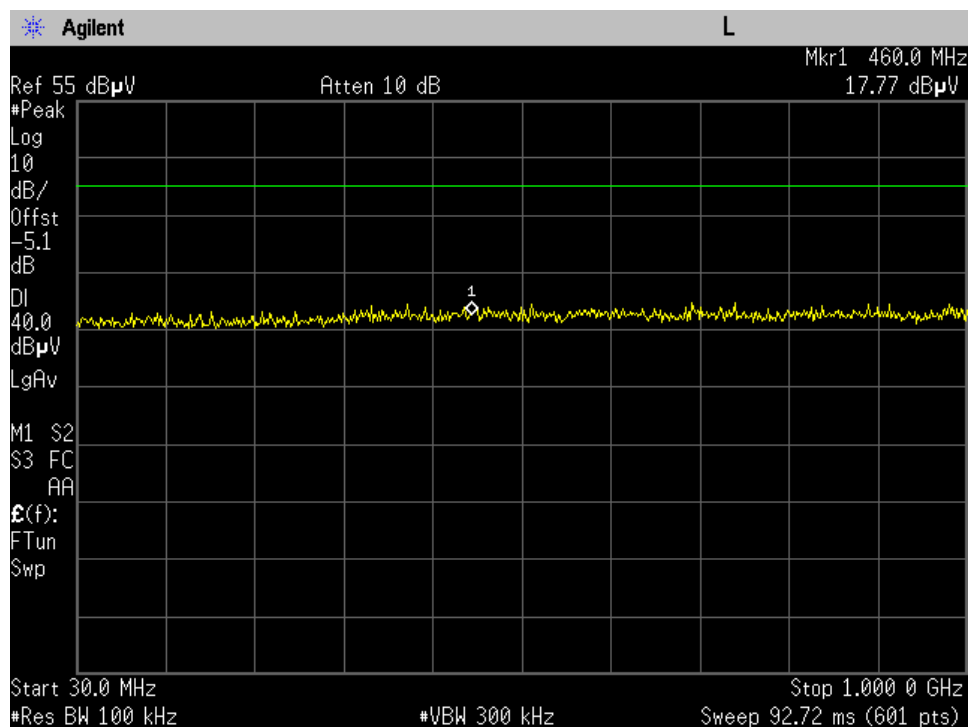




30MHz Mid Ch 30 - 1000MHz Port J4

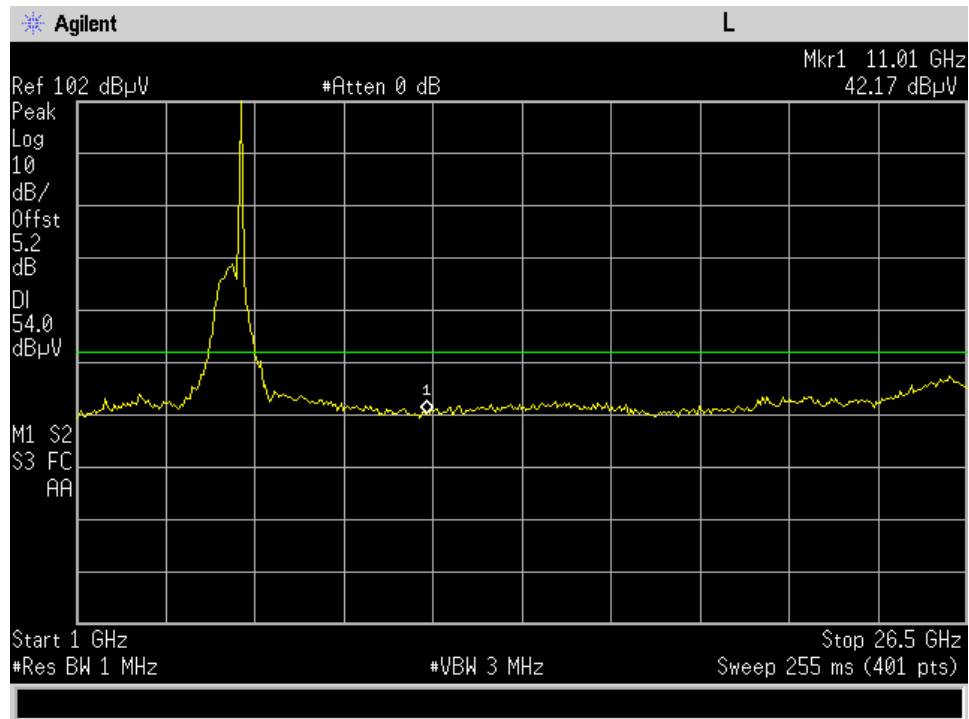


30MHz Mid Ch 30 - 1000MHz Port J7

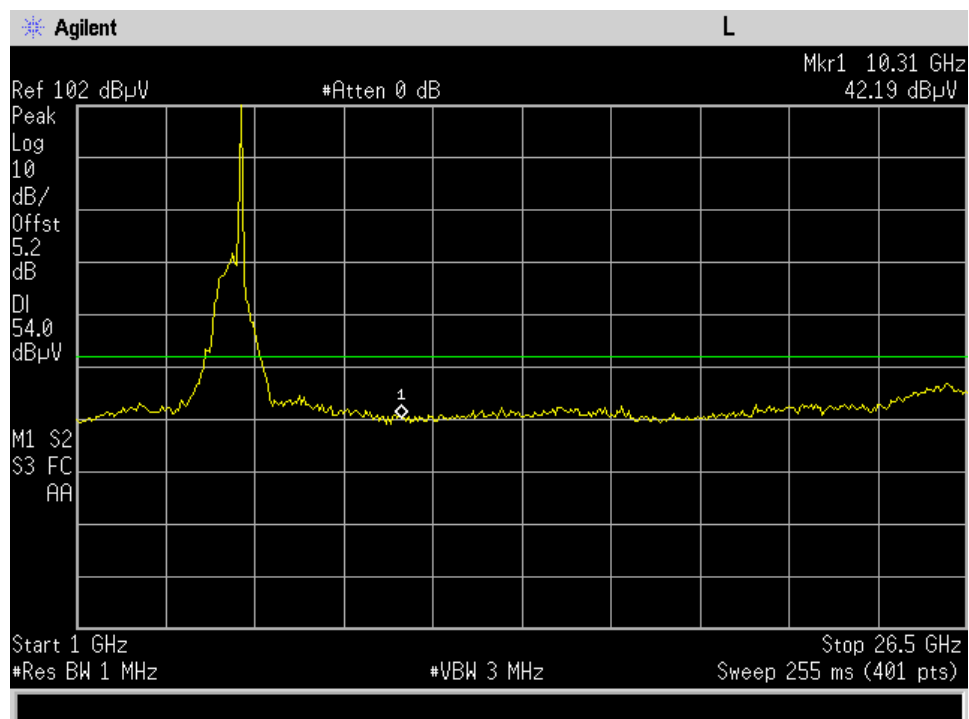




40MHz High Ch 1-26.5GHz Port J4

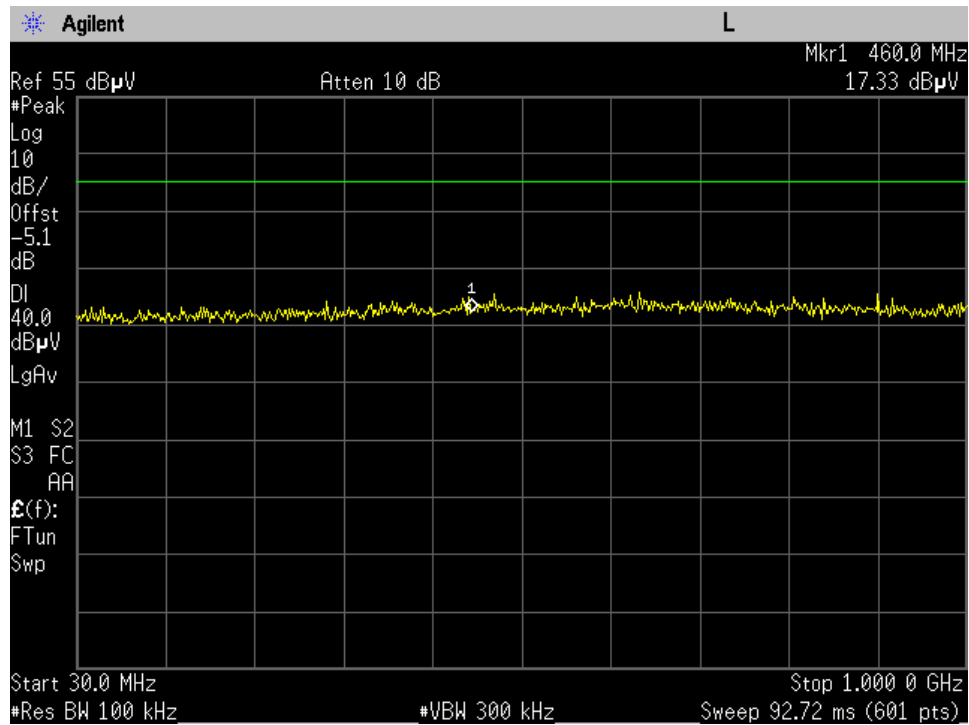


40MHz High Ch 1-26.5GHz Port J7

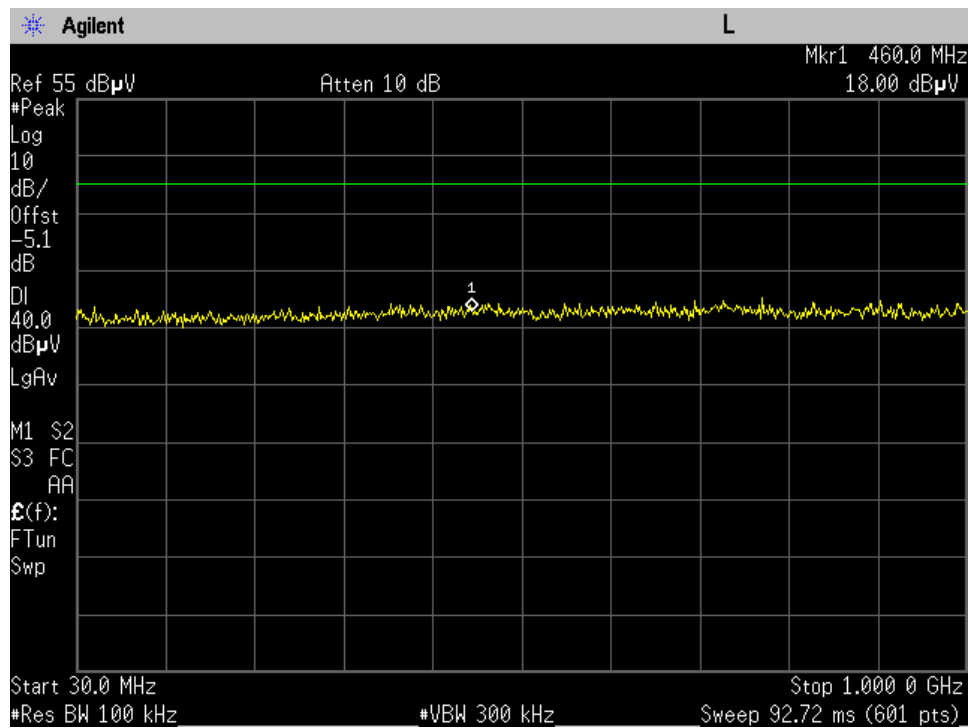




40MHz High Ch 30 - 1000MHz Port J4

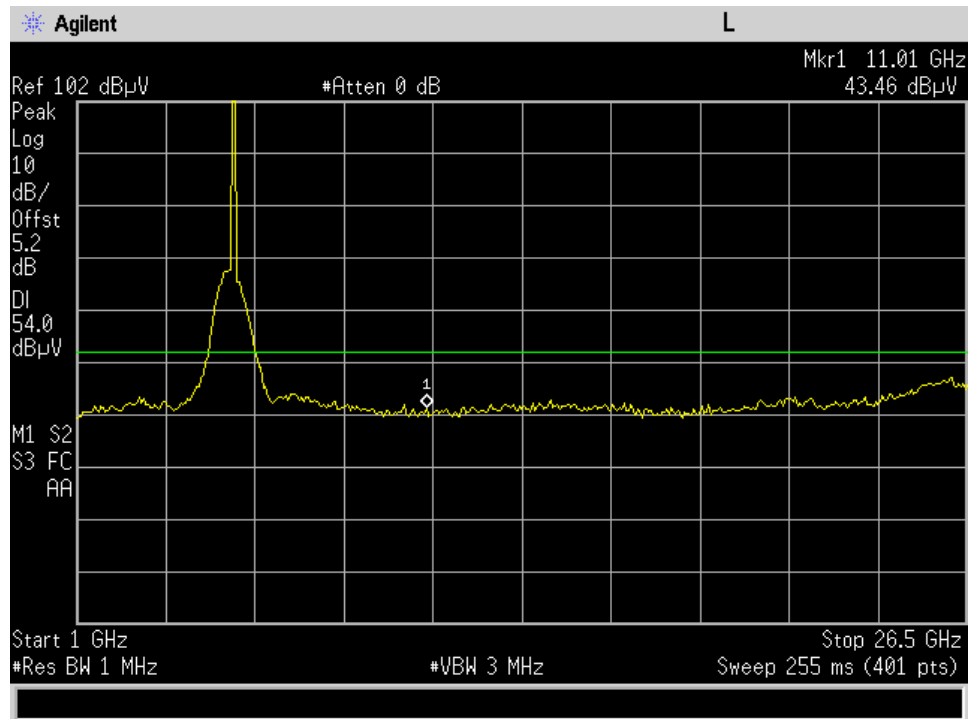


40MHz High Ch 30 - 1000MHz Port J7

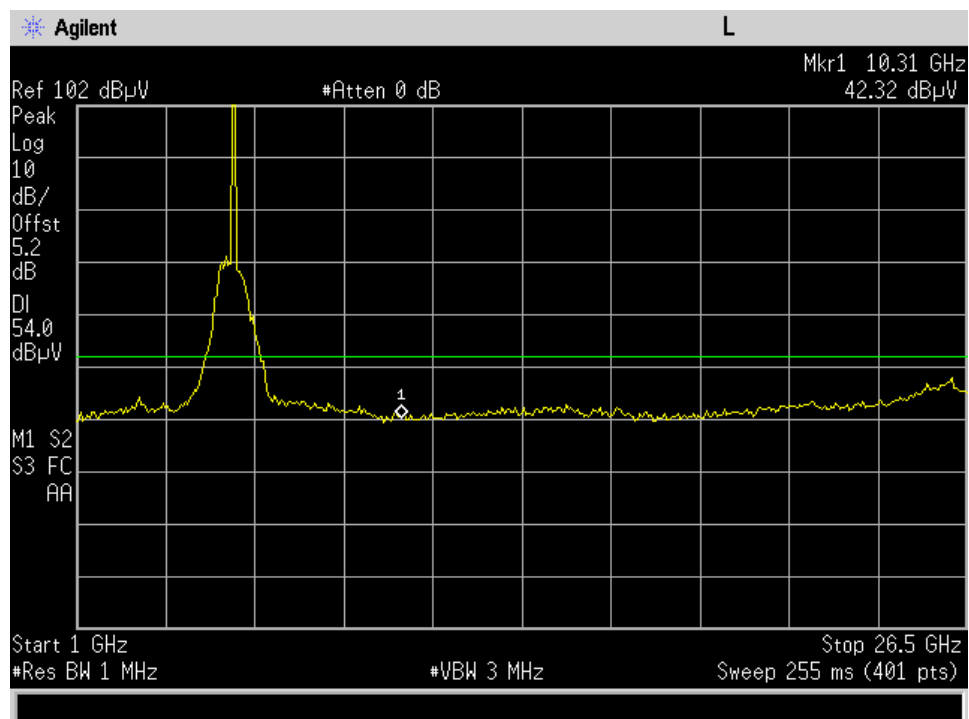




40MHz Low Ch 1-26.5GHz Port J4

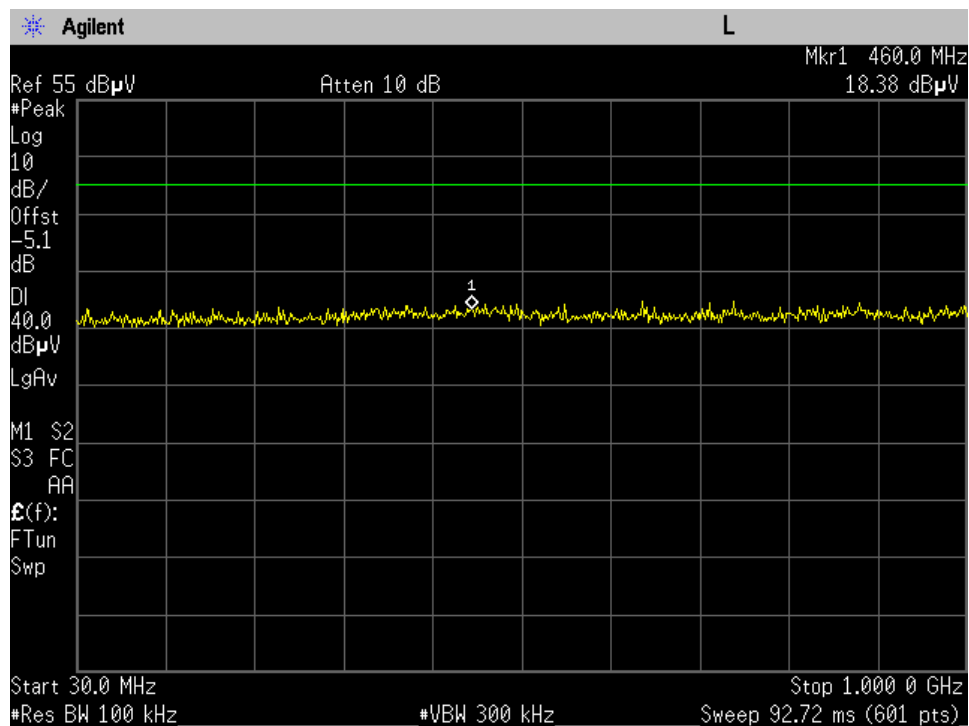


40MHz Low Ch 1-26.5GHz Port J7

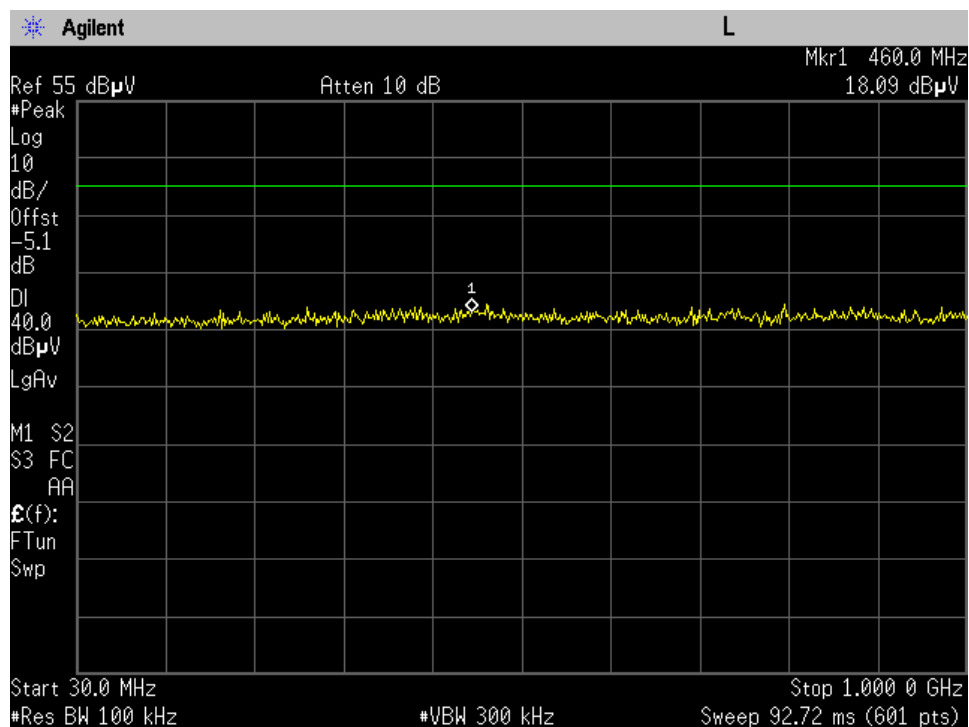




40MHz Low Ch 30 - 1000MHz Port J4

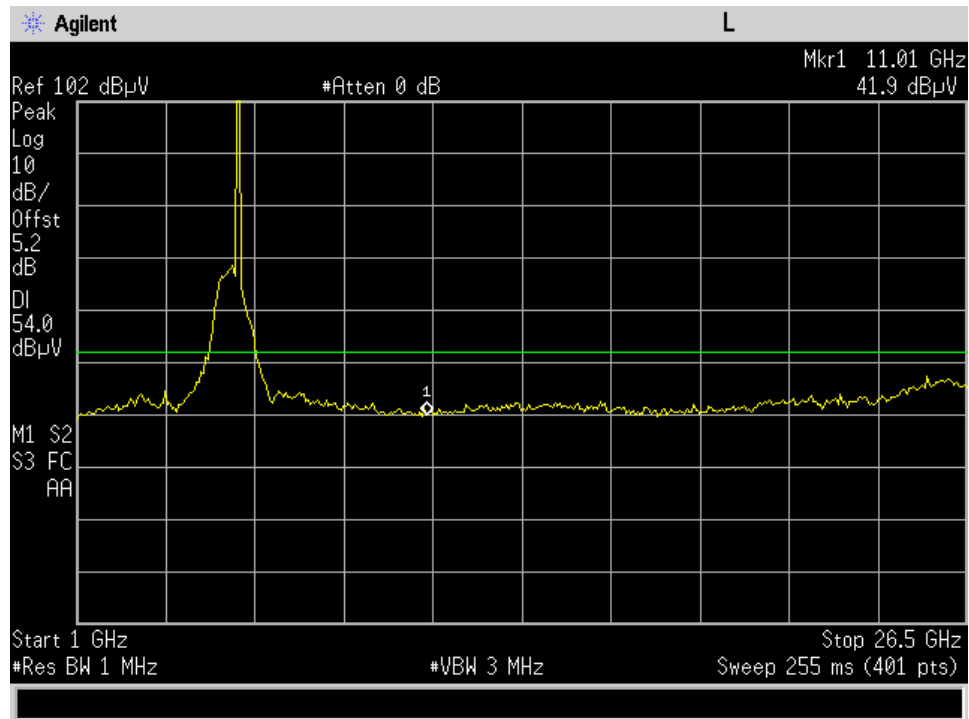


40MHz Low Ch 30 - 1000MHz Port J7

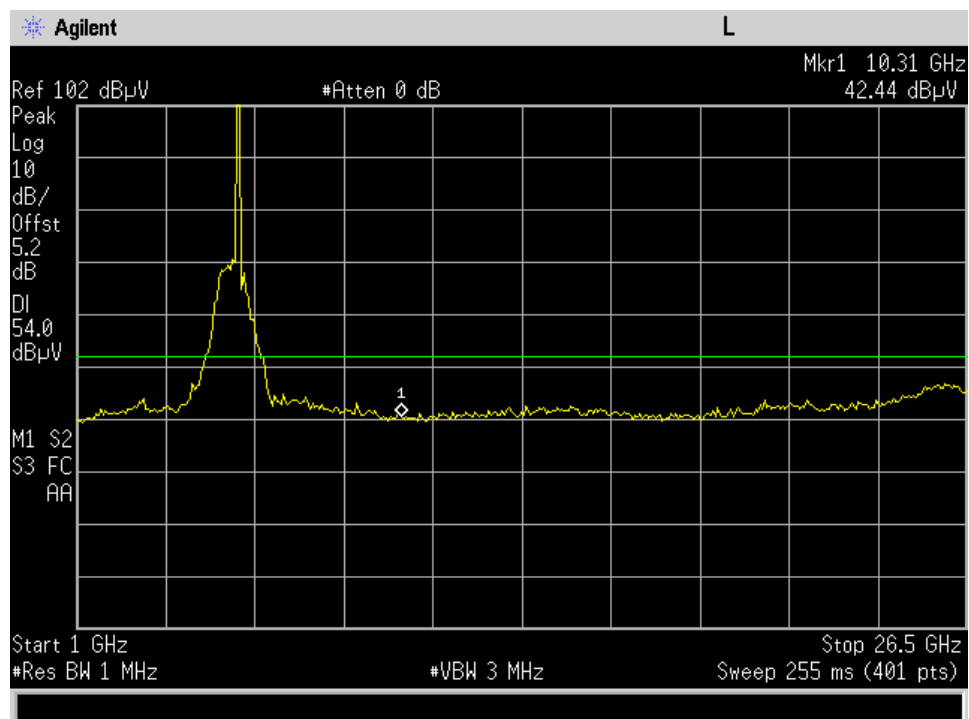




40MHz Mid Ch 1-26.5GHz Port J4

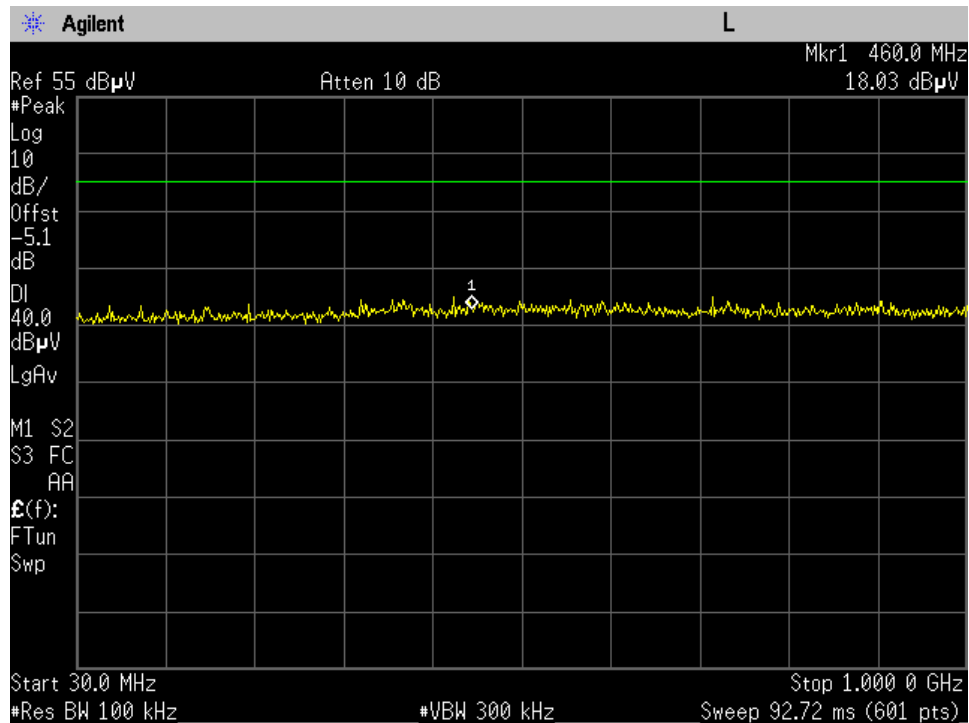


40MHz Mid Ch 1-26.5GHz Port J7

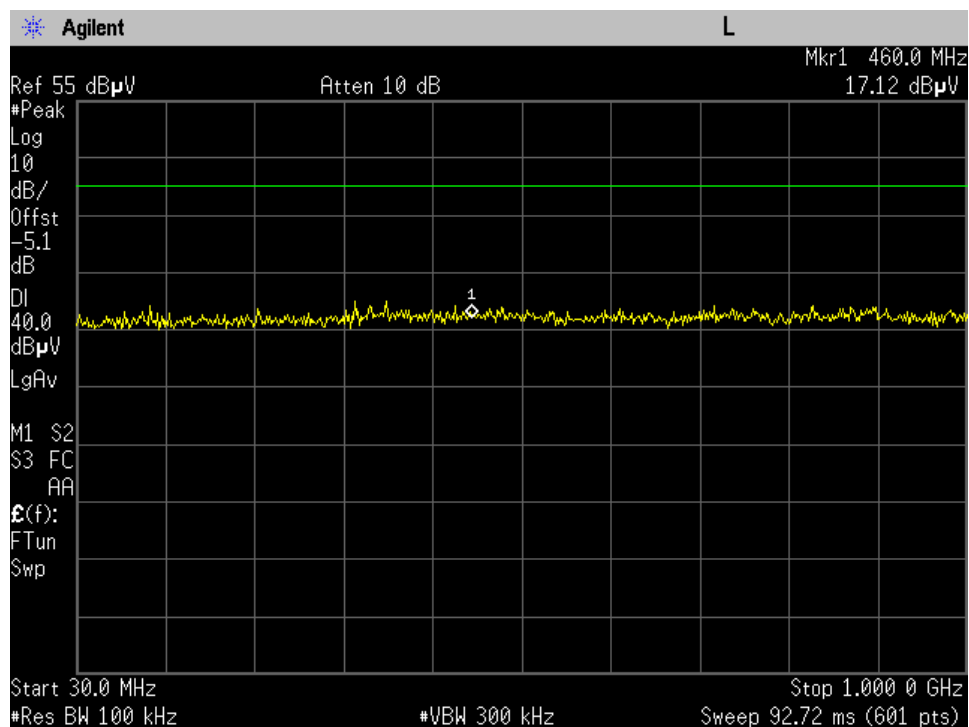




40MHz Mid Ch 30 - 1000MHz Port J4

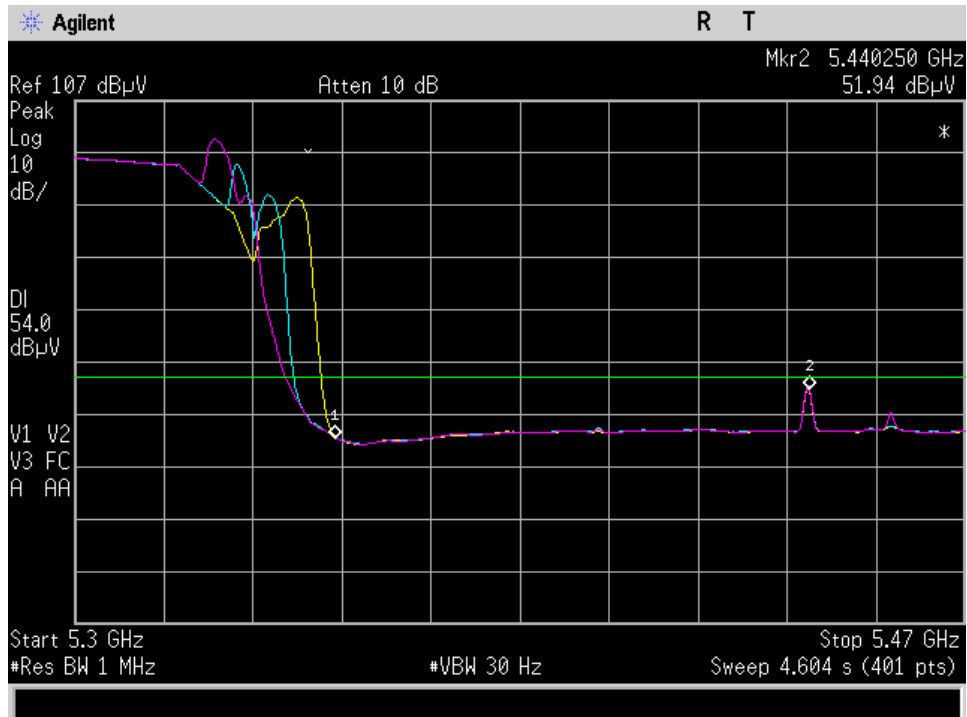


40MHz Mid Ch 30 - 1000MHz Port J7

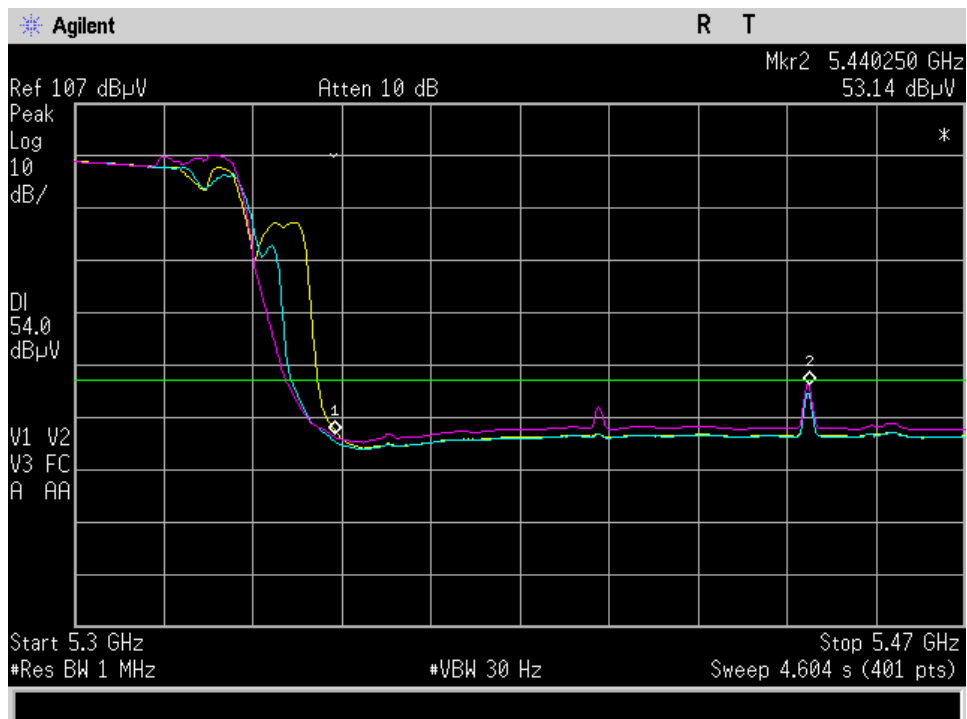




Radiated Band Edge Measurements UNII-2A 10 Mhz Band edge

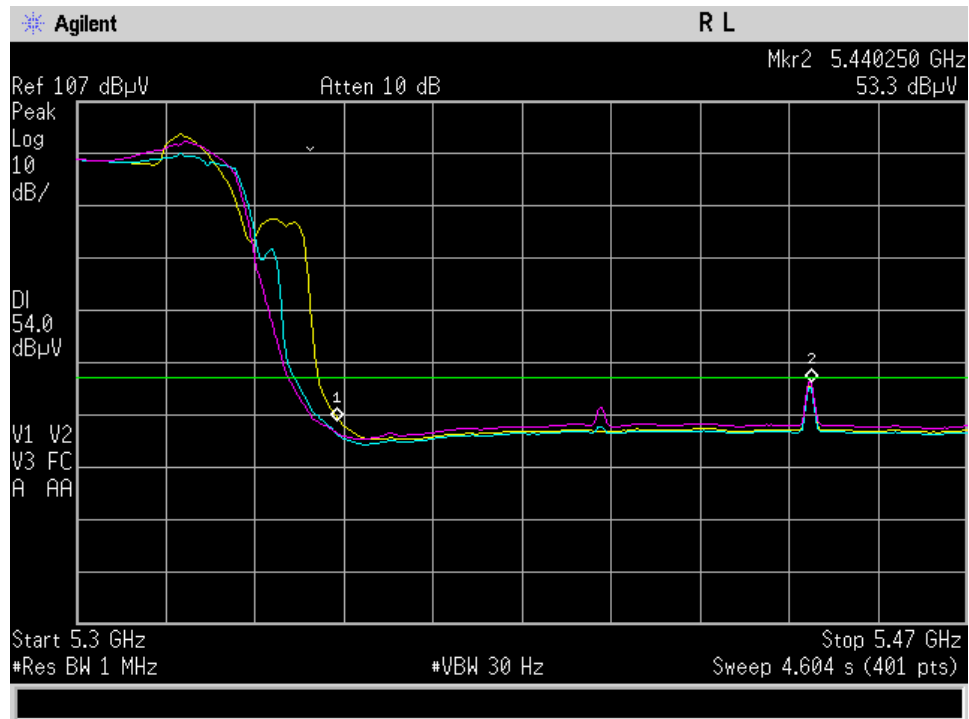


20 Mhz Band edge

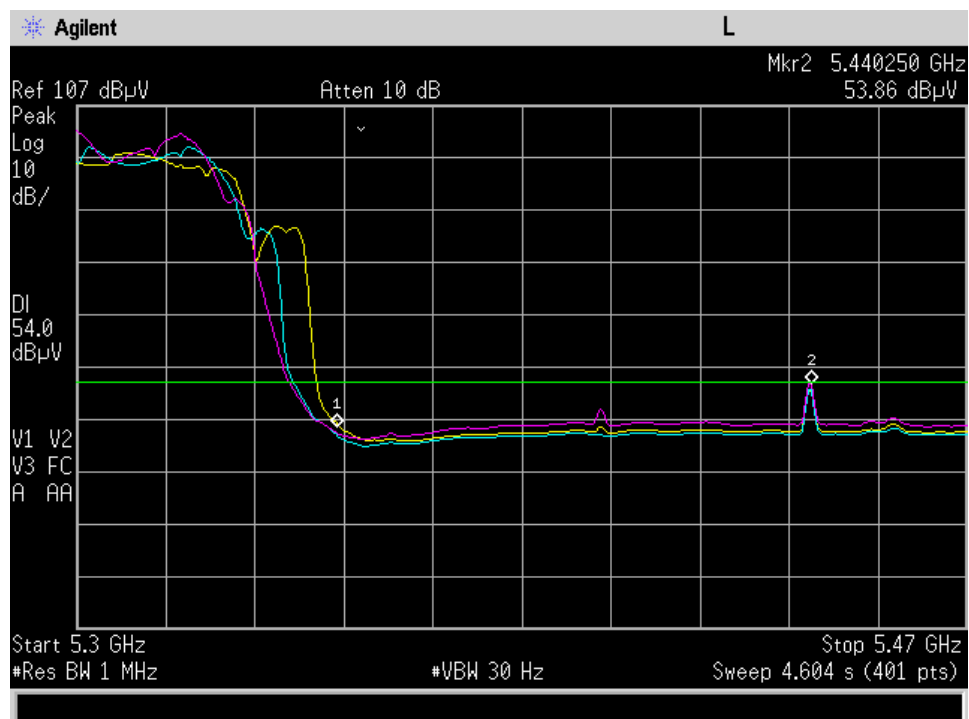




30 Mhz Band edge

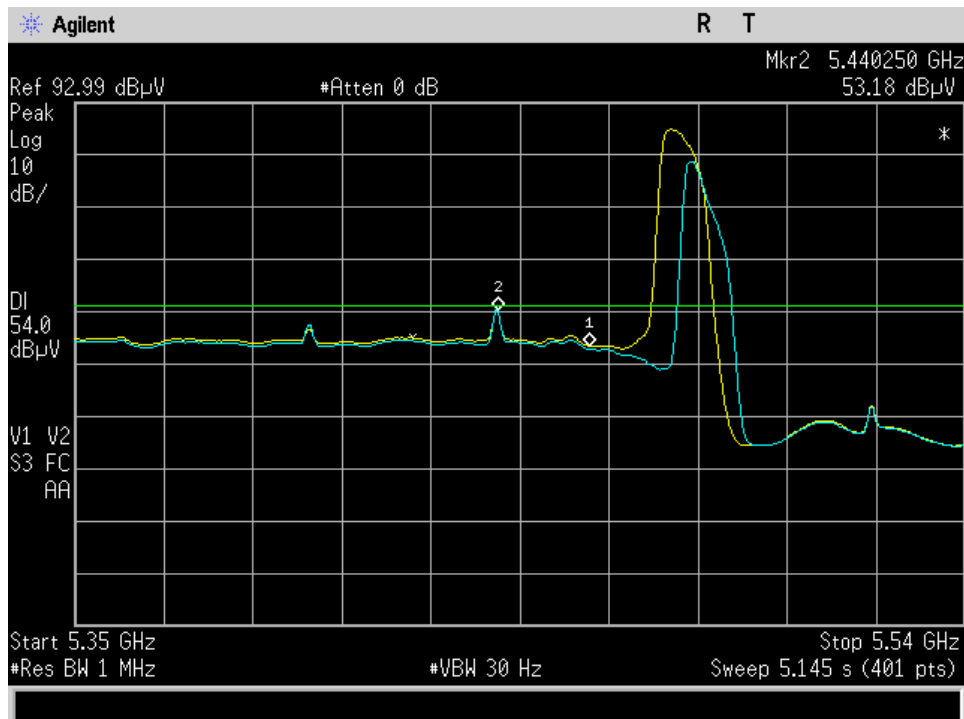


40 Mhz Band edge

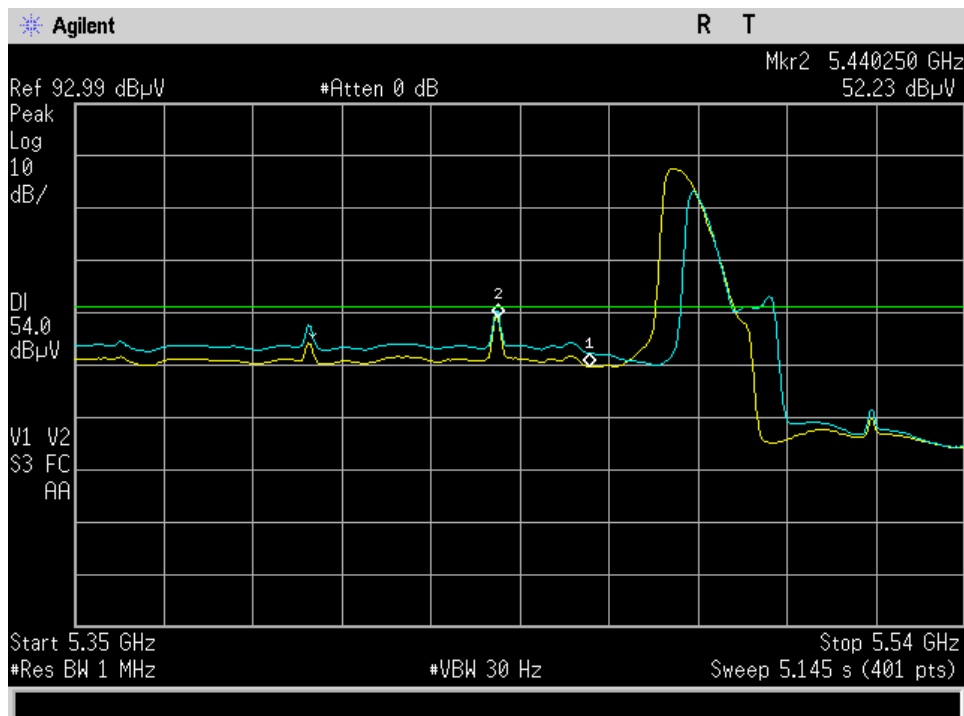




Radiated Band Edge Measurements UNII-2C 10 Mhz Band Edge

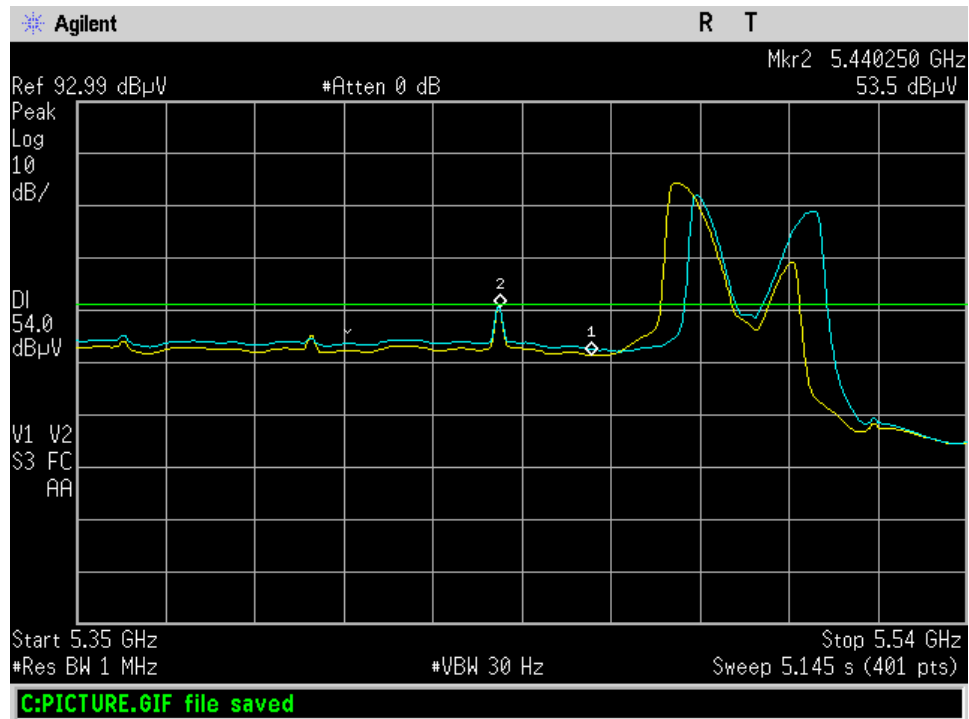


20 Mhz Band Edge





30 Mhz band edge



40 Mhz band Edge

