



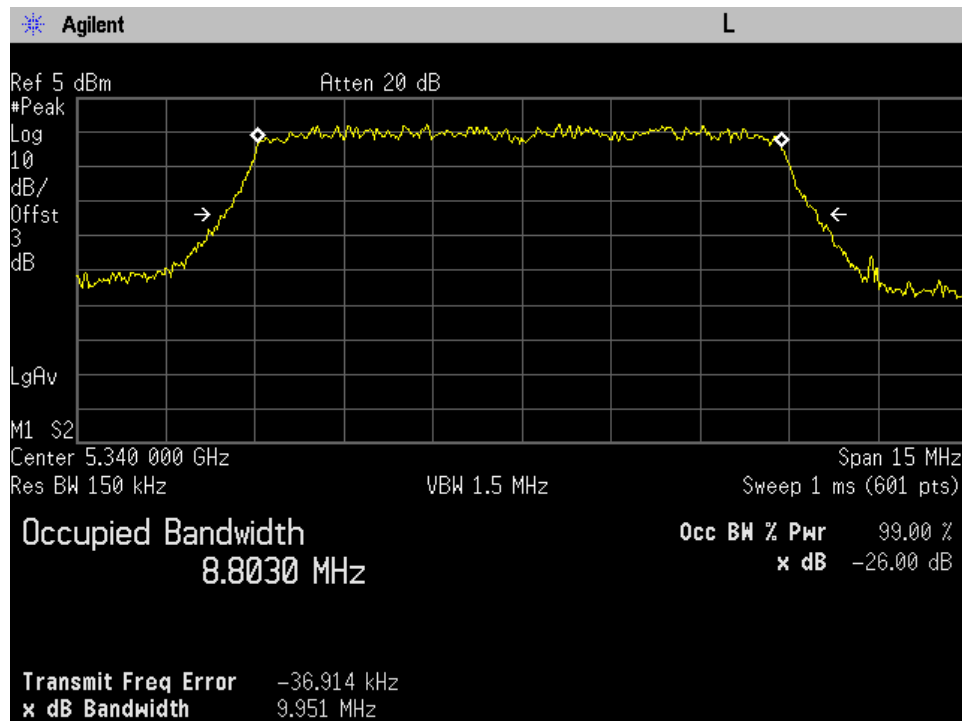
Annex C

Occupied Bandwidth

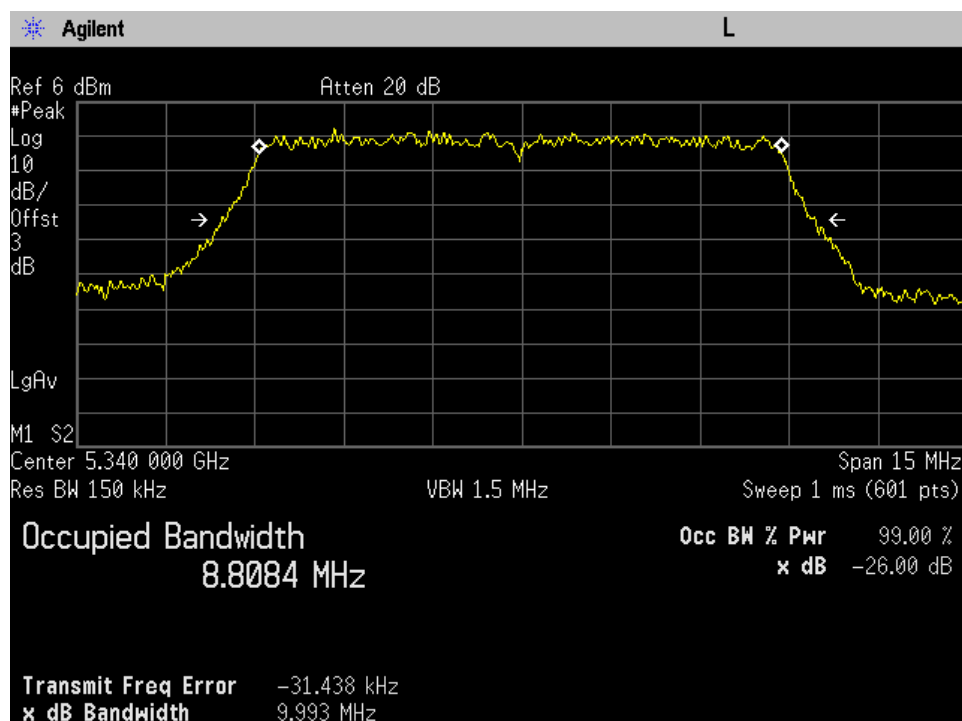
UNII-2A and UNII-2C



UNII-2A
10 Mhz High Ch Port J4

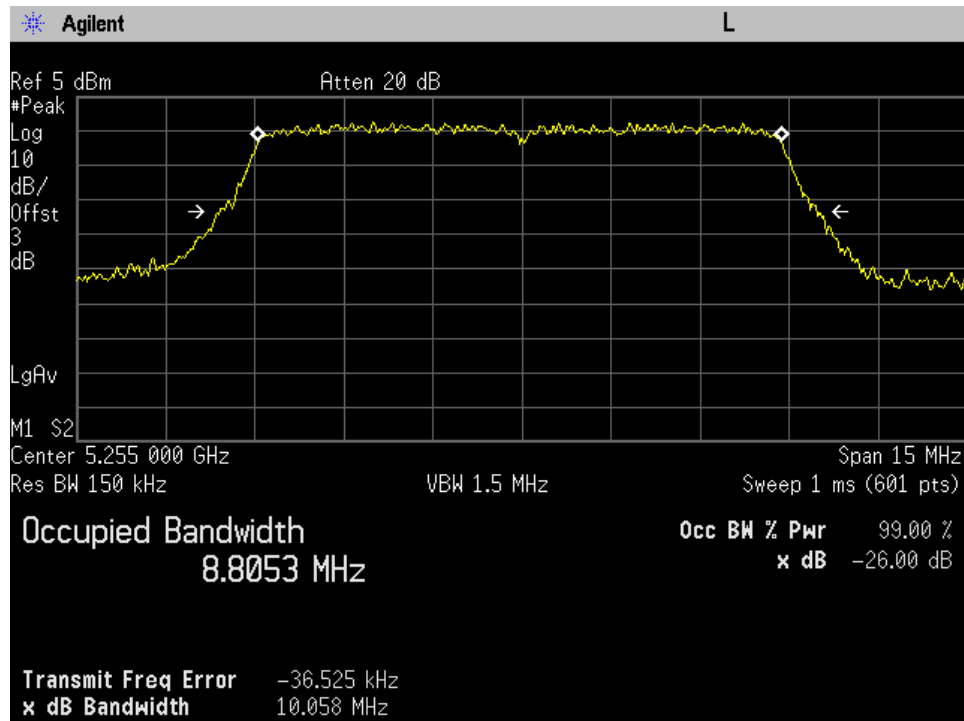


10 Mhz High Ch Port J7

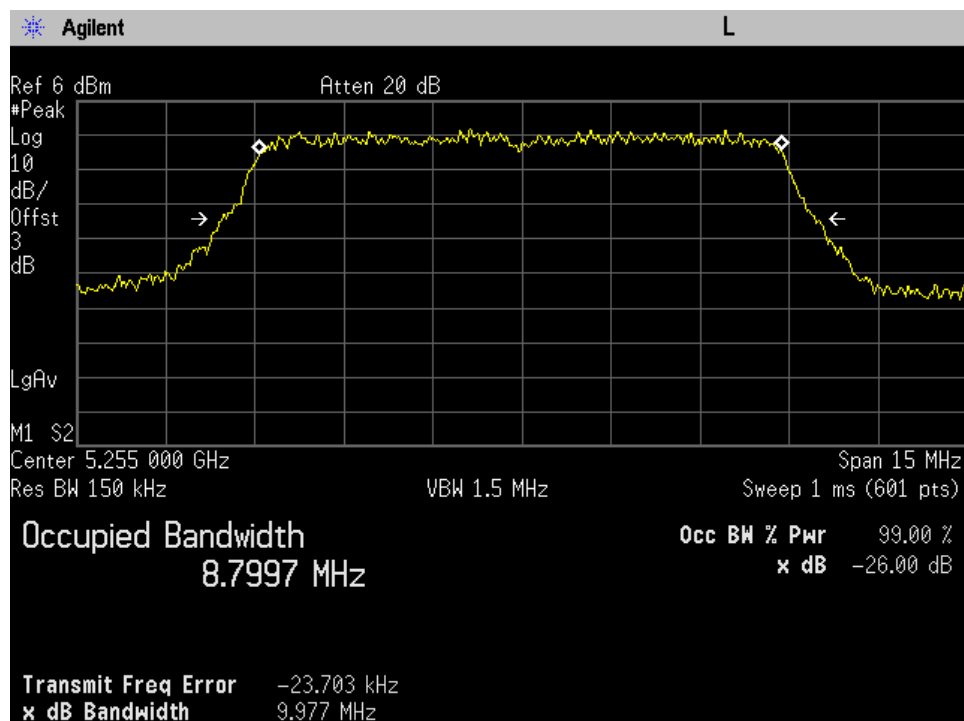




10 Mhz Low Ch Port J4

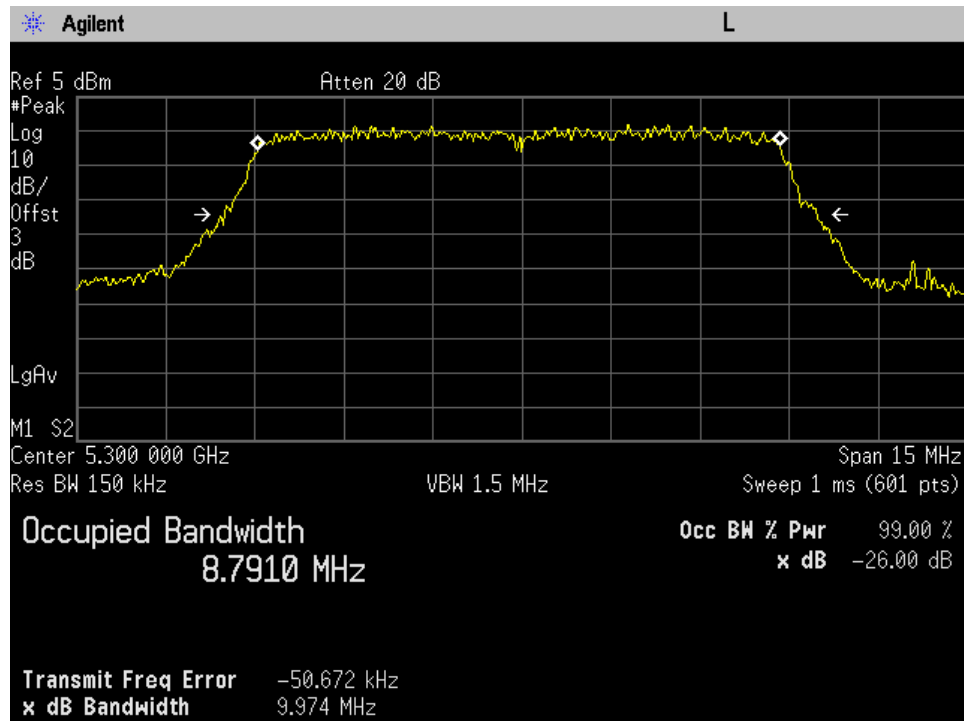


10 Mhz Low Ch Port J7

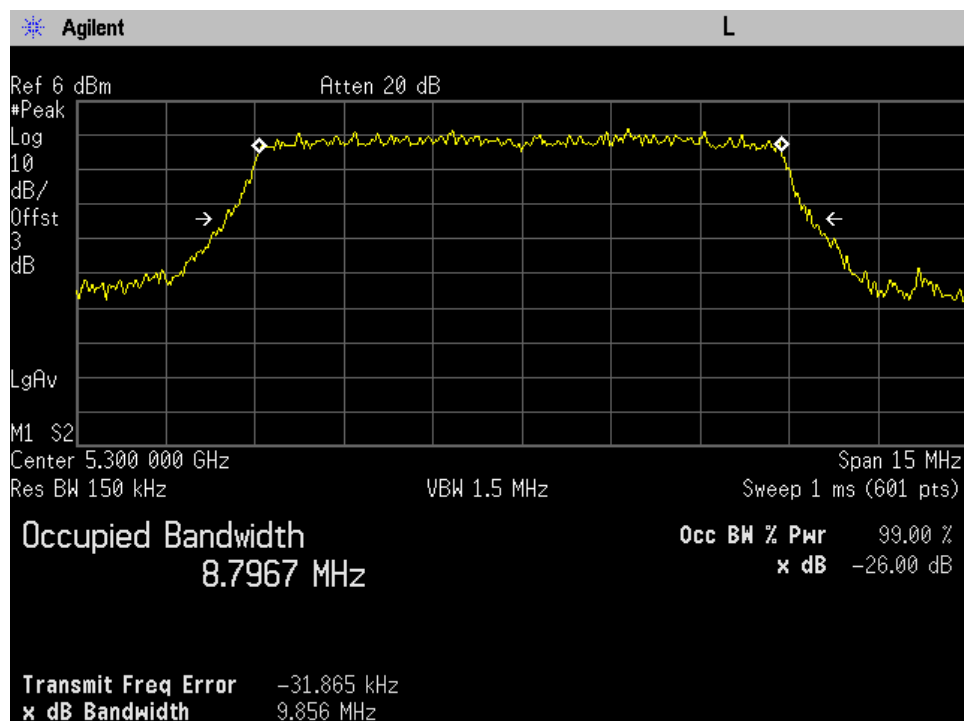




10 Mhz Mid Ch Port J4

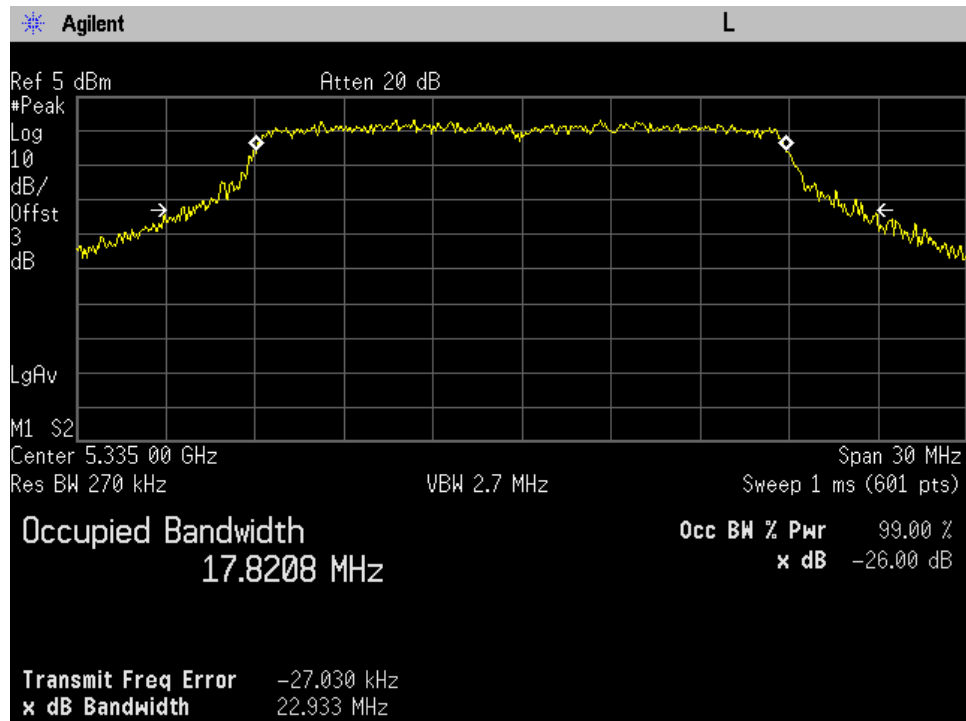


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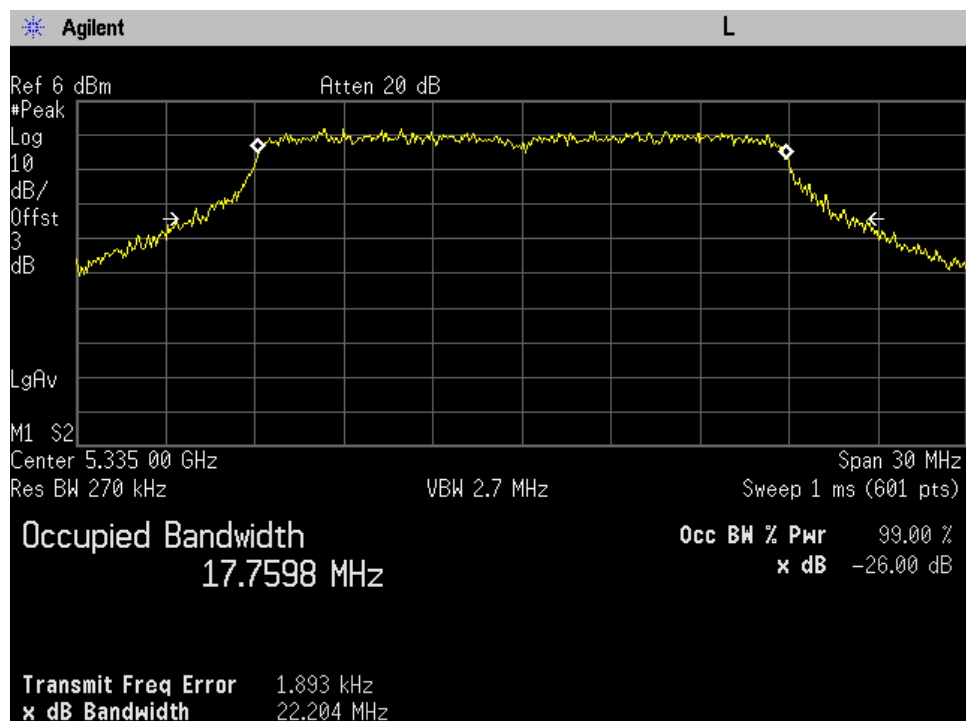




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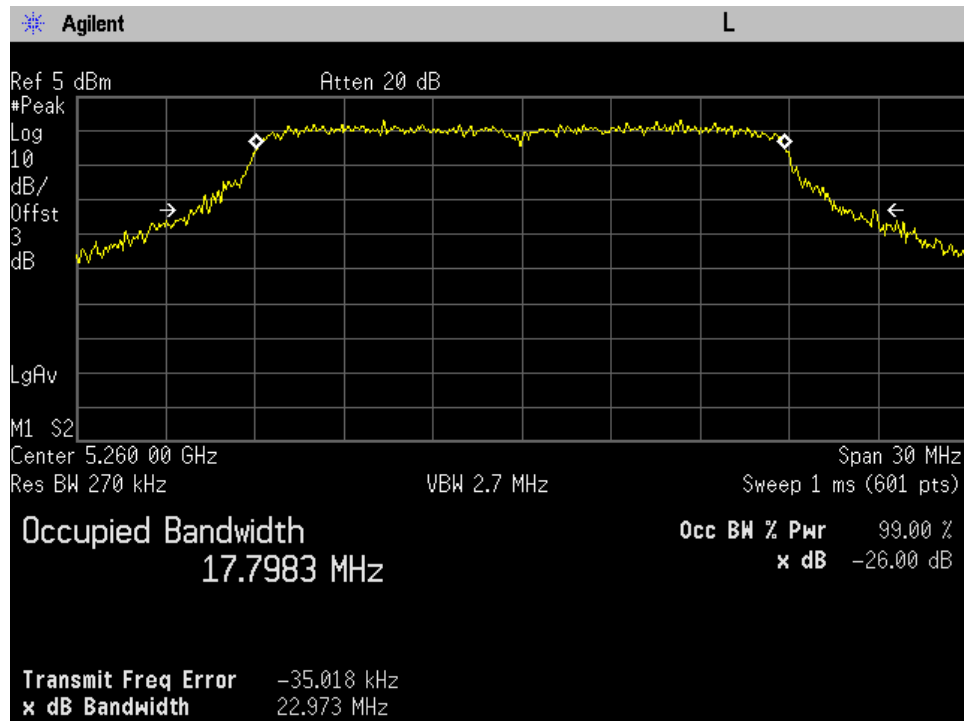


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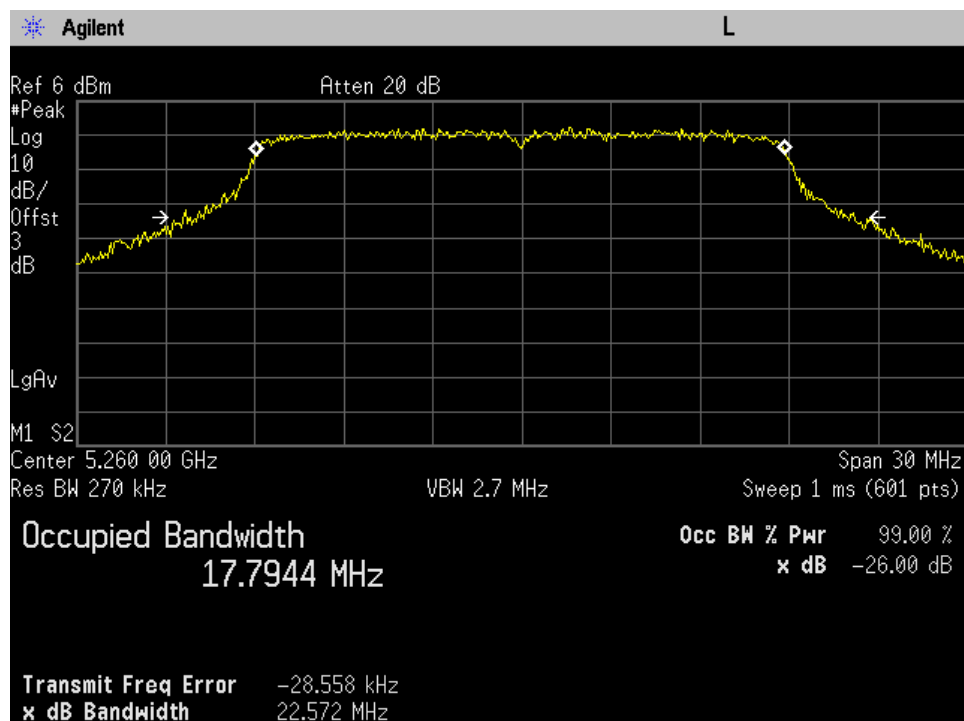




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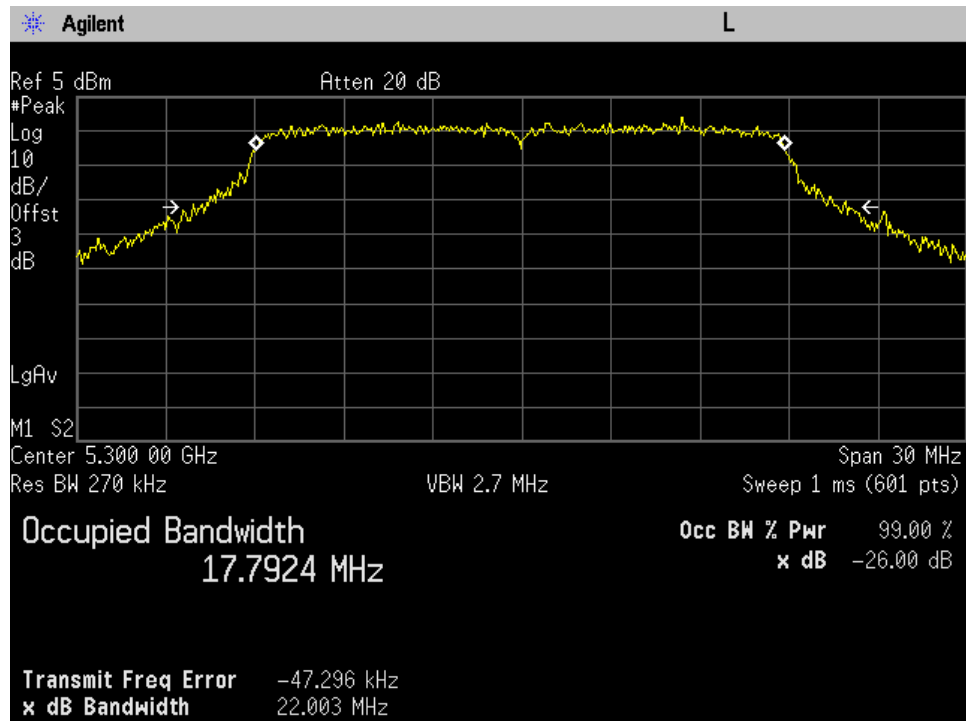


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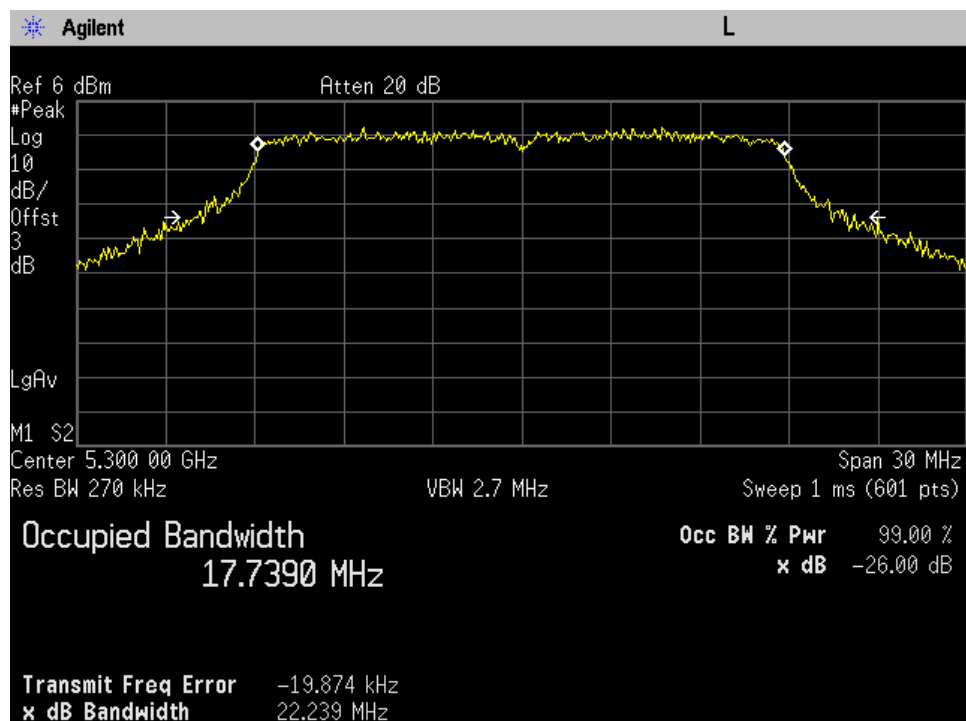




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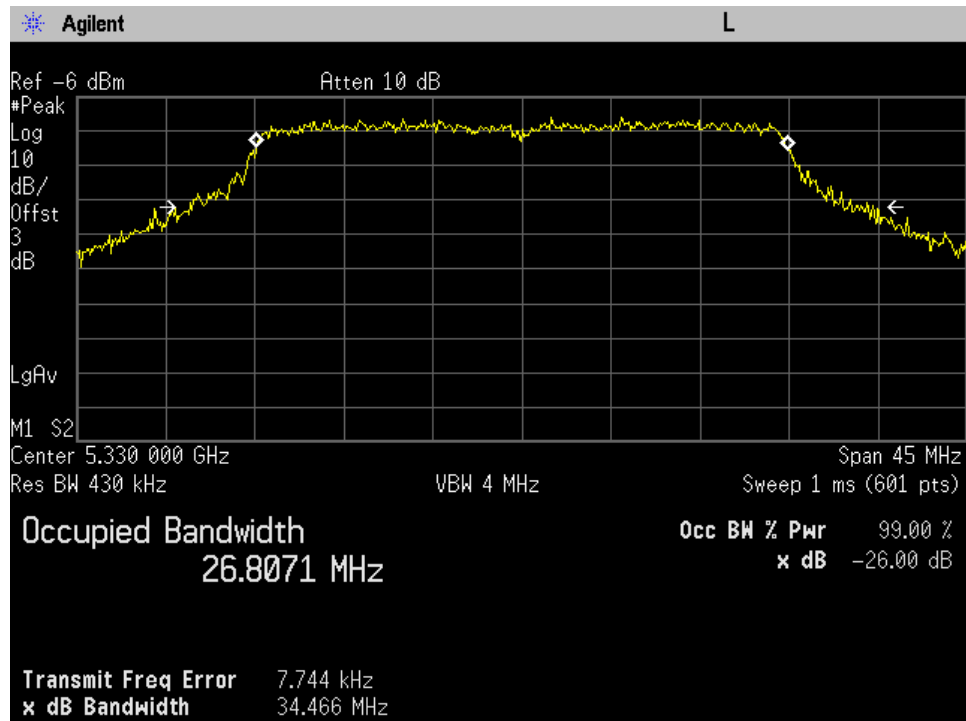


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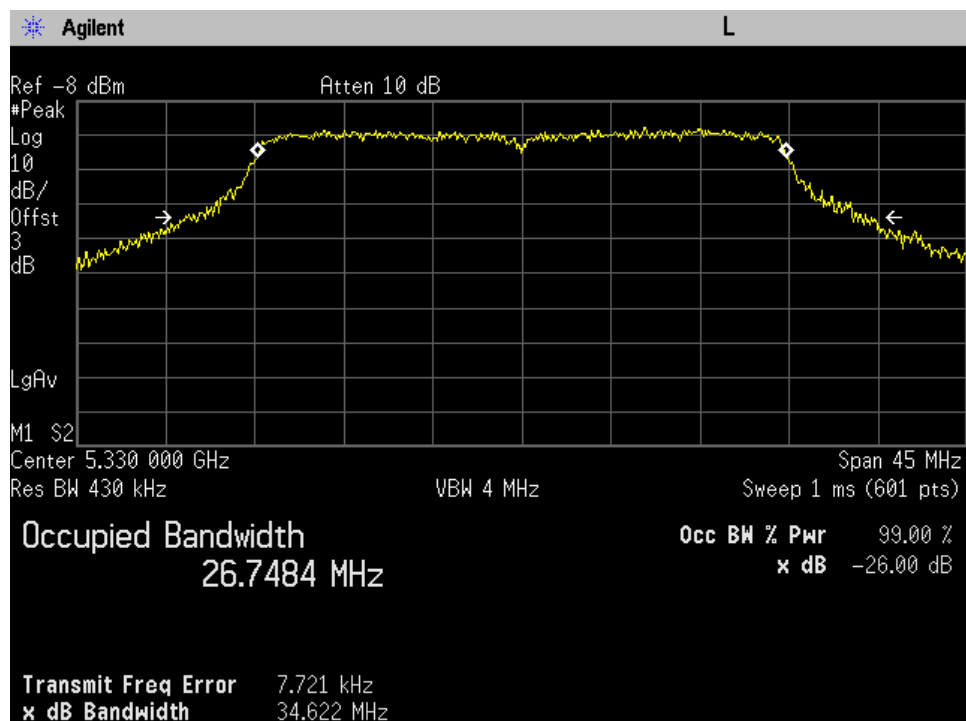




30 Mhz High Ch Port J4

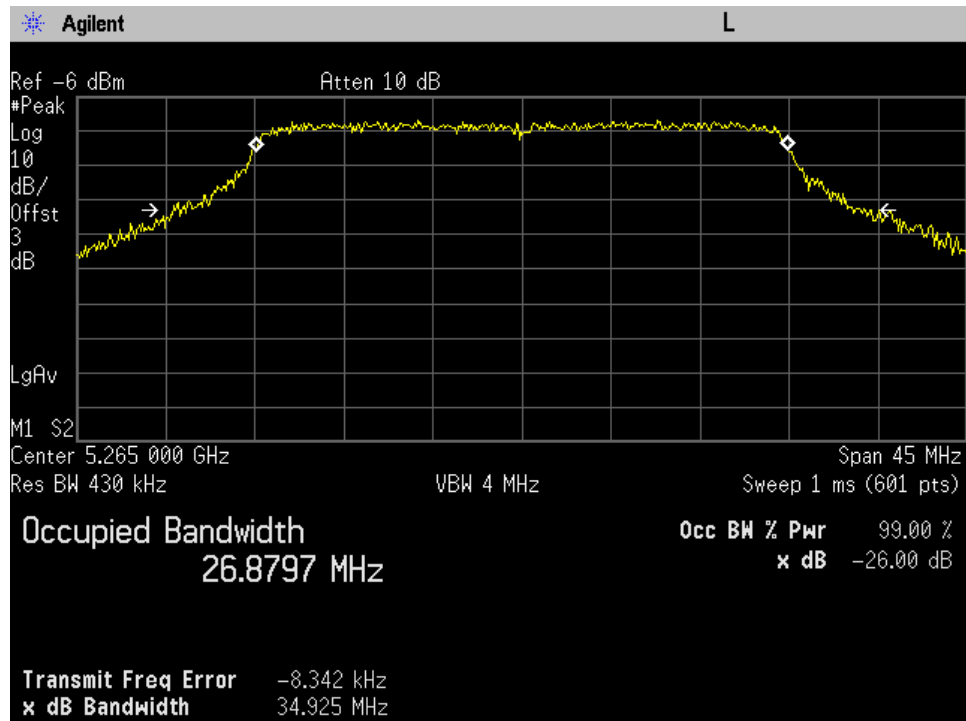


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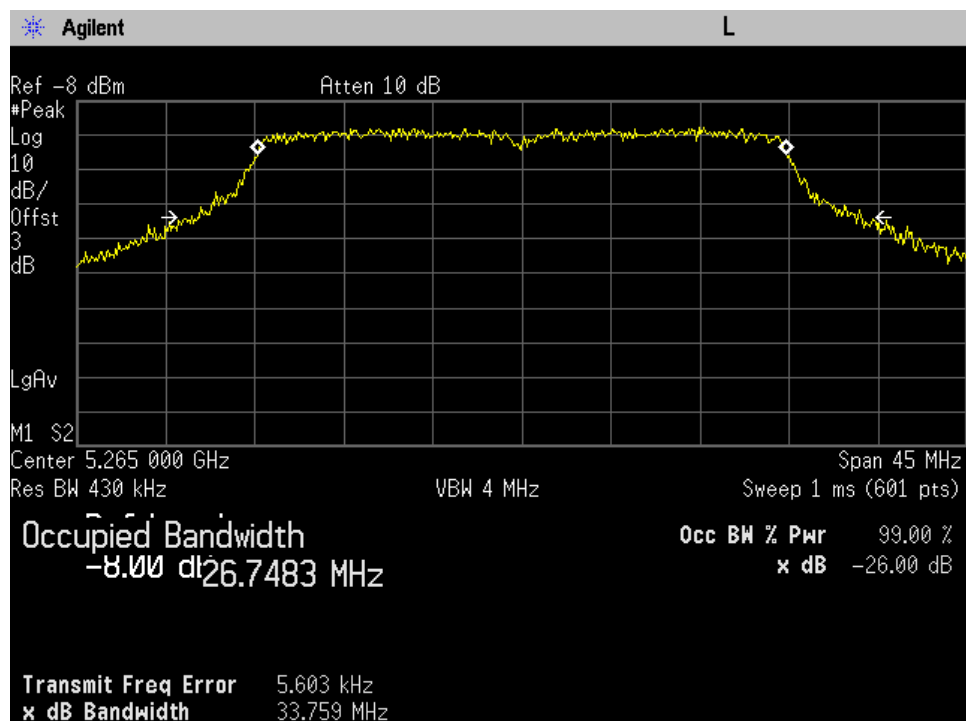




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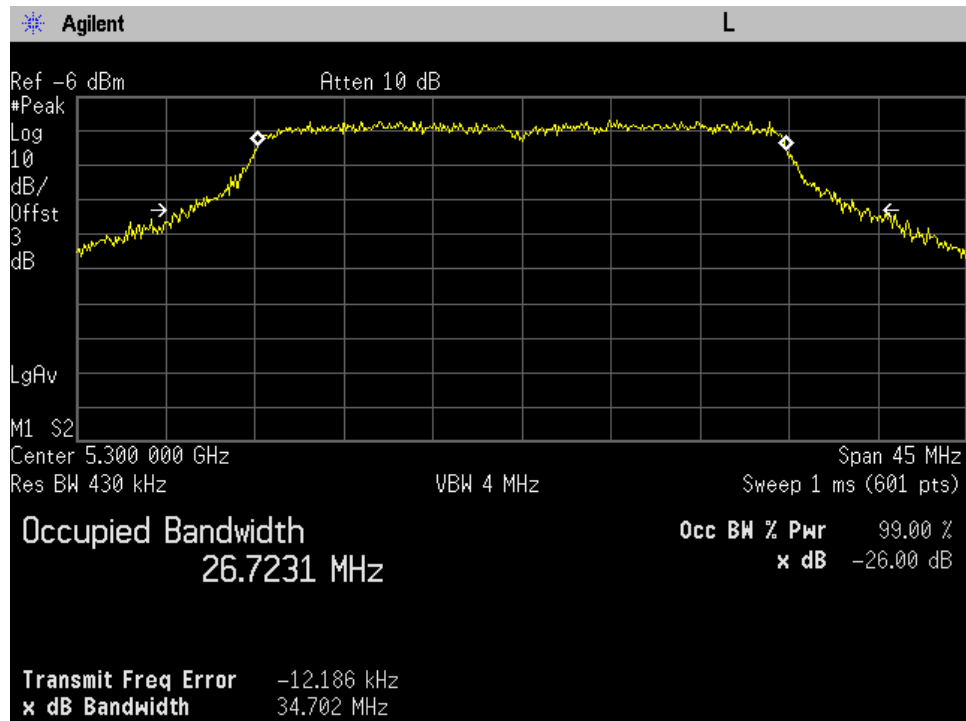


30 Mhz Low Ch Port J7

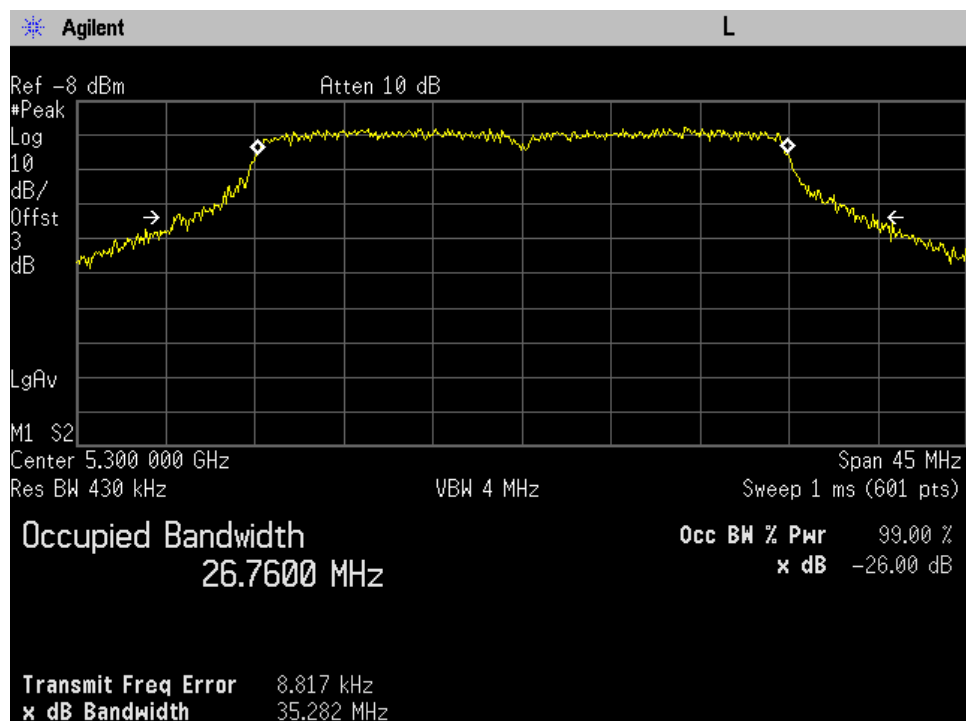




30 Mhz Mid Ch Port J4

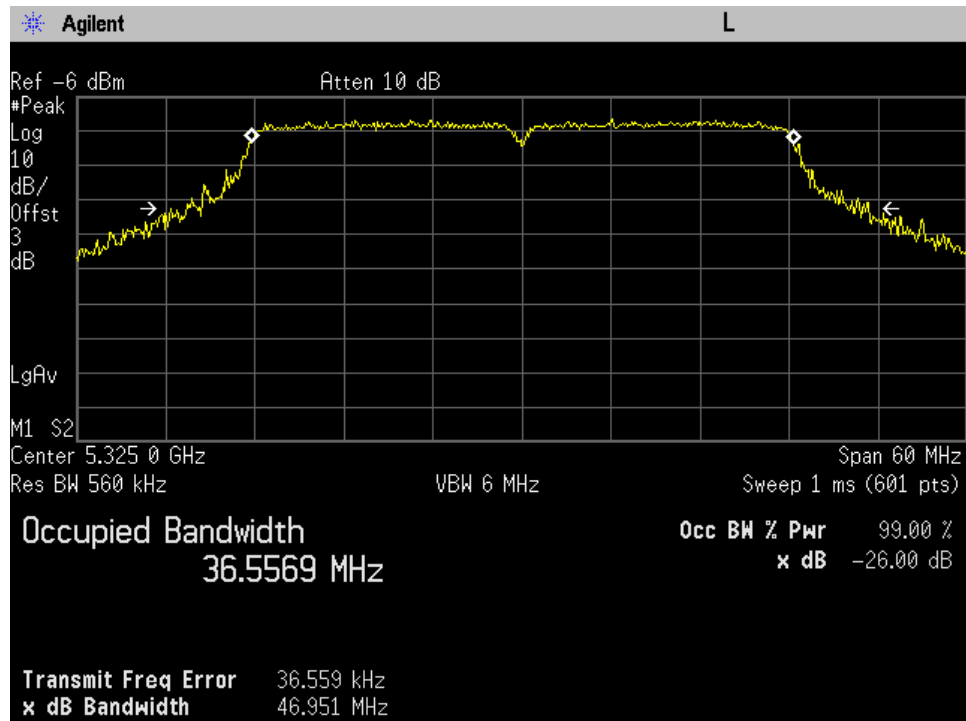


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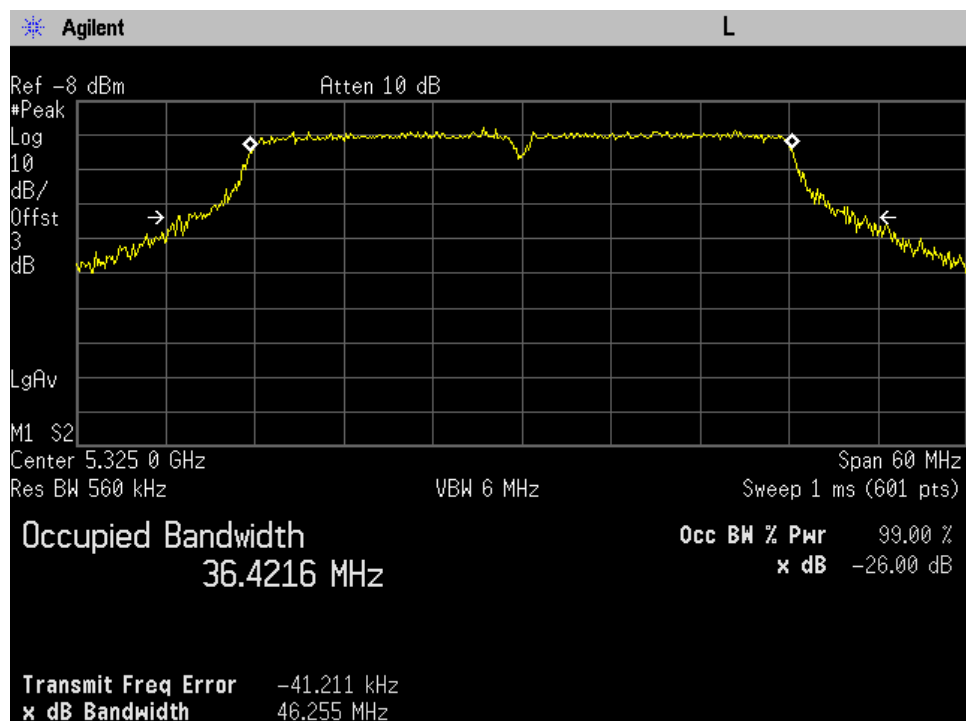




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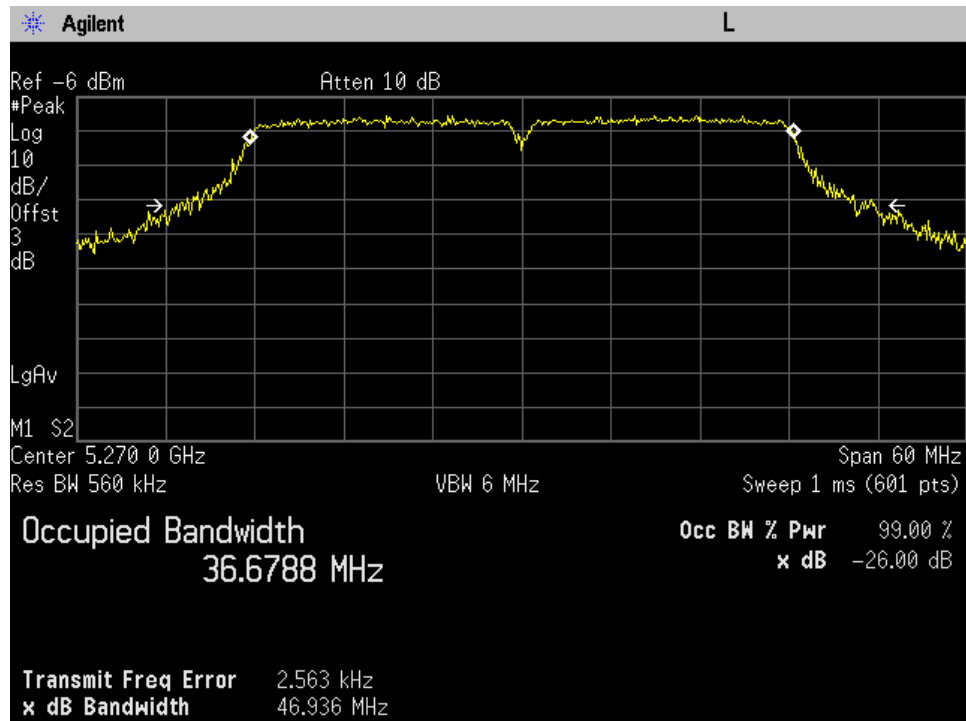


40 Mhz High Ch Port J7

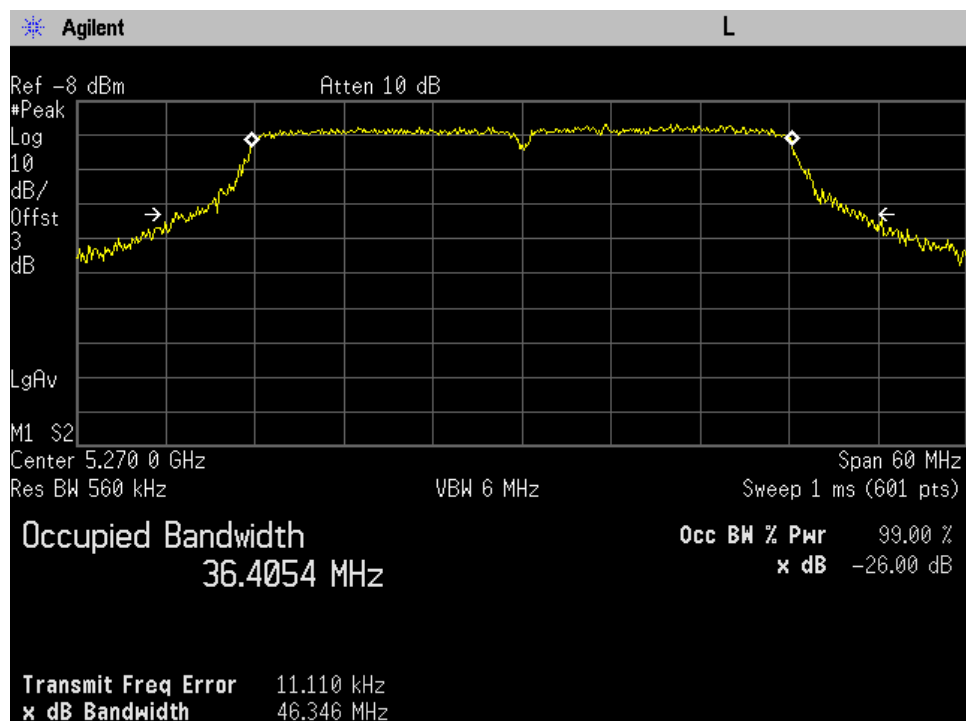




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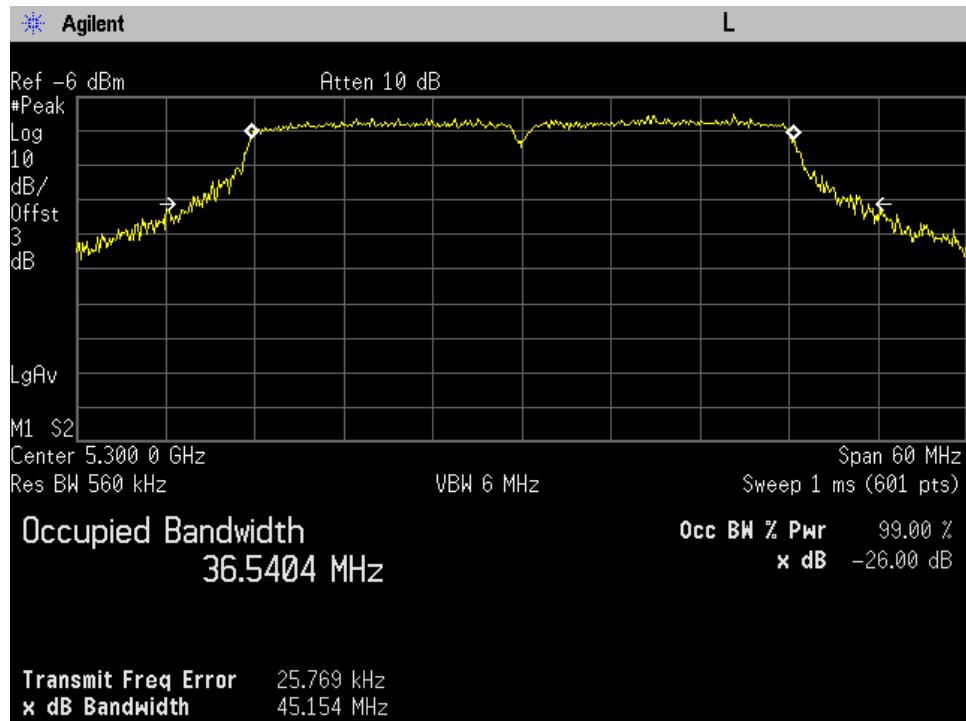


40 Mhz Low Ch Port J7

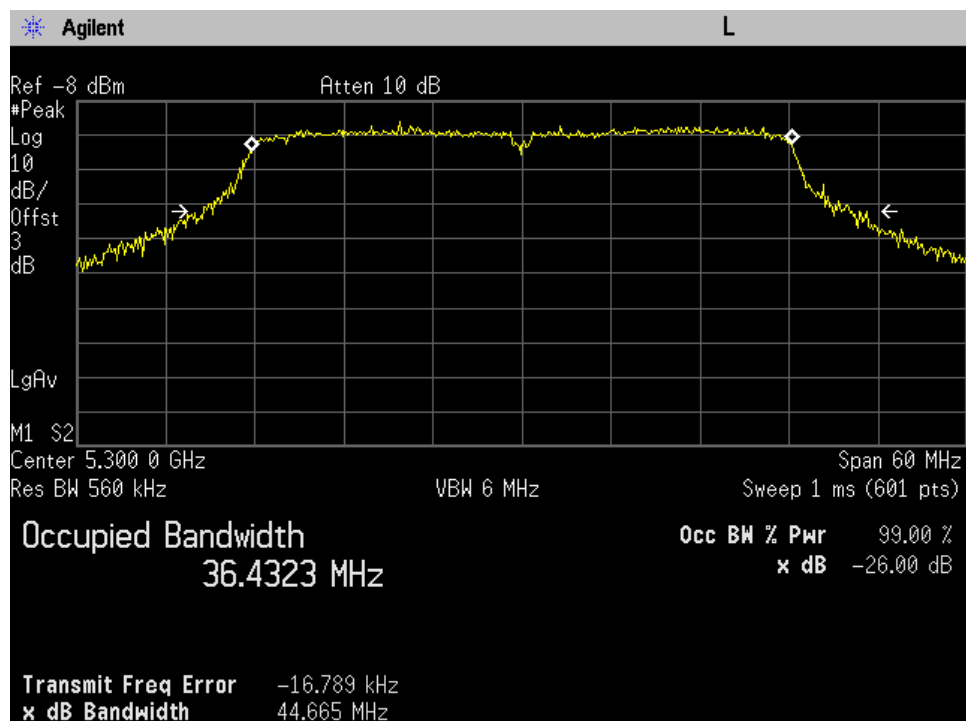




40 Mhz Mid Ch Port J4



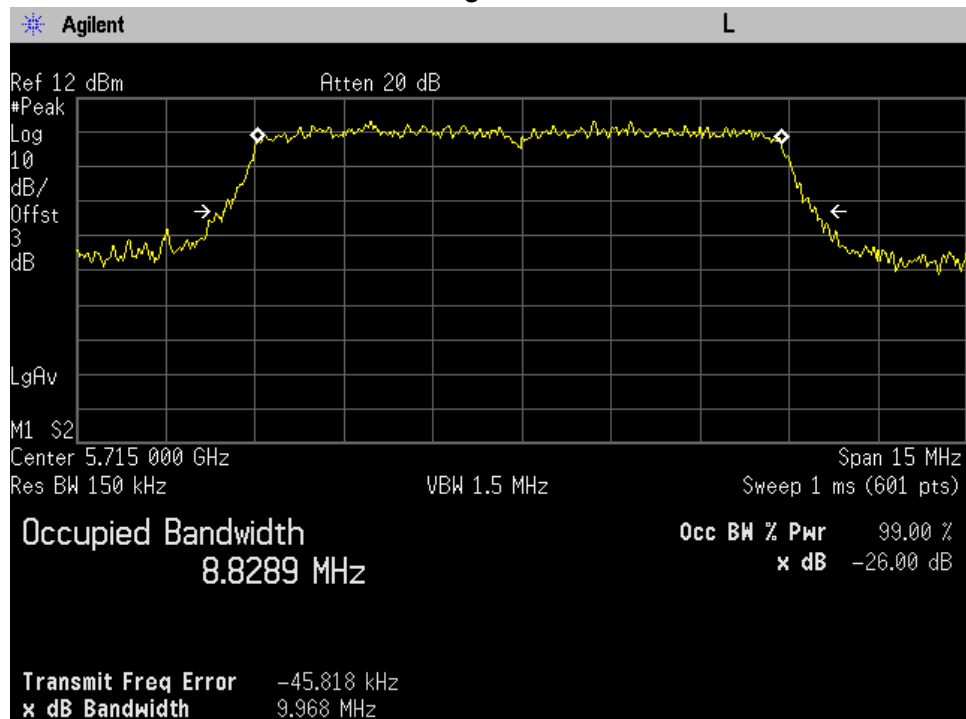
40 Mhz Mid Ch Port J7



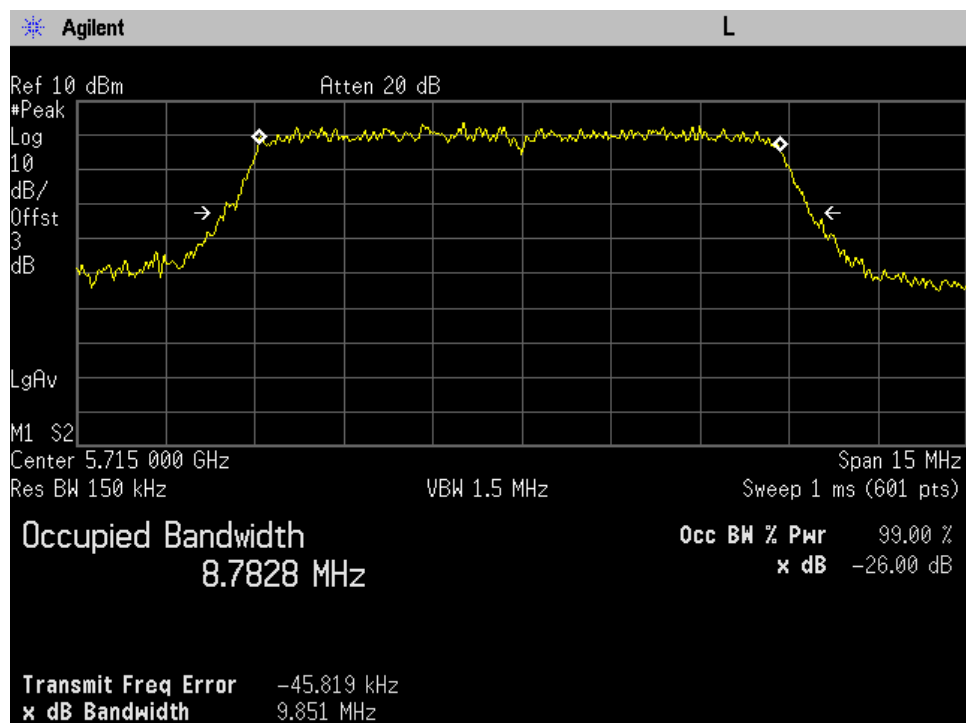


UNII-2C

10 Mhz High Ch Port J4

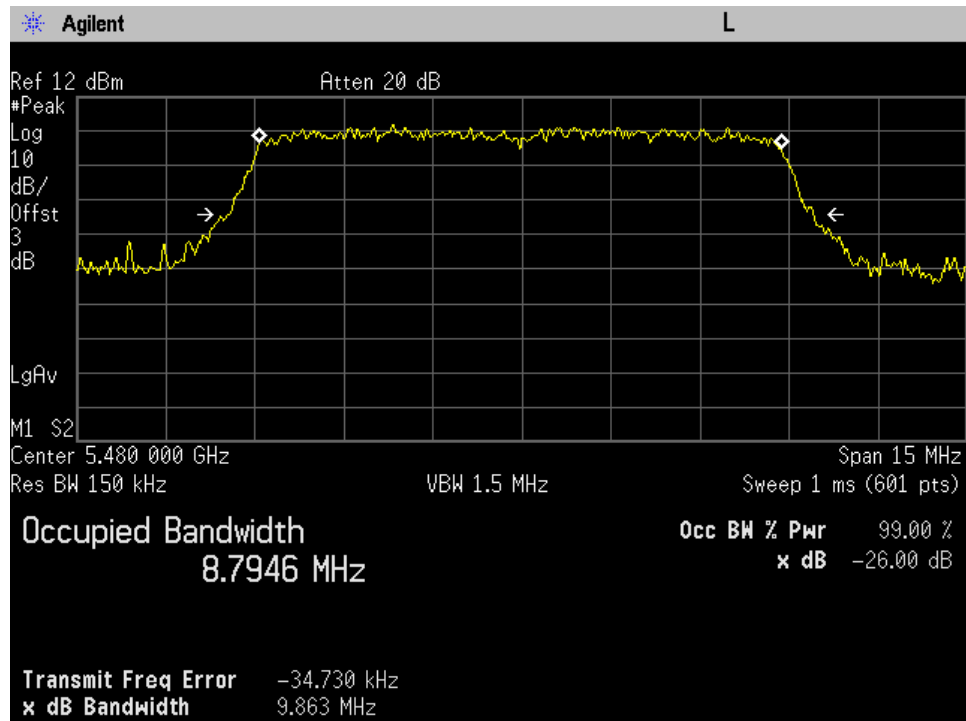


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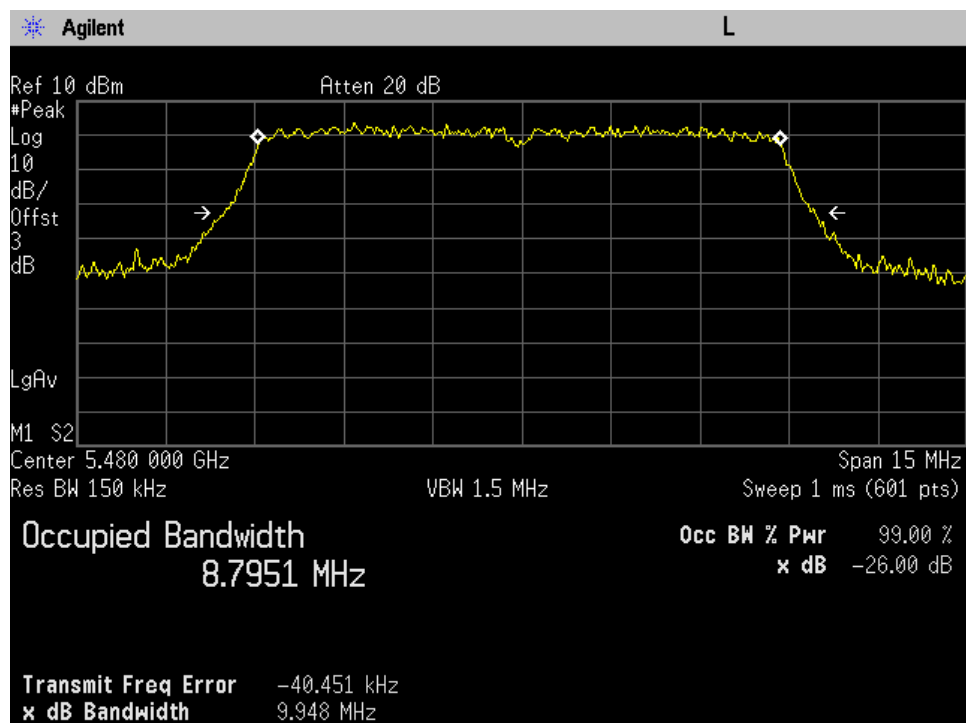




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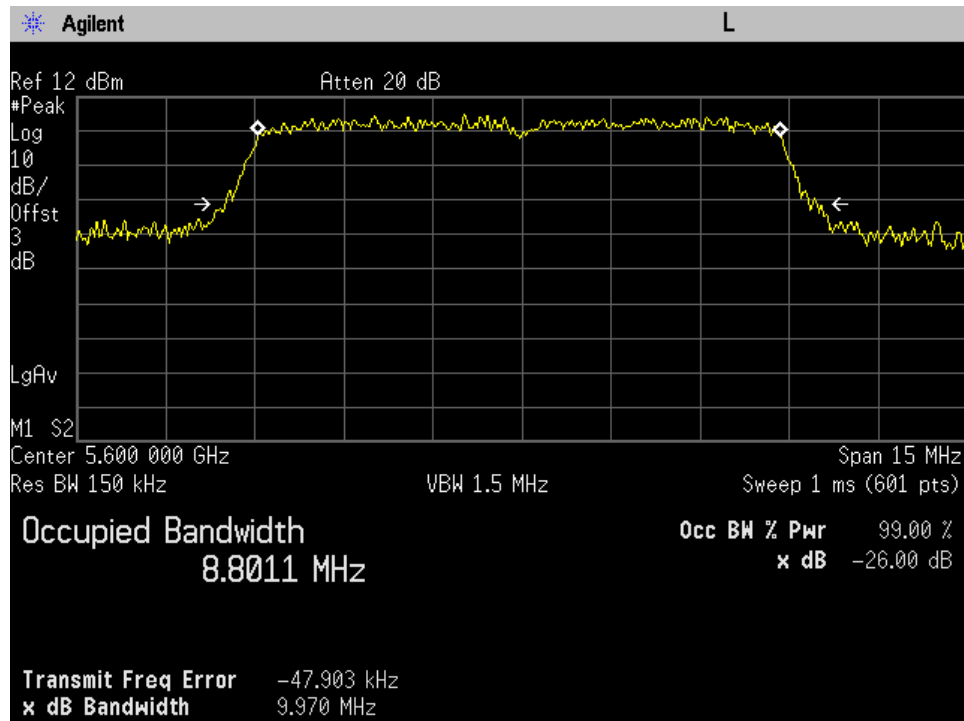


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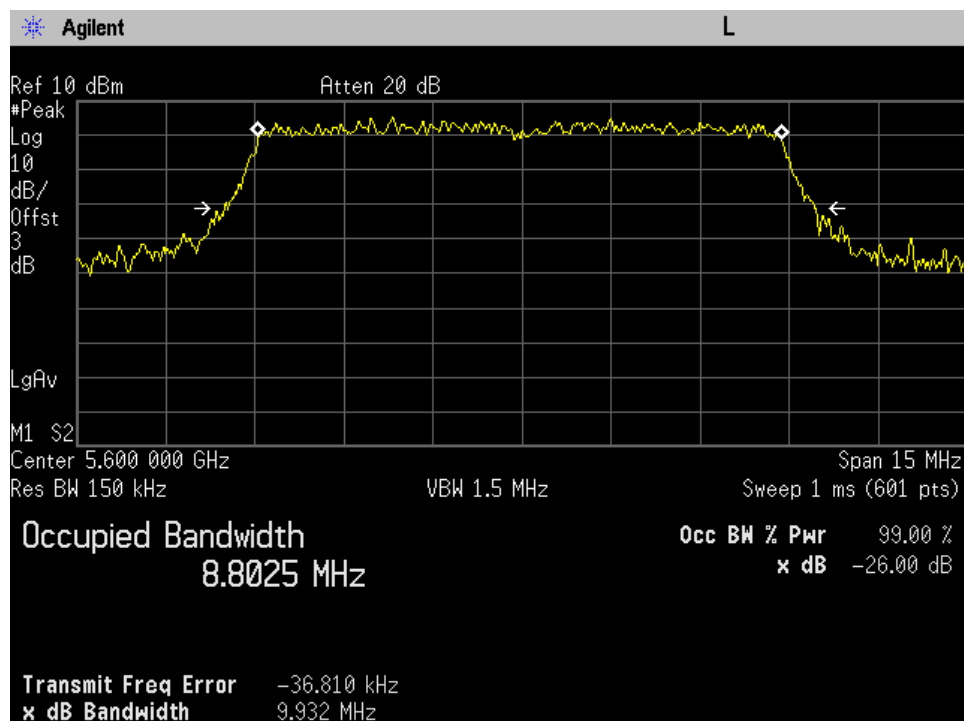




10 Mhz Mid Ch Port J4

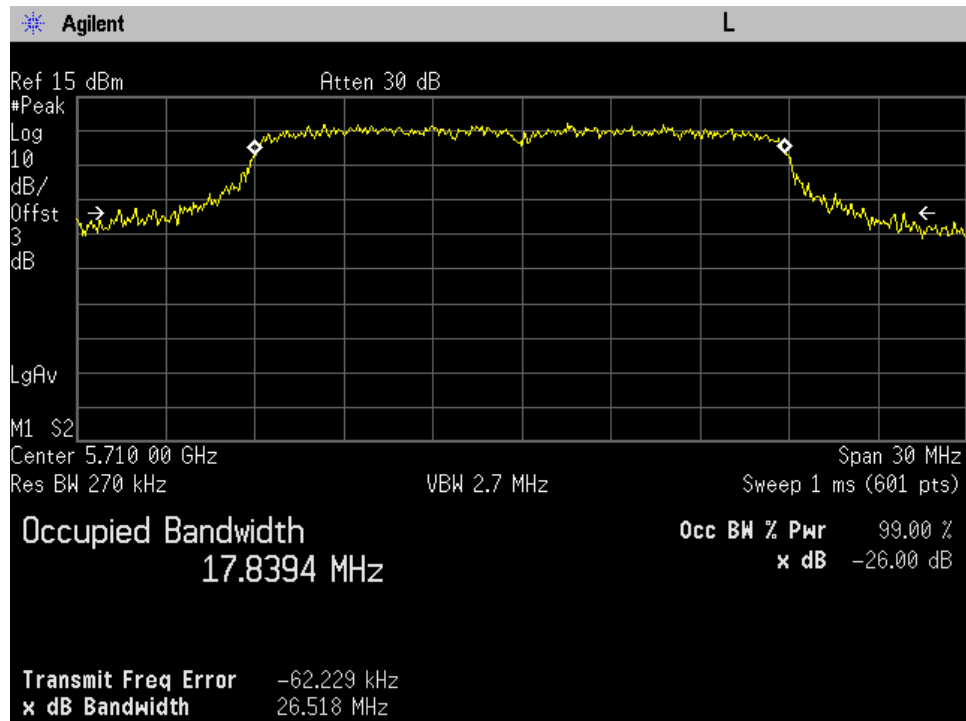


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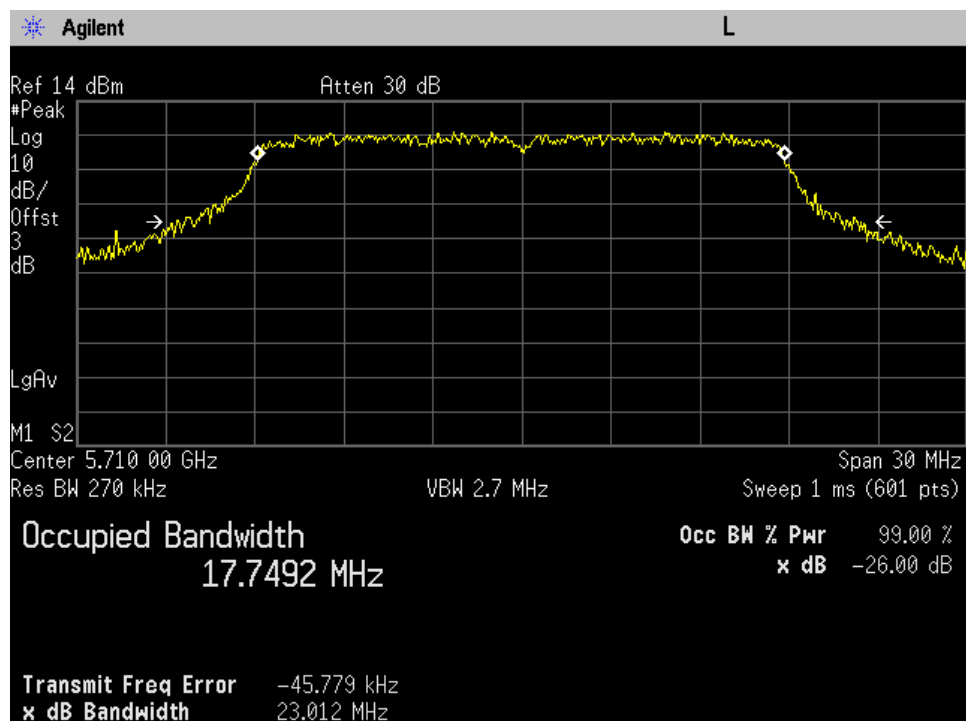




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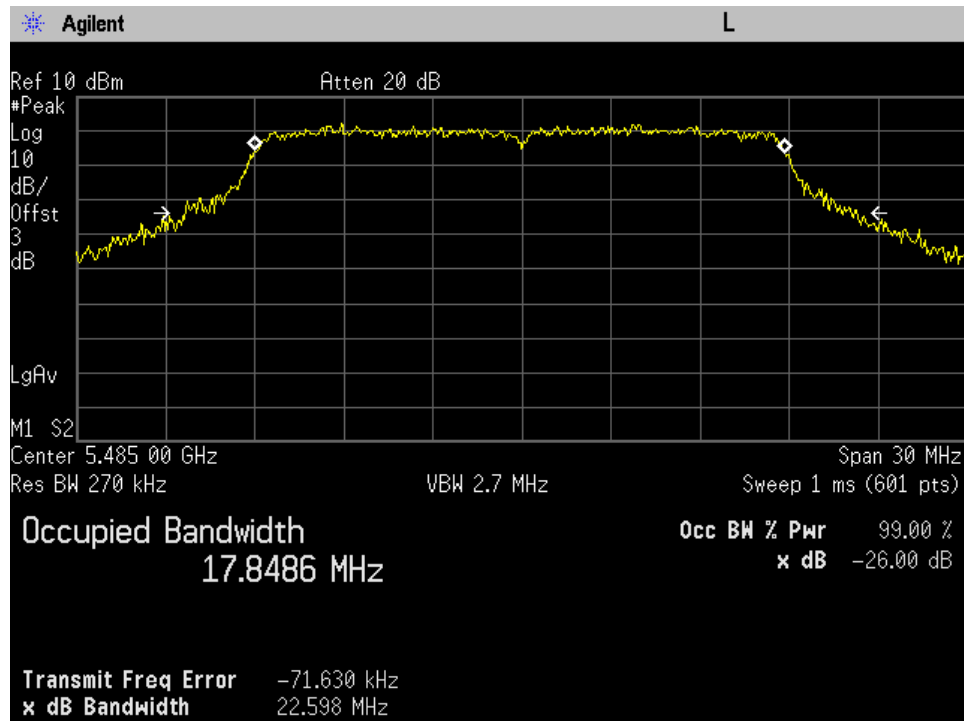


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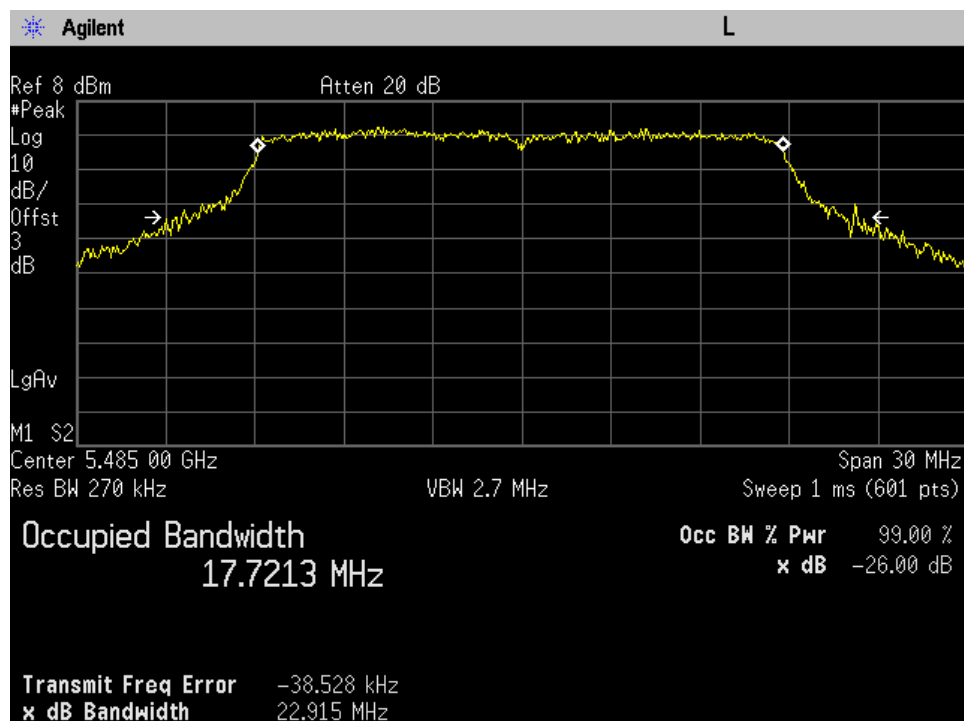




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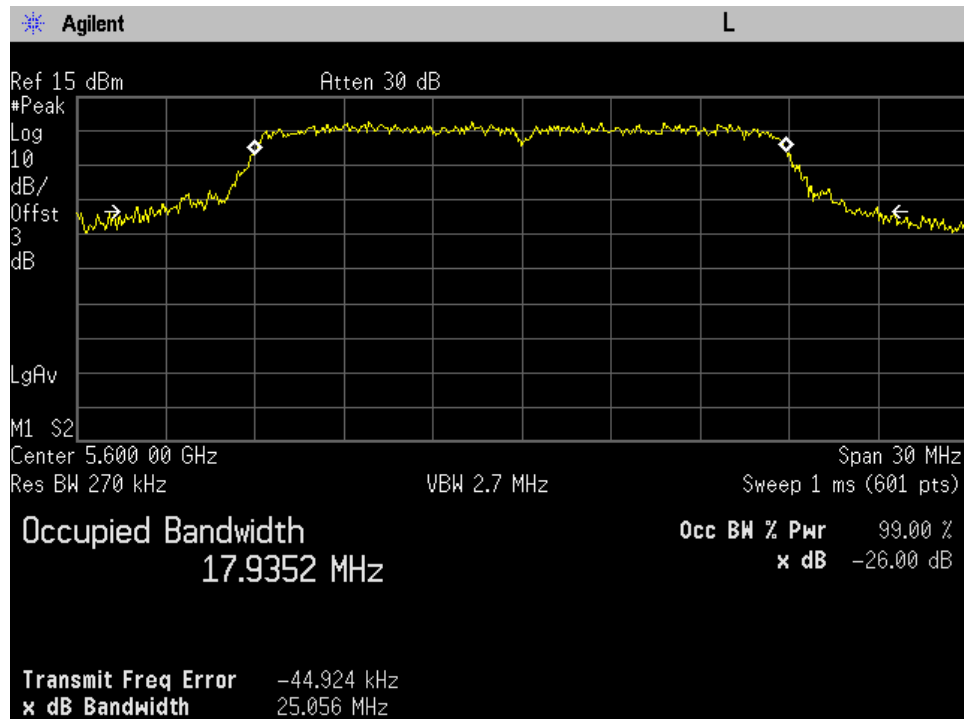


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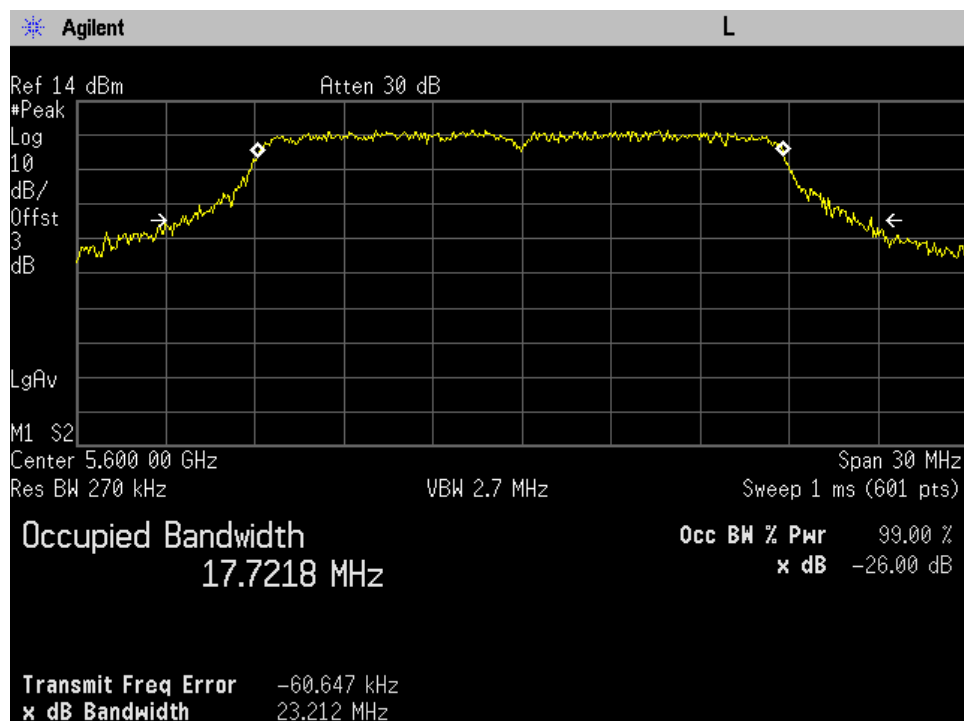




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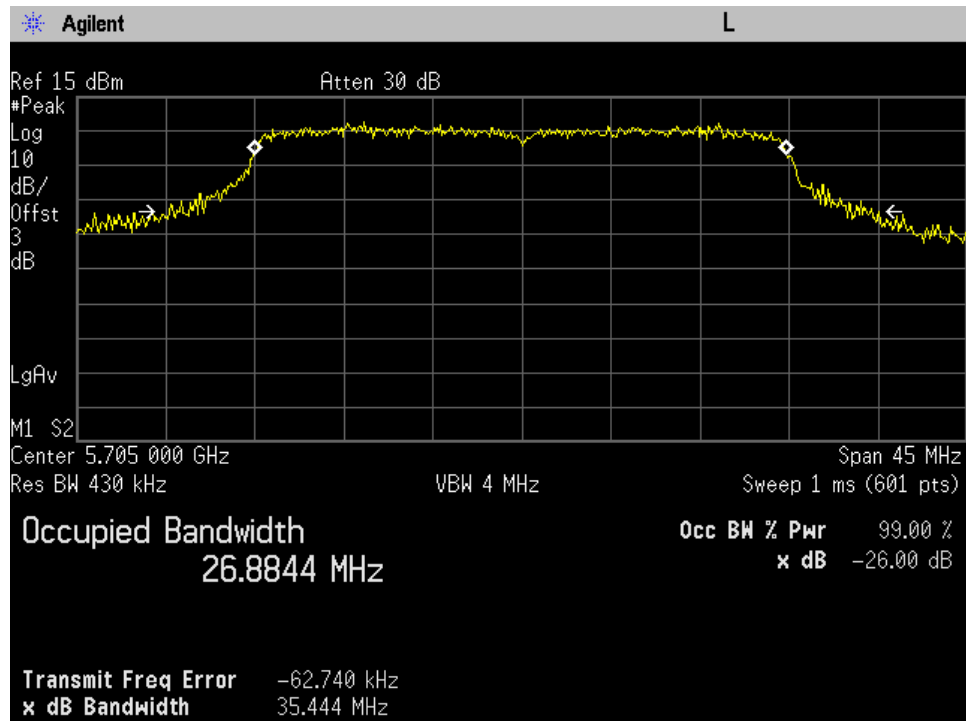


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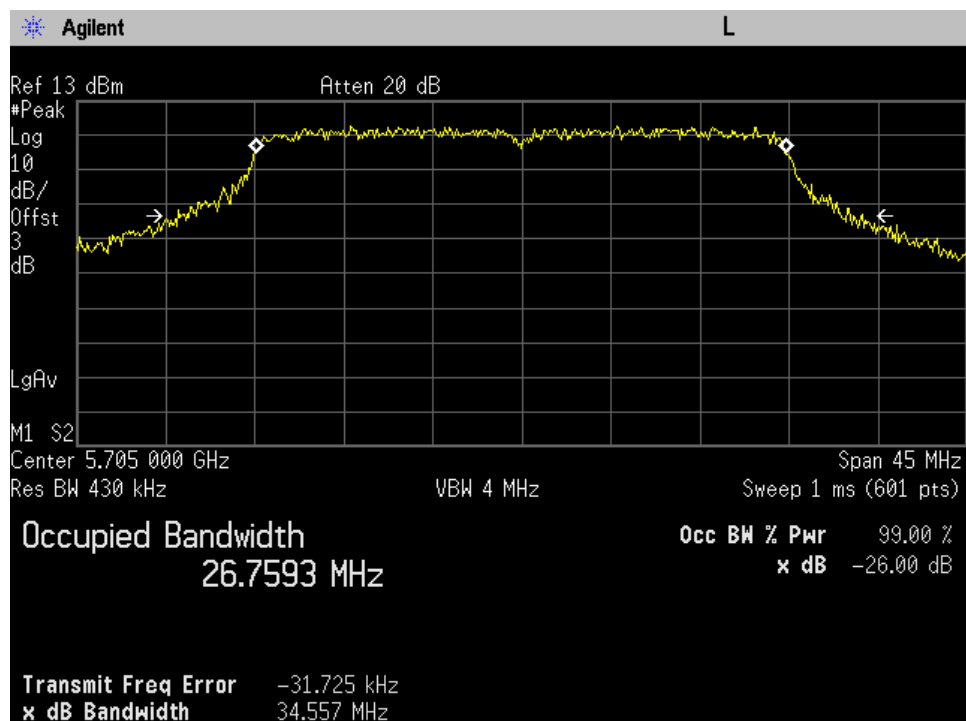




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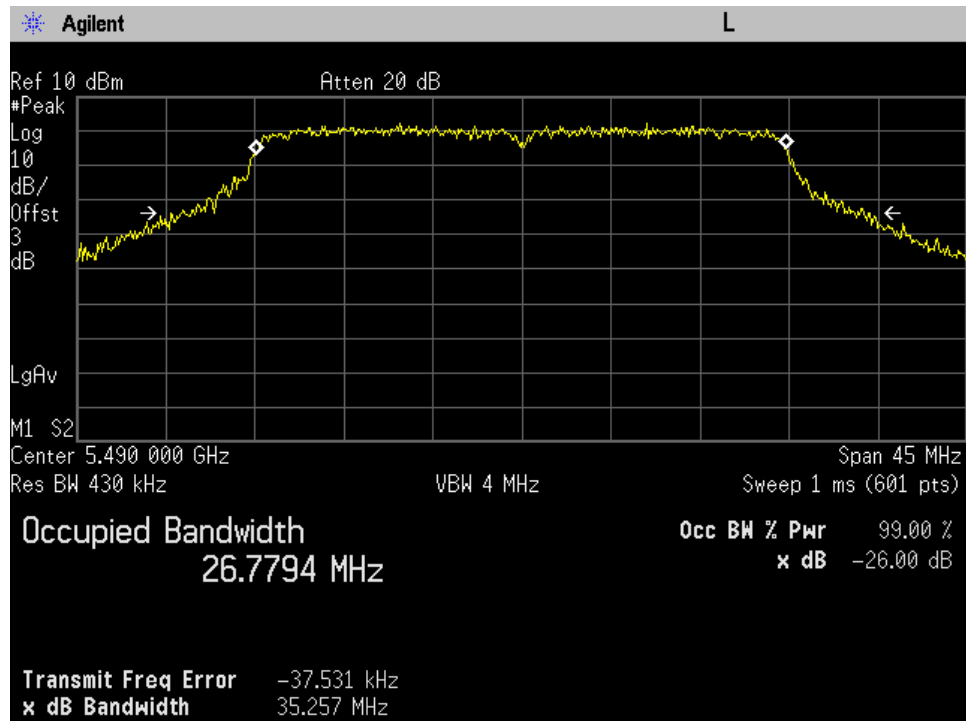


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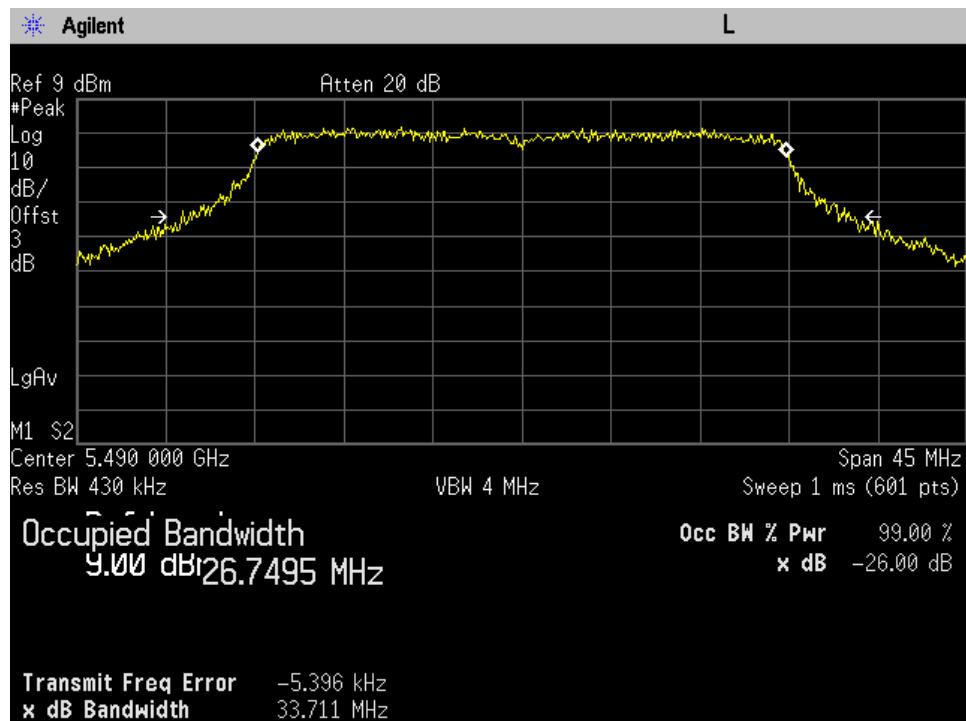




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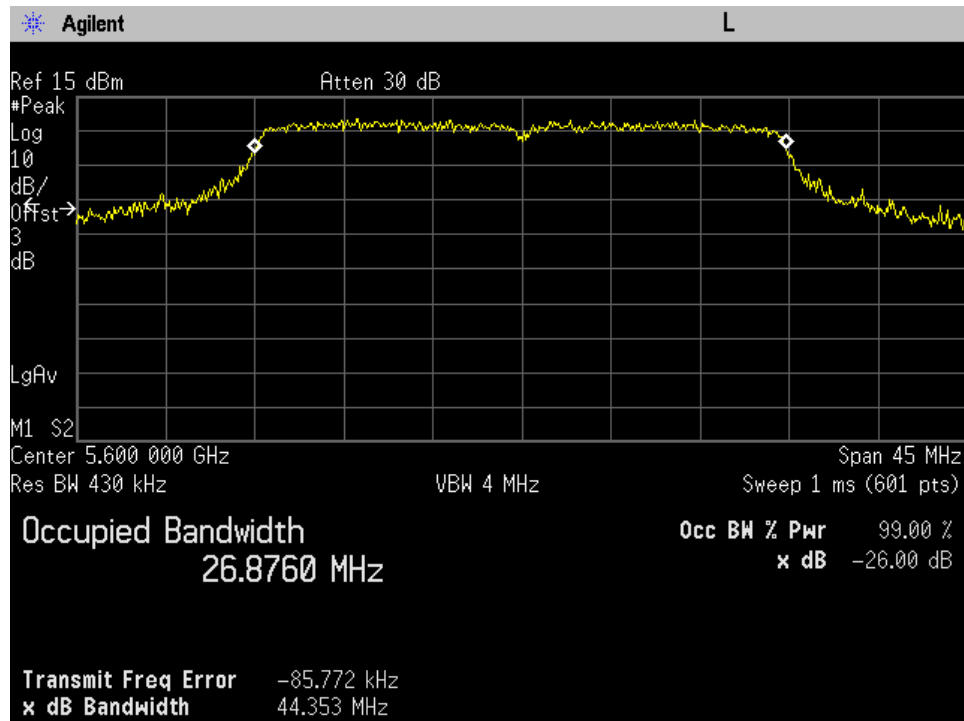


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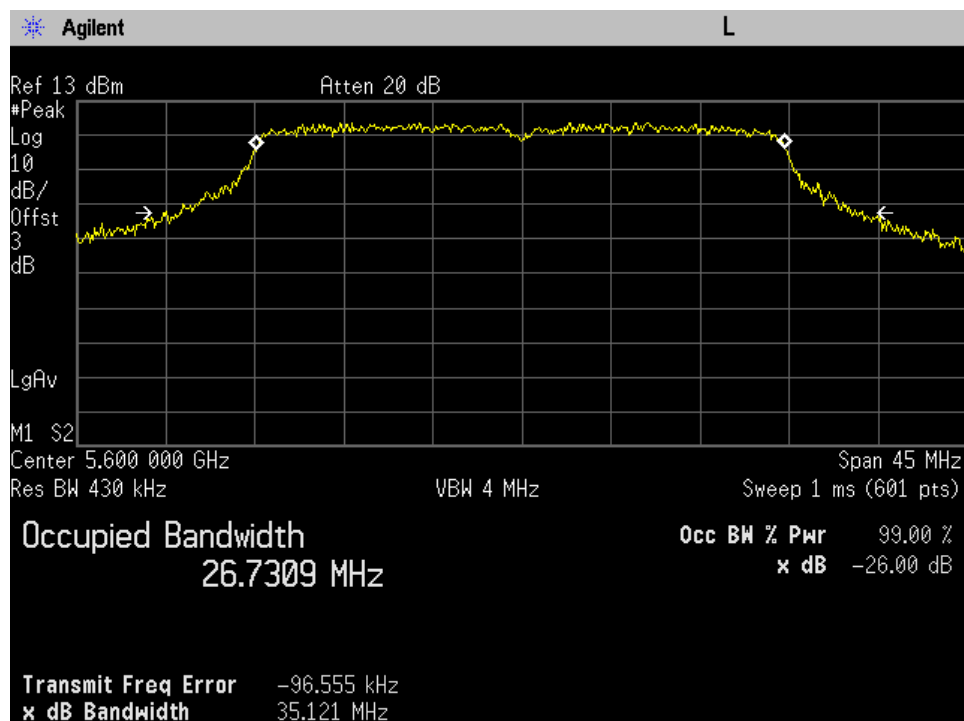




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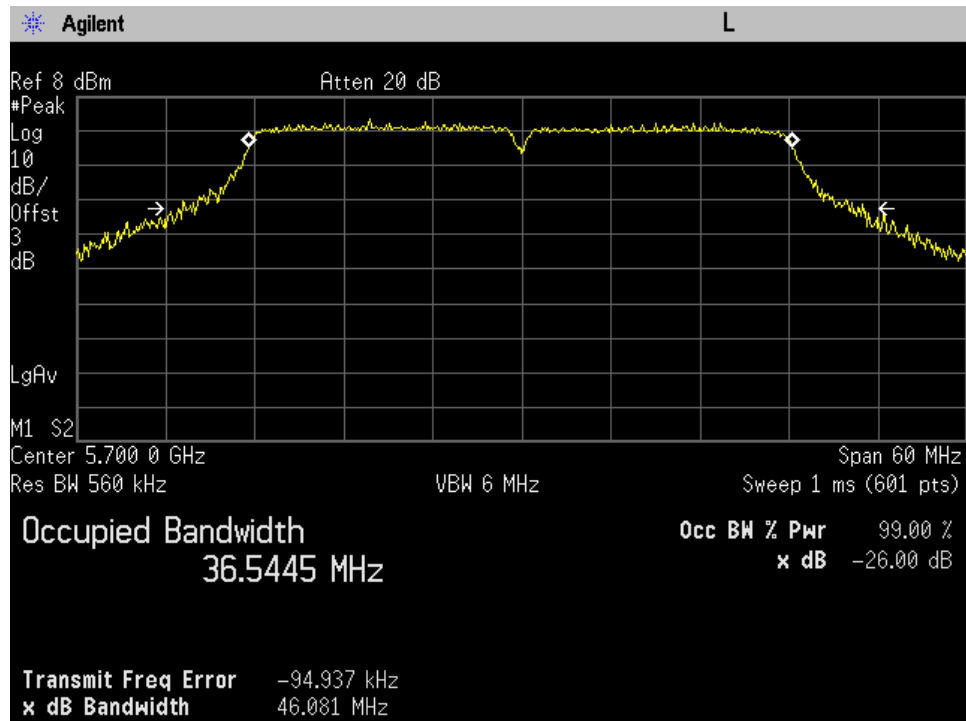


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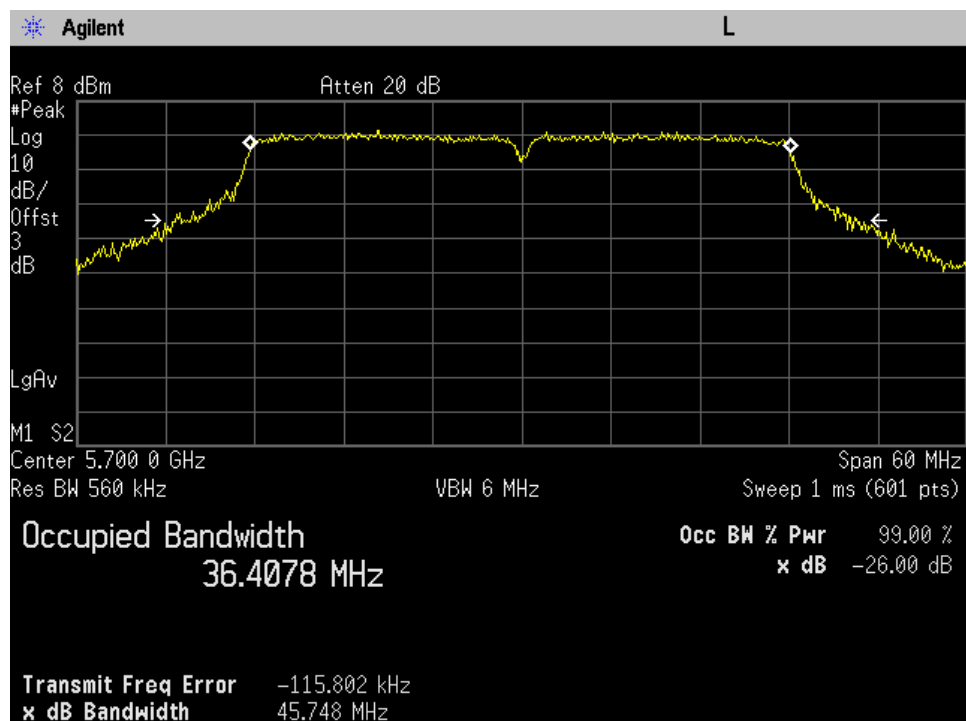




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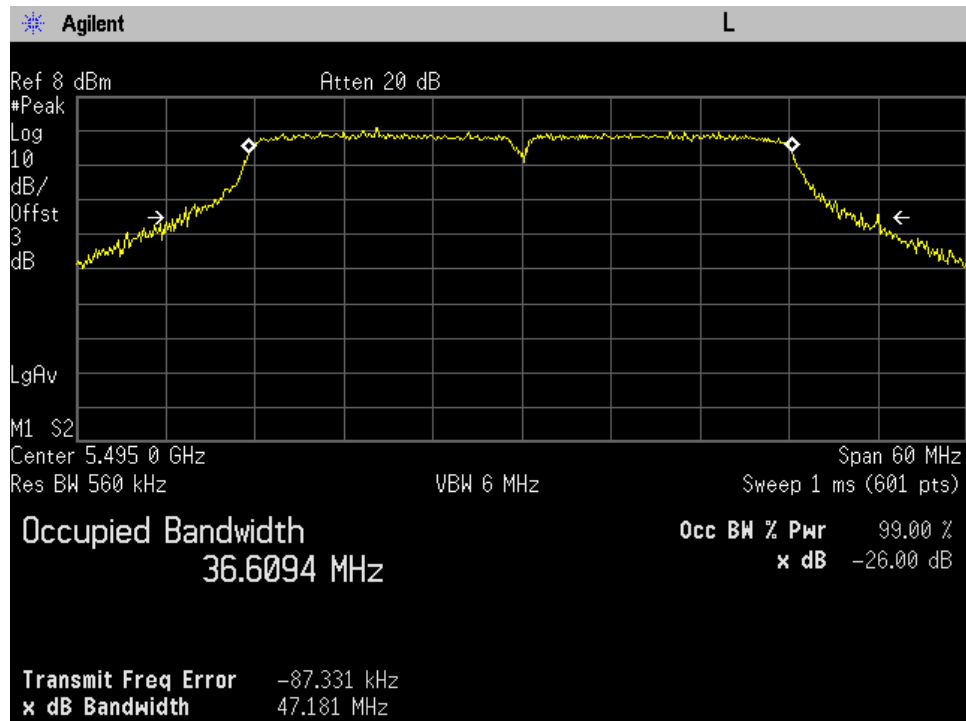


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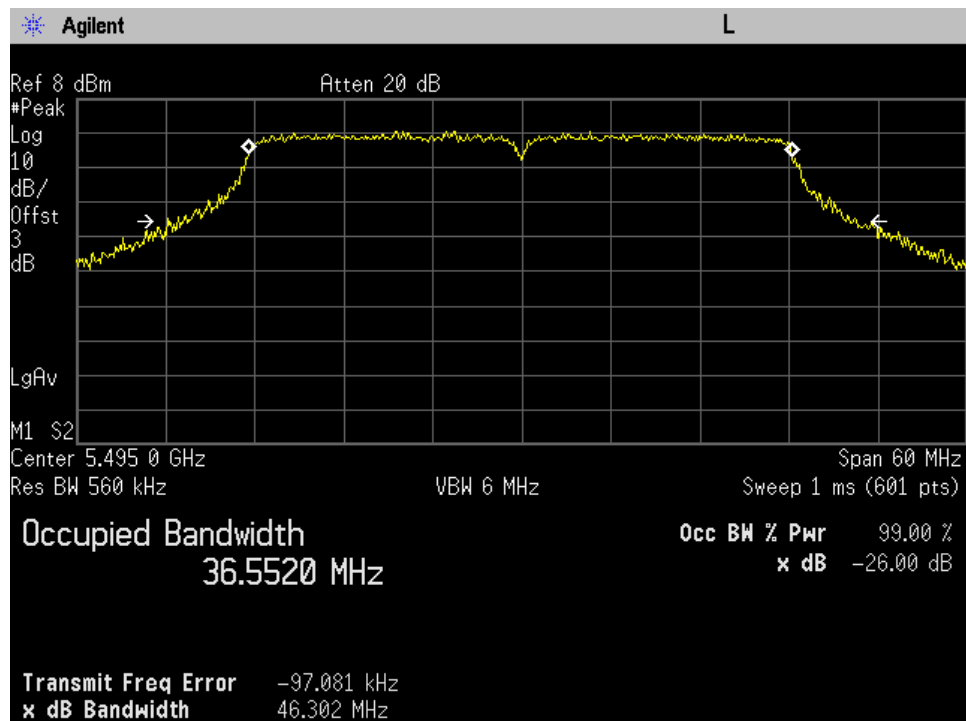




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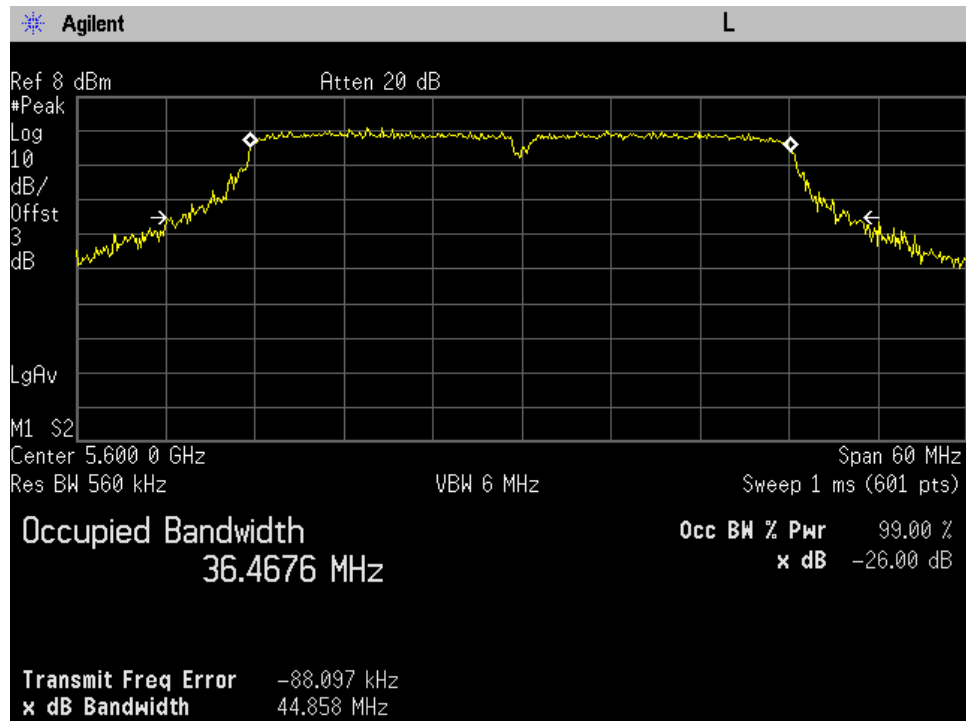


40 Mhz Low Ch Port J7





40 Mhz Mid Ch Port J4



40 Mhz Mid Ch Port J7

