

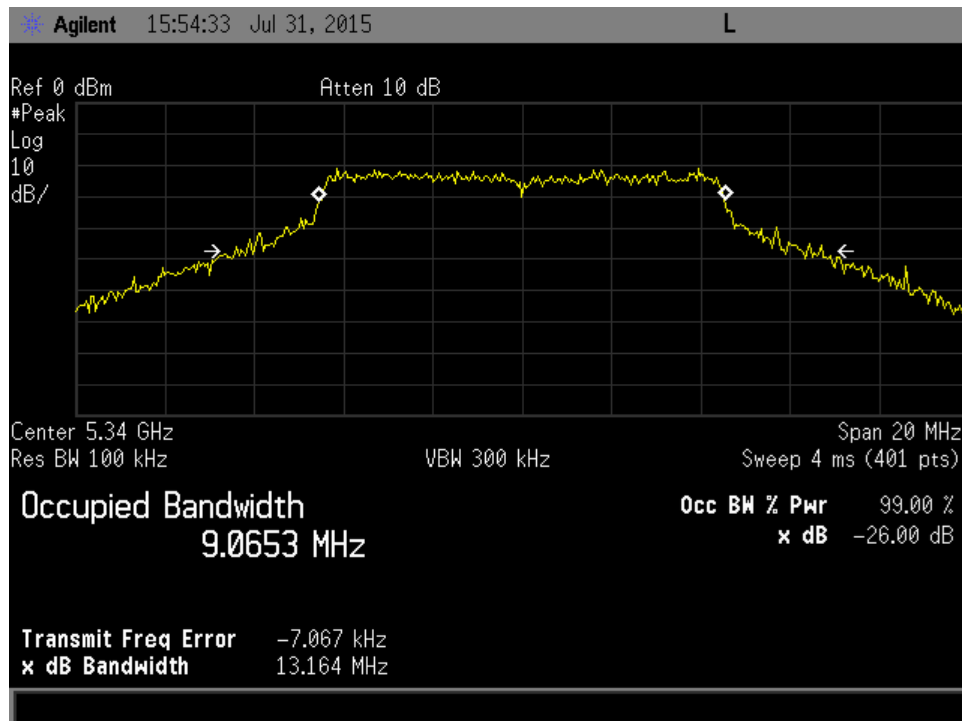
Annex C

Occupied Bandwidth

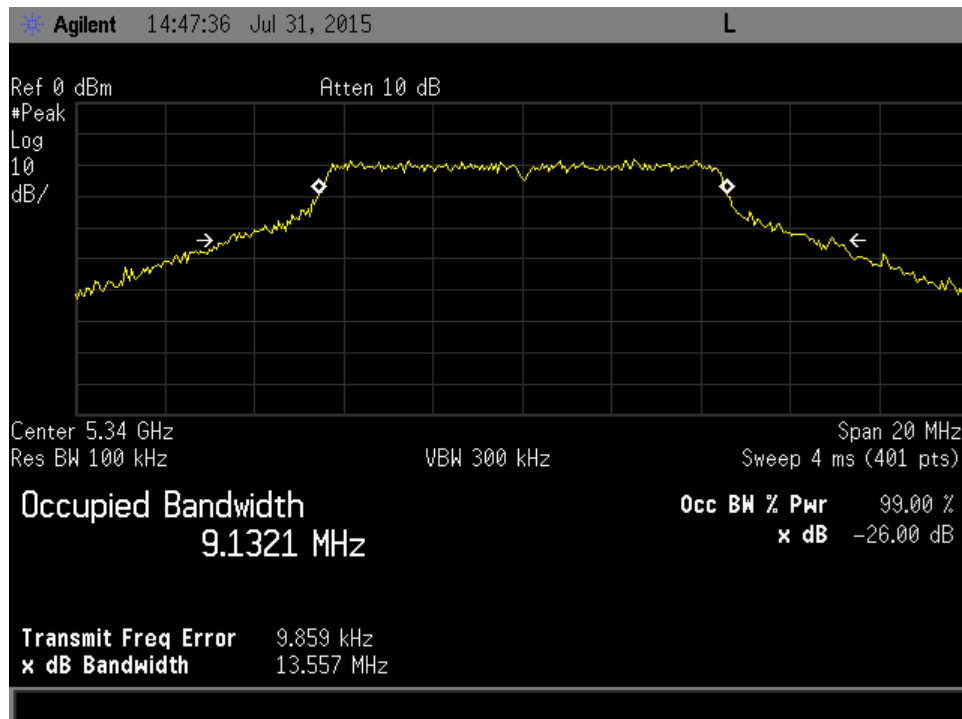
UNII-2A and UNII-2C



10MHz high ch_J13

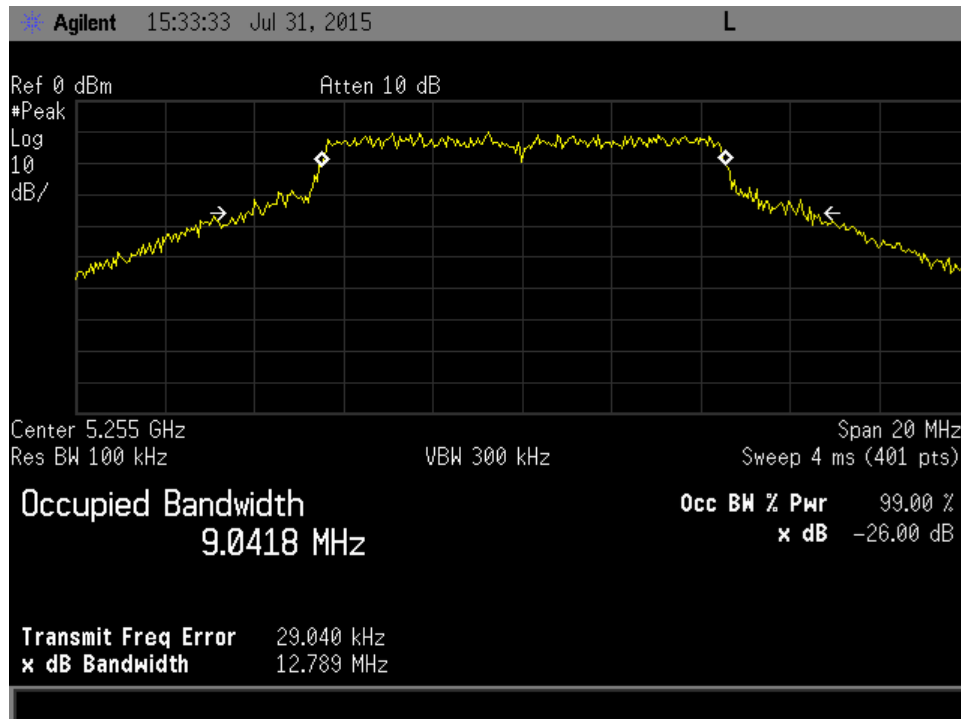


10MHz high ch_J7

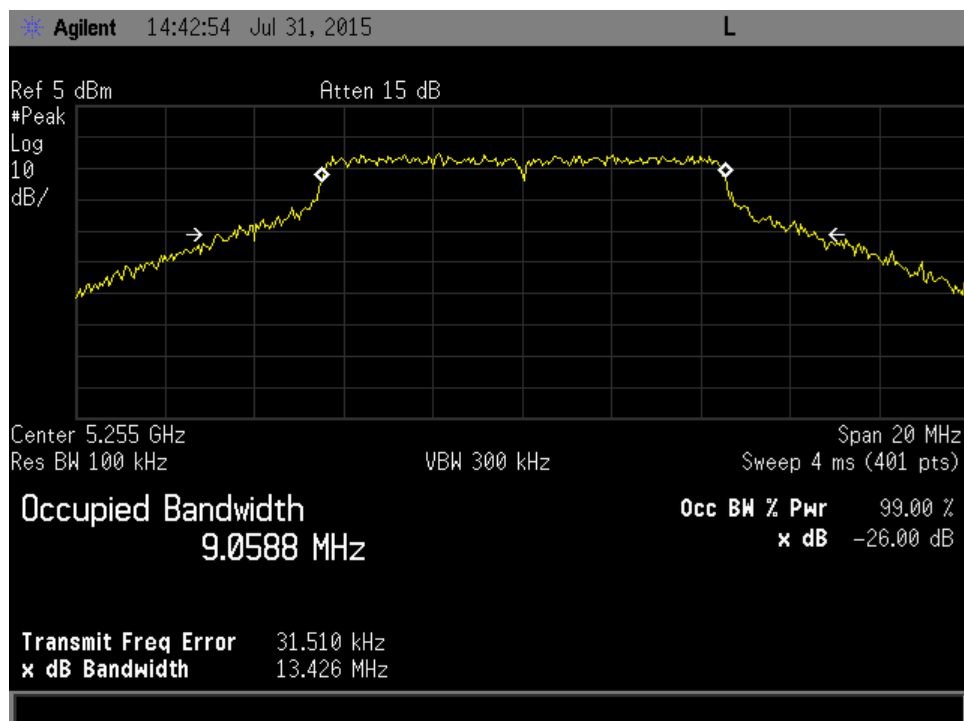




10MHz low ch_J13

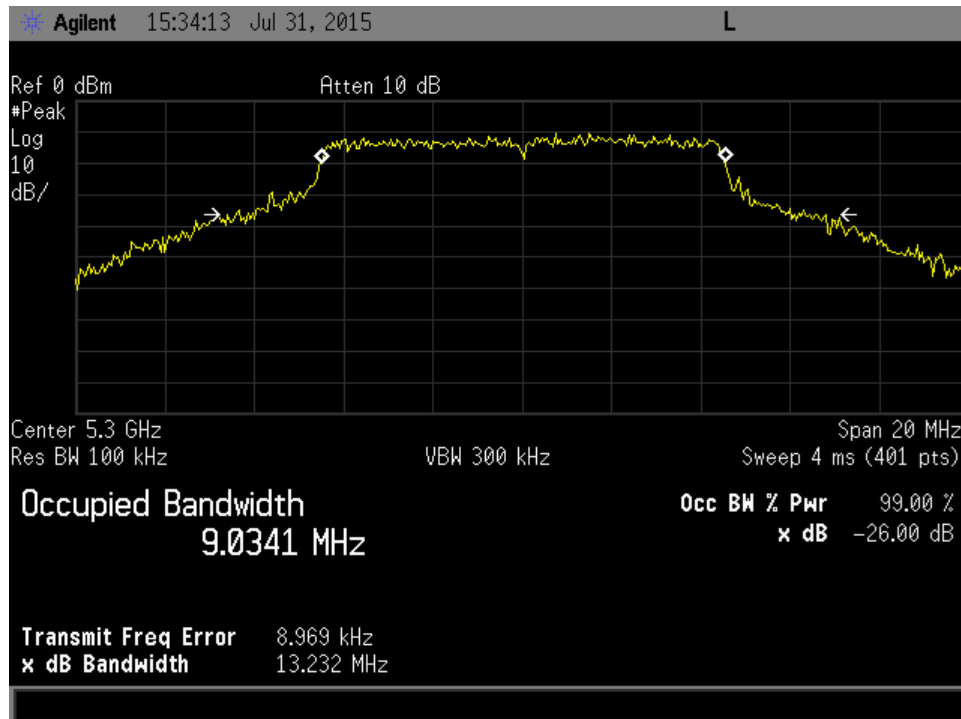


10MHz low ch_J7

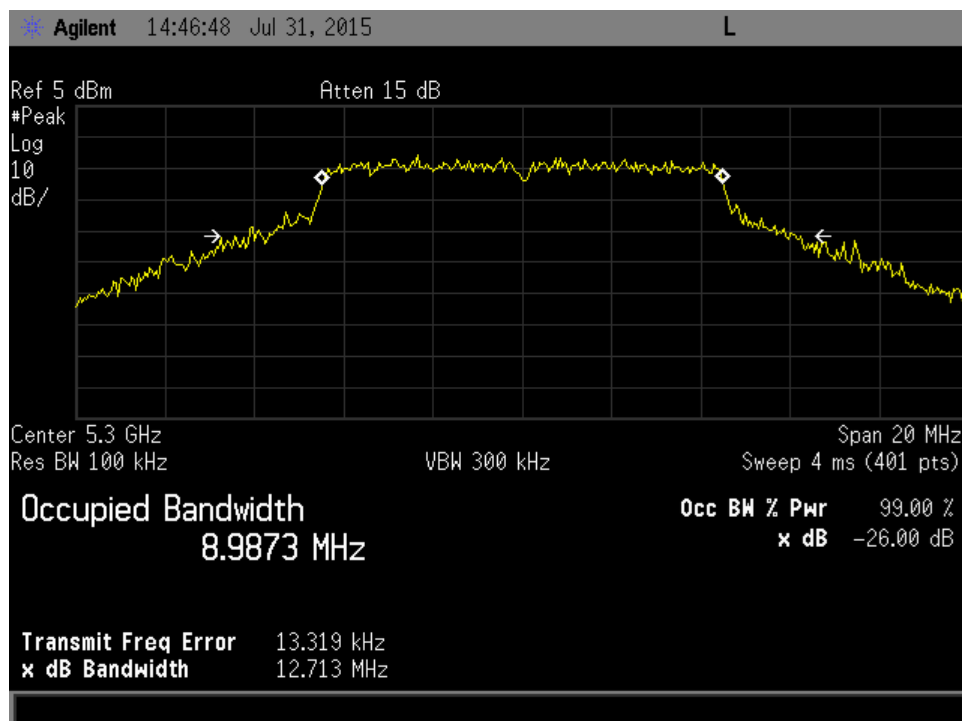




10MHz mid ch_J13

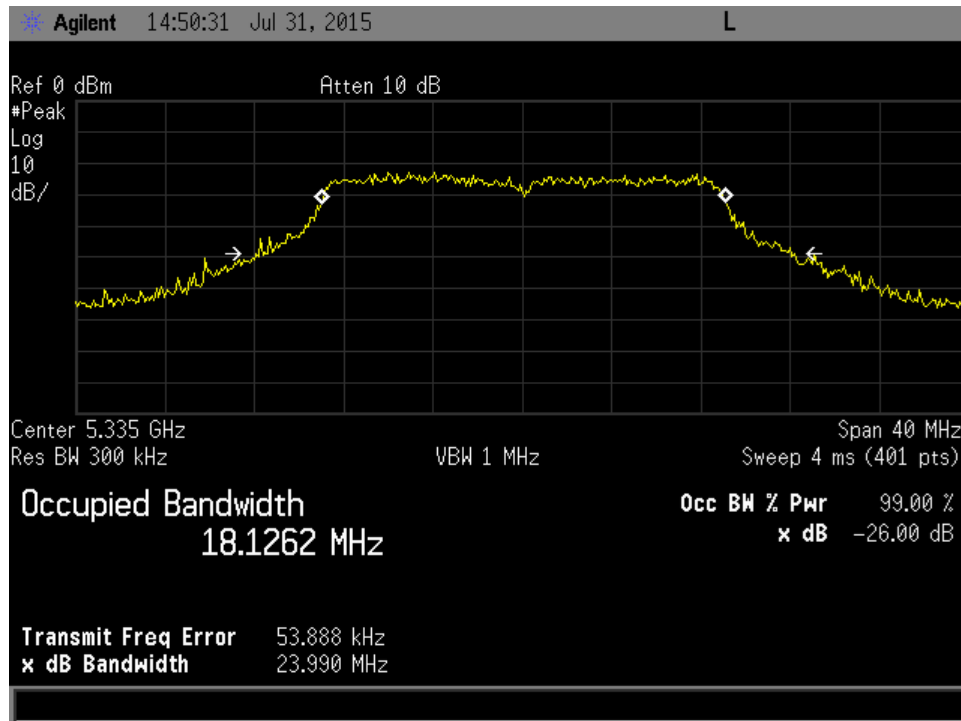


10MHz mid ch_J7

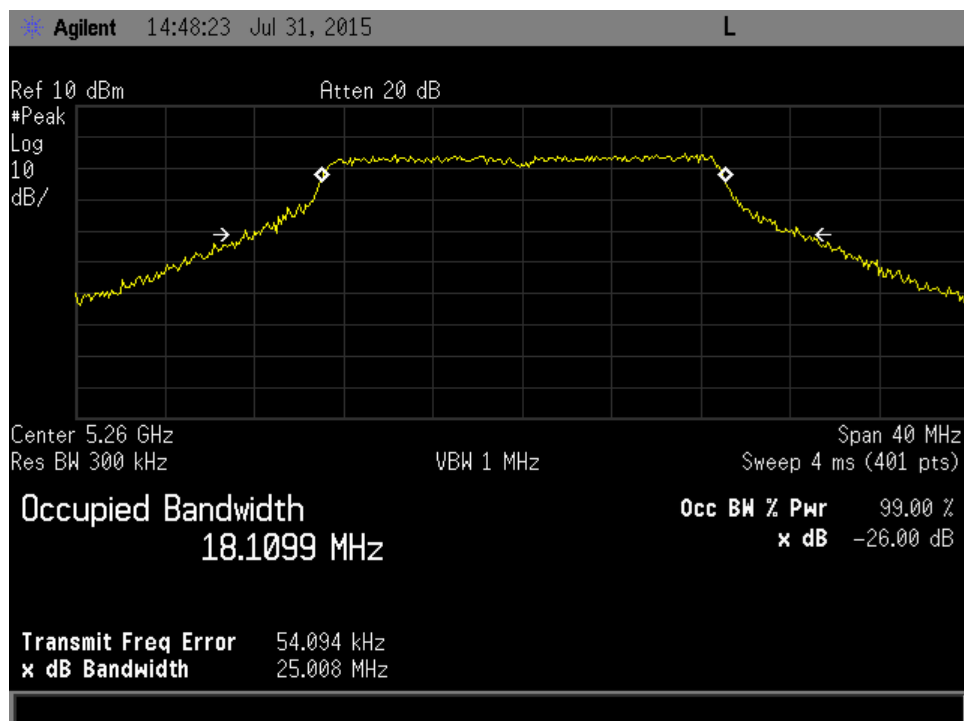




20MHz high ch_J7

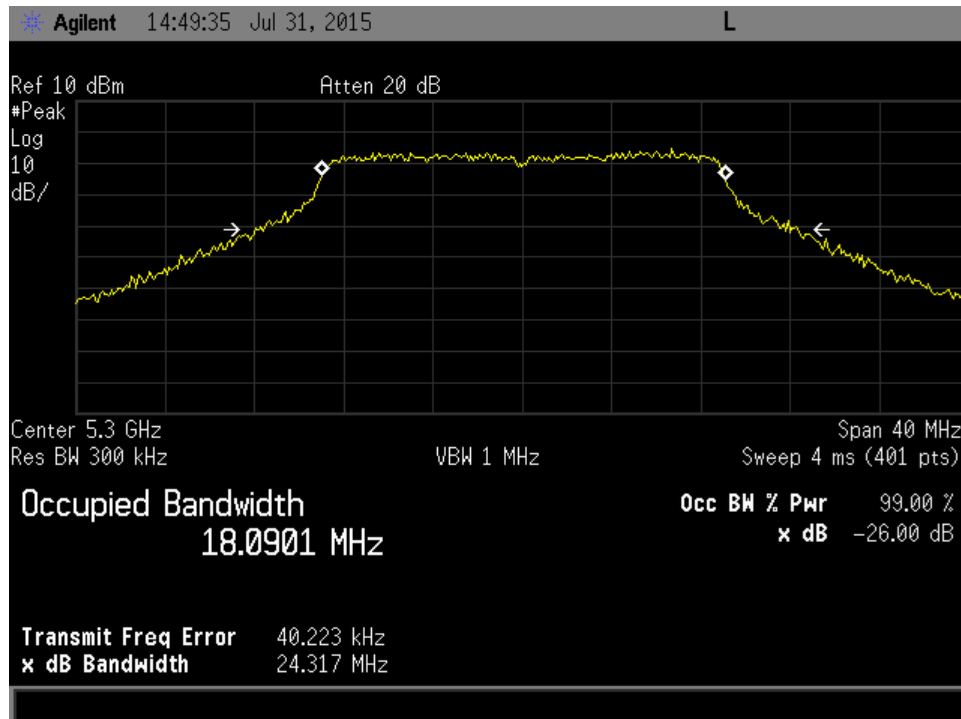


20MHz low ch_J7

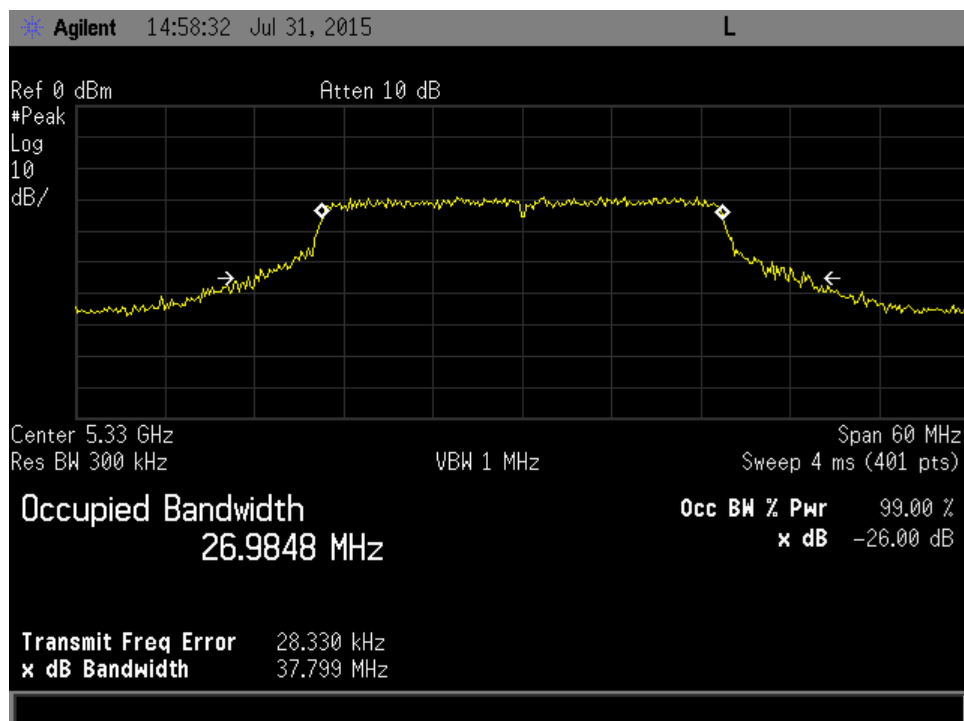




20MHz mid ch_J7

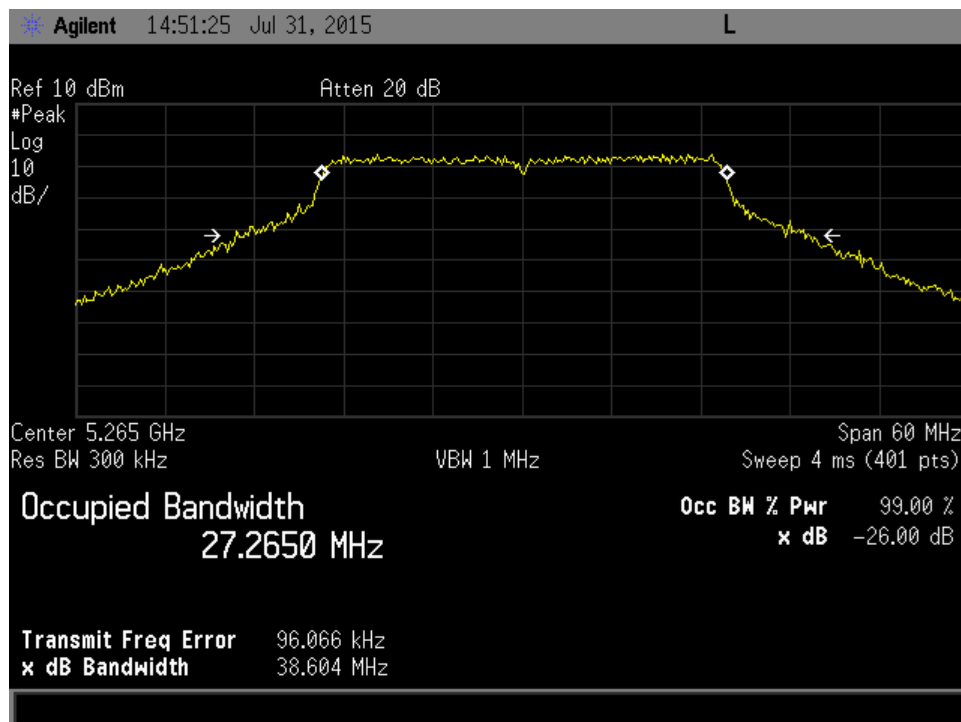


30MHz high ch_J7

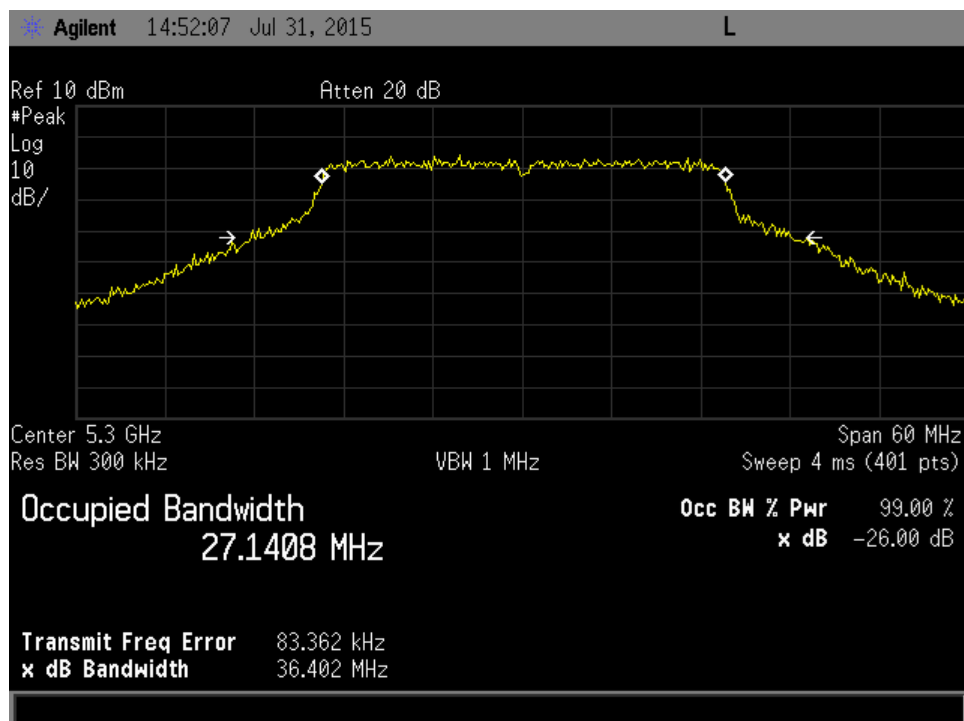




30MHz low ch_J7

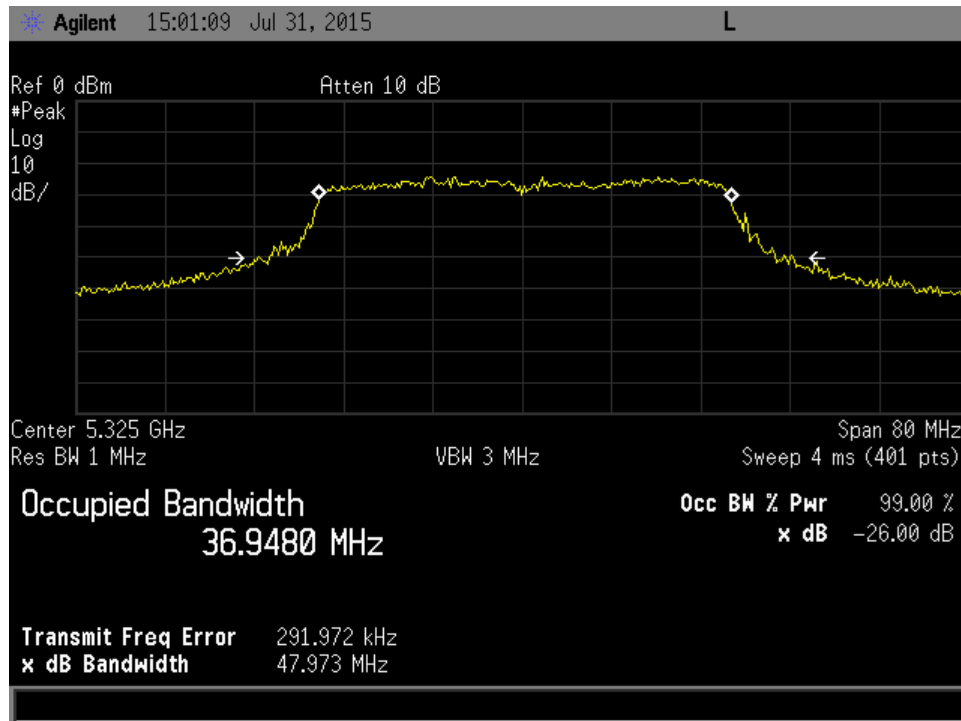


30MHz mid ch_J7

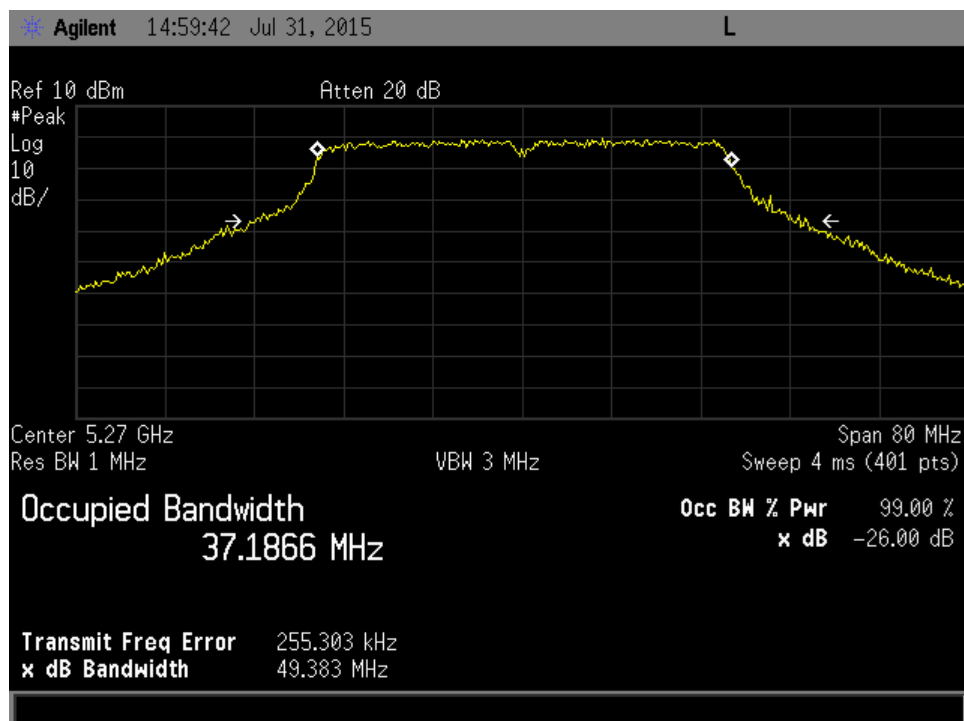




40MHz high ch_J7

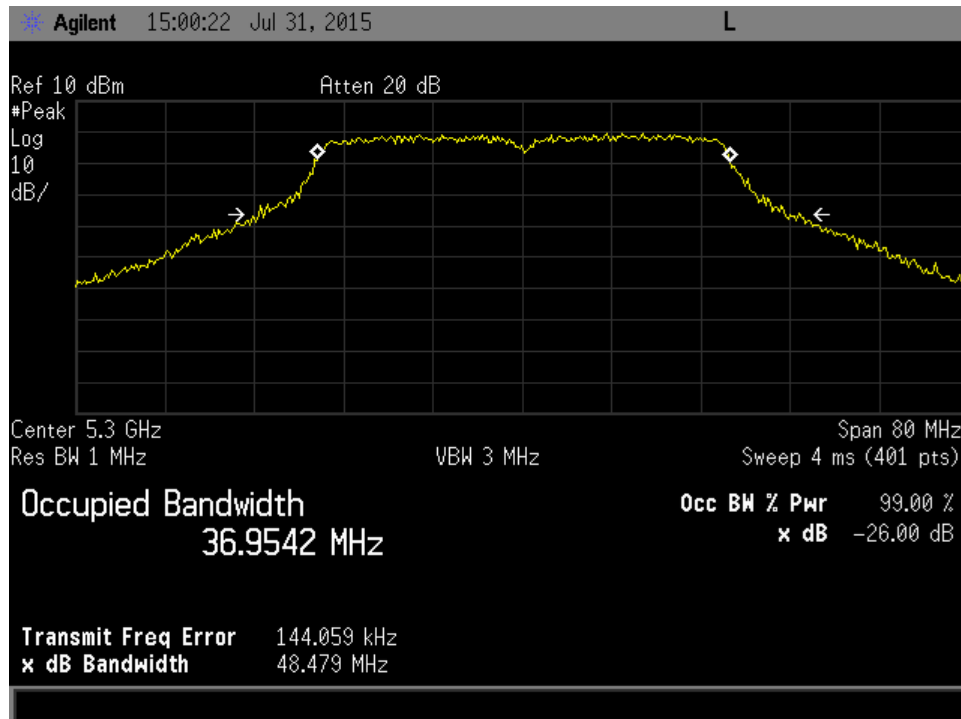


40MHz low ch_J7

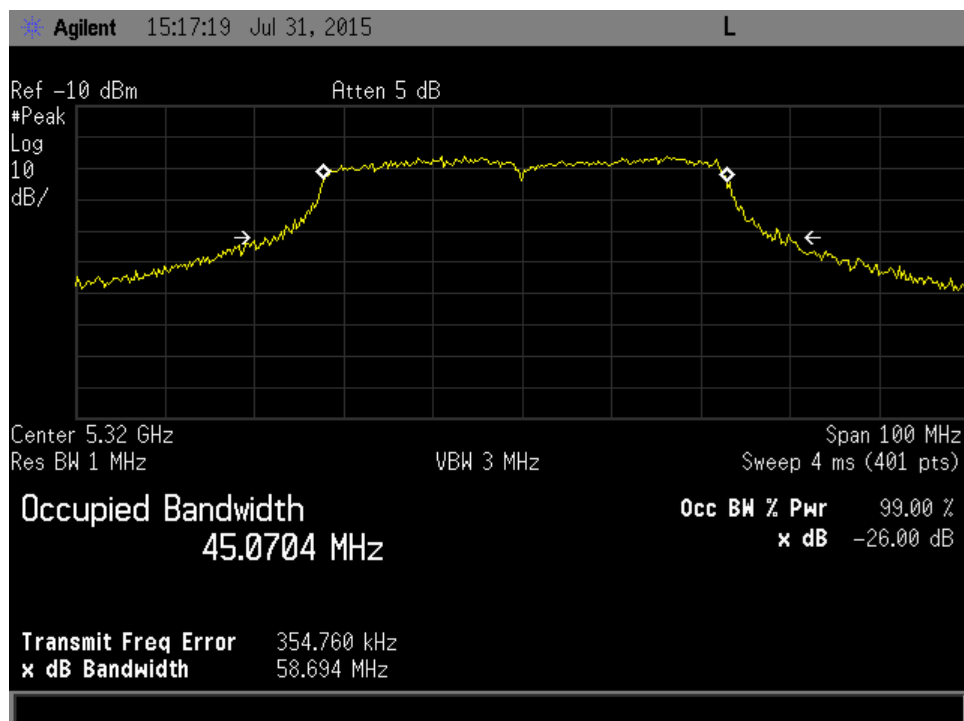




40MHz mid ch_J7

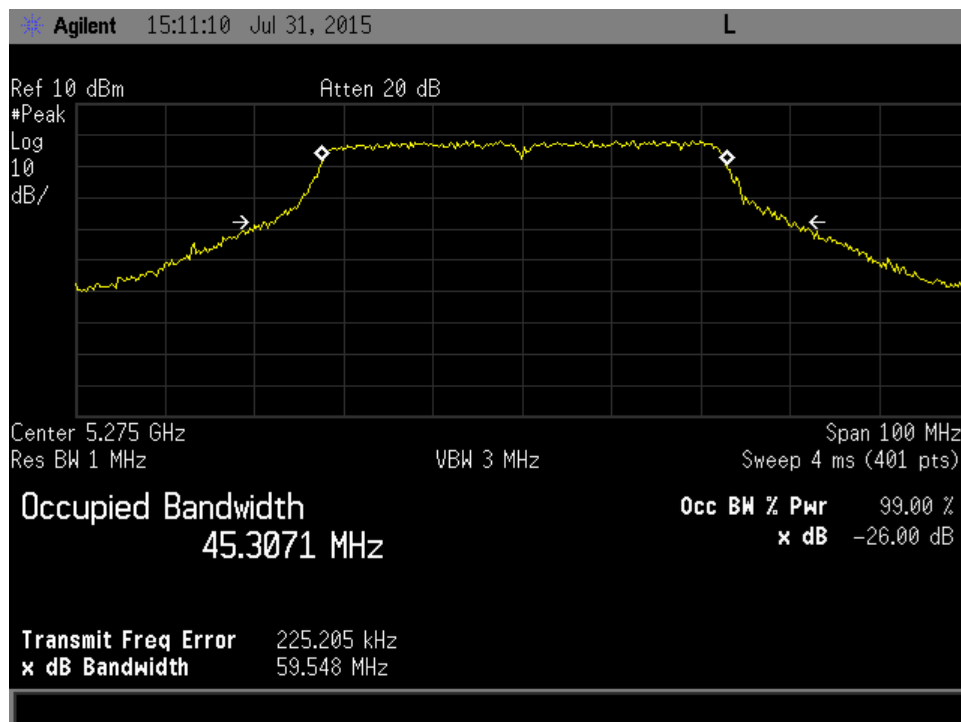


50MHz high ch_J7

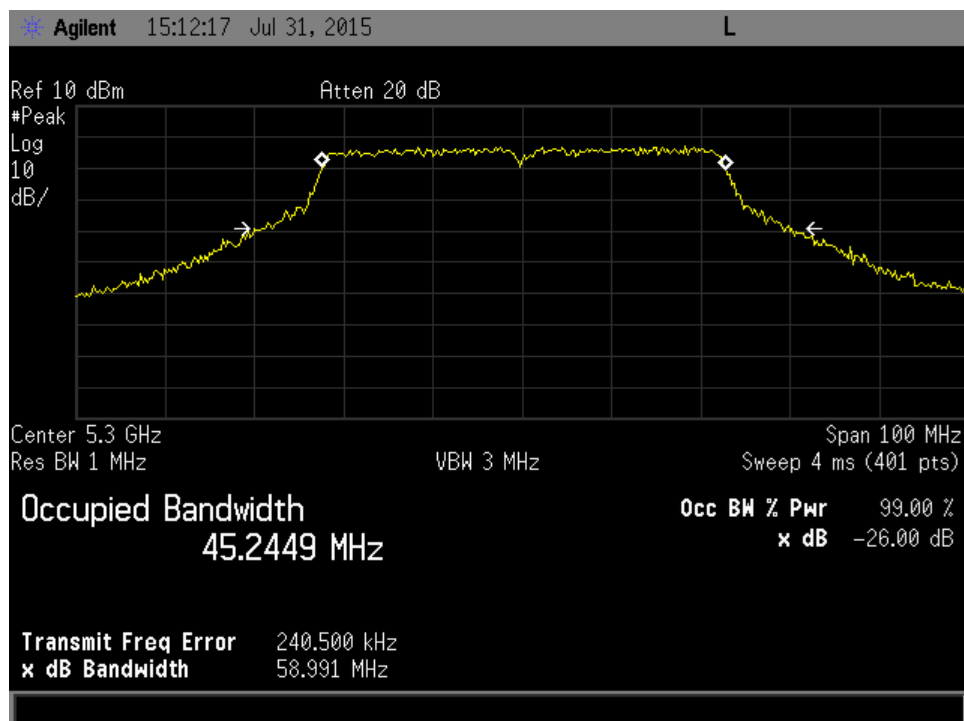




50MHz low ch_J7

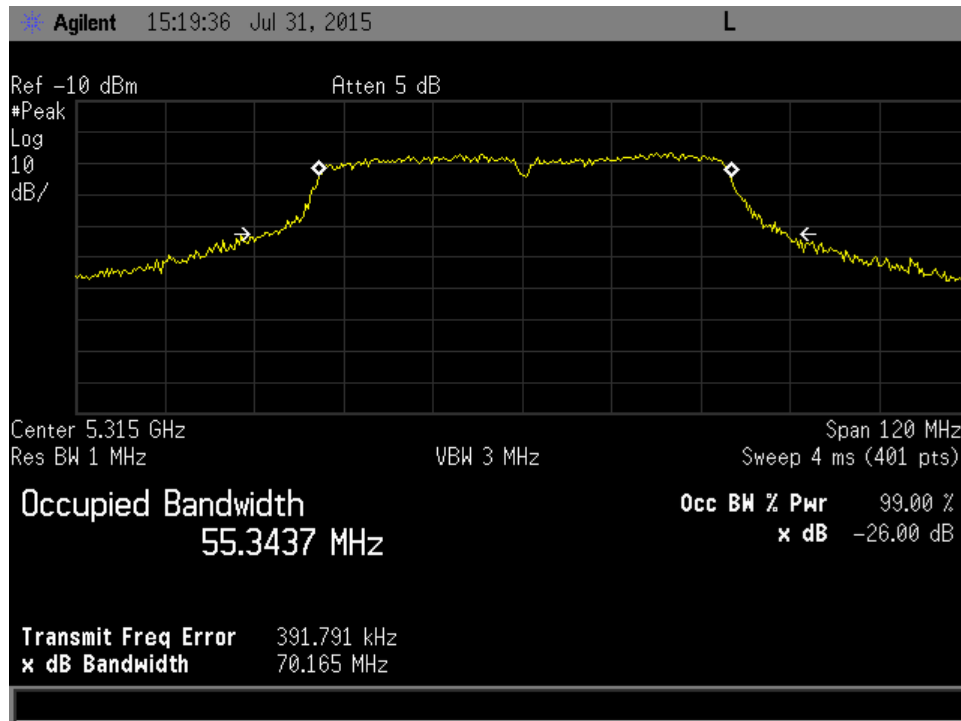


50MHz mid ch_J7

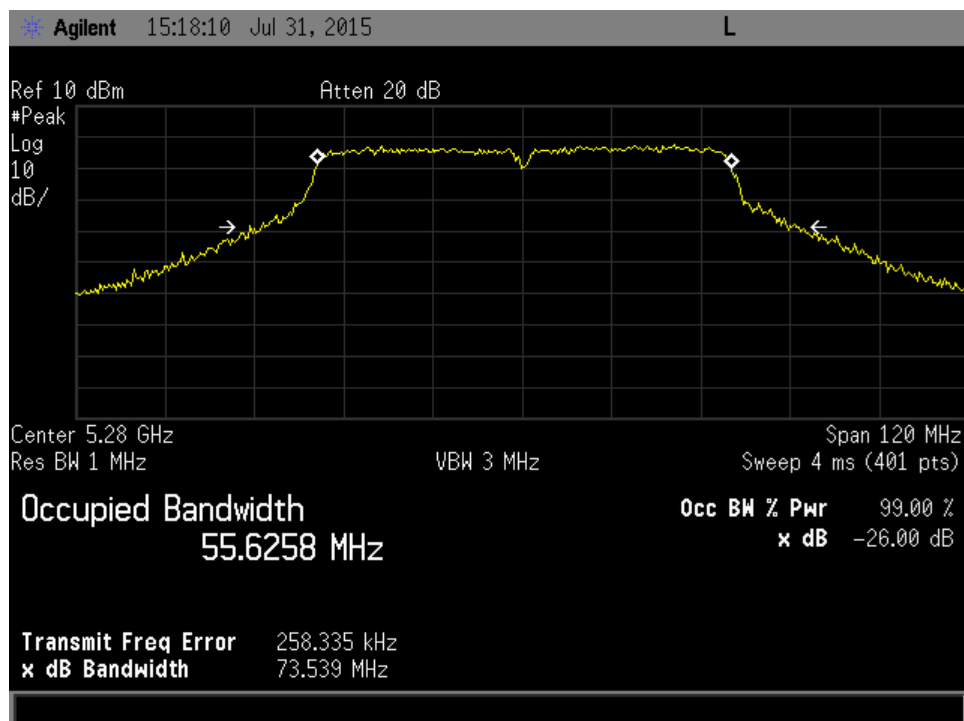




60MHz high ch_J7

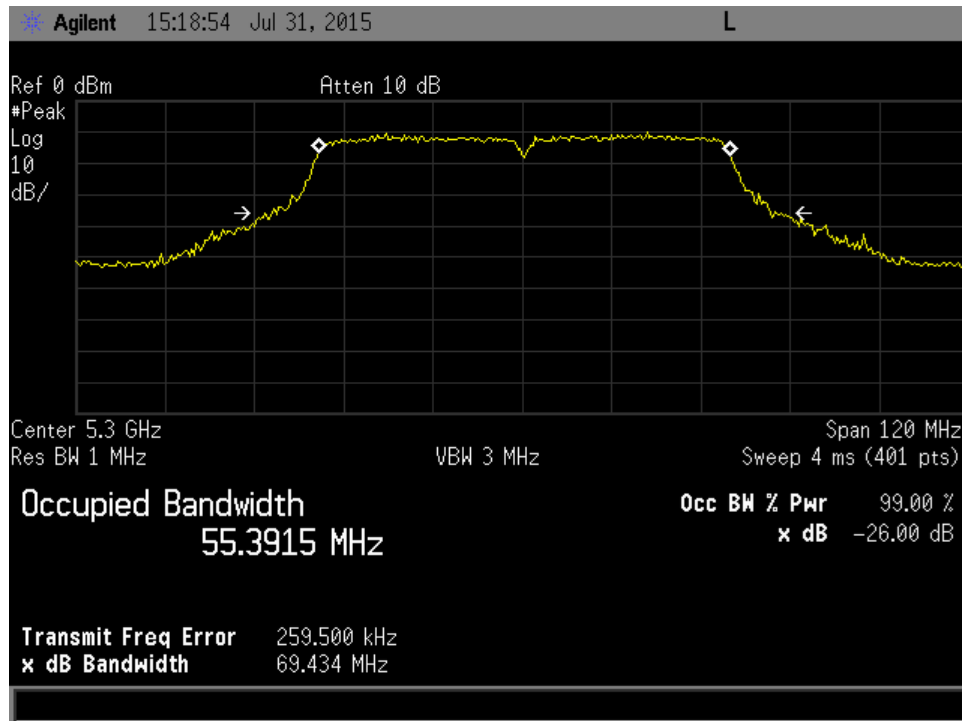


60MHz low ch_J7

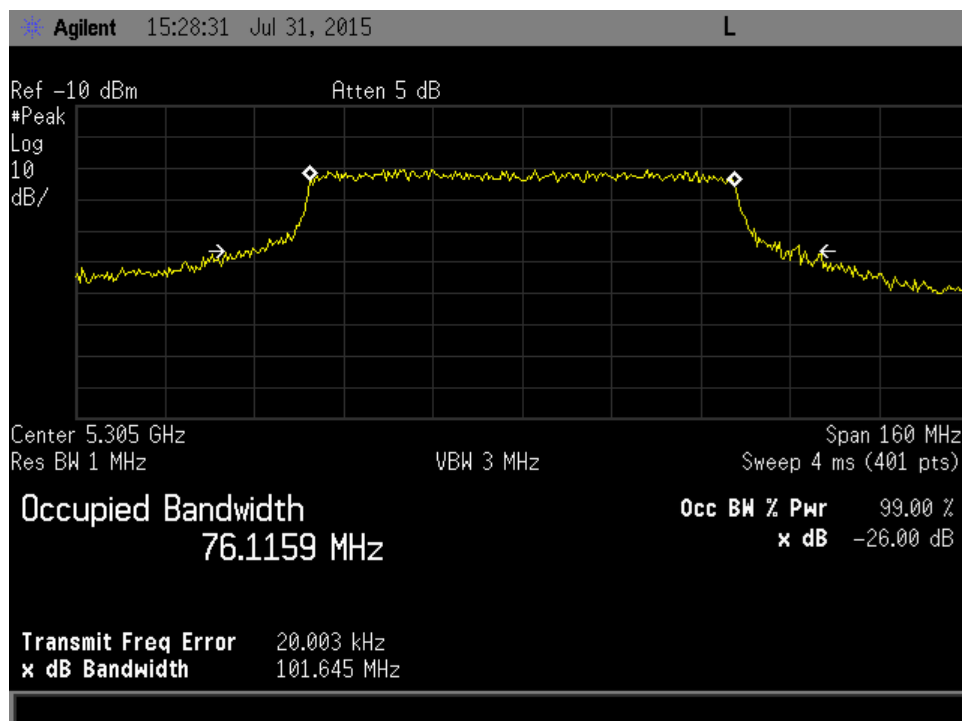




60MHz mid ch_J7

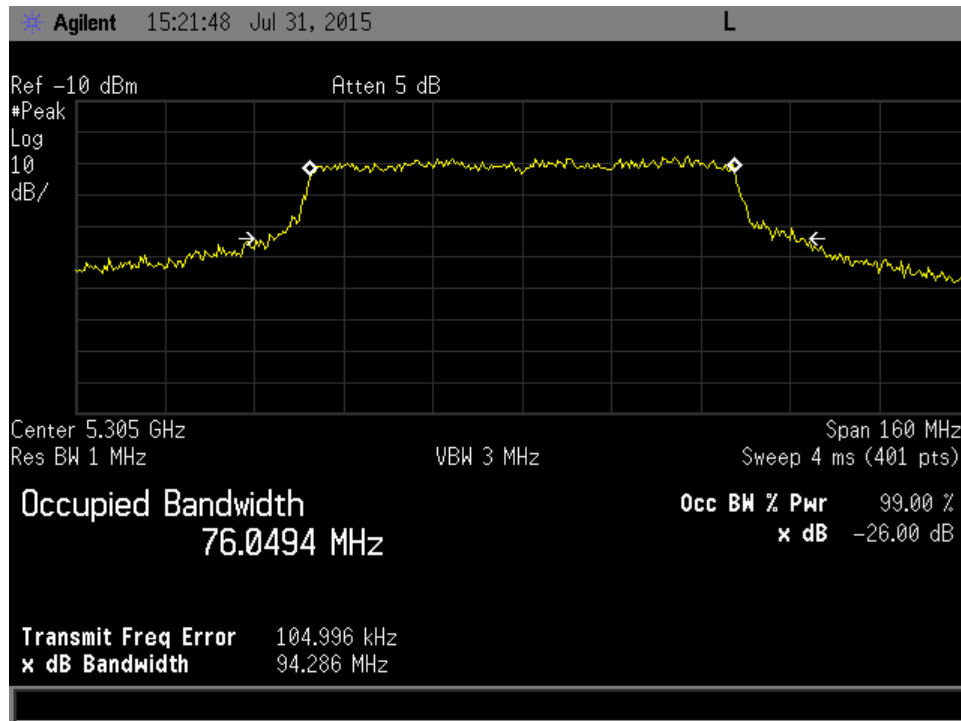


80MHz high ch_J13

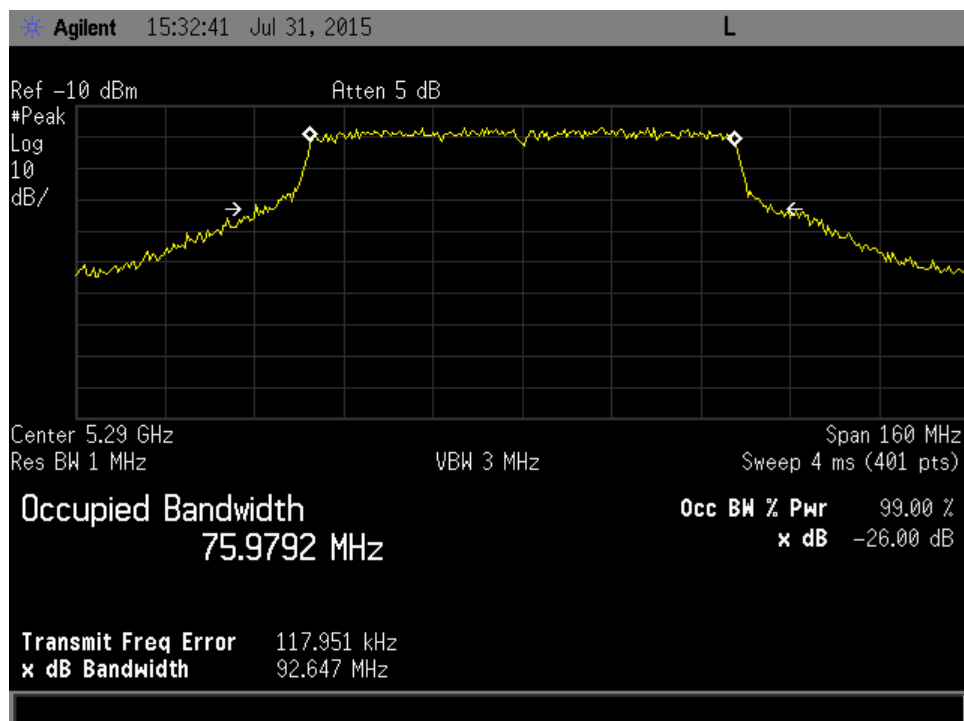




80MHz high ch_J7

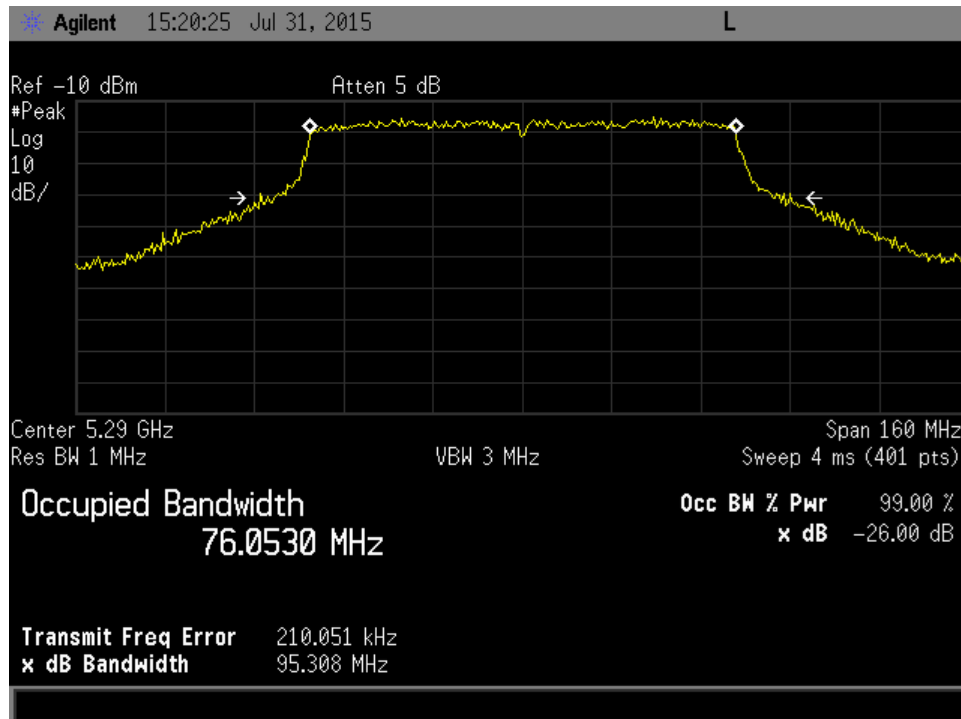


80MHz low ch_J13

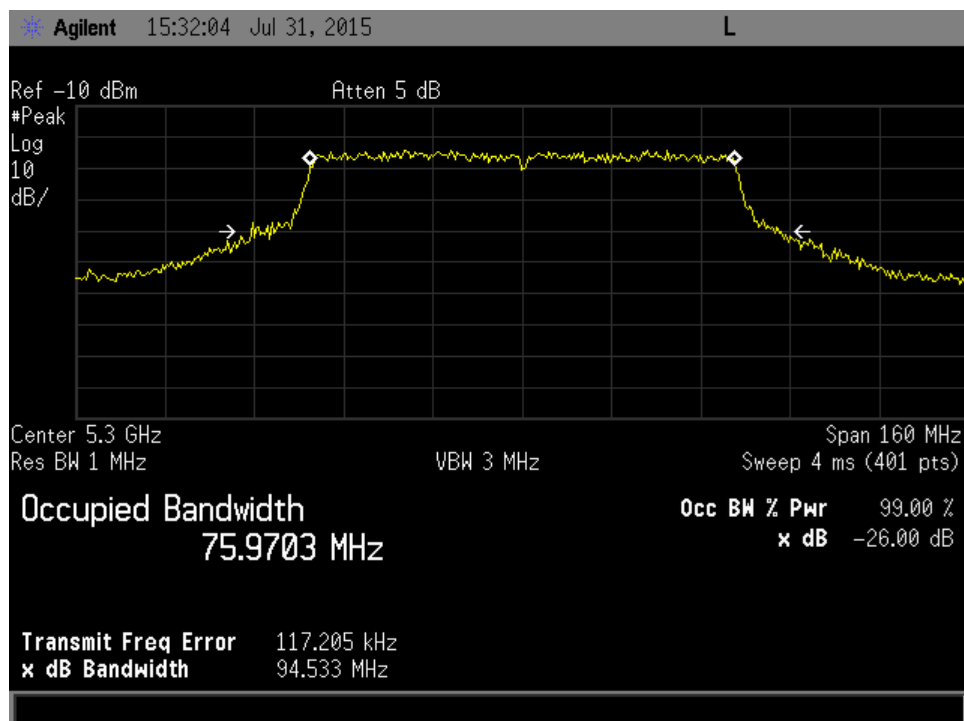




80MHz low ch_J7

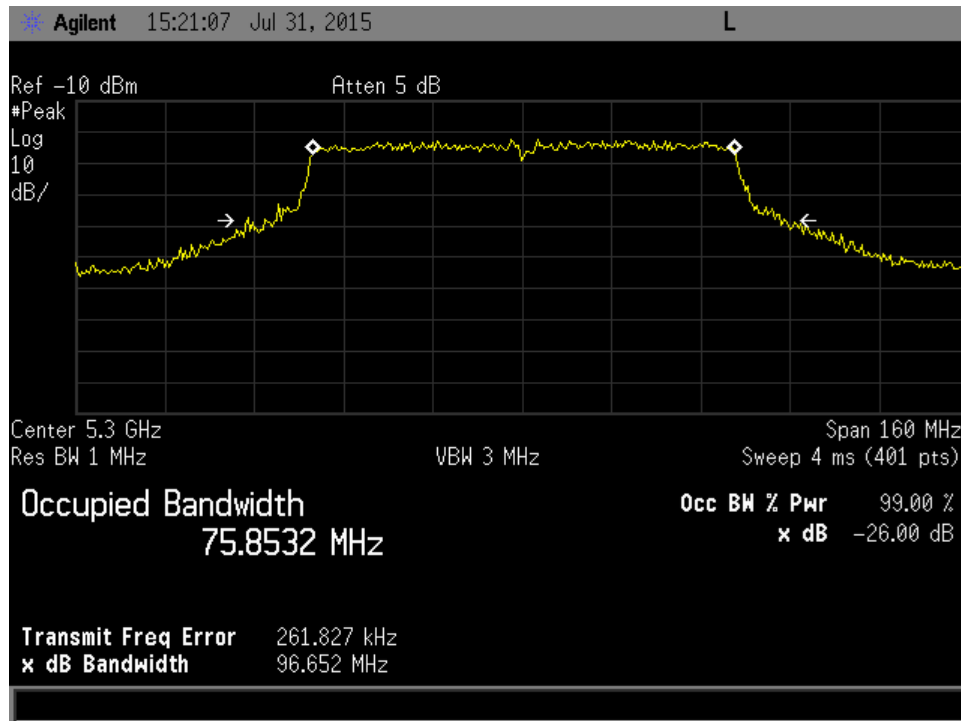


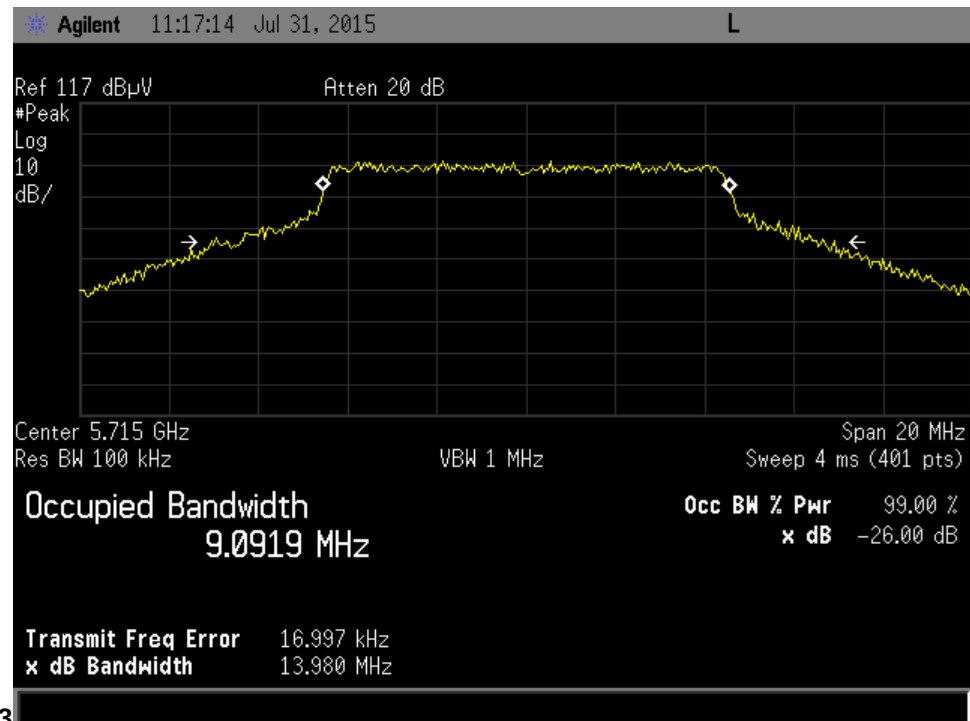
80MHz mid ch_J13





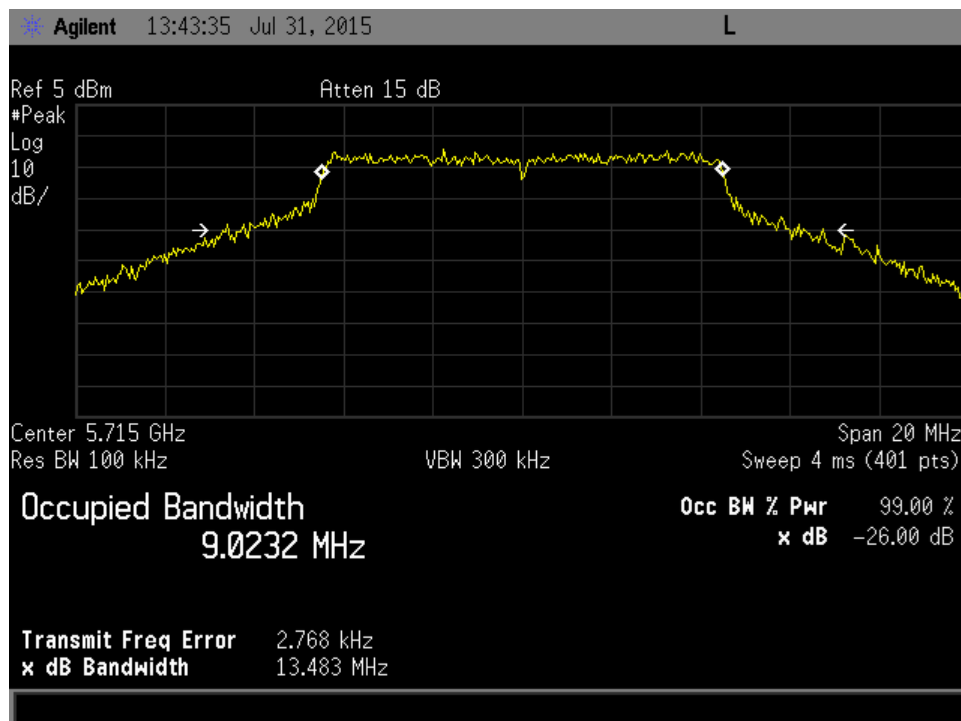
80MHz mid ch_J7





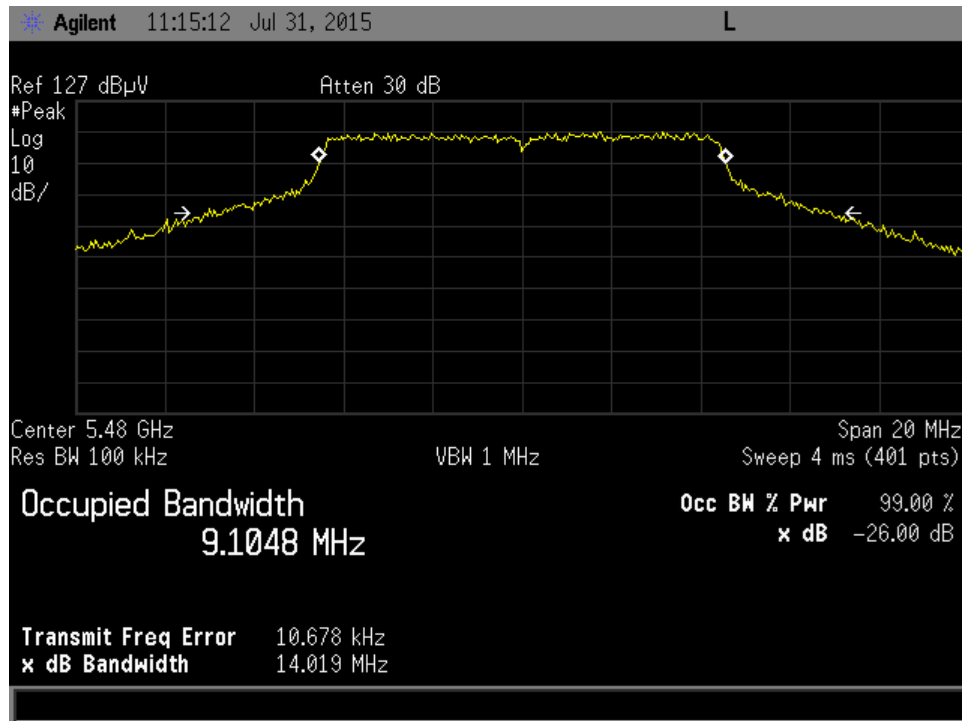
10MHz high ch_J13

10MHz high ch_J7

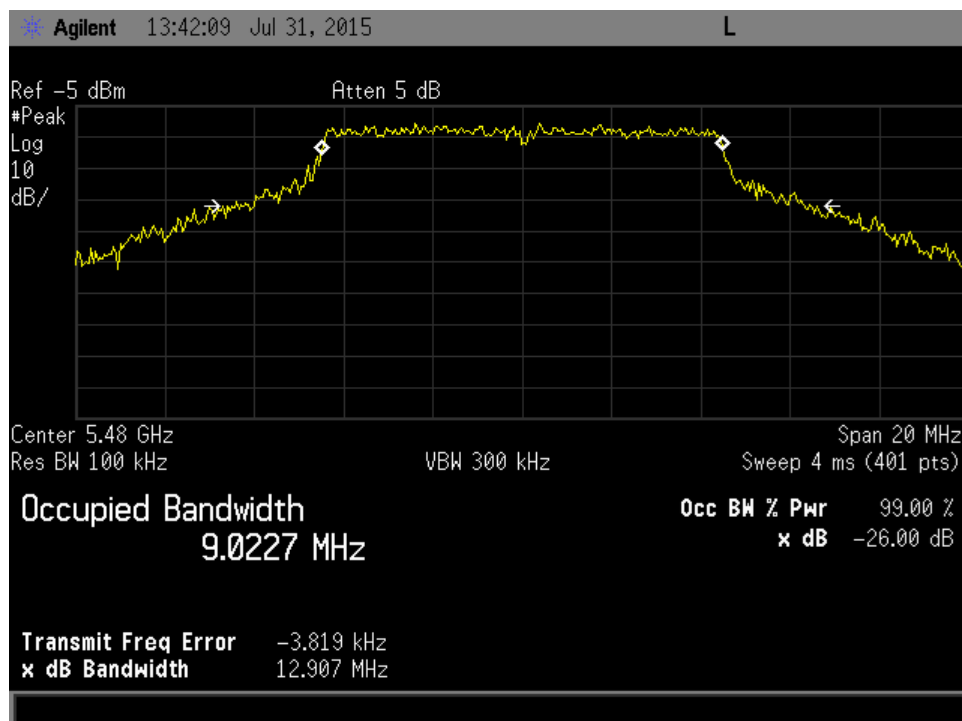




10MHz low ch_J13

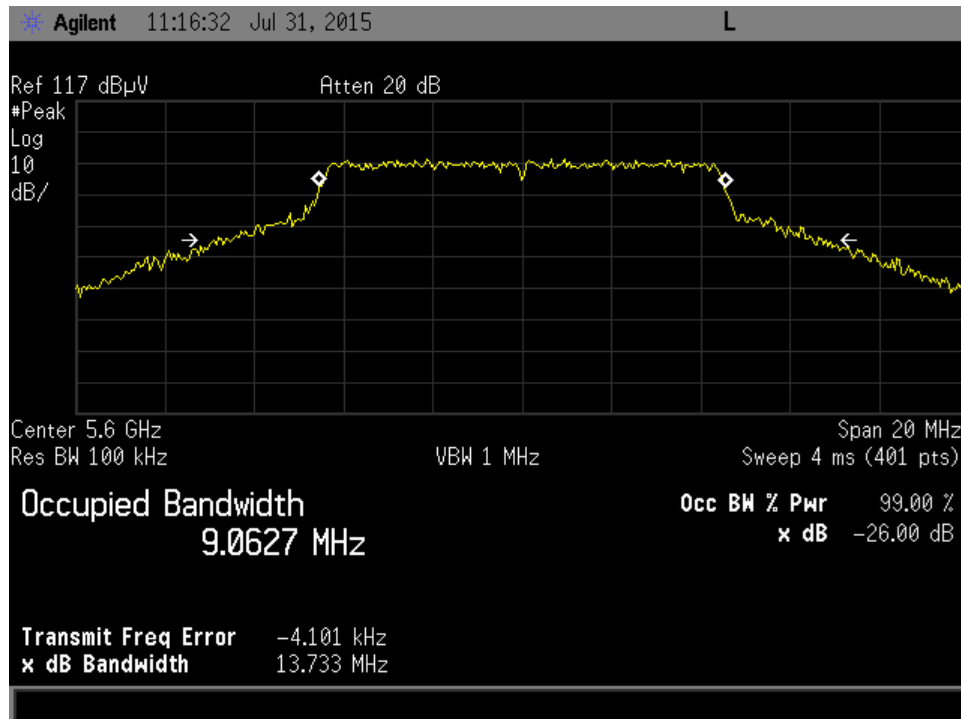


10MHz low ch_J7

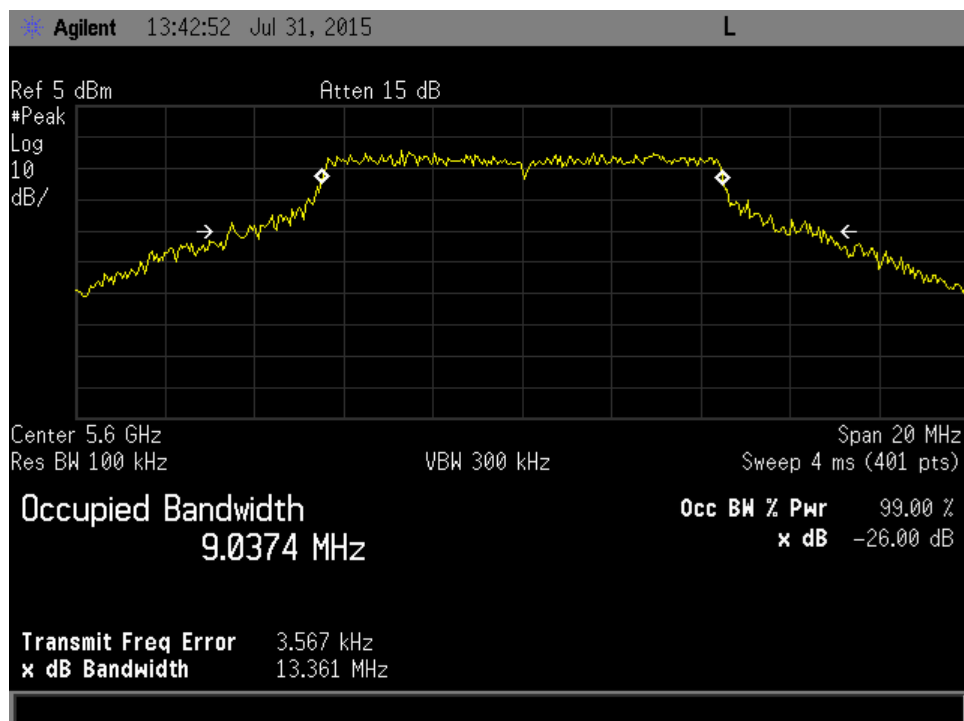




10MHz mid ch_J13

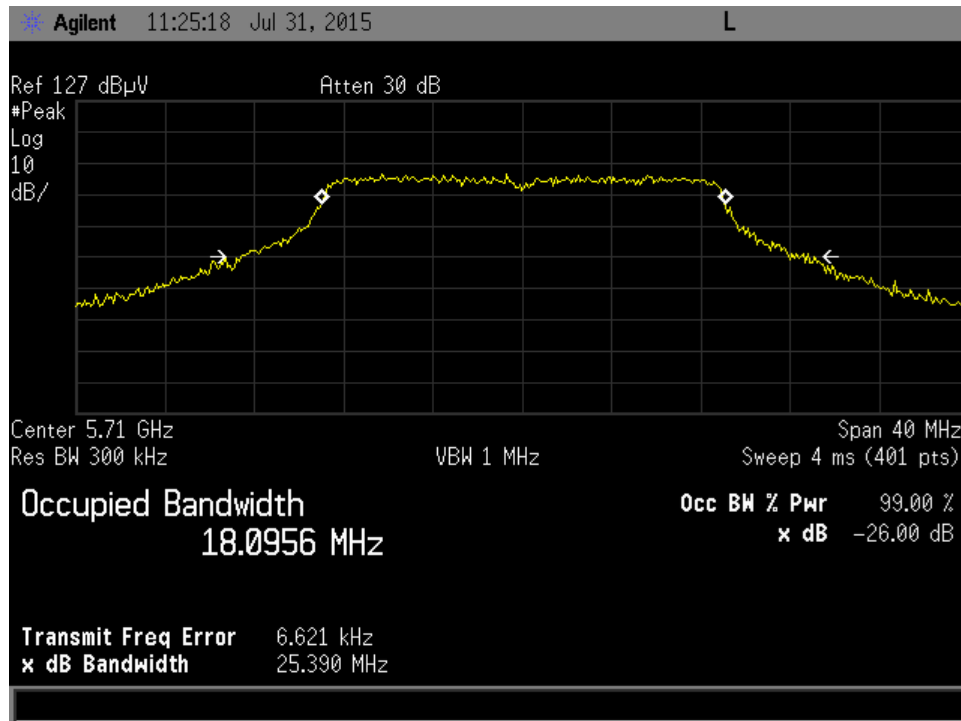


10MHz mid ch_J7

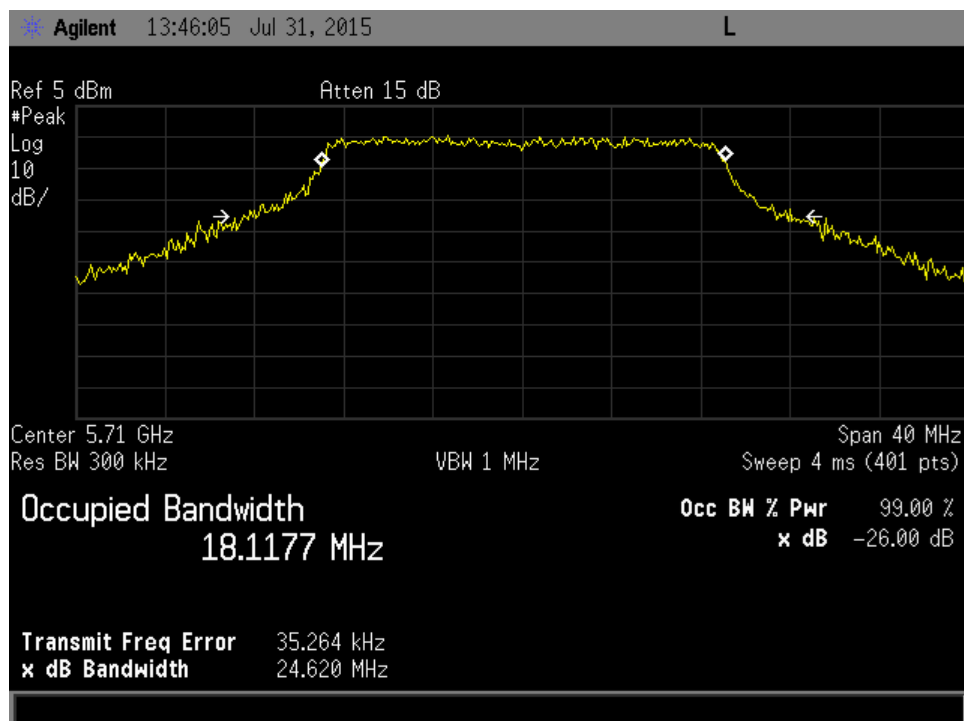




20MHz high ch_J13

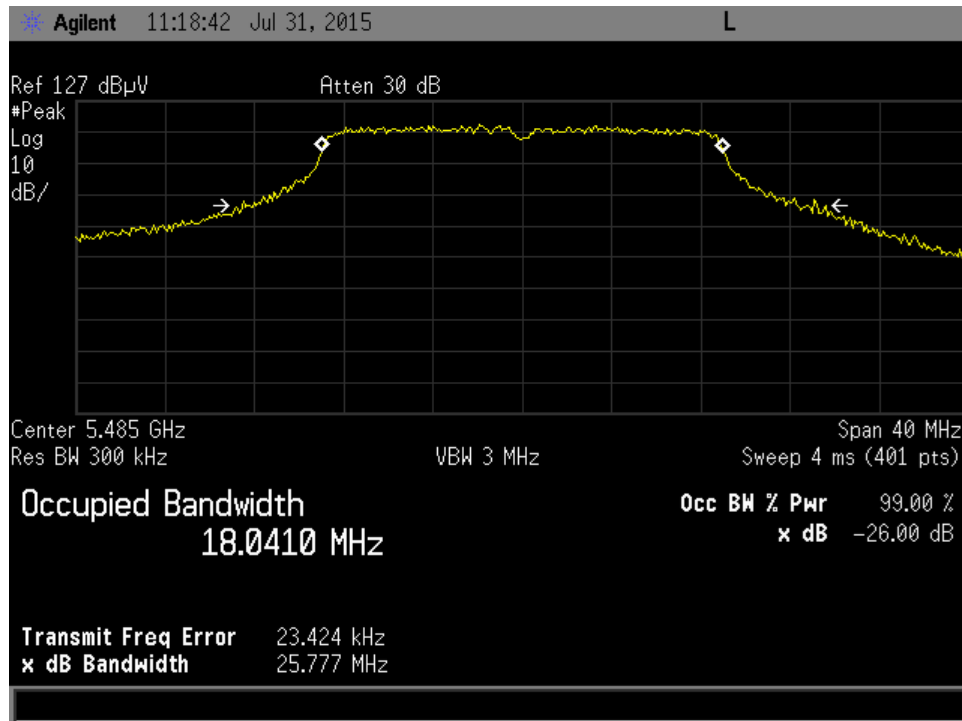


20MHz high ch_J7

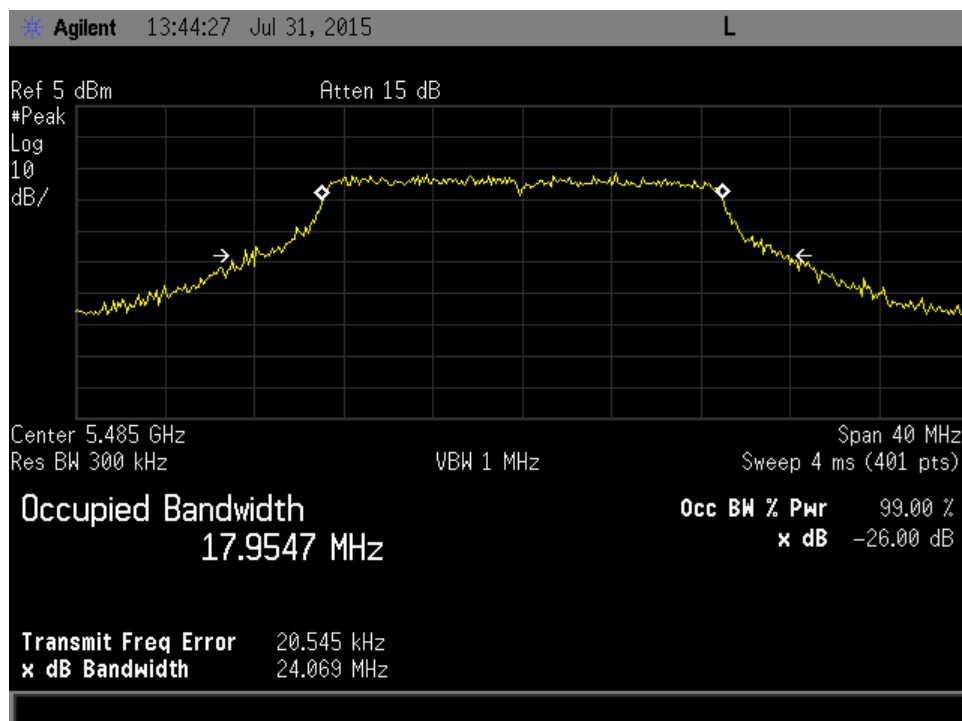




20MHz low ch_J13

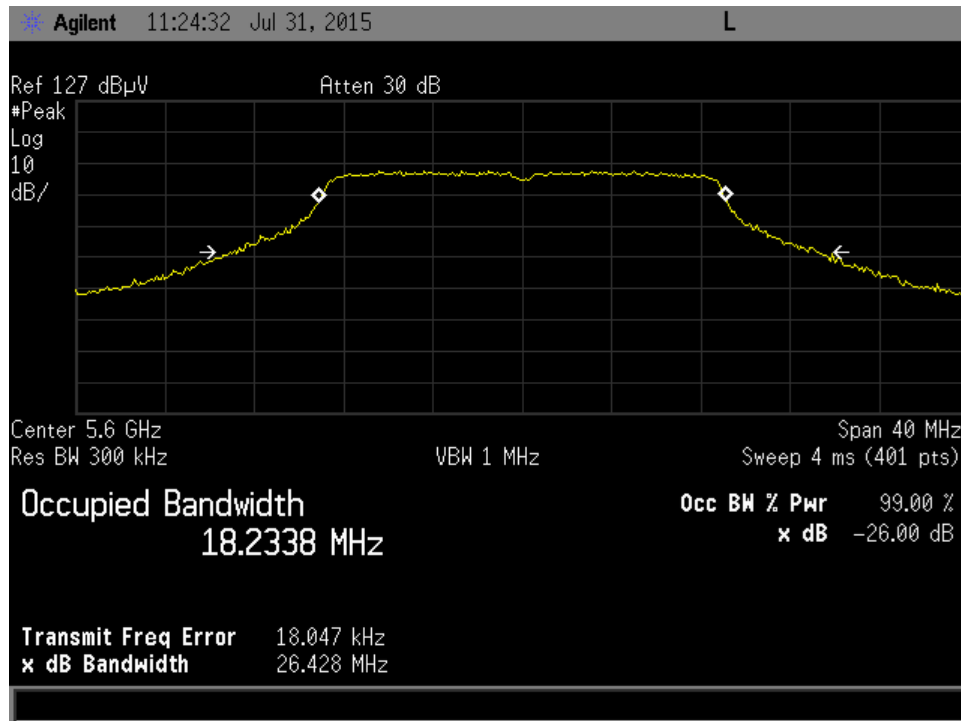


20MHz low ch_J7

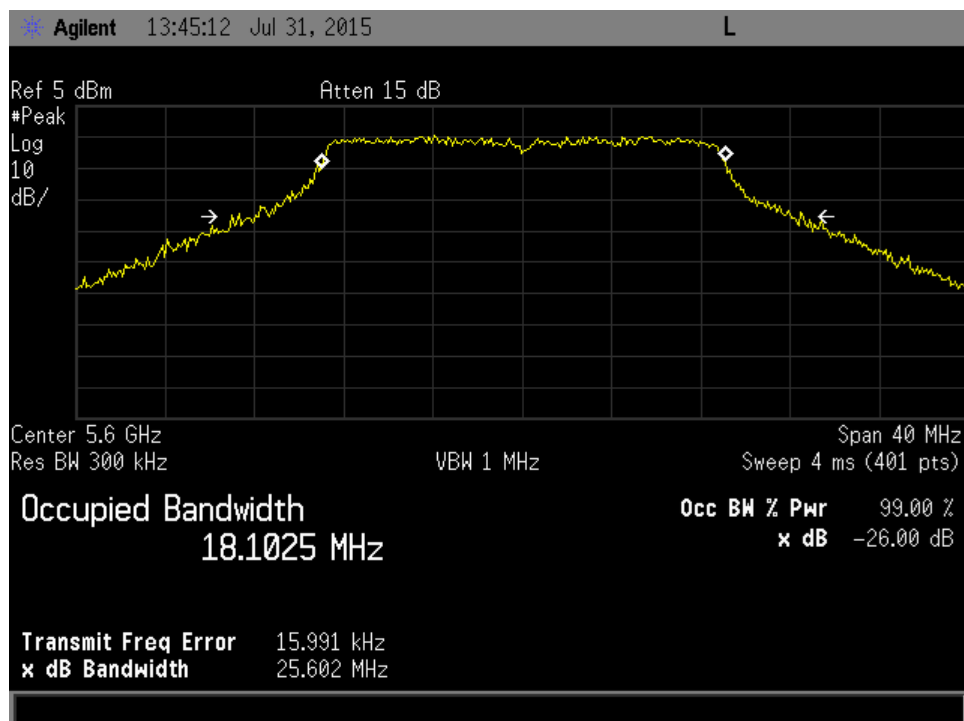




20MHz mid ch_J13

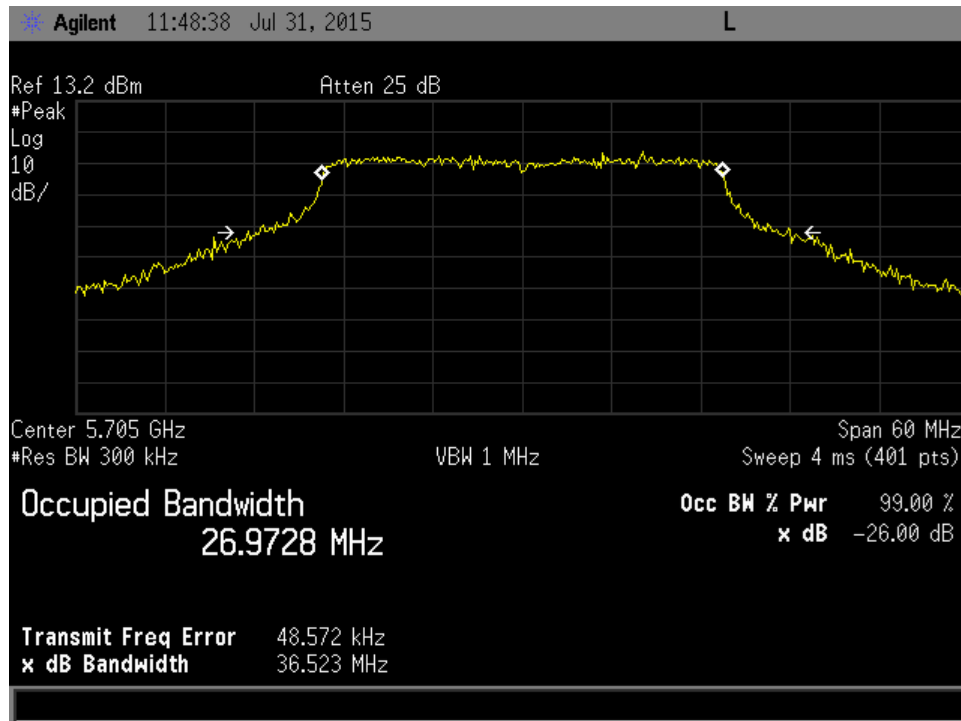


20MHz mid ch_J7

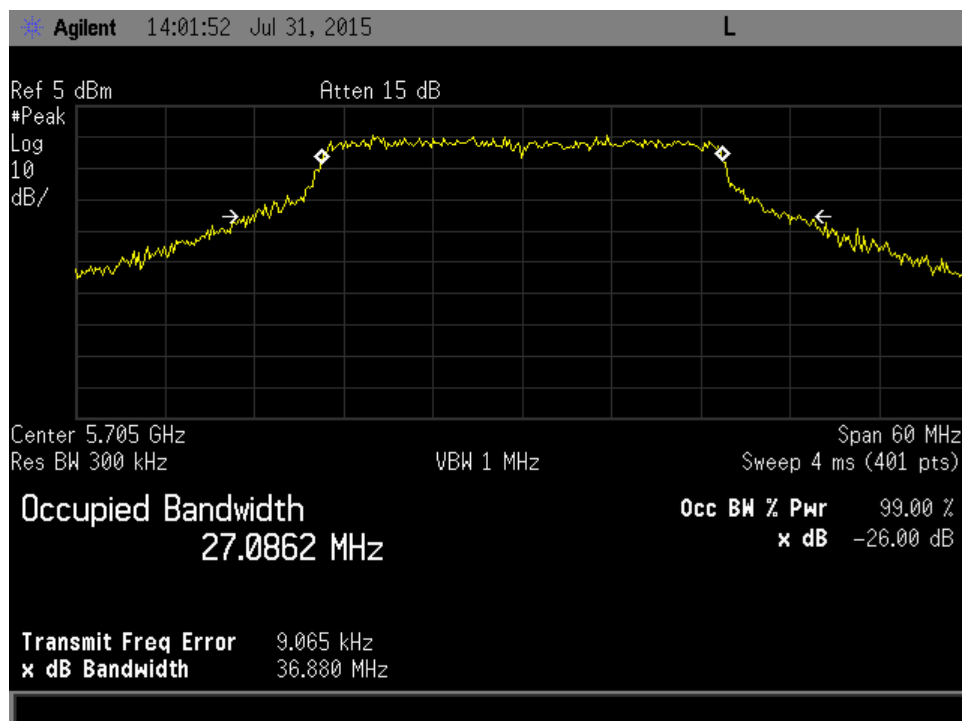




30MHz high ch_J13

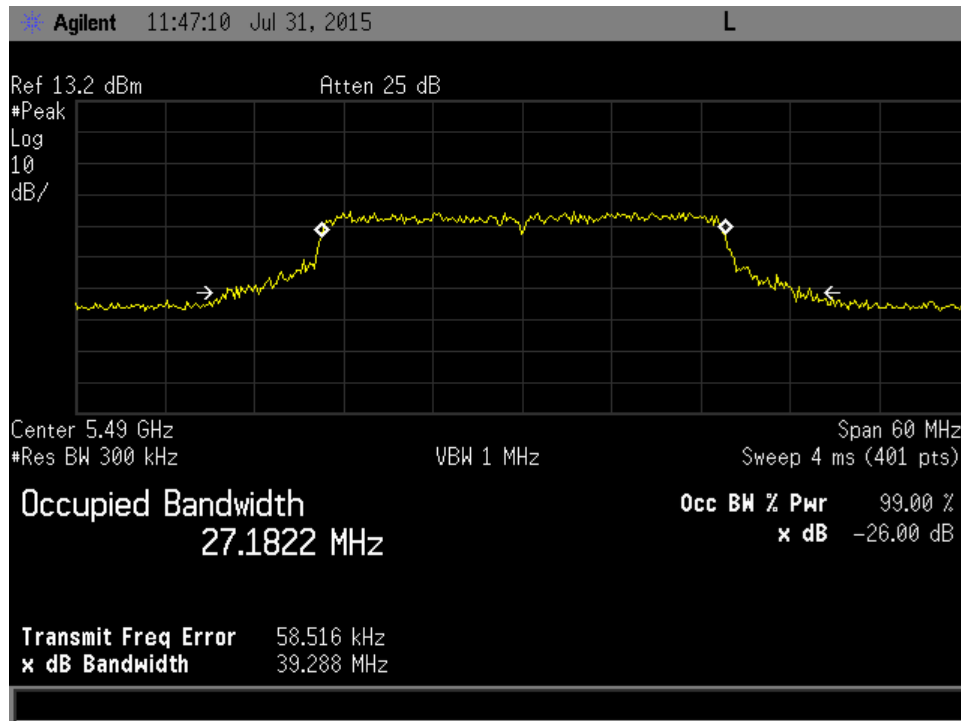


30MHz high ch_J7

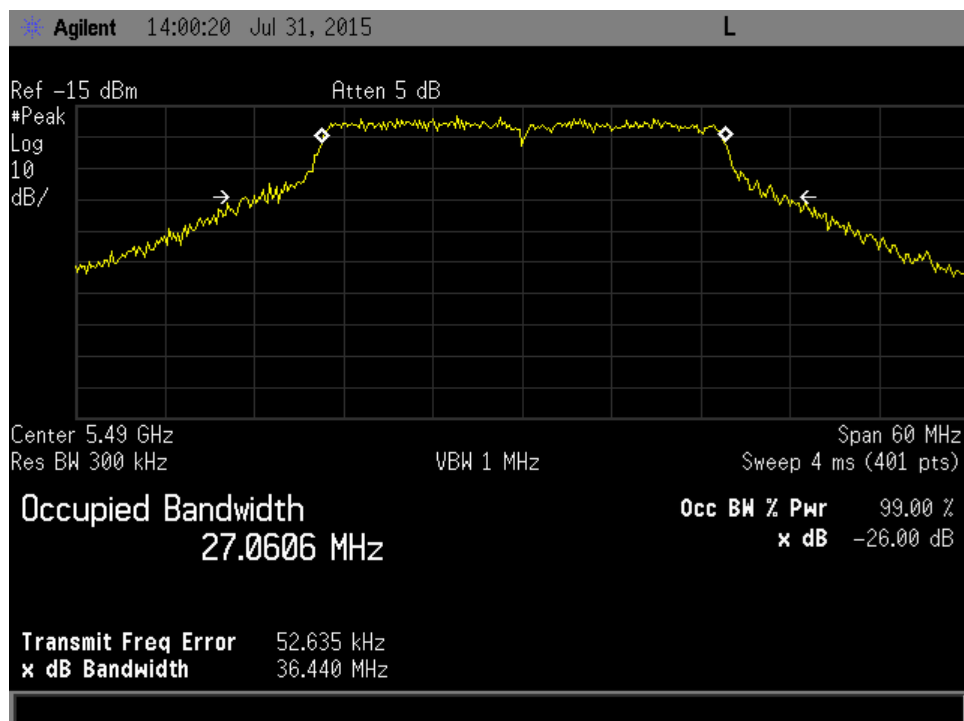




30MHz low ch_J13

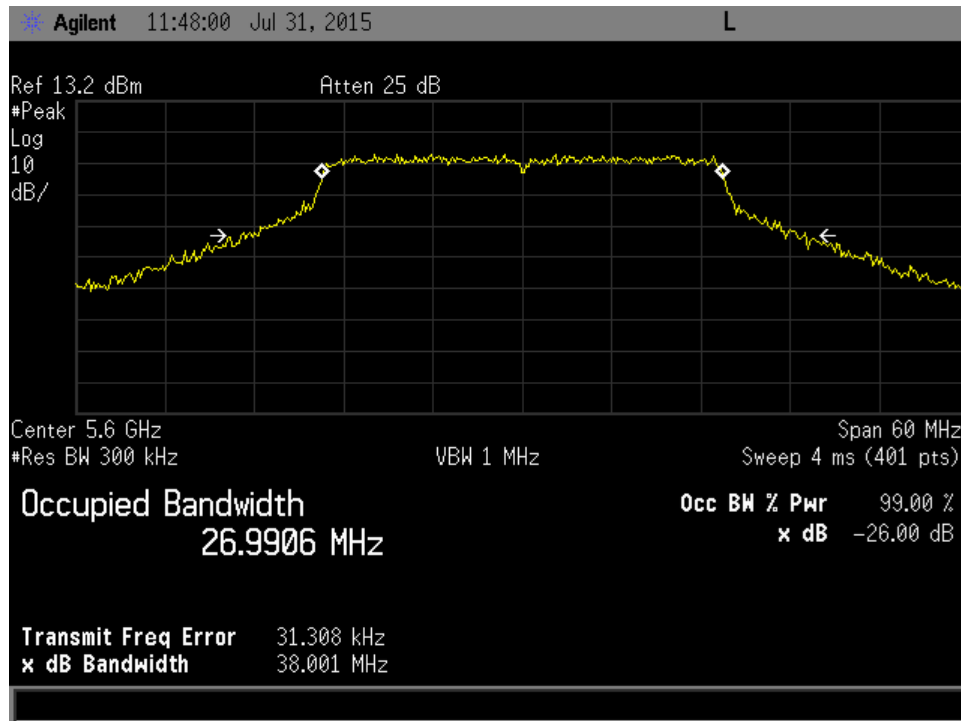


30MHz low ch_J7

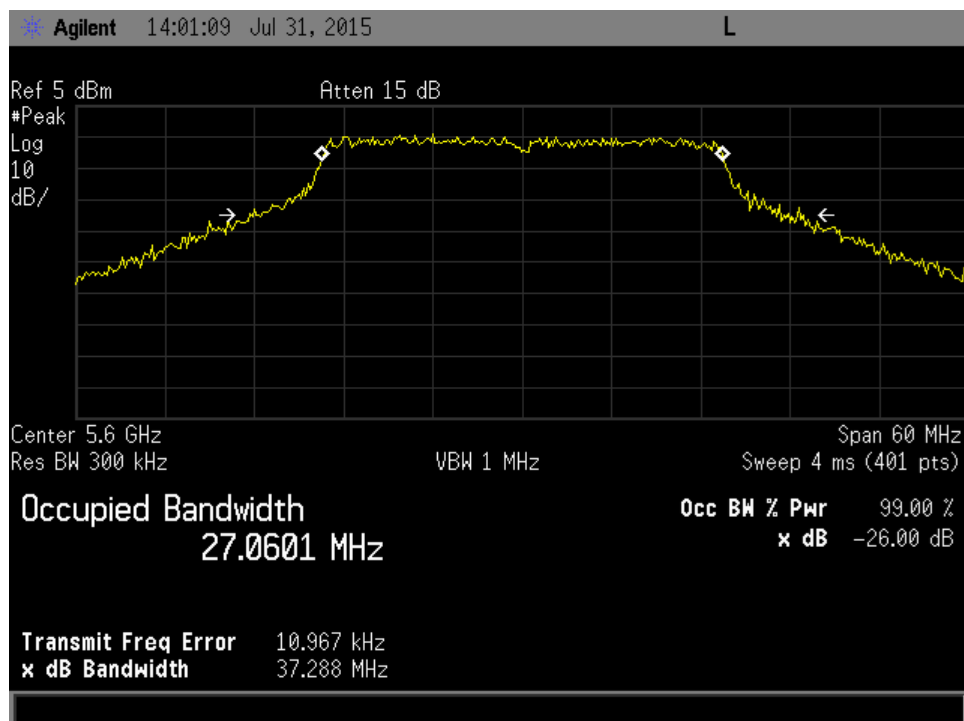




30MHz mid ch_J13

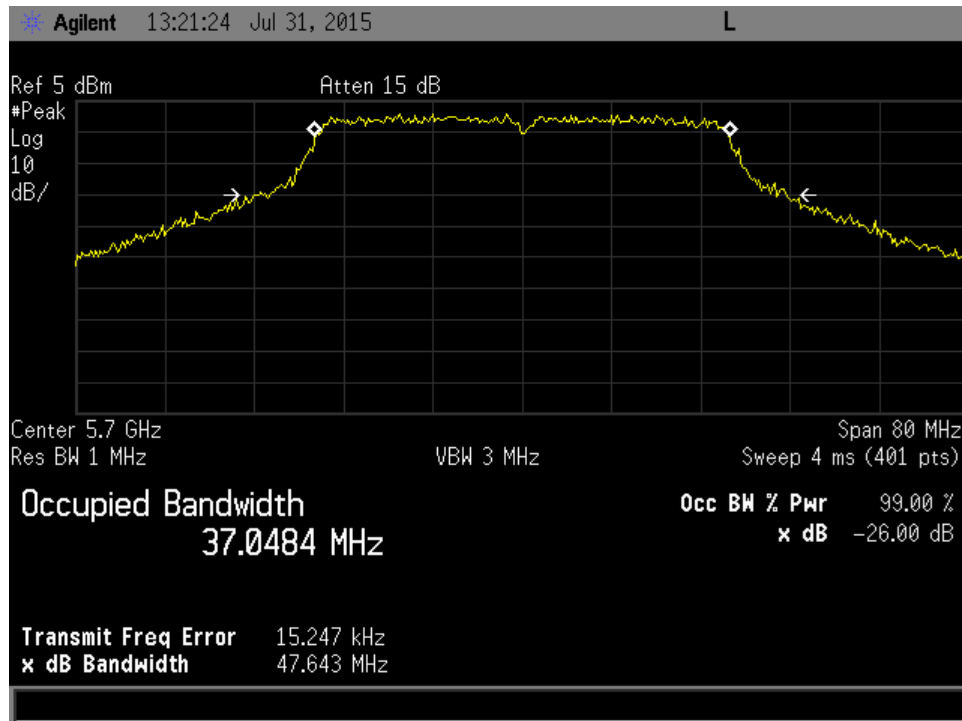


30MHz mid ch_J7

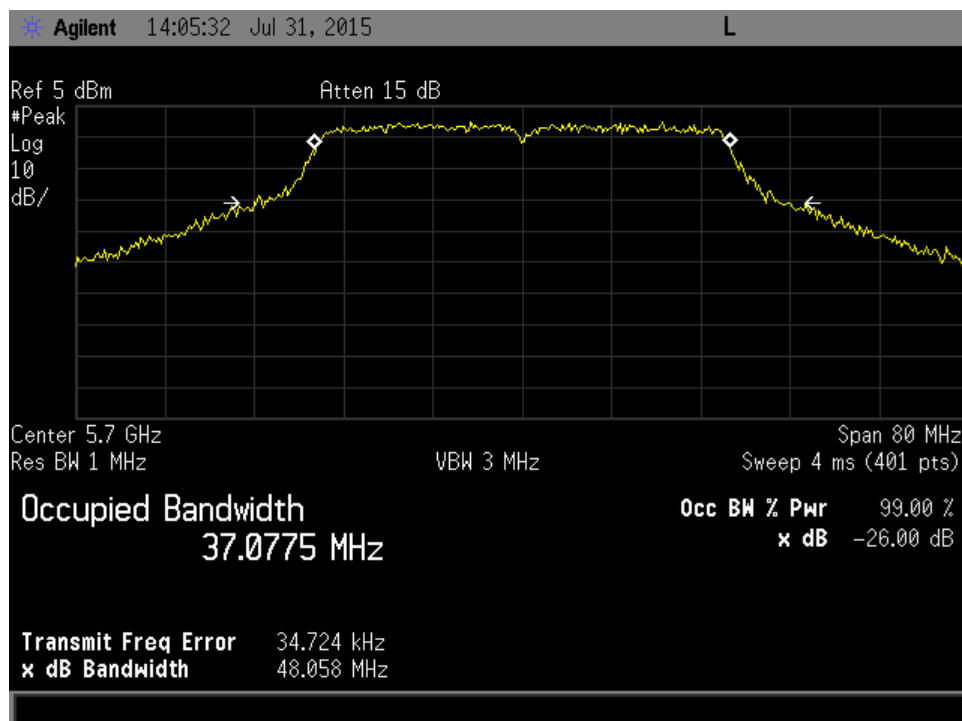




40MHz high ch_J13

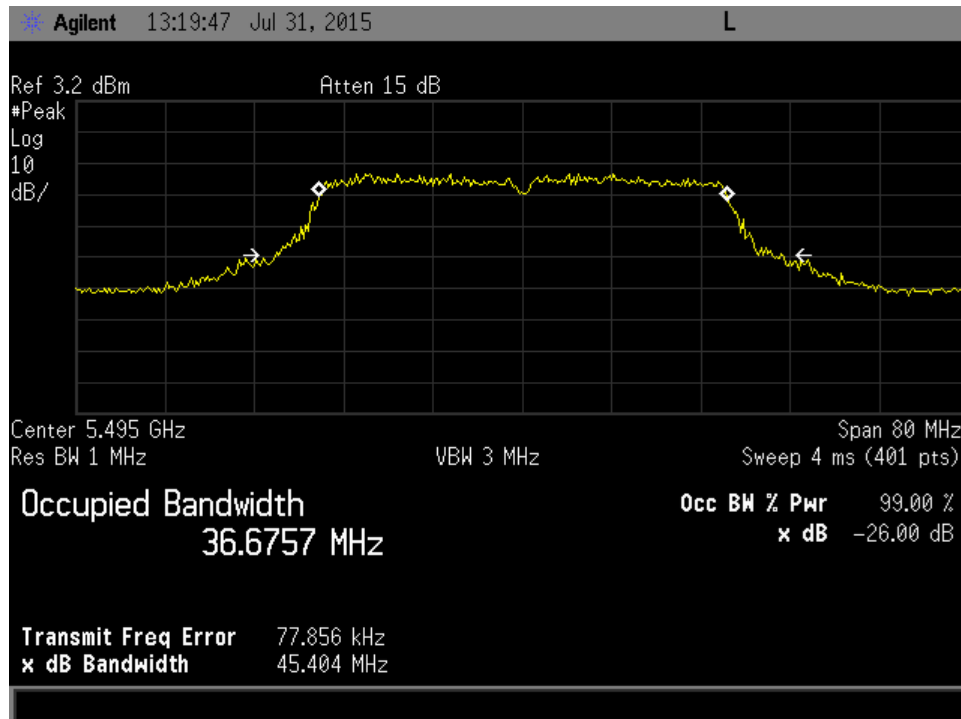


40MHz high ch_J7

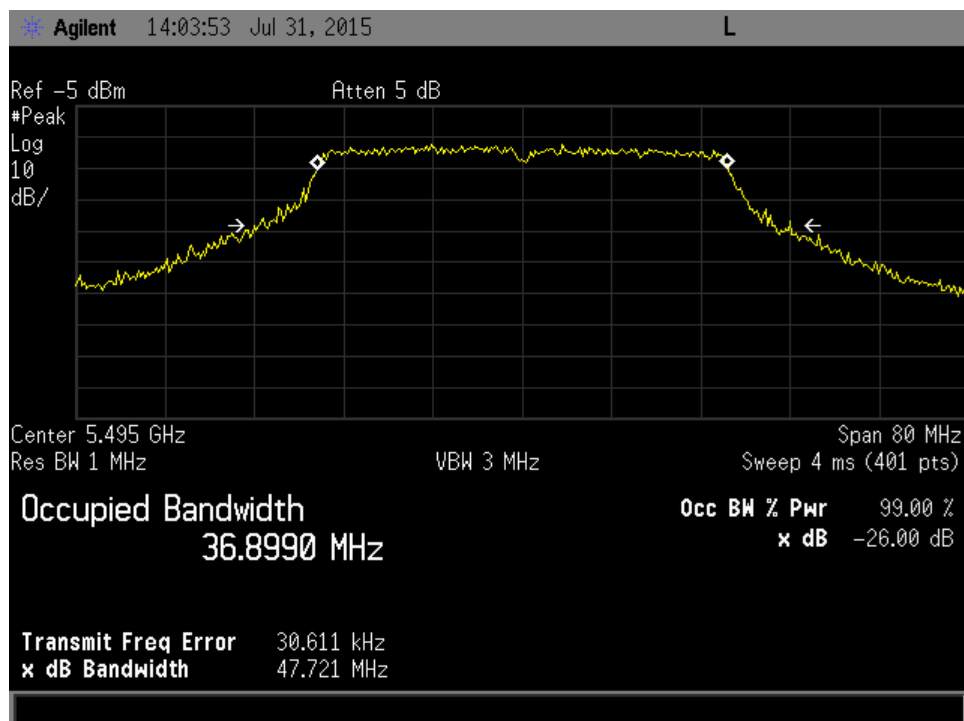




40MHz low ch_J13

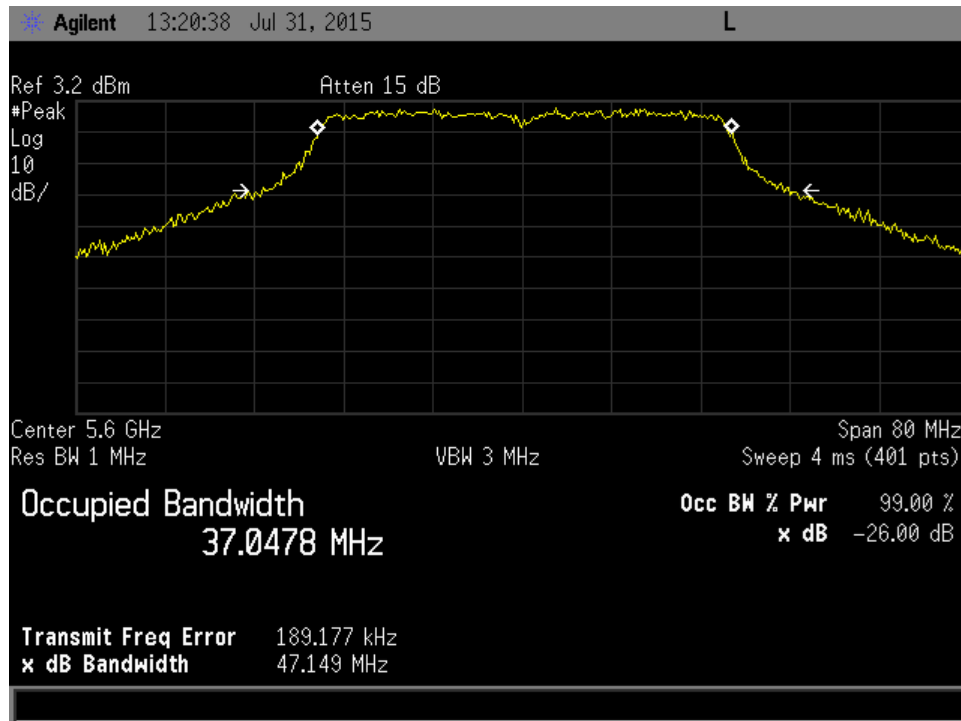


40MHz low ch_J7

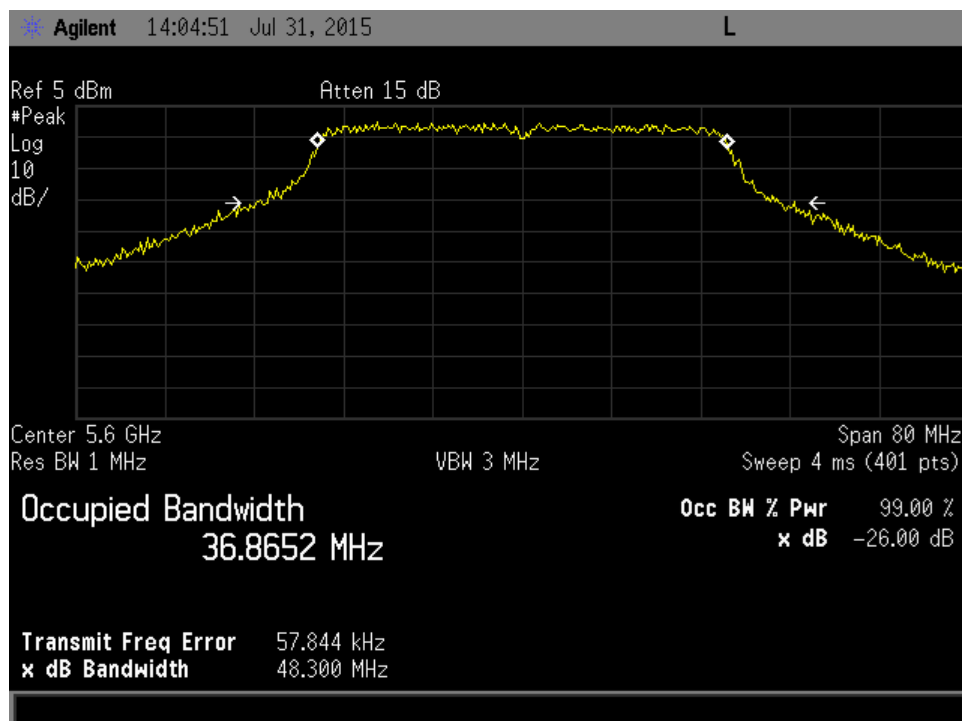




40MHz mid ch_J13

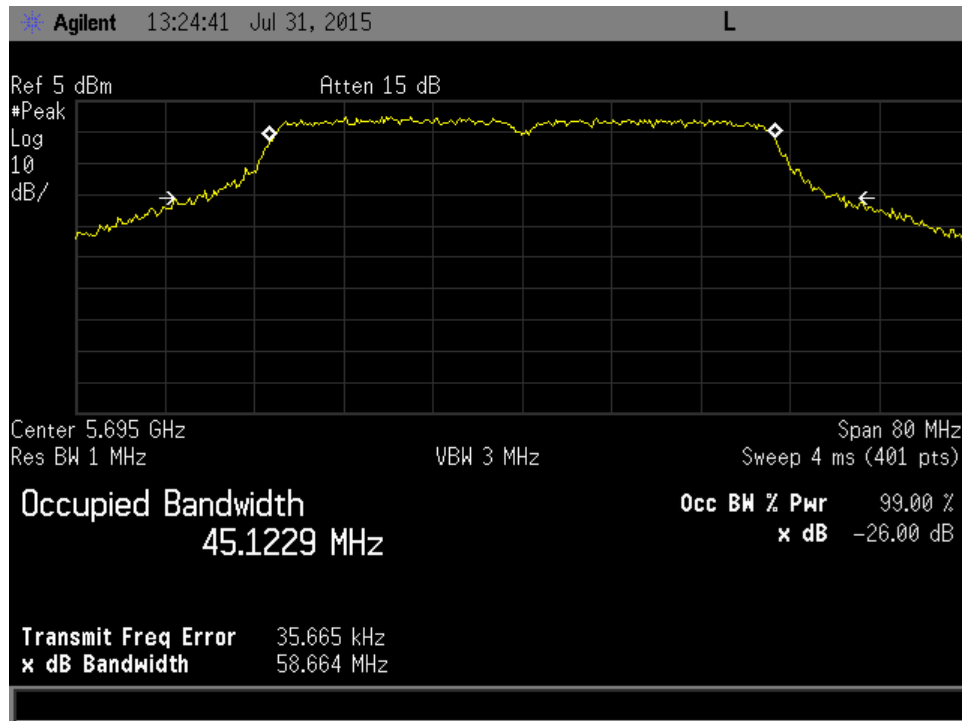


40MHz mid ch_J7

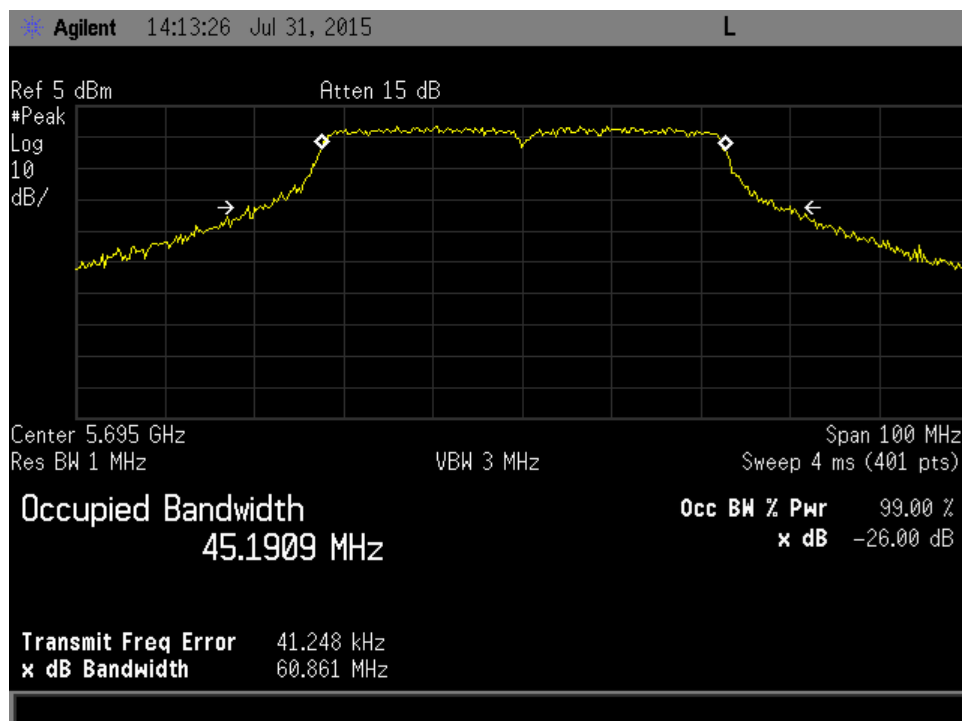




50MHz high ch_J13

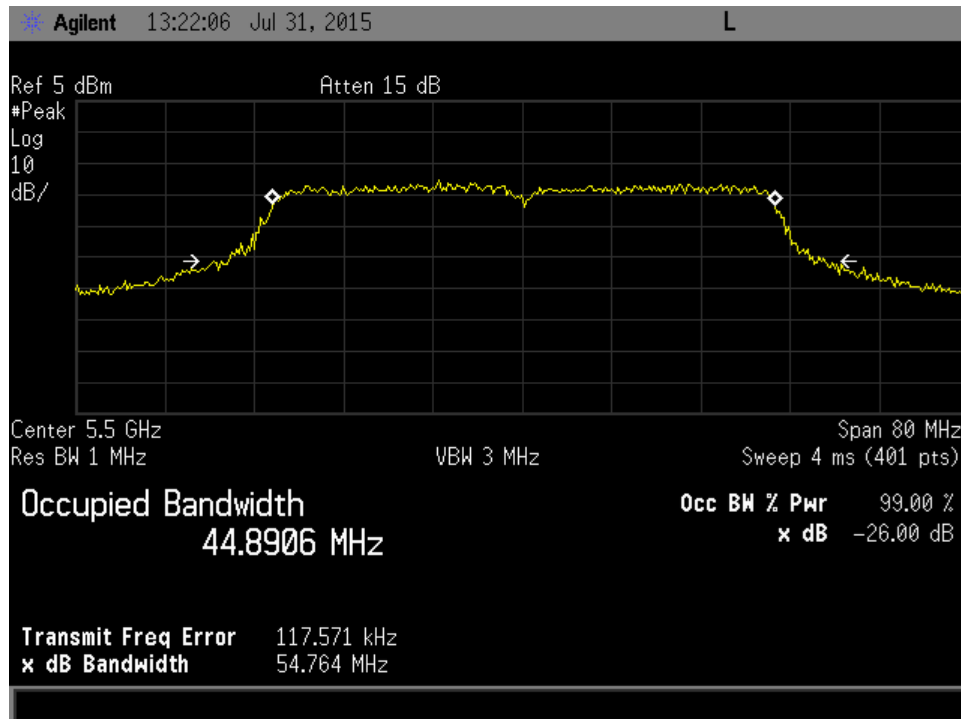


50MHz high ch_J7

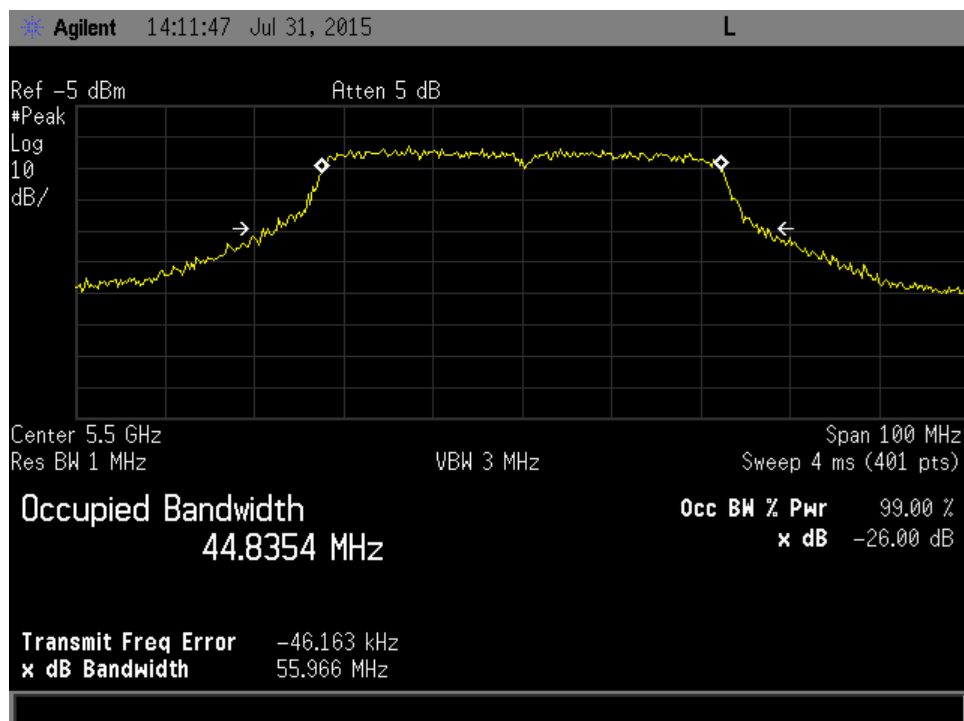




50MHz low ch_J13

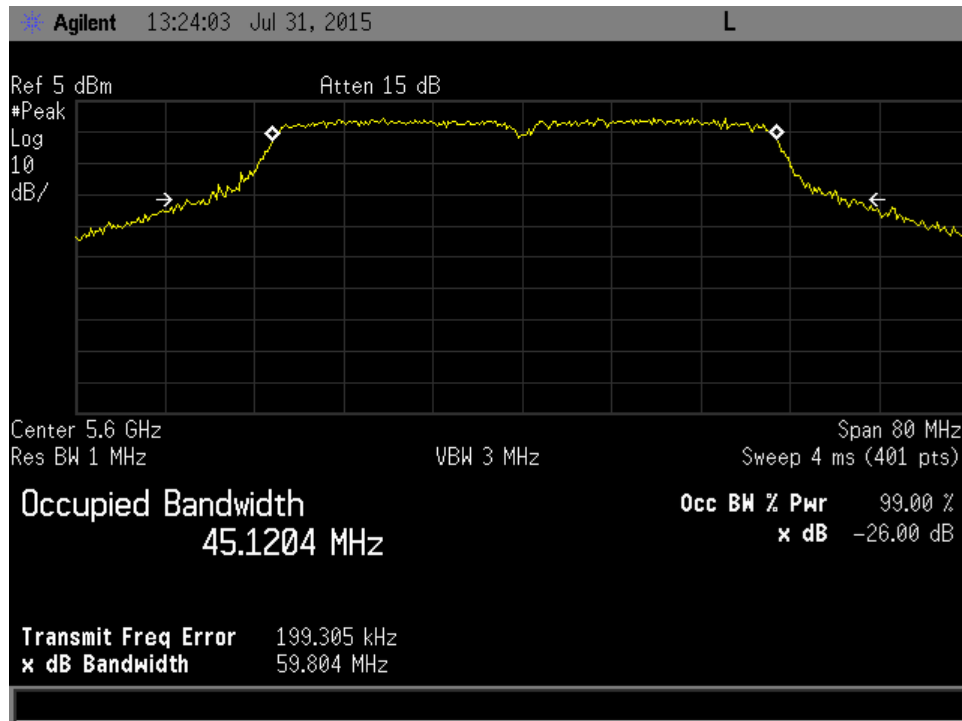


50MHz low ch_J7

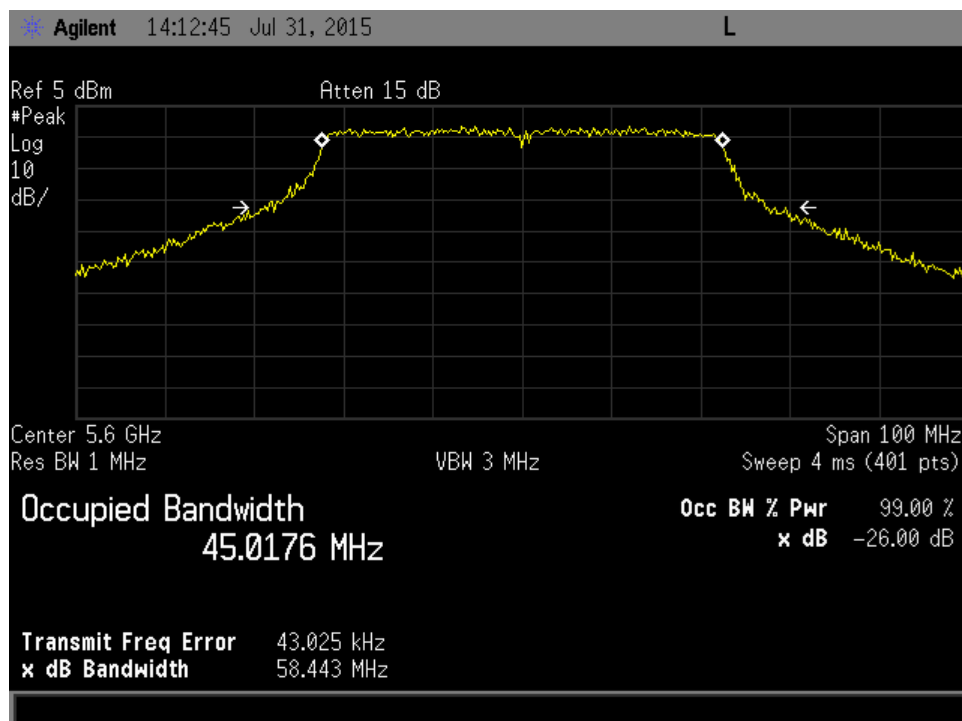




50MHz mid ch_J13

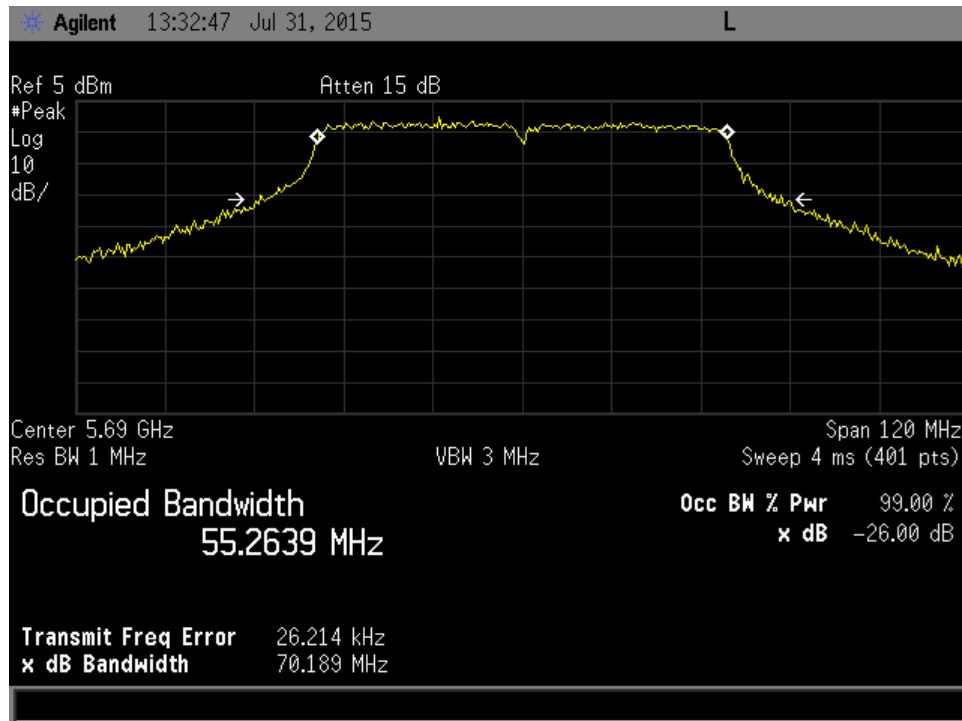


50MHz mid ch_J7

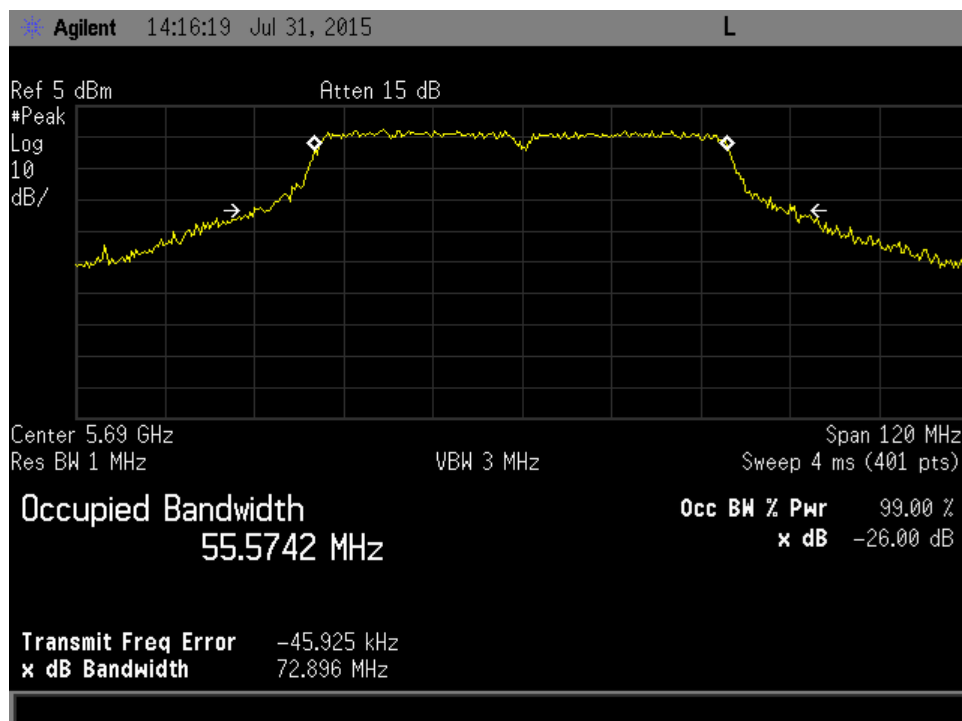




60MHz high ch_J13

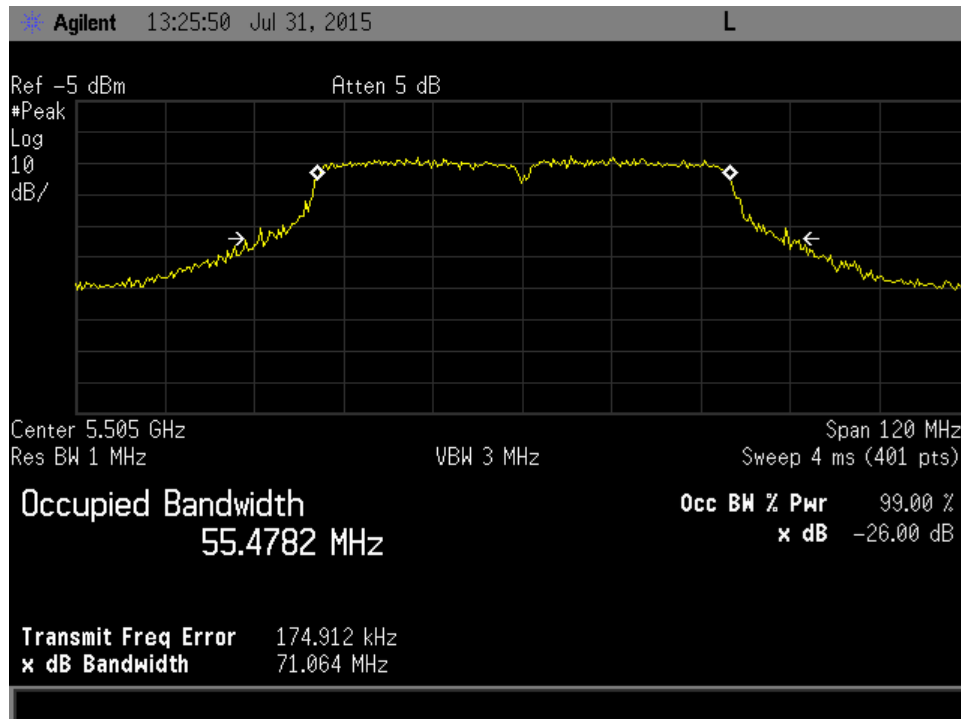


60MHz high ch_J7

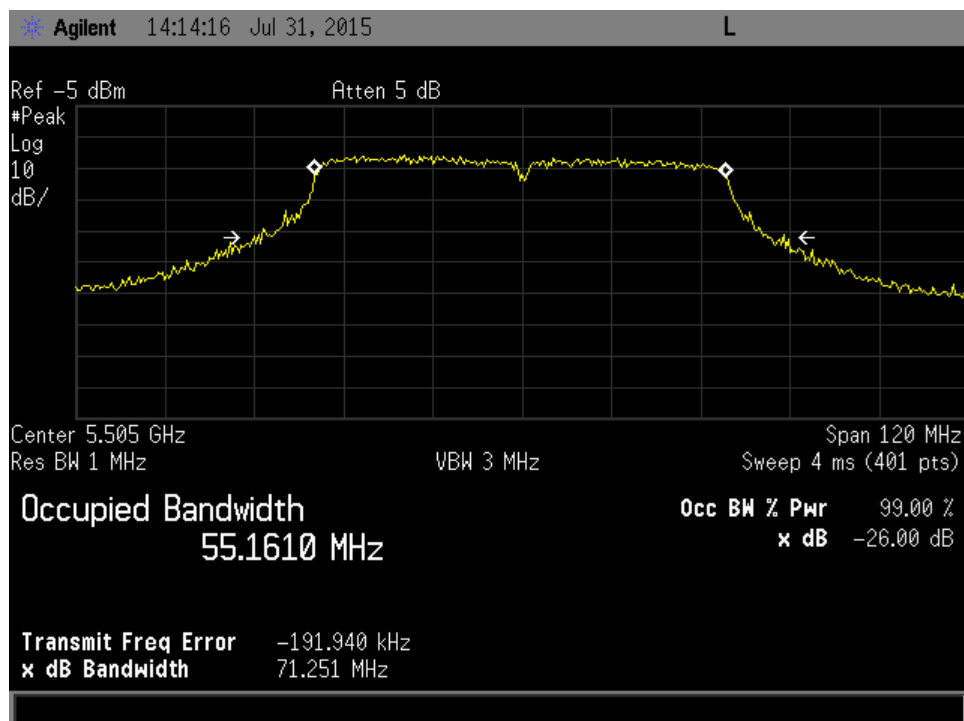




60MHz low ch_J13

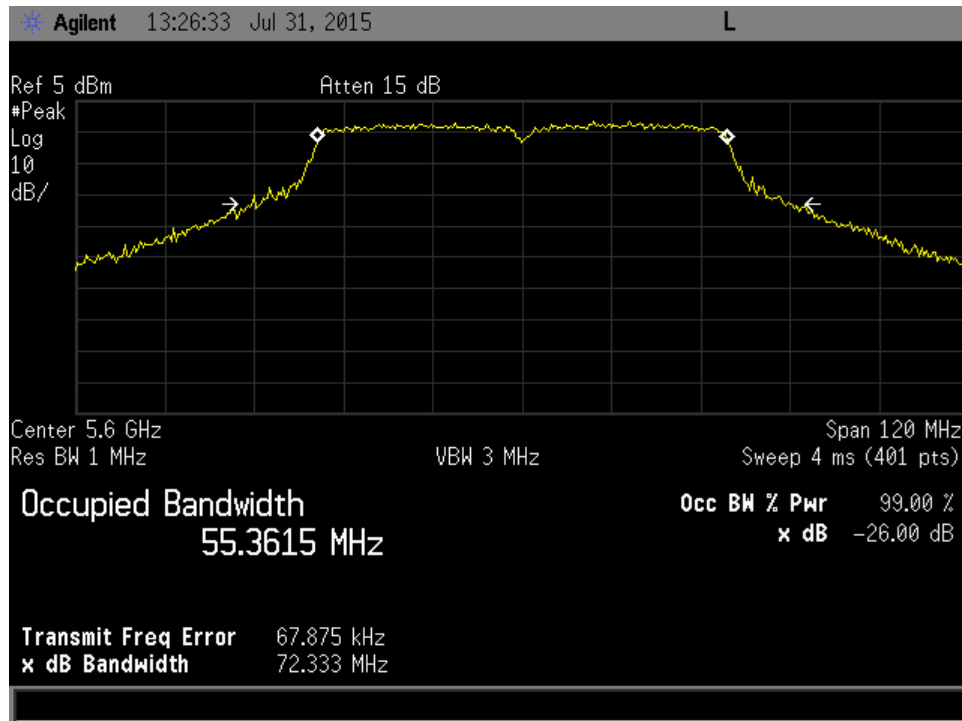


60MHz low ch_J7

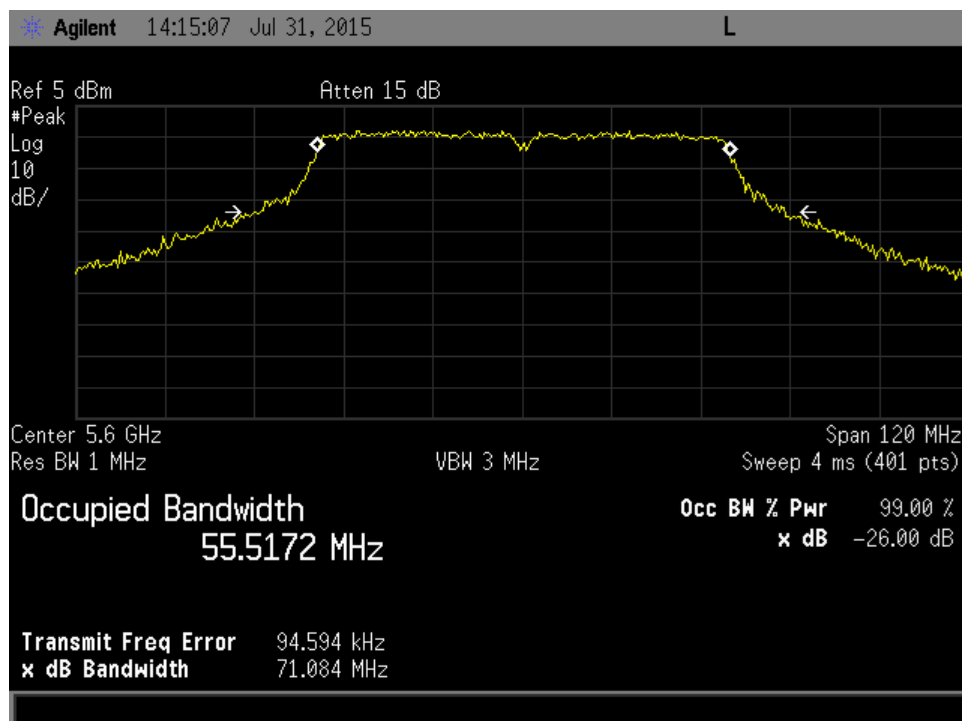




60MHz mid ch_J13

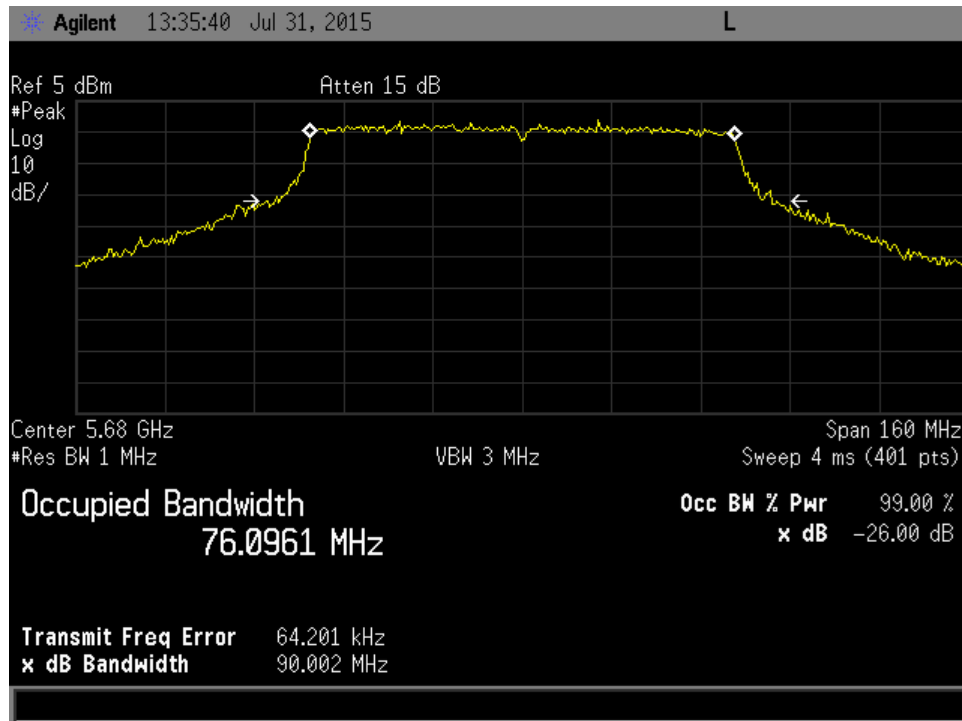


60MHz mid ch_J7

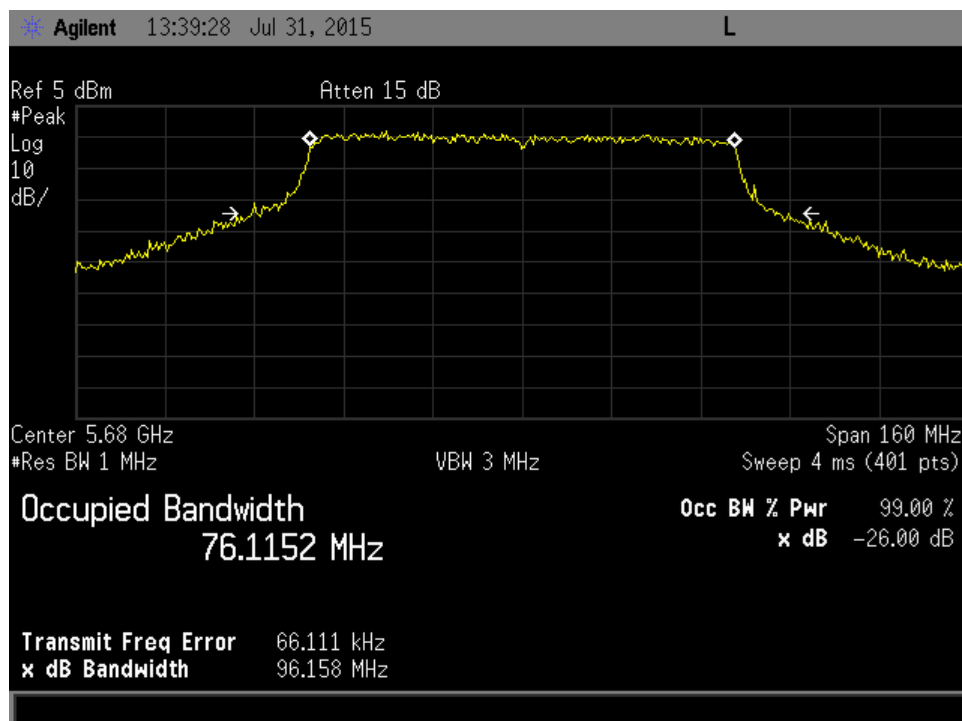




80MHz high ch_J13

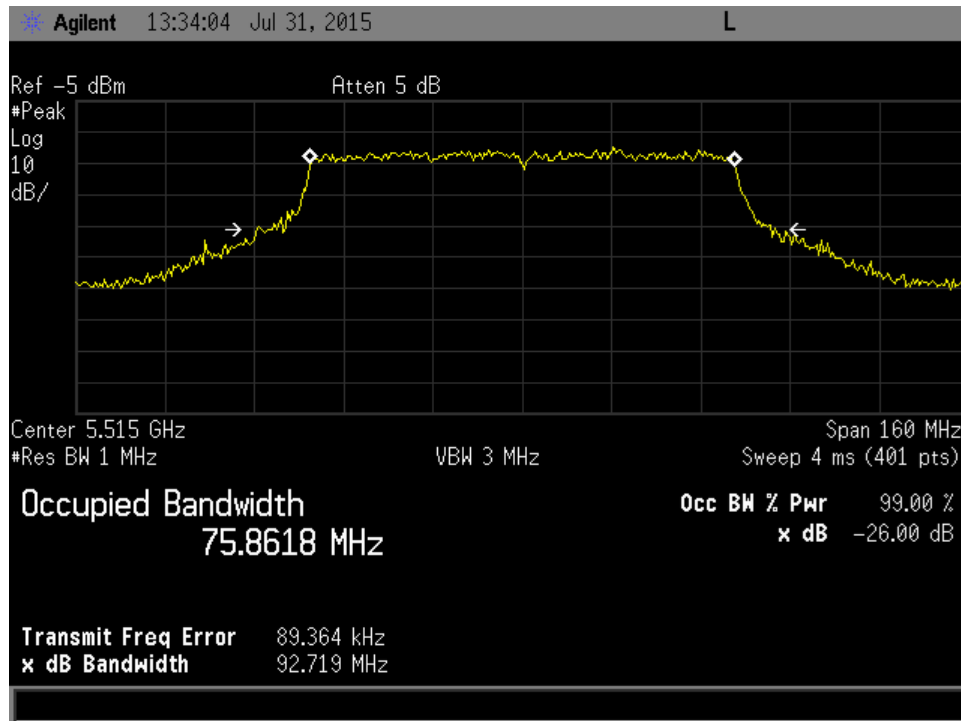


80MHz high ch_J7

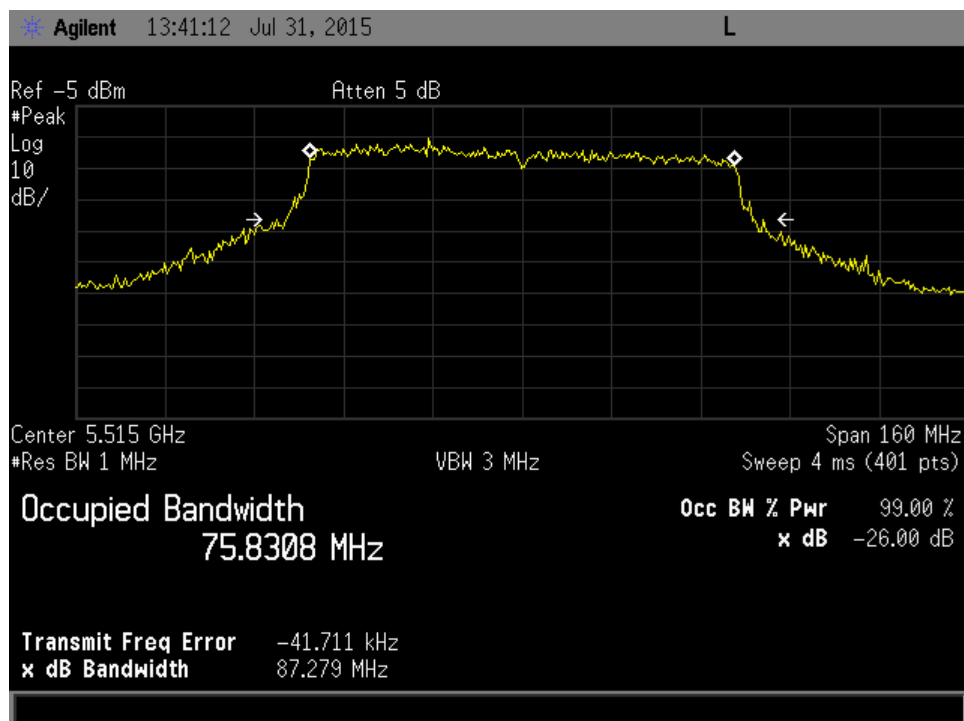




80MHz low ch_J13

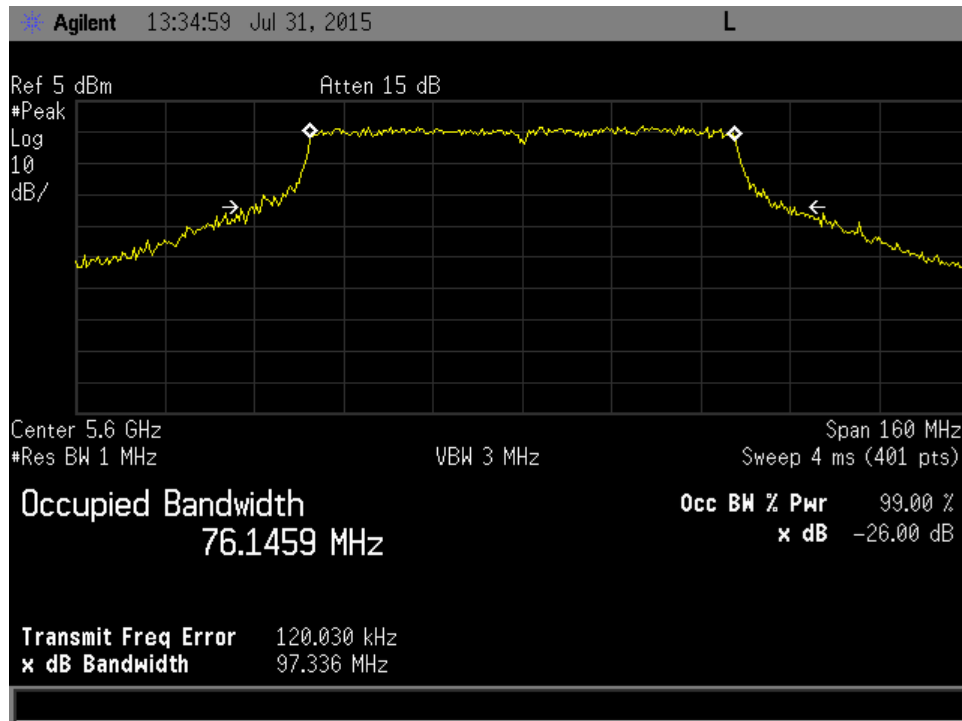


80MHz low ch_J7





80MHz mid ch_J13



80MHz mid ch_J7

