



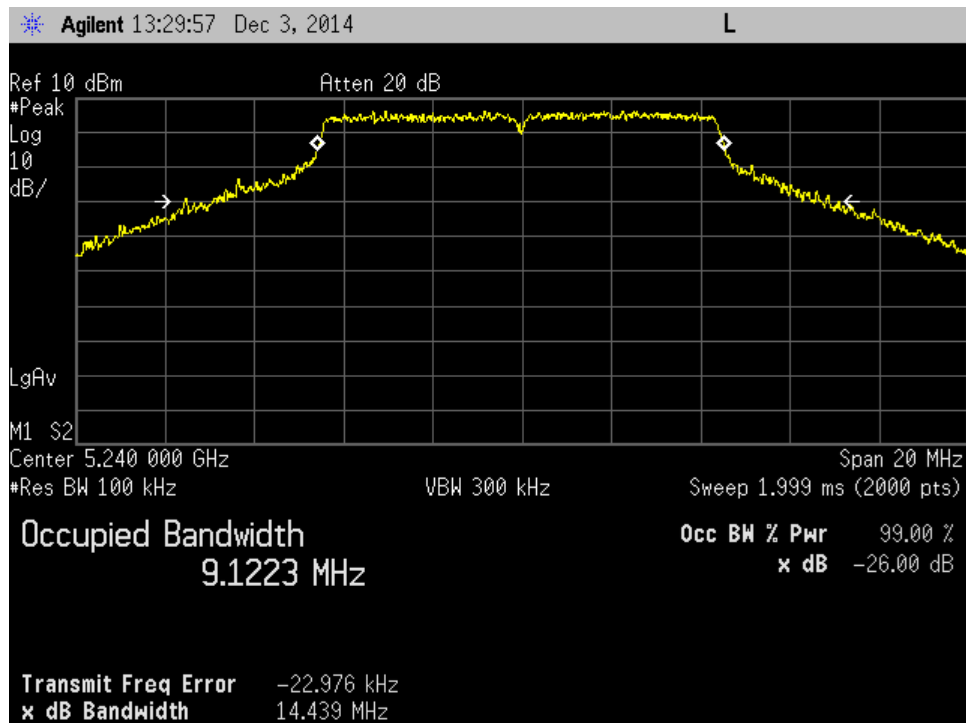
# **Annex C**

## **Occupied Bandwidth**

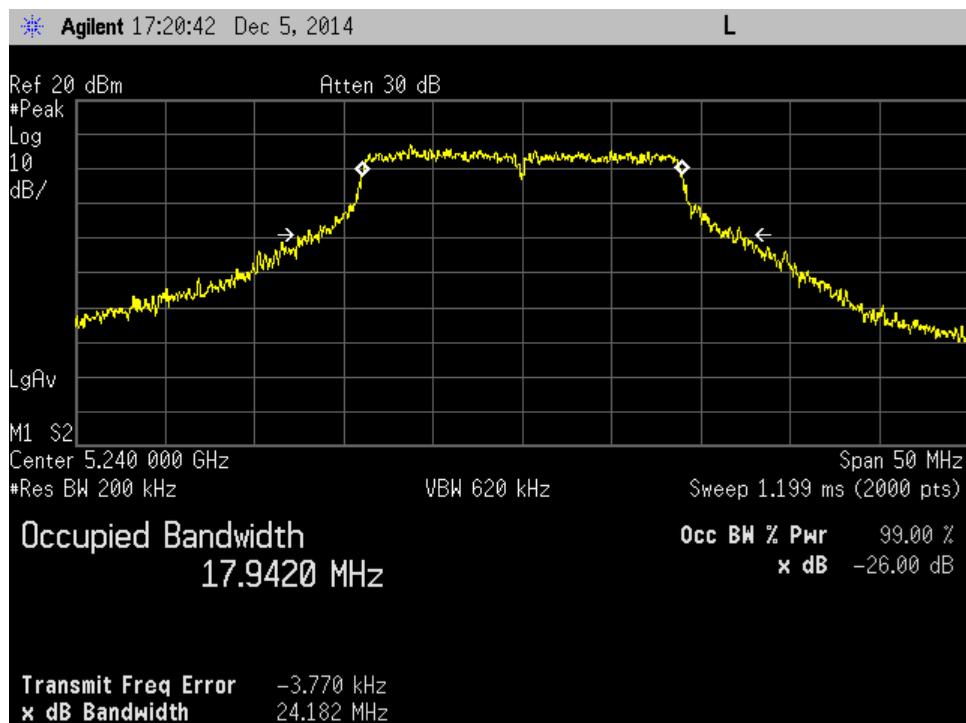
### **Ports J7 and J13**



### J13\_hi ch\_bw=10 MHz

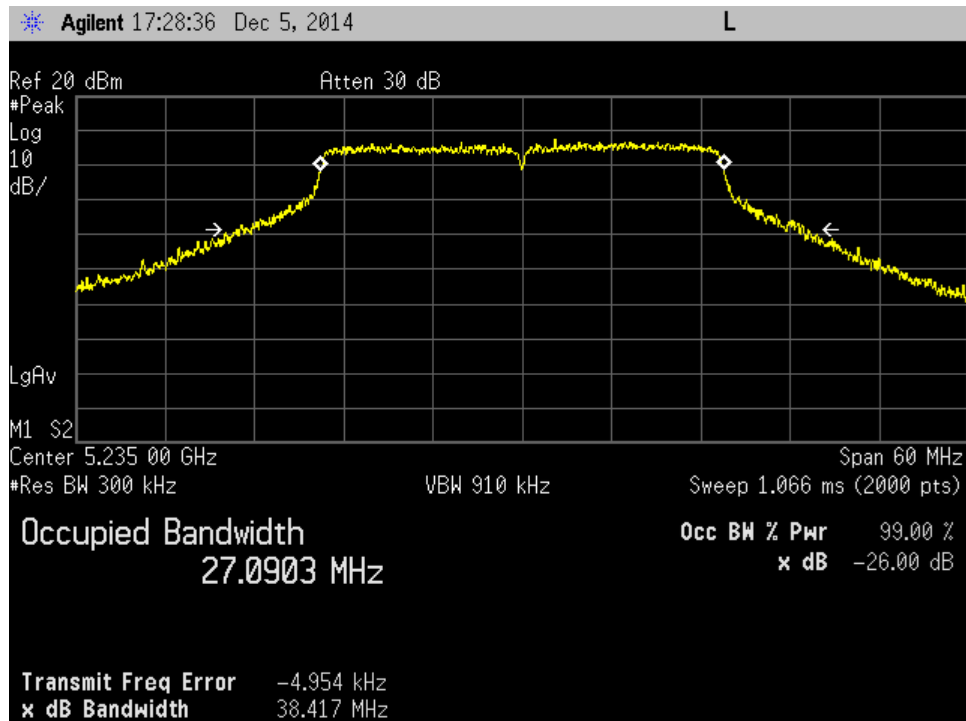


### J13\_hi ch\_bw=20 MHz

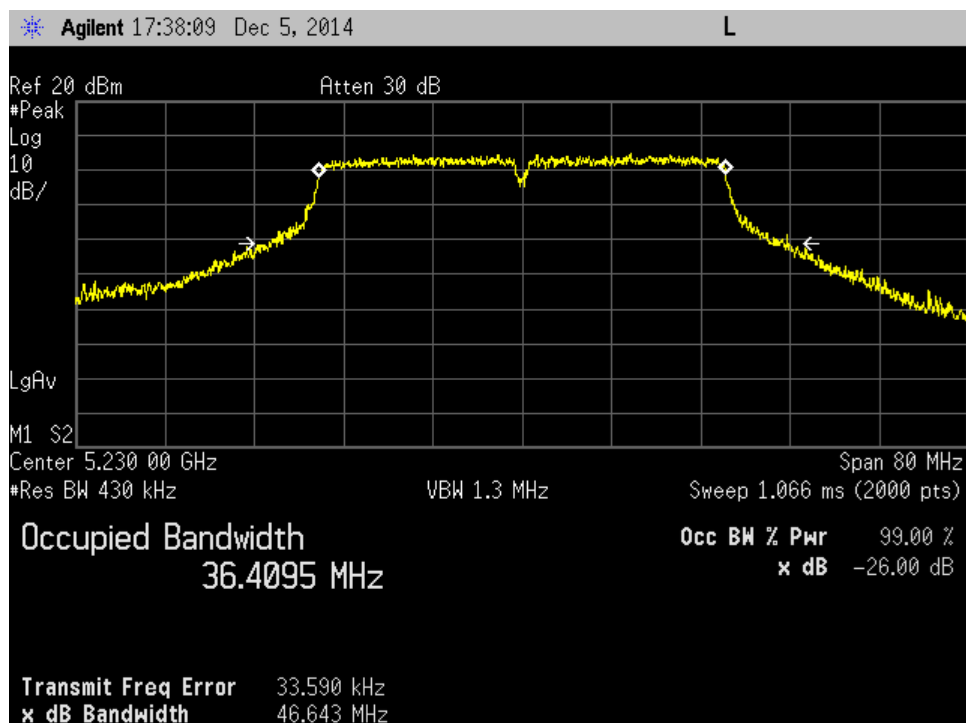




### J13\_hi ch\_bw=30 MHz

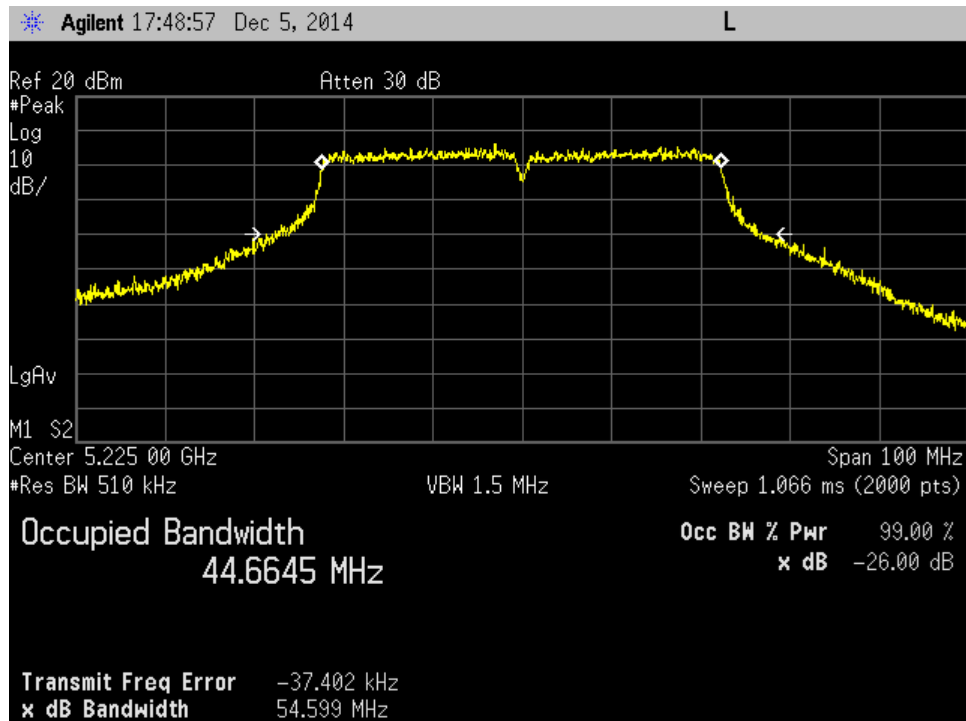


### J13\_hi ch\_bw=40 MHz

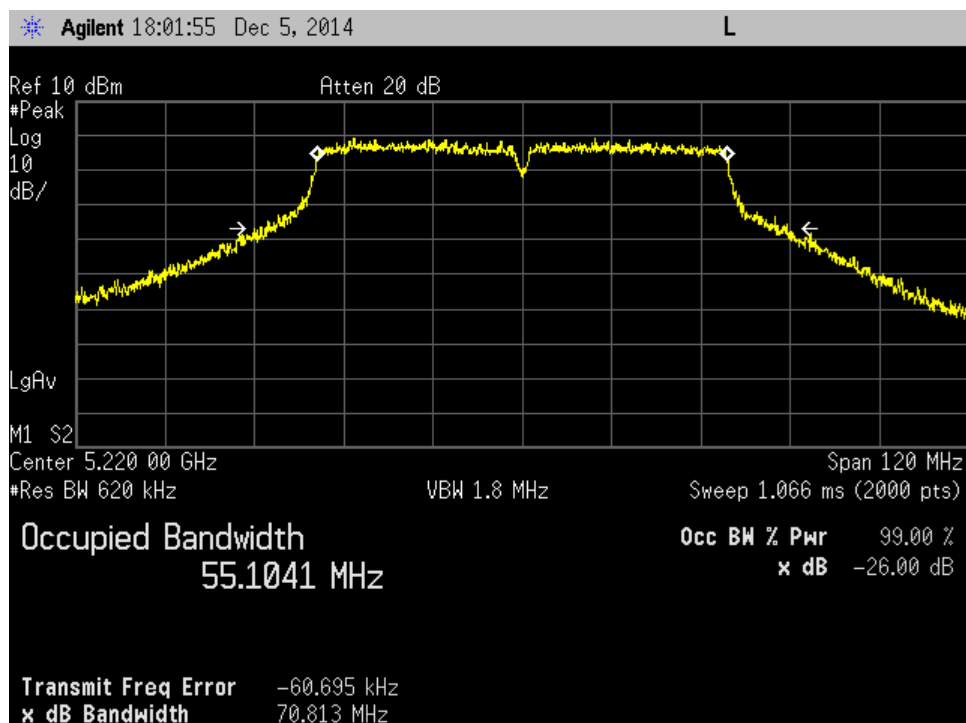




**J13\_hi ch\_bw=50 MHz**

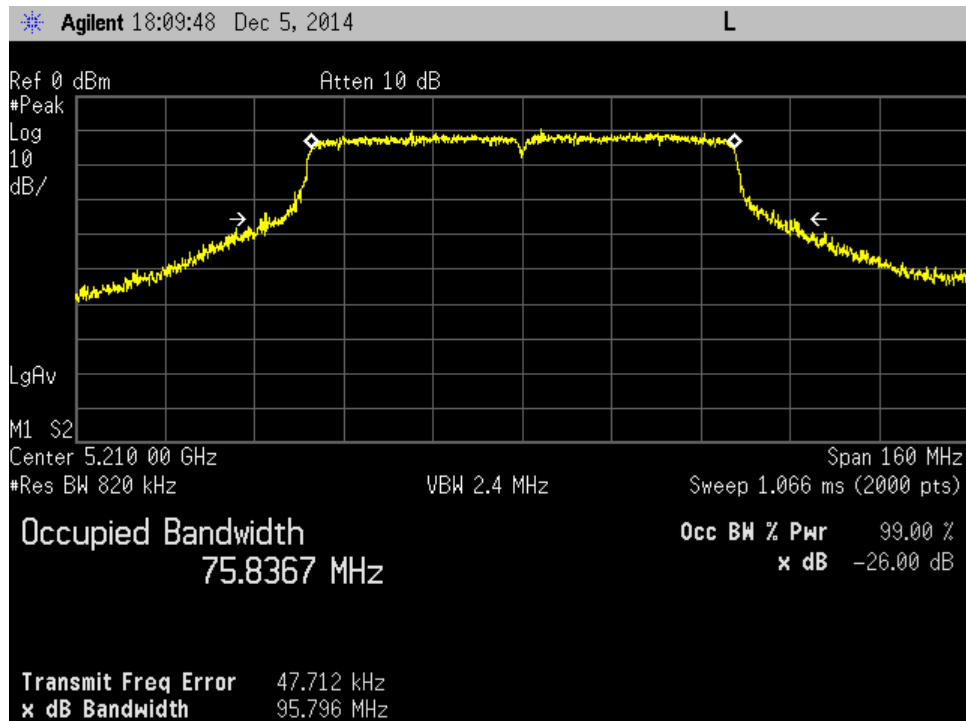


**J13\_hi ch\_bw=60 MHz**

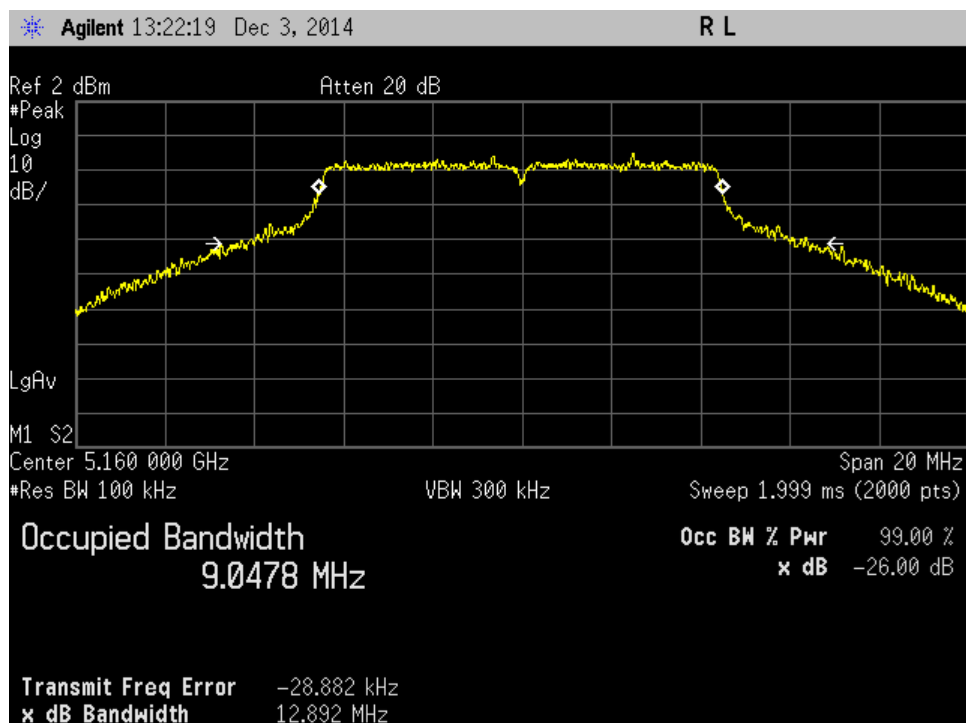




### J13\_hi ch\_bw=80 MHz

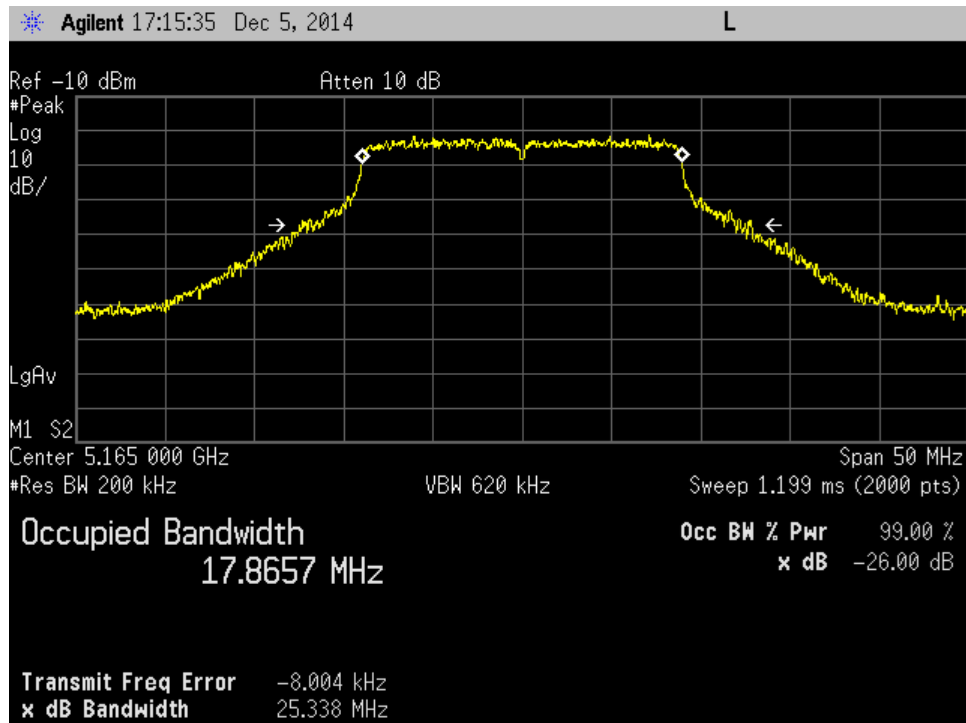


### J13\_low ch\_bw=10 MHz

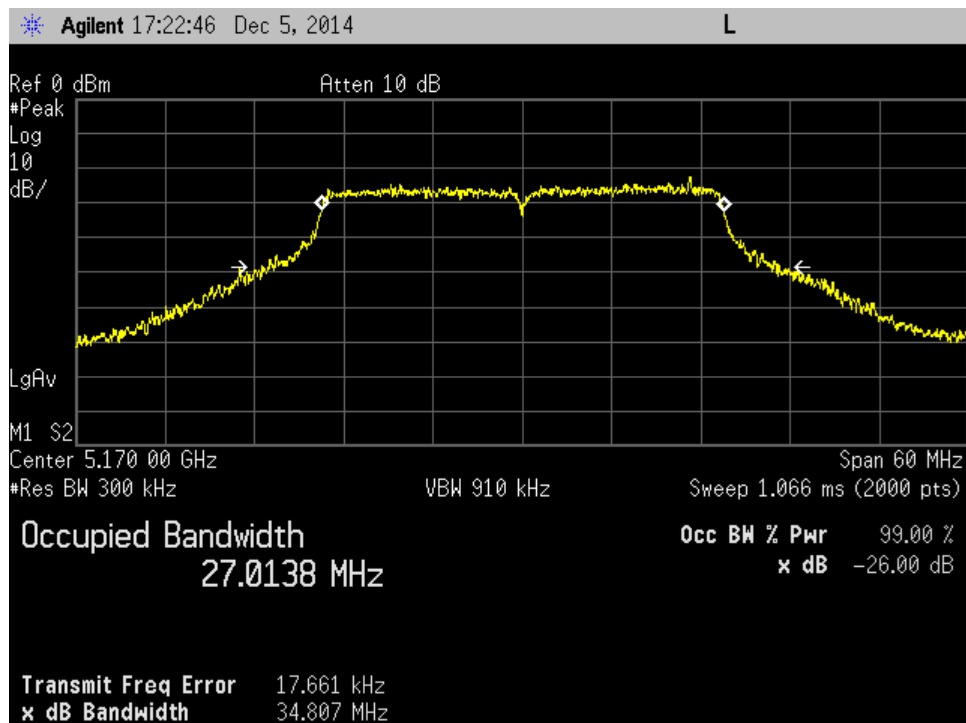




### J13\_low ch\_bw=20 MHz

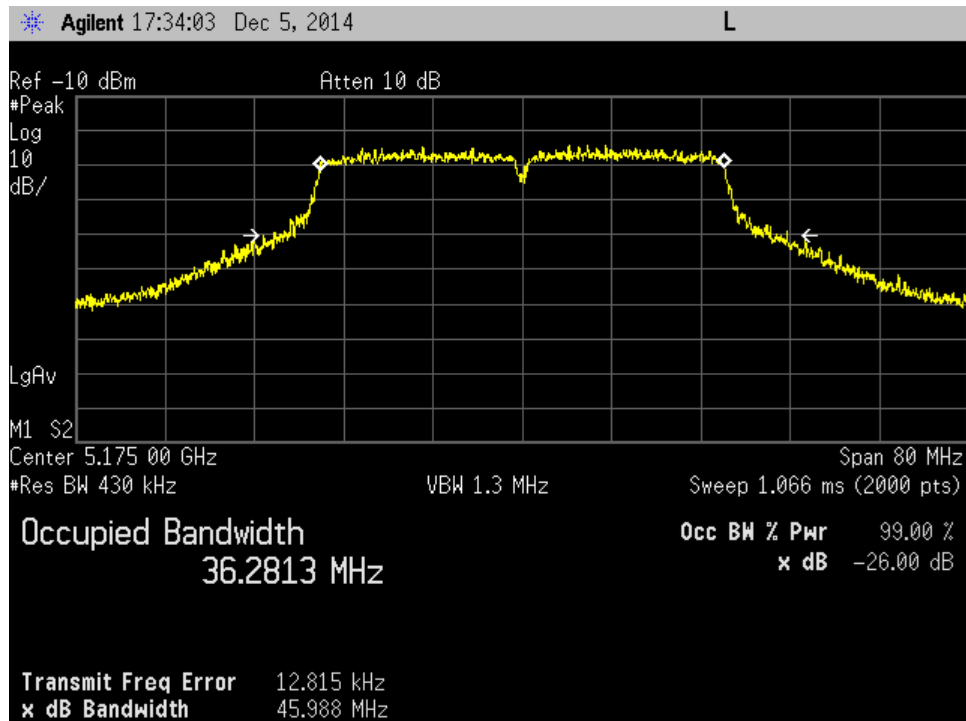


### J13\_low ch\_bw=30 MHz

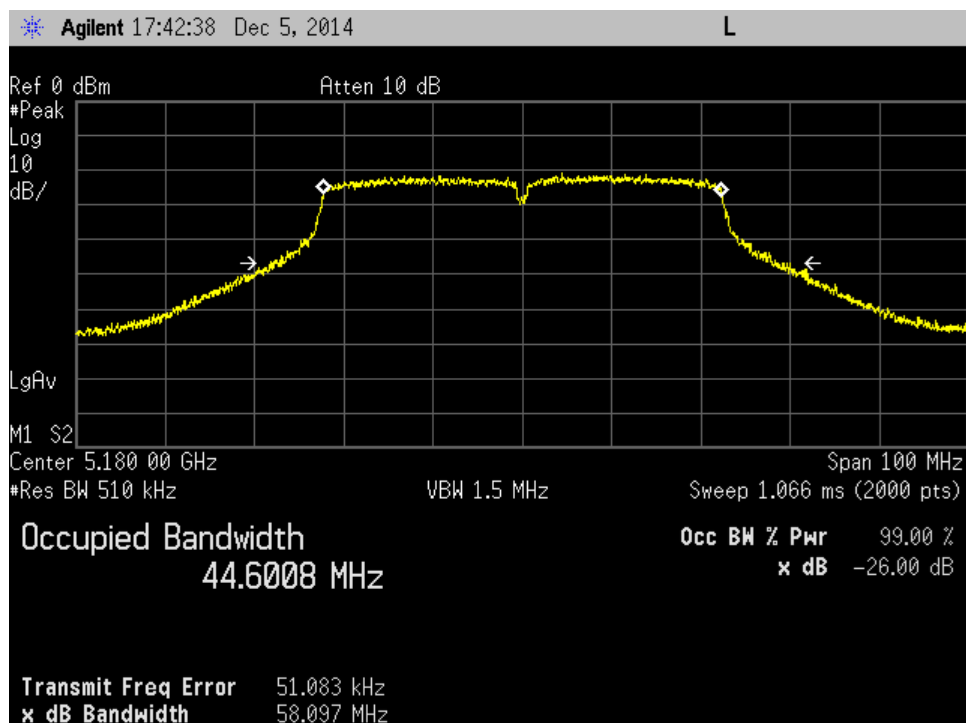




### J13\_low ch\_bw=40 MHz

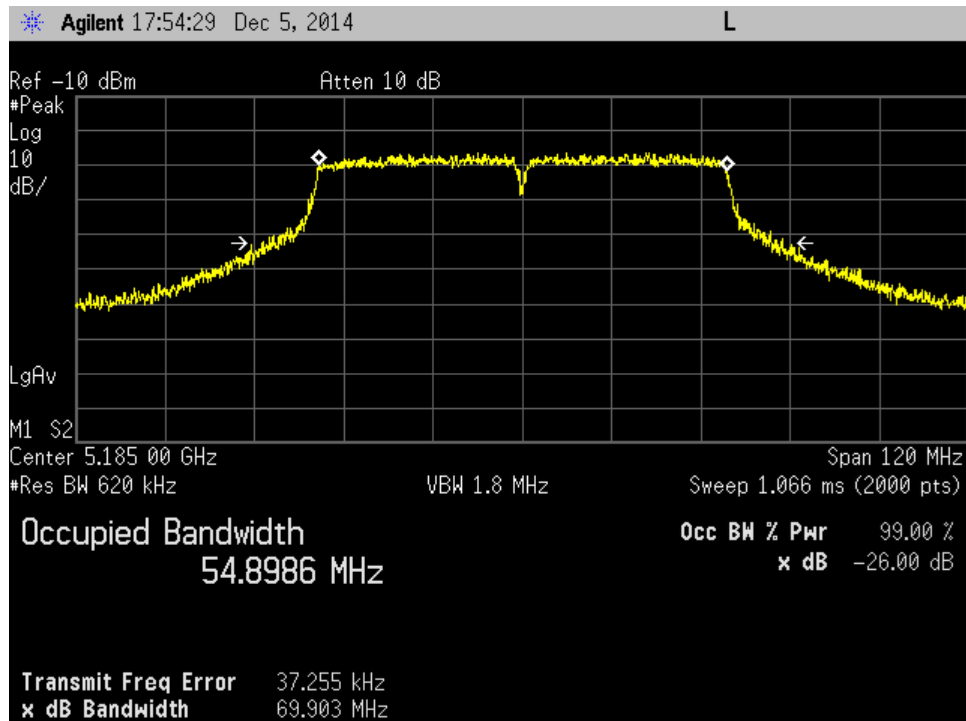


### J13\_low ch\_bw=50 MHz

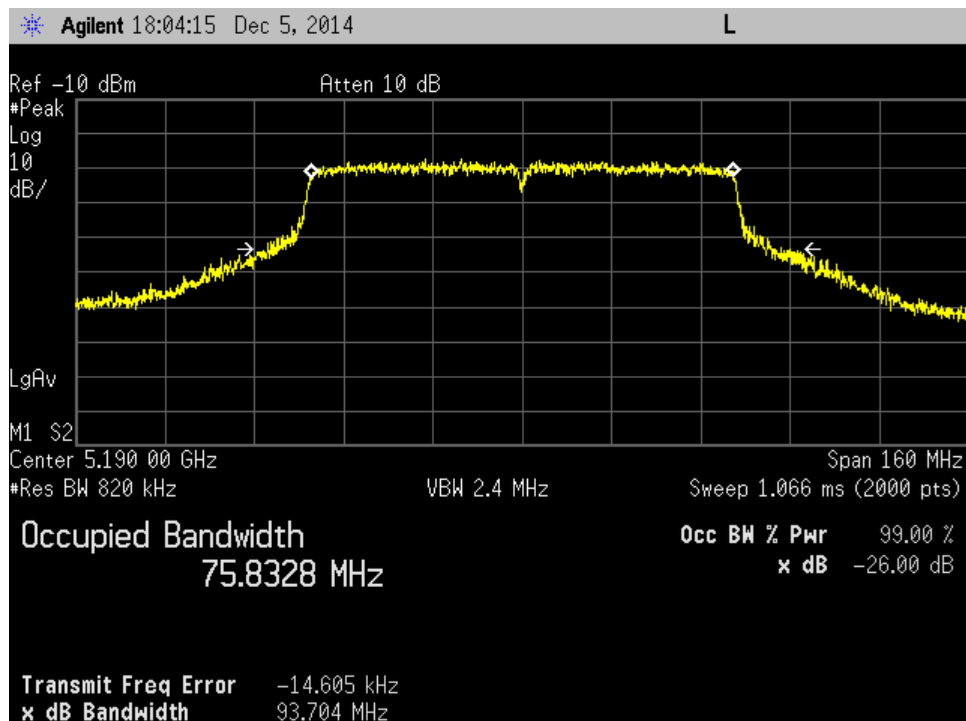




**J13\_low ch\_bw=60 MHz**

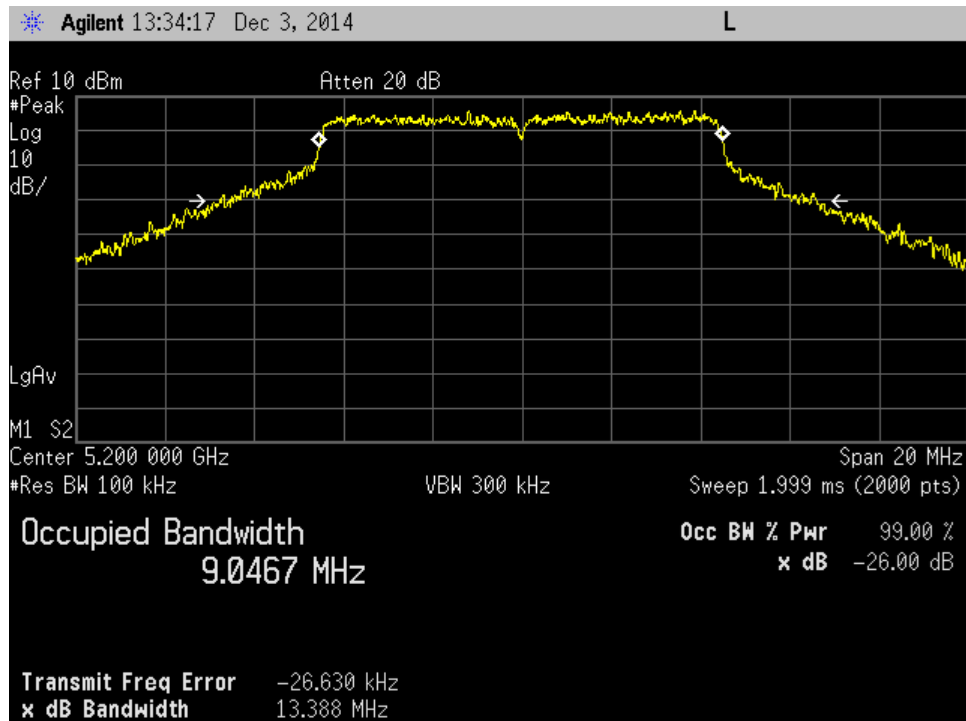


**J13\_low ch\_bw=80 MHz**

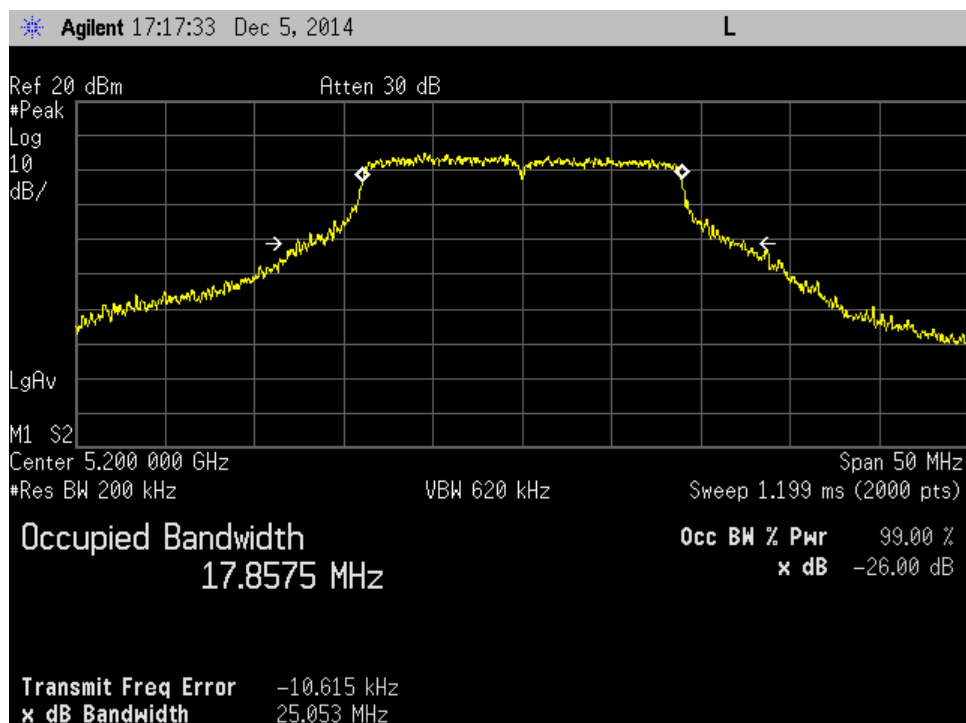




### J13\_mid ch\_bw=10 MHz

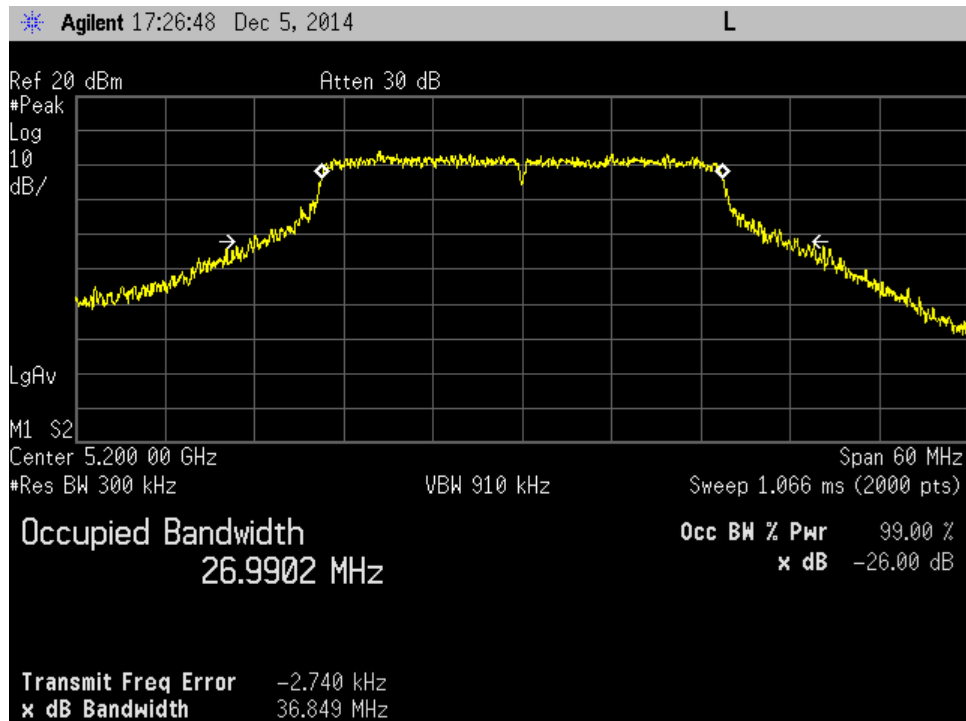


### J13\_mid ch\_bw=20 MHz

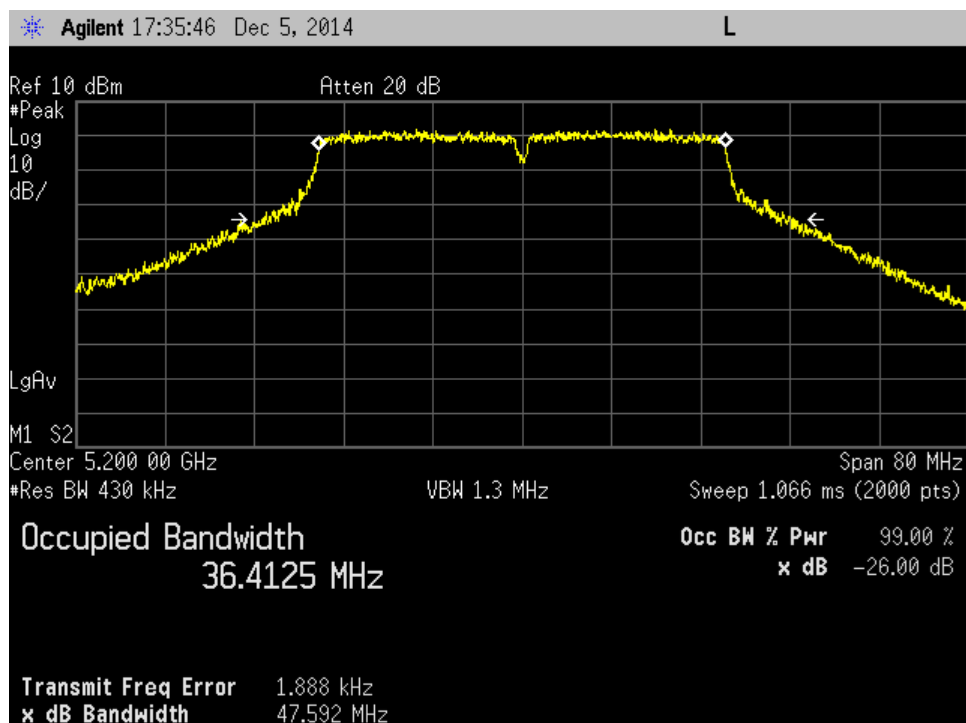




### J13\_mid ch\_bw=30 MHz

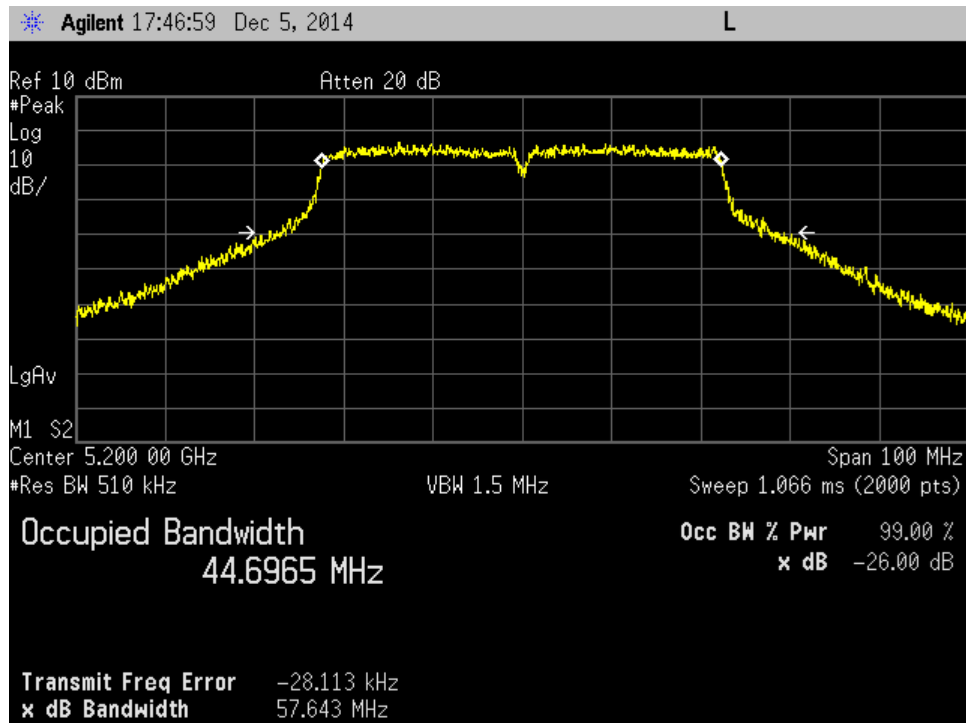


### J13\_mid ch\_bw=40 MHz

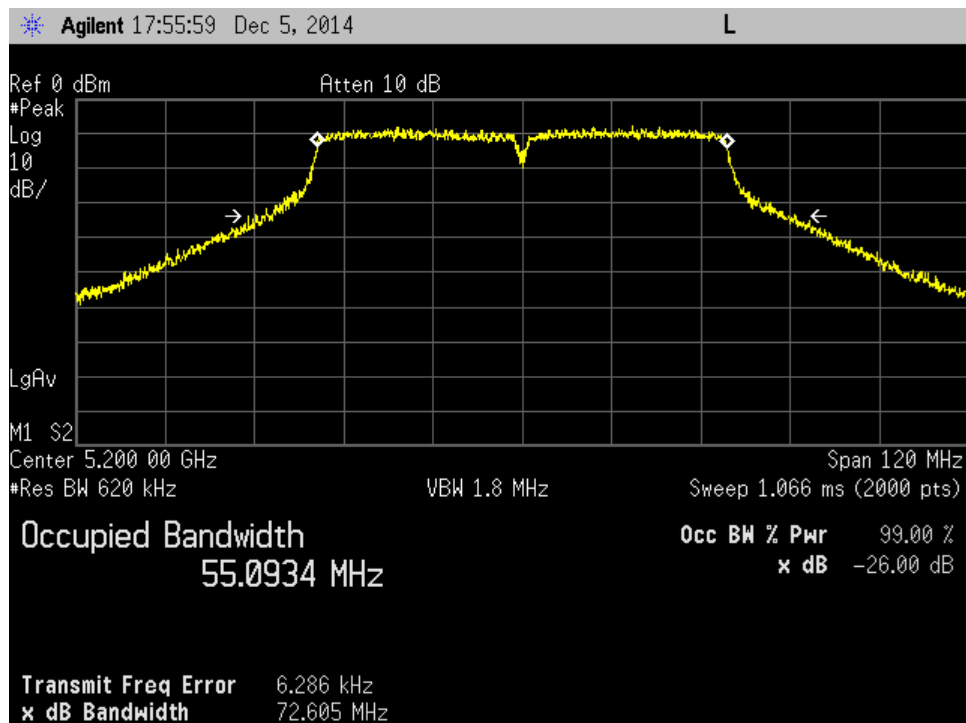




### J13\_mid ch\_bw=50 MHz

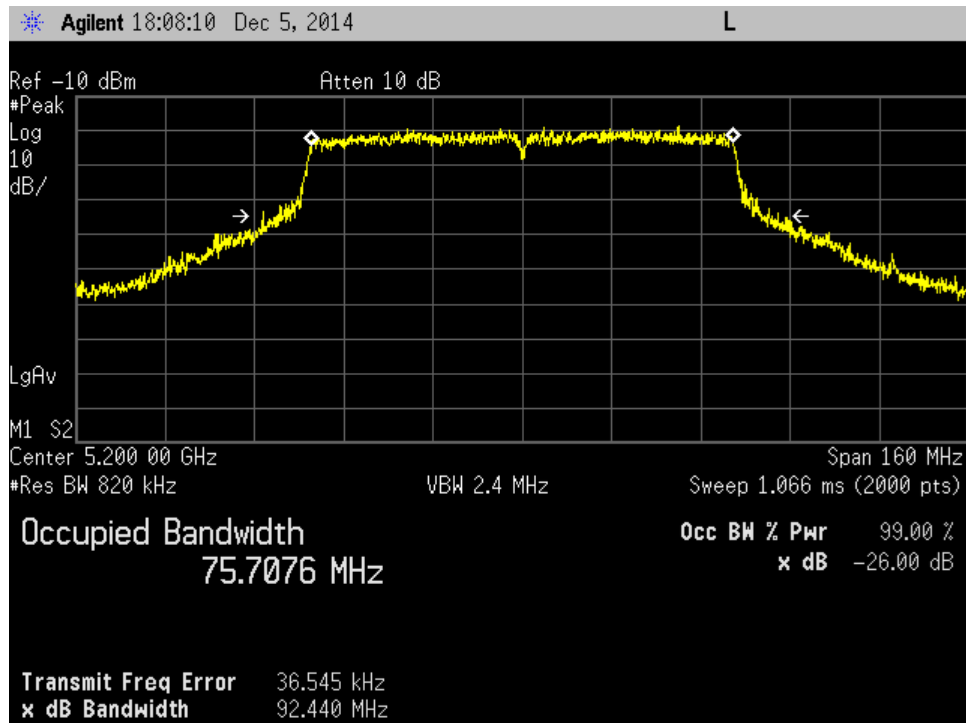


### J13\_mid ch\_bw=60 MHz

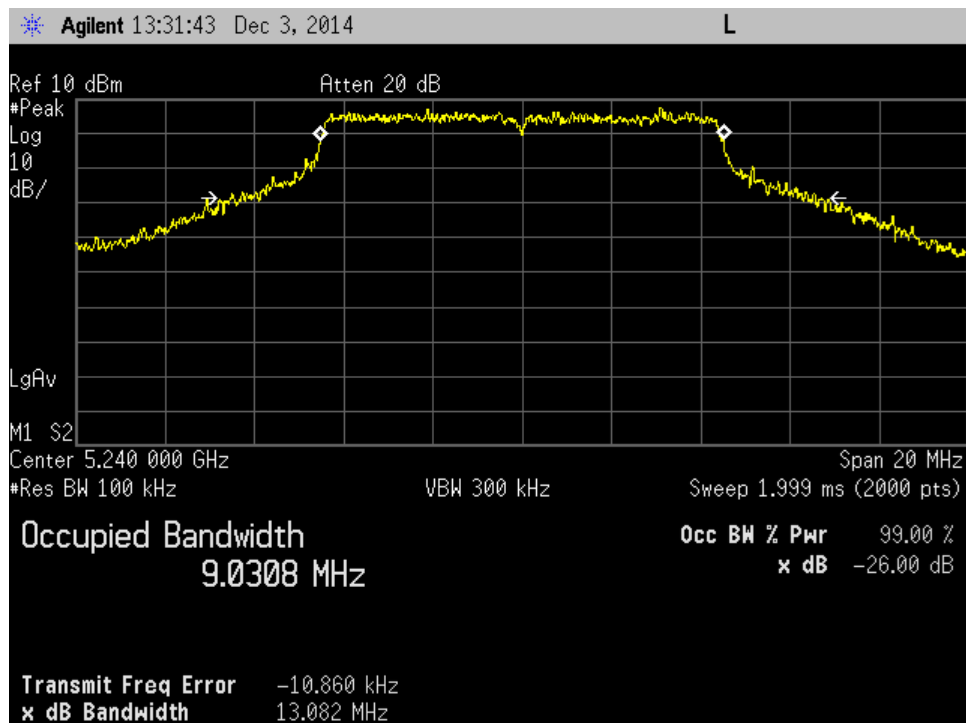




**J13\_mid ch\_bw=80 MHz**

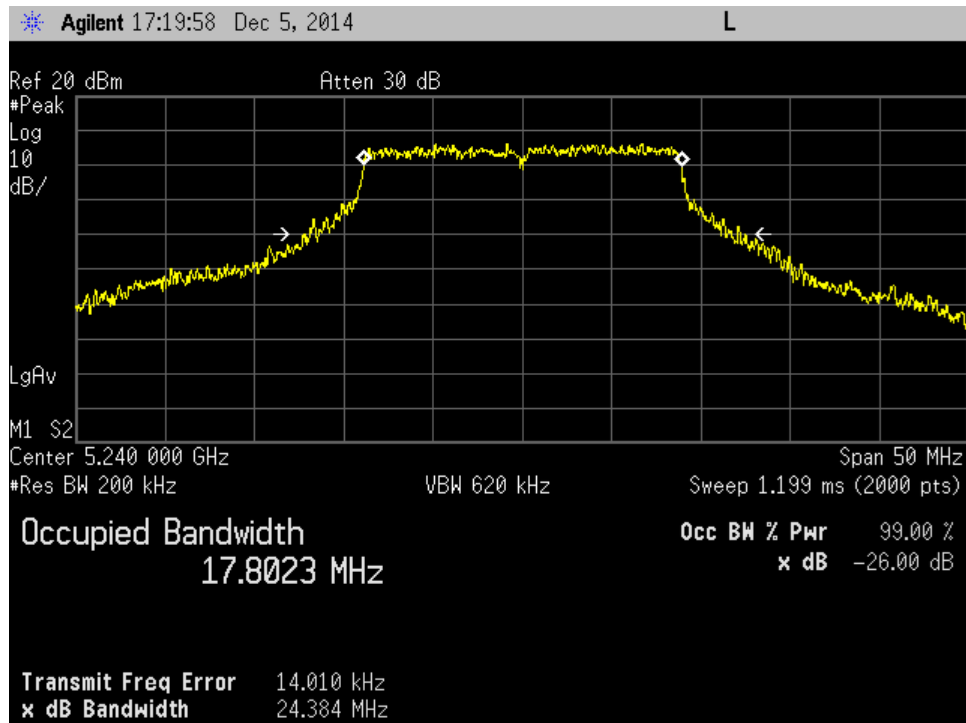


**J7\_hi ch\_bw=10 MHz**

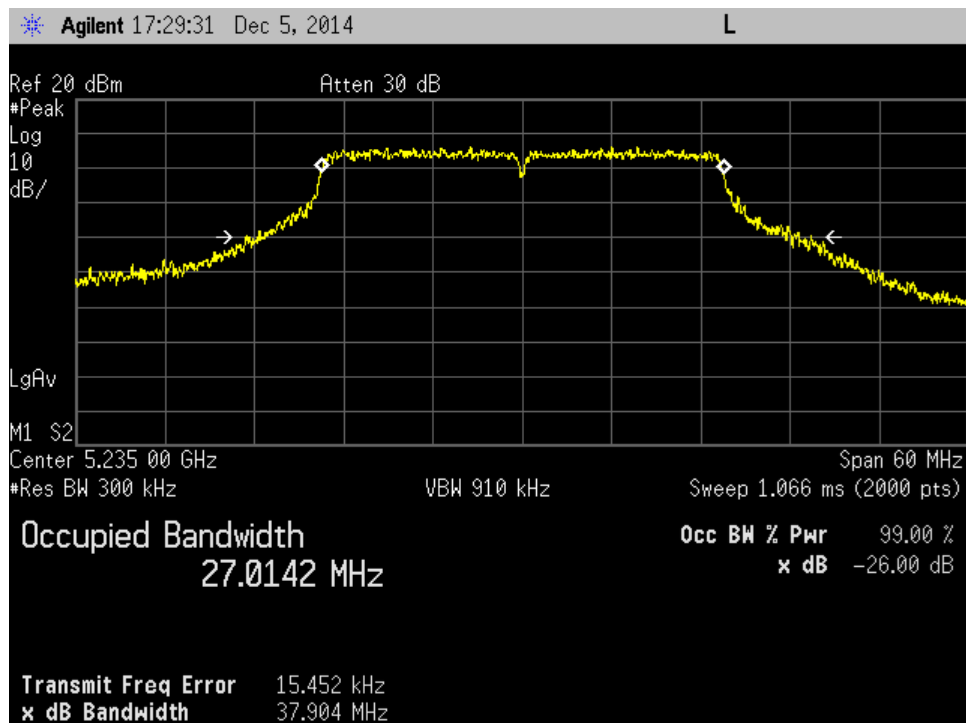




**J7\_hi ch\_bw=20 MHz**

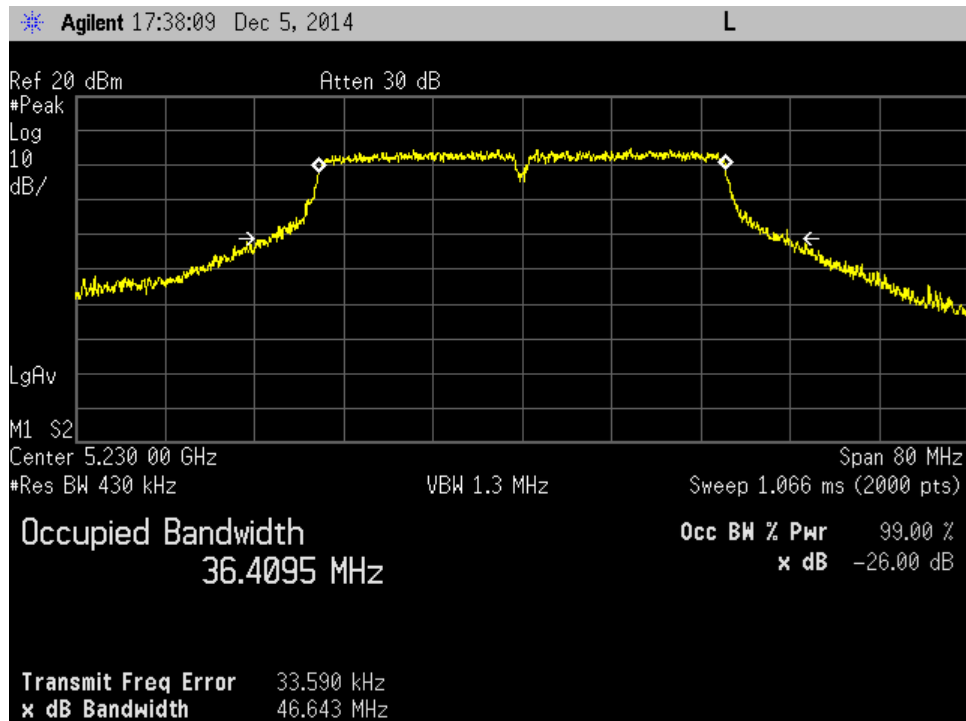


**J7\_hi ch\_bw=30 MHz**

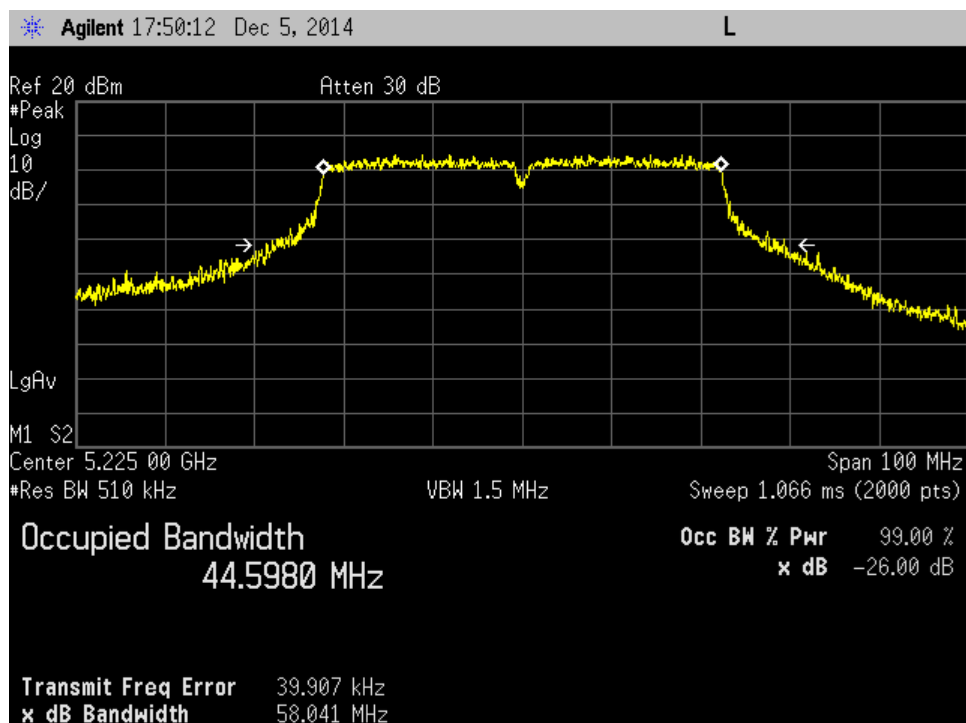




**J7\_hi ch\_bw=40 MHz**

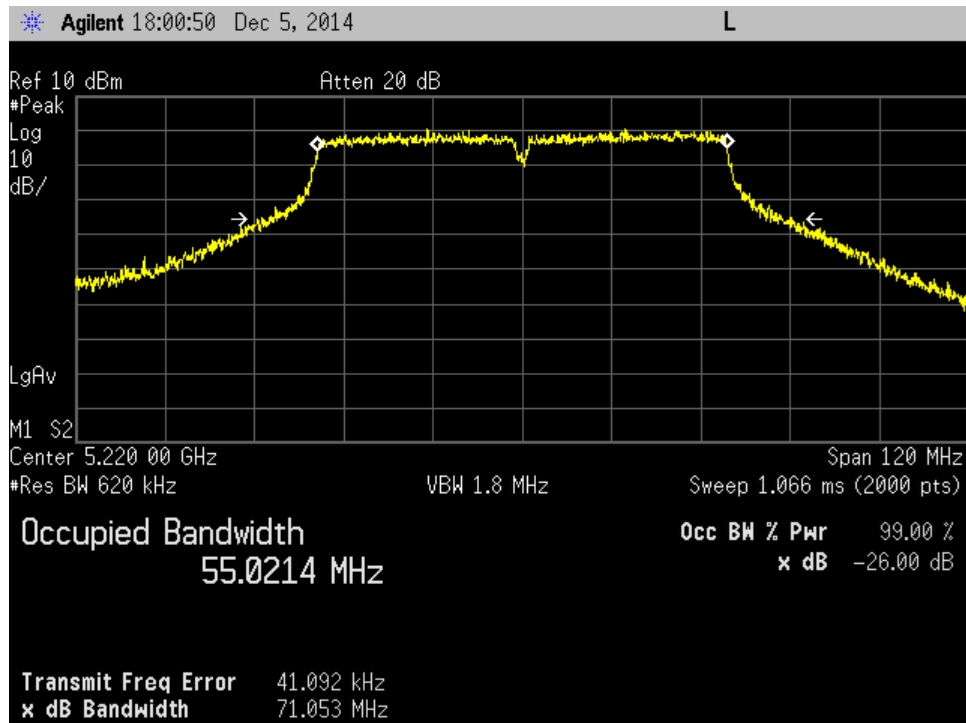


**J7\_hi ch\_bw=50 MHz**

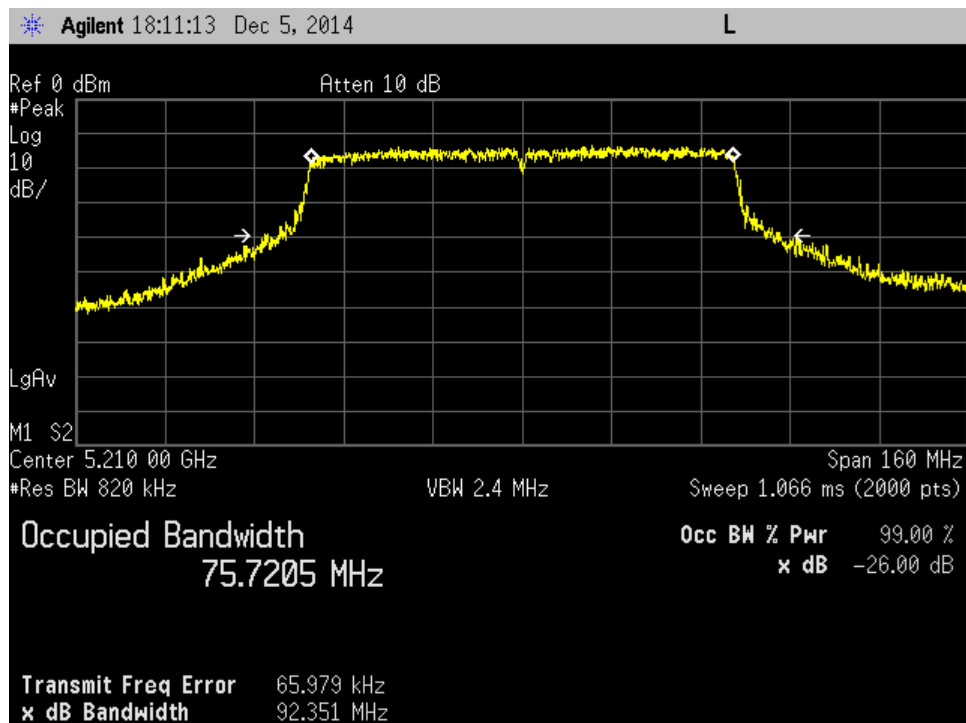




**J7\_hi ch\_bw=60 MHz**

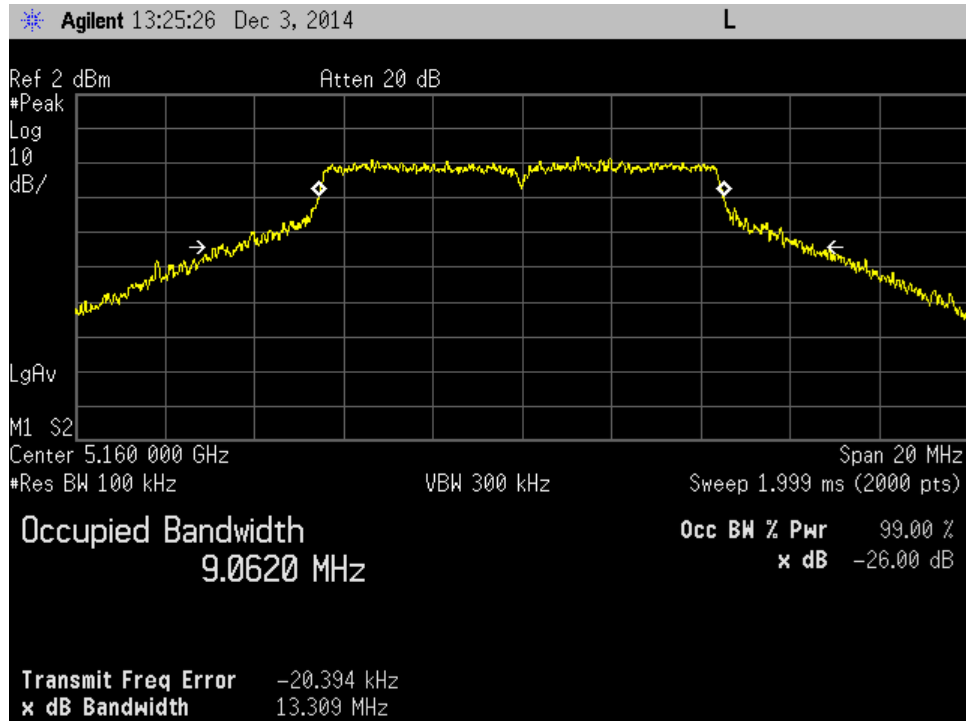


**J7\_hi ch\_bw=80 MHz**

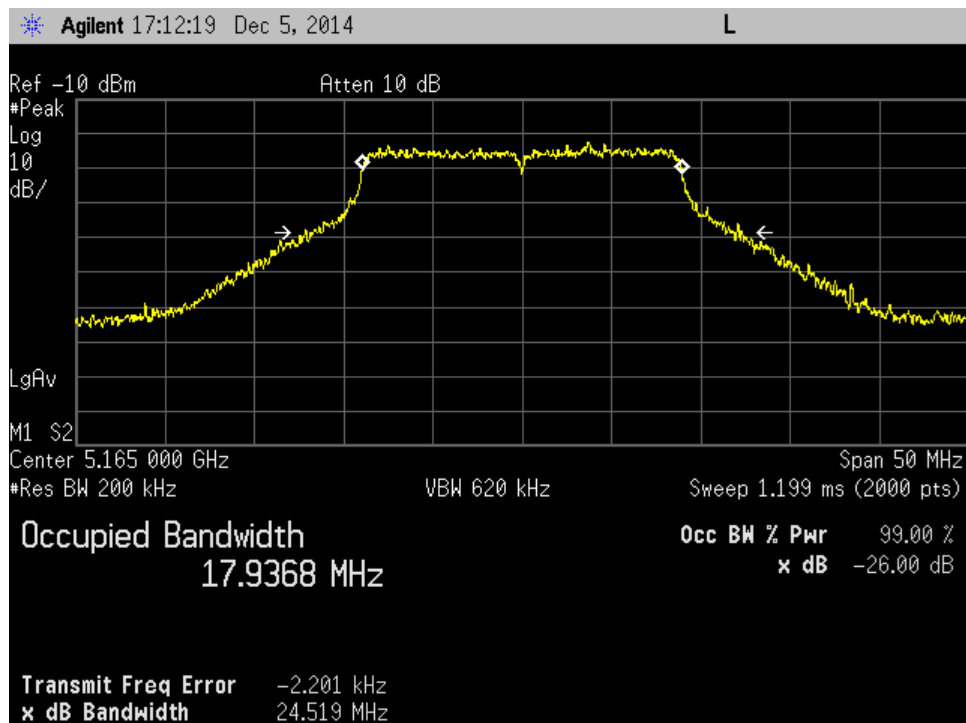




### J7\_low ch\_bw=10 MHz

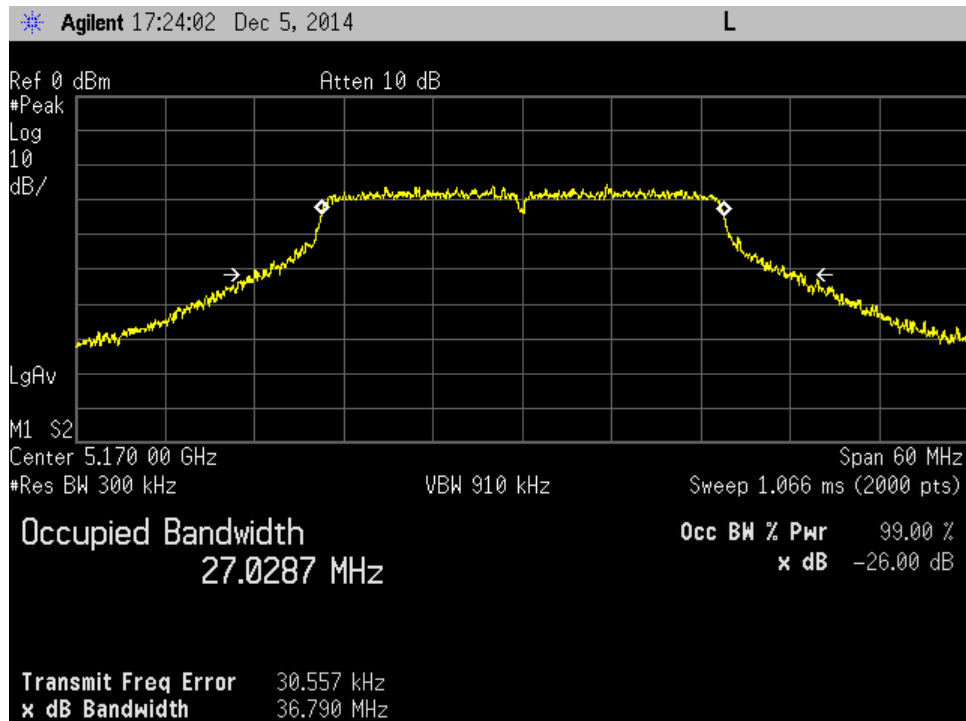


### J7\_low ch\_bw=20 MHz

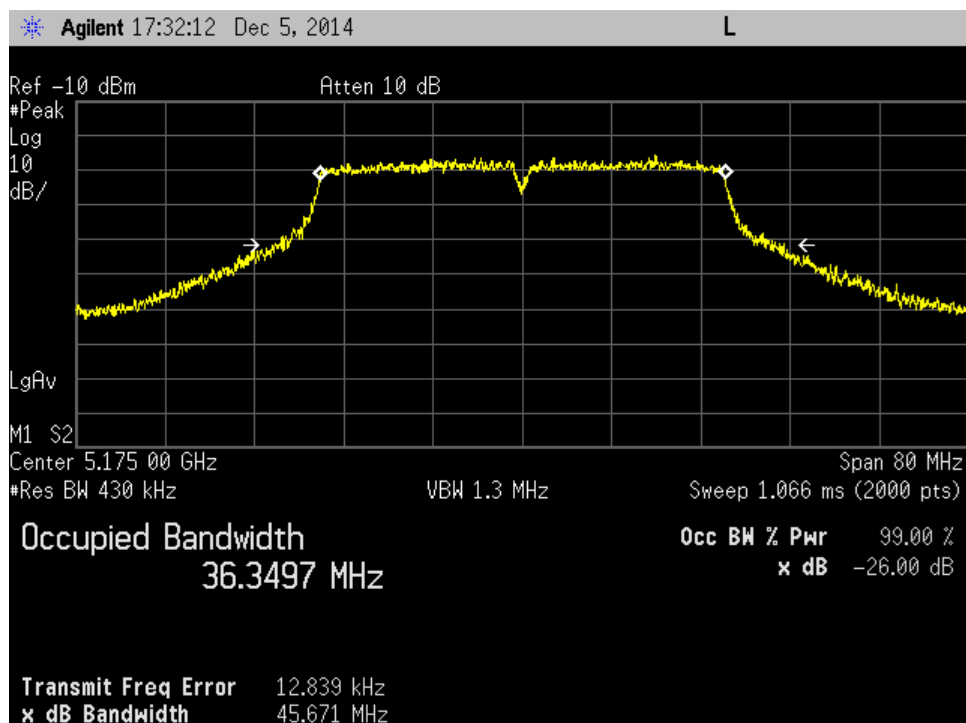




### J7\_low ch\_bw=30 MHz

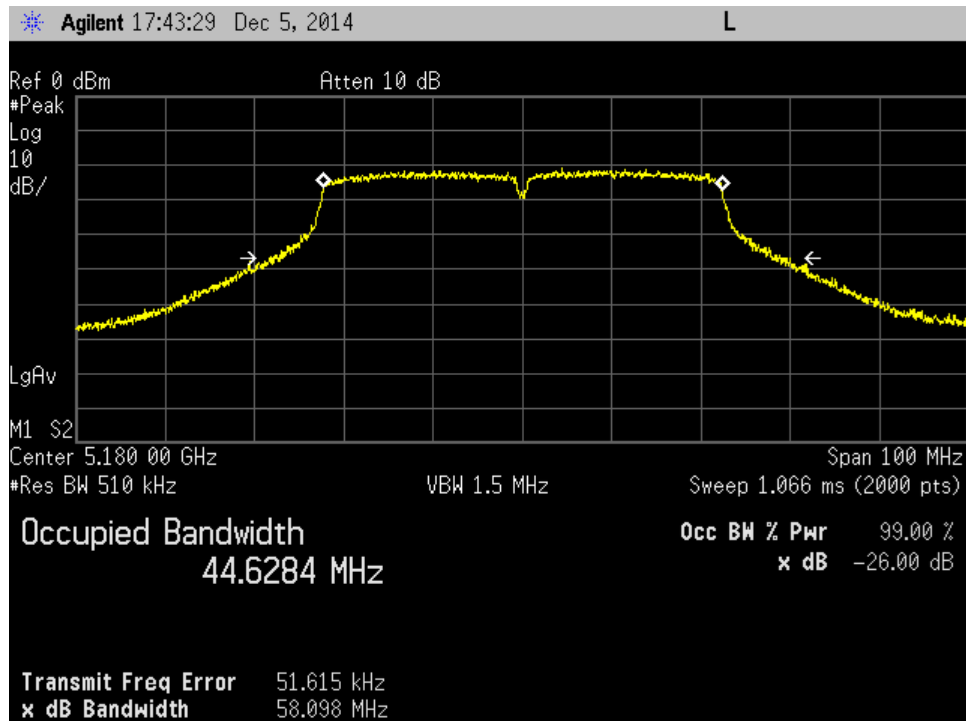


### J7\_low ch\_bw=40 MHz

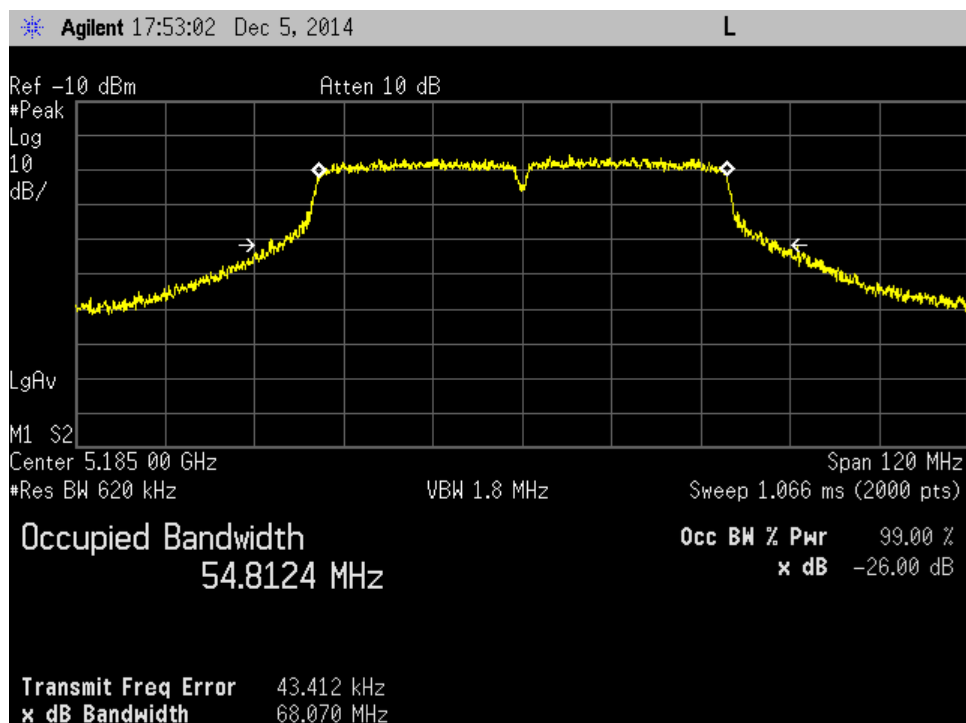




### J7\_low ch\_bw=50 MHz

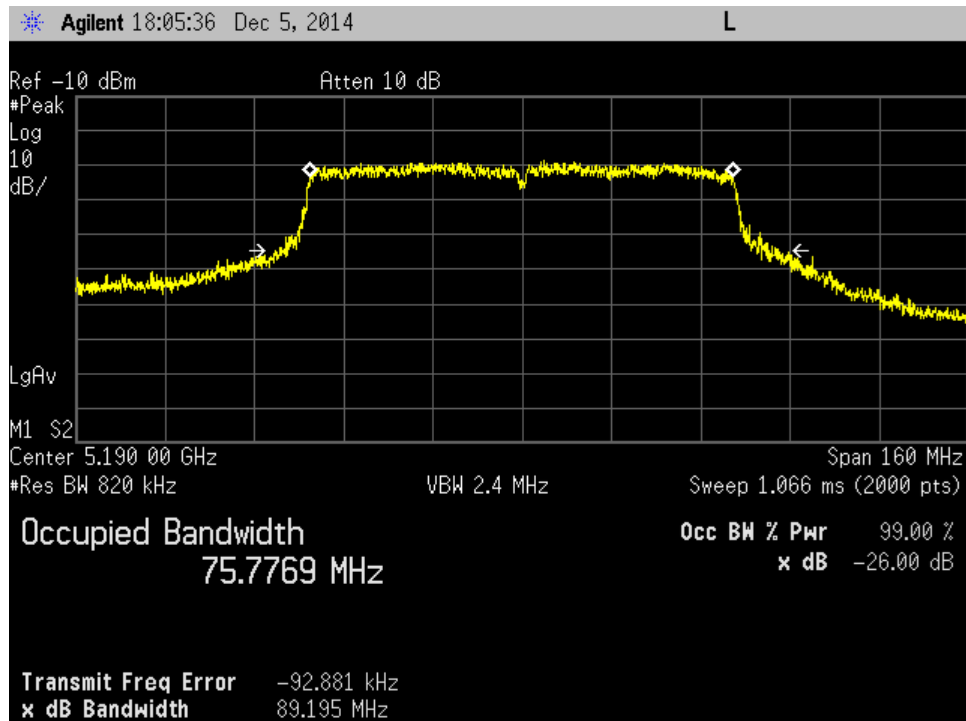


### J7\_low ch\_bw=60 MHz

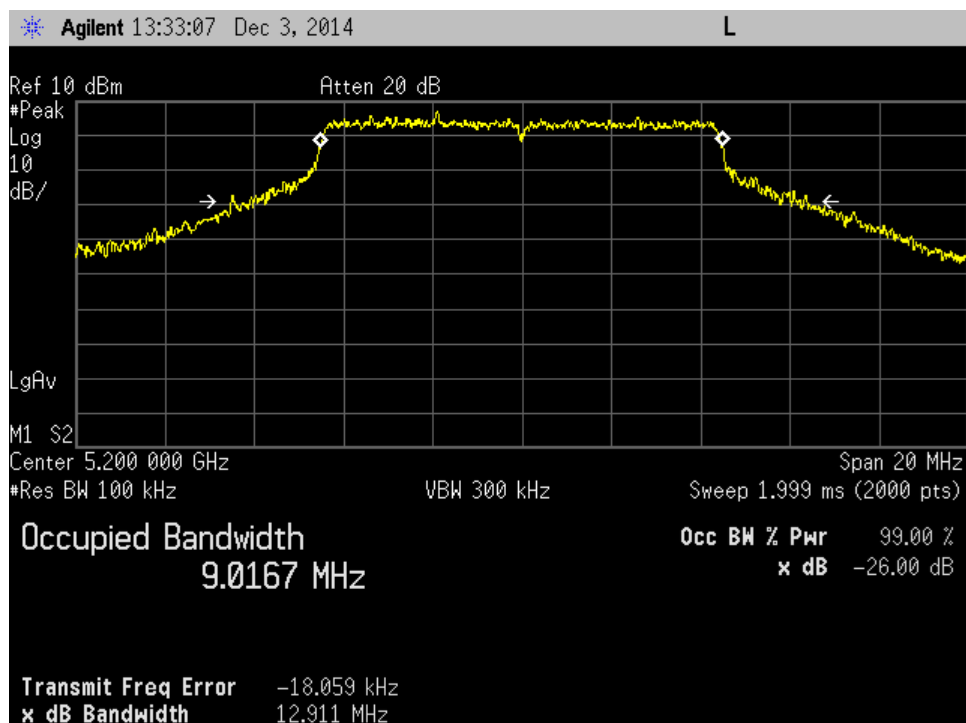




### J7\_low ch\_bw=80 MHz

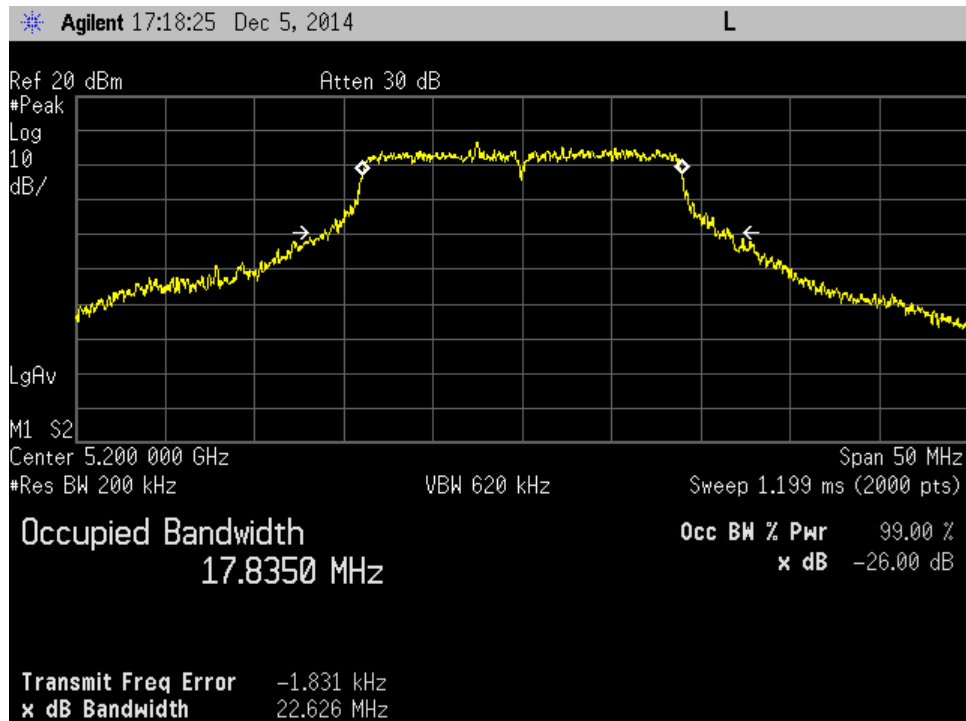


### J7\_mid ch\_bw=10 MHz

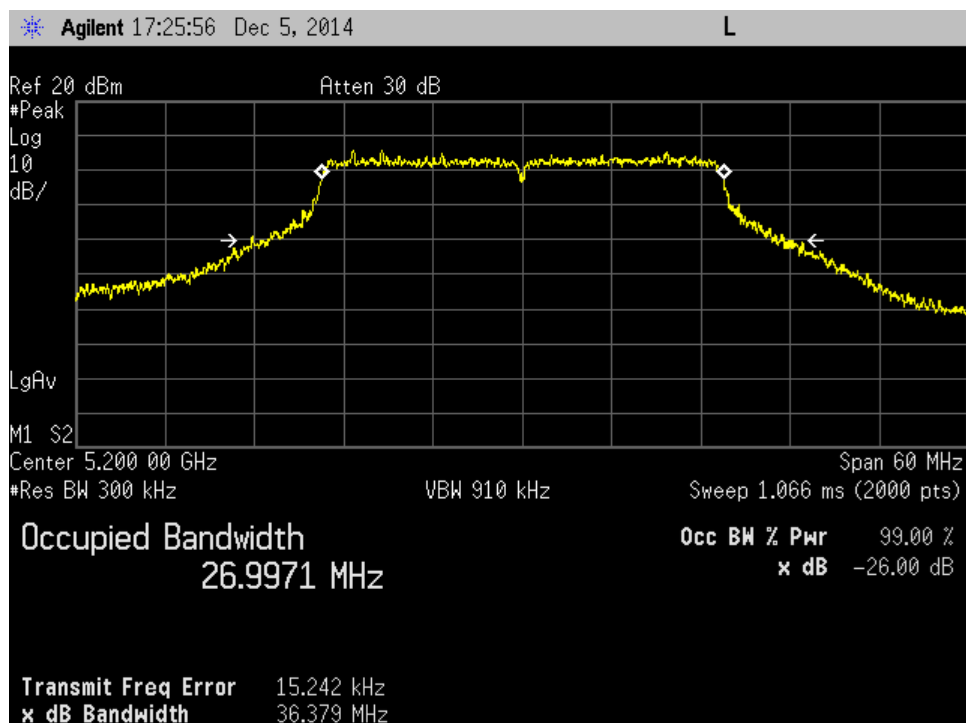




### J7\_mid ch\_bw=20 MHz

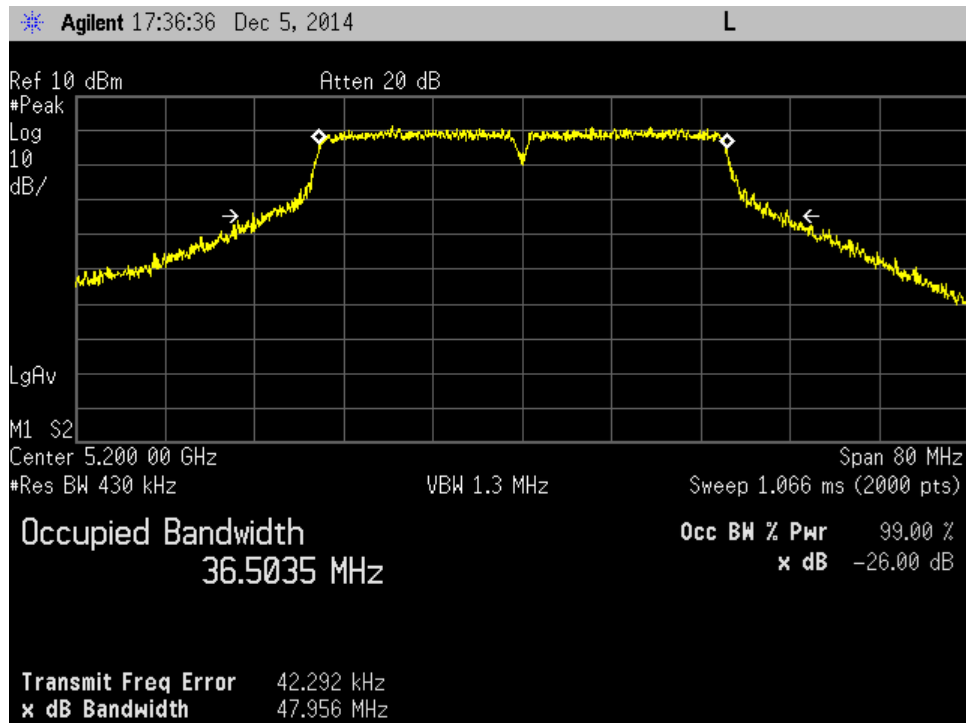


### J7\_mid ch\_bw=30 MHz

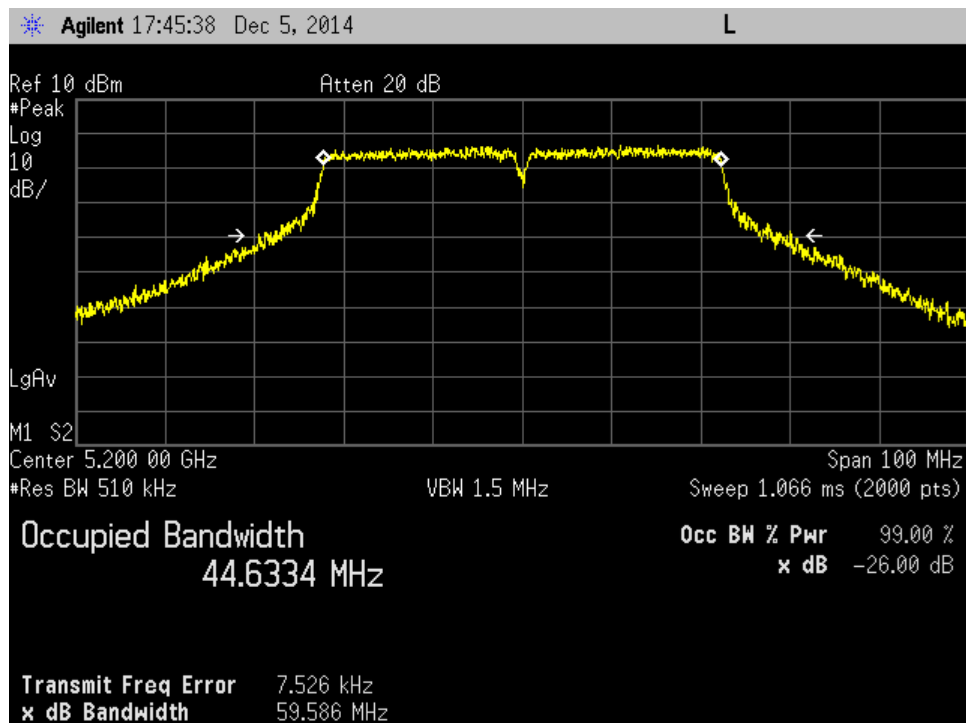




### J7\_mid ch\_bw=40 MHz

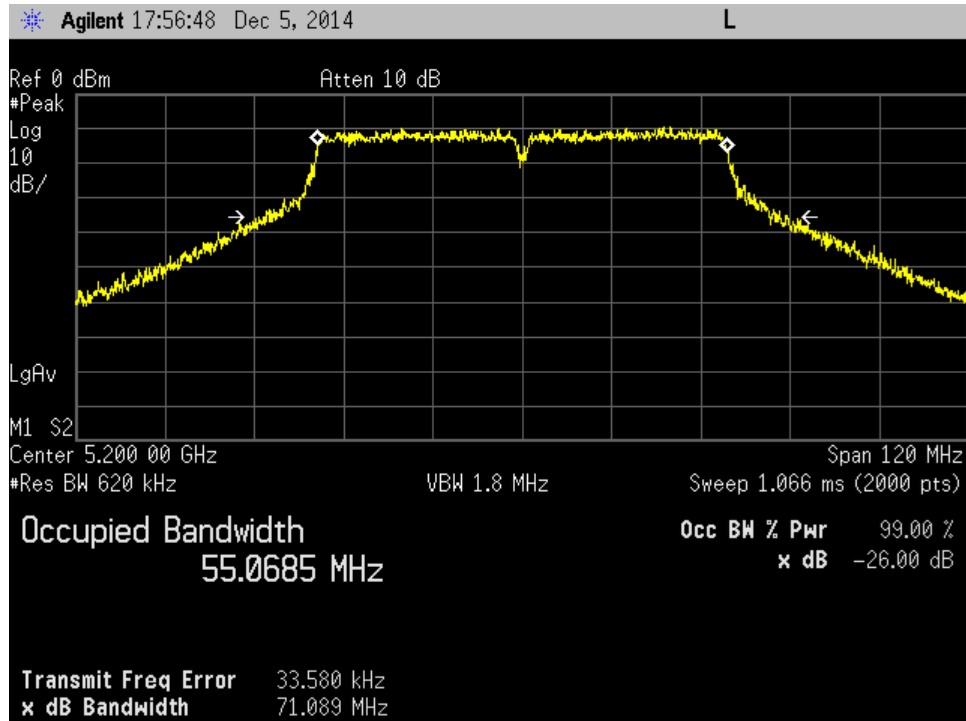


### J7\_mid ch\_bw=50 MHz





**J7\_mid ch\_bw=60 MHz**



**J7\_mid ch\_bw=80 MHz**

