



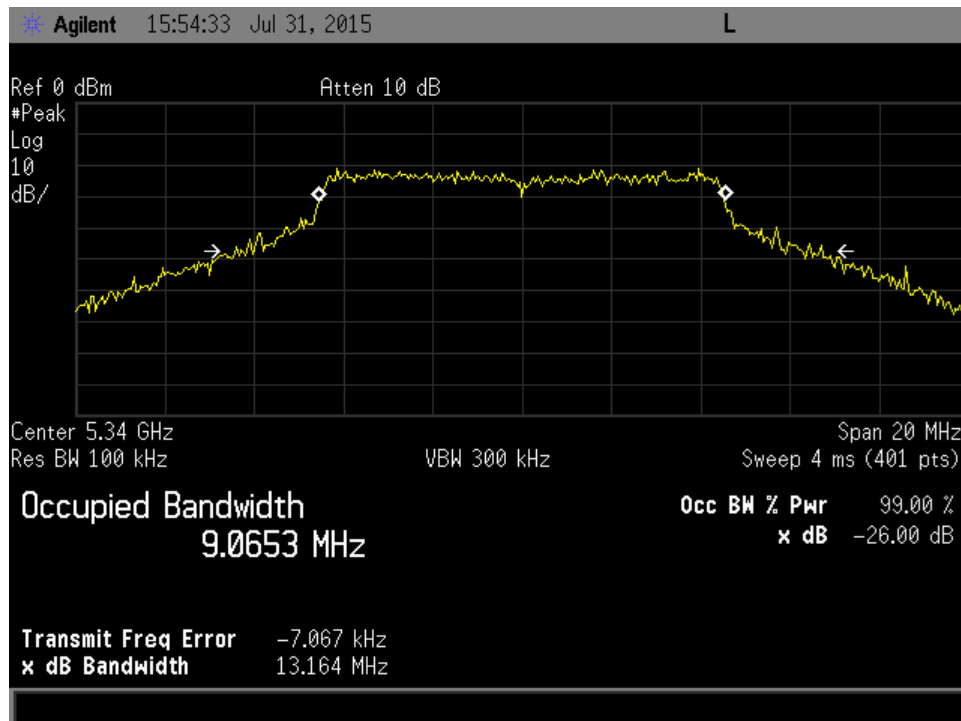
# **Annex C**

## **Occupied Bandwidth**

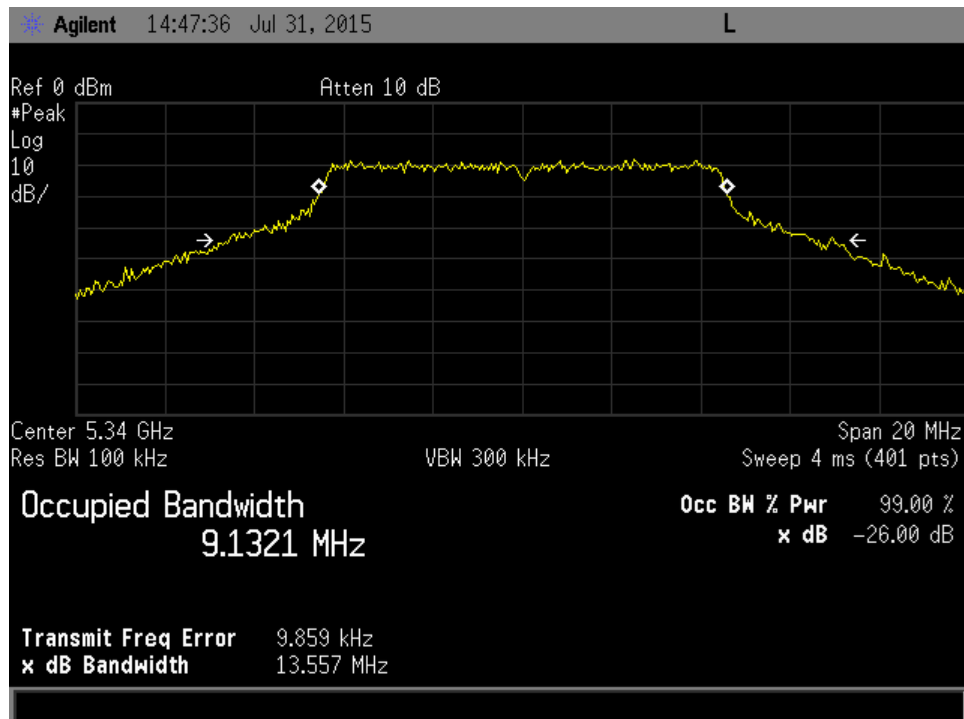
### **UNII-2A and UNII-2C**



### 10MHz high ch\_J13

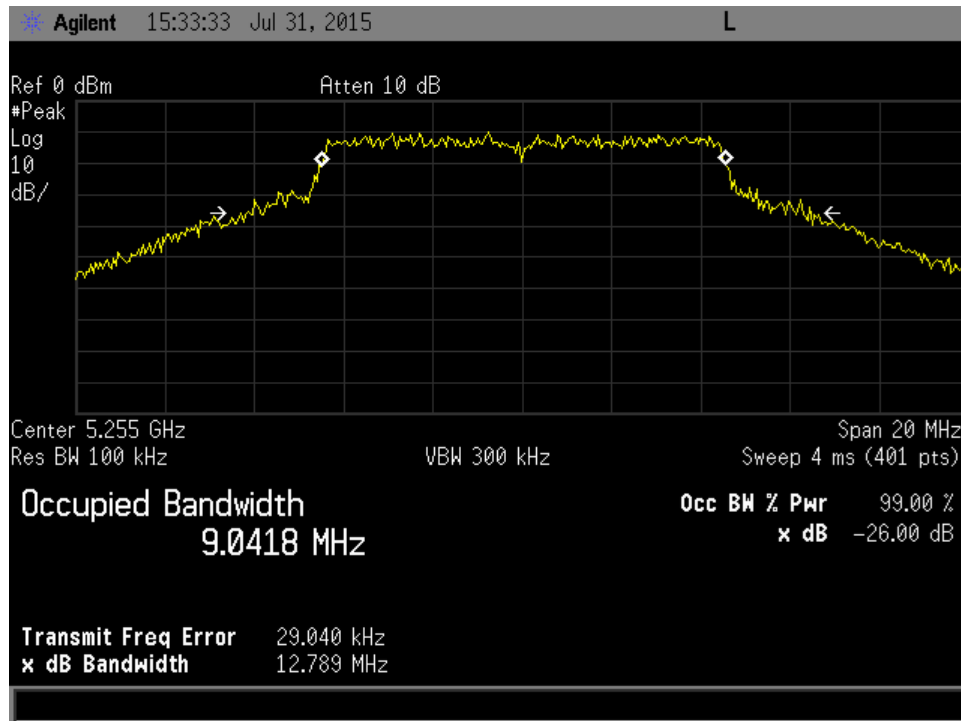


### 10MHz high ch\_J7

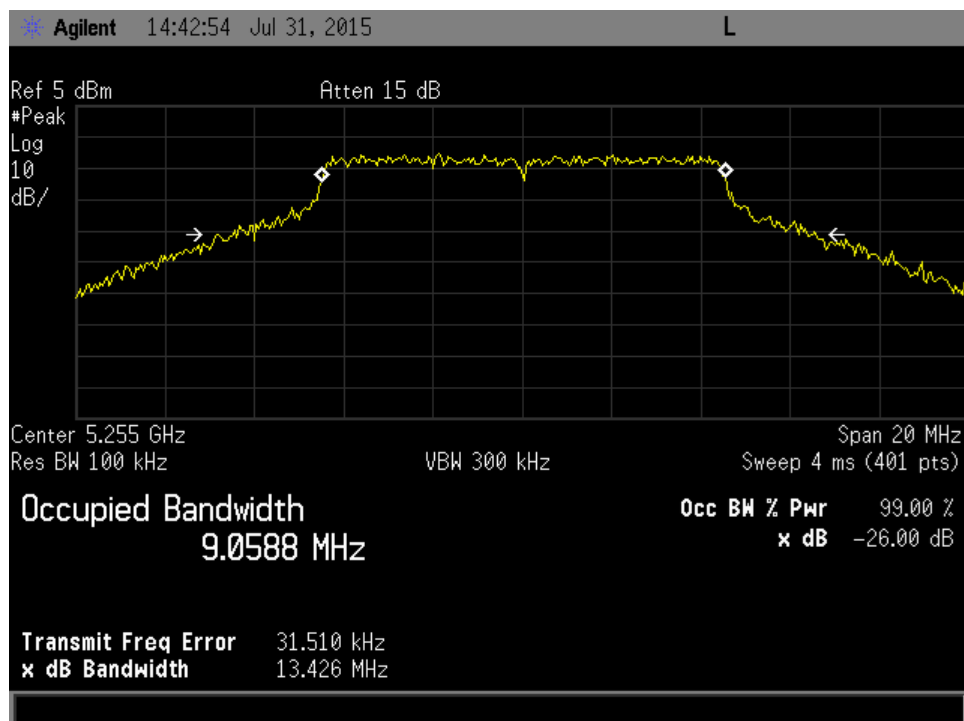




### 10MHz low ch\_J13

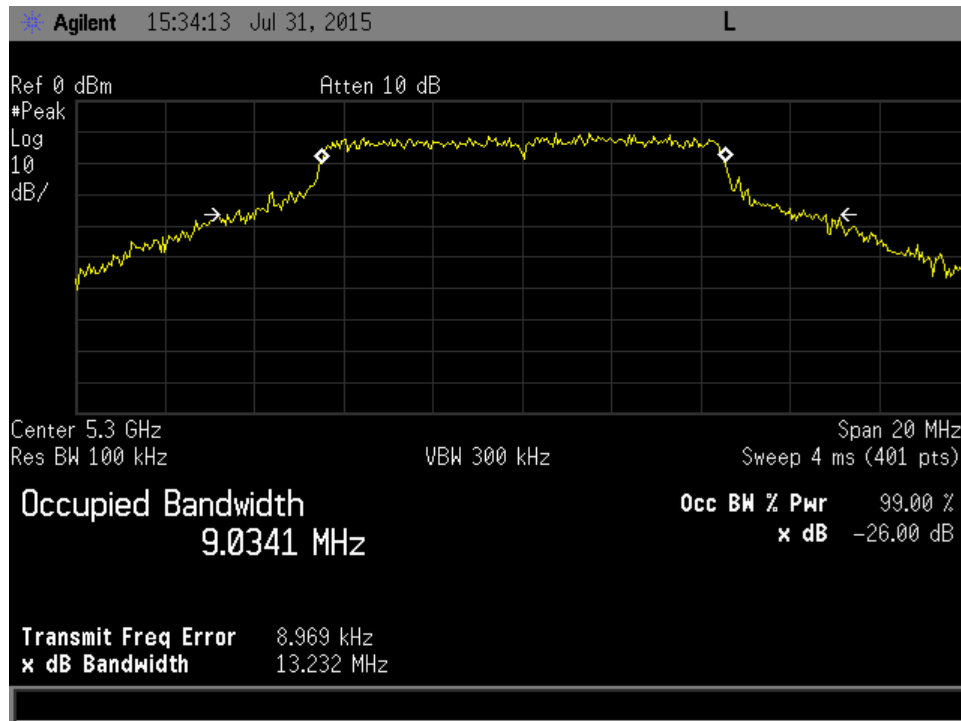


### 10MHz low ch\_J7

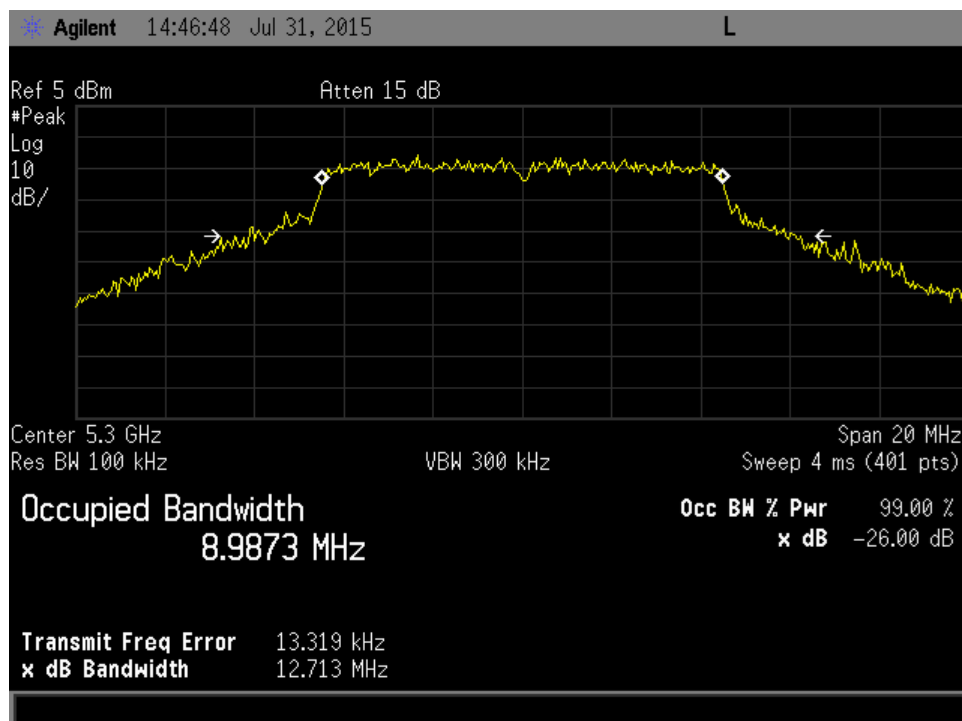




### 10MHz mid ch\_J13

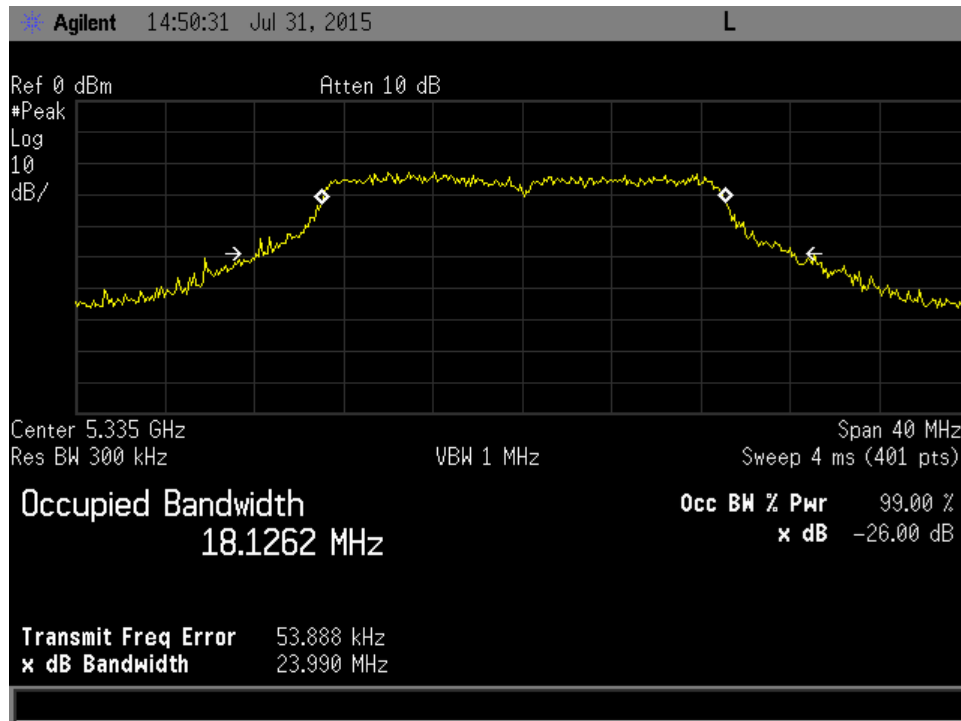


### 10MHz mid ch\_J7

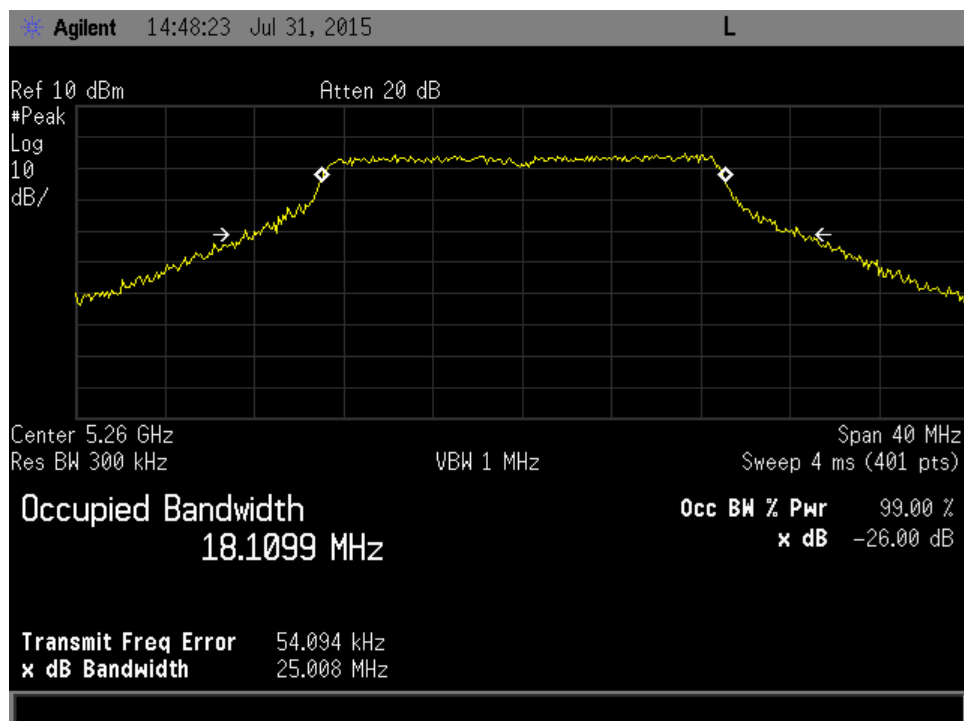




### 20MHz high ch\_J7

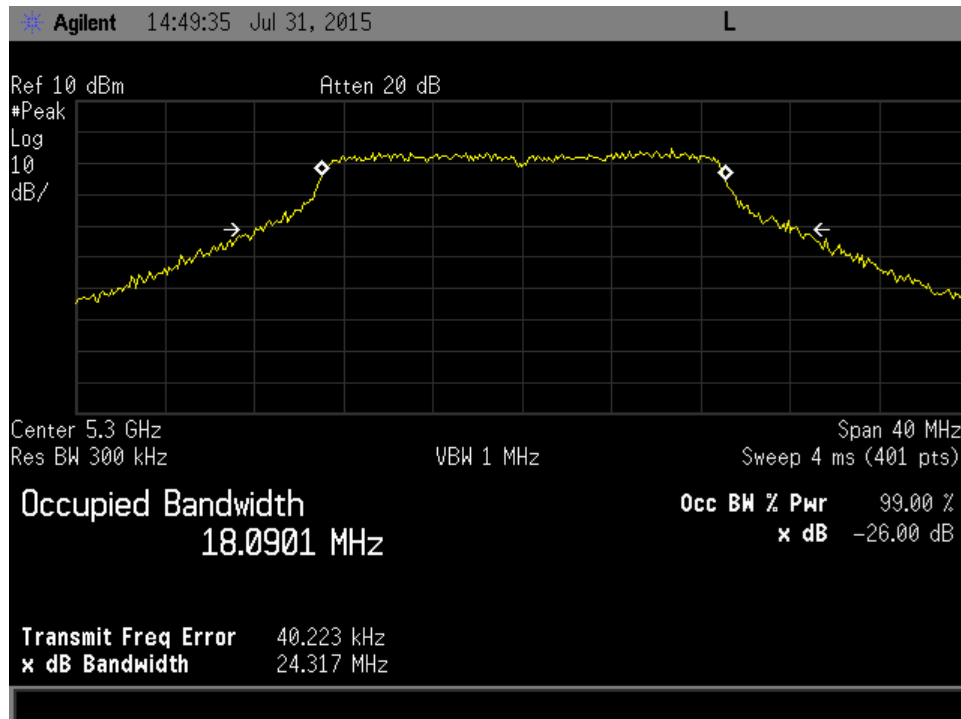


### 20MHz low ch\_J7

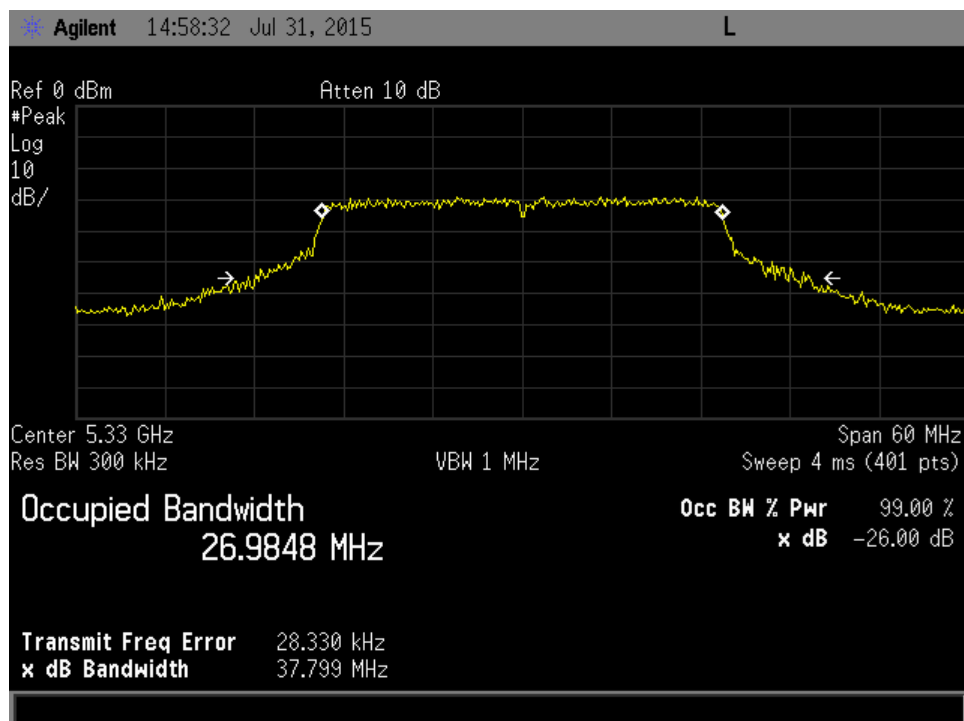




### 20MHz mid ch\_J7

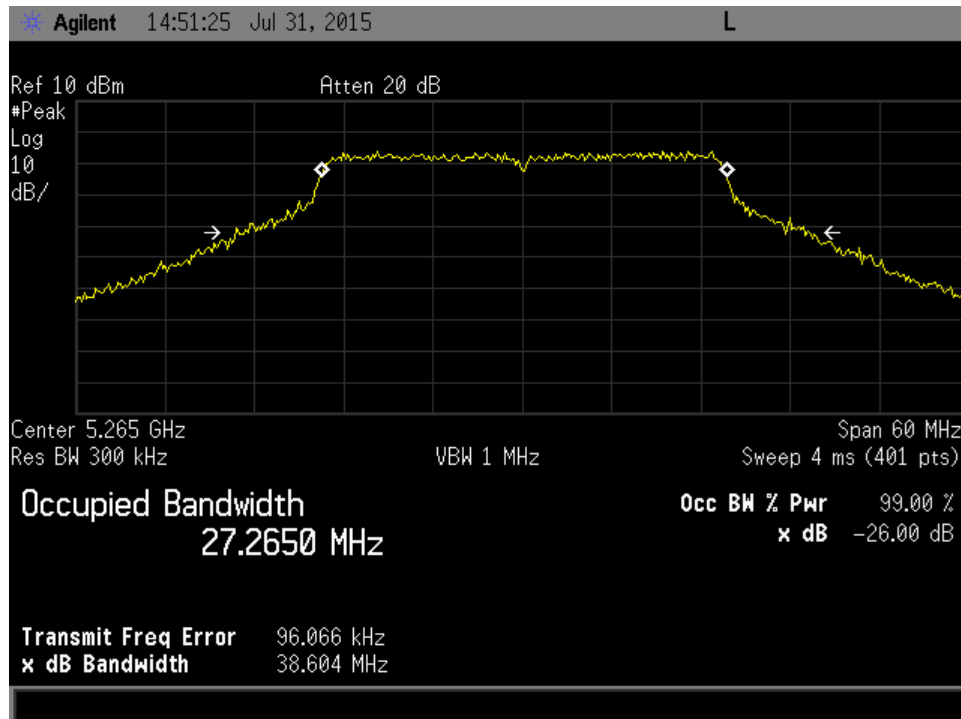


### 30MHz high ch\_J7

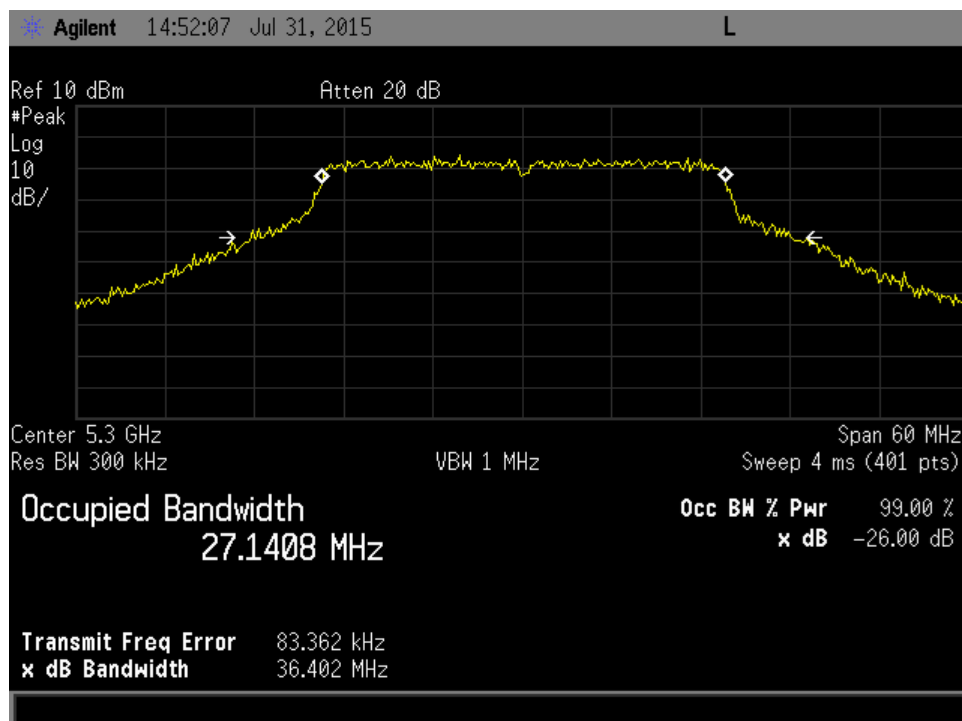




### 30MHz low ch\_J7

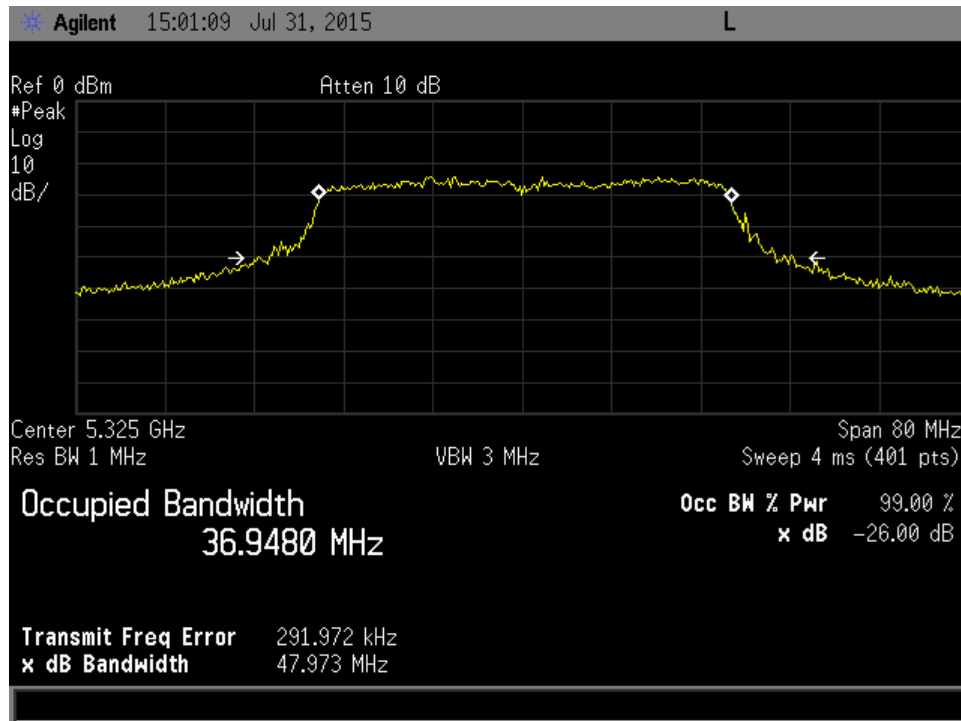


### 30MHz mid ch\_J7

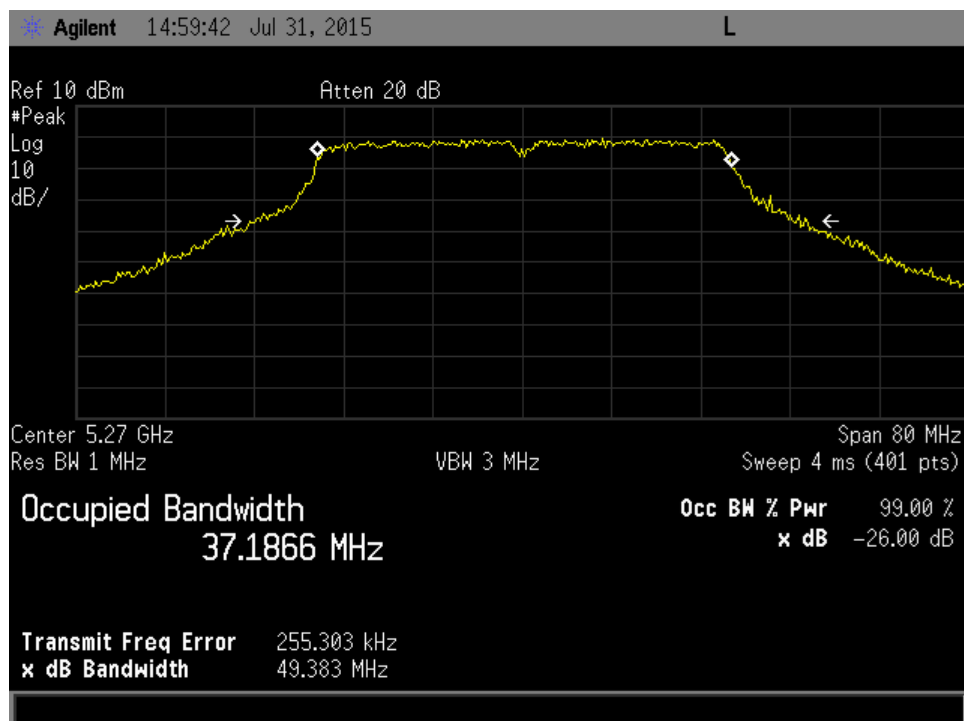




### 40MHz high ch\_J7

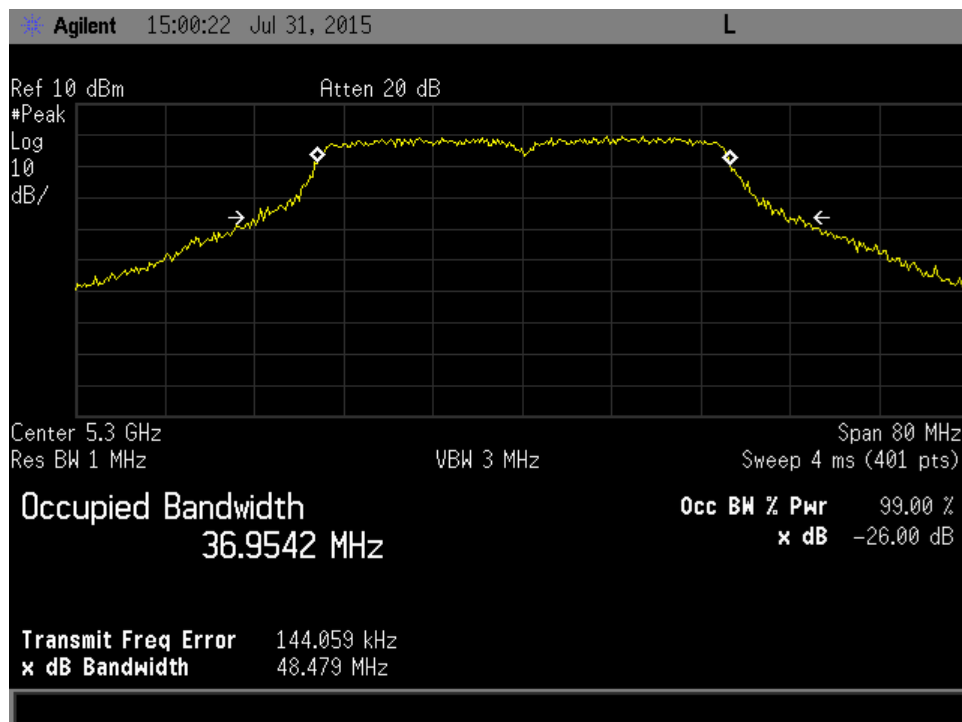


### 40MHz low ch\_J7

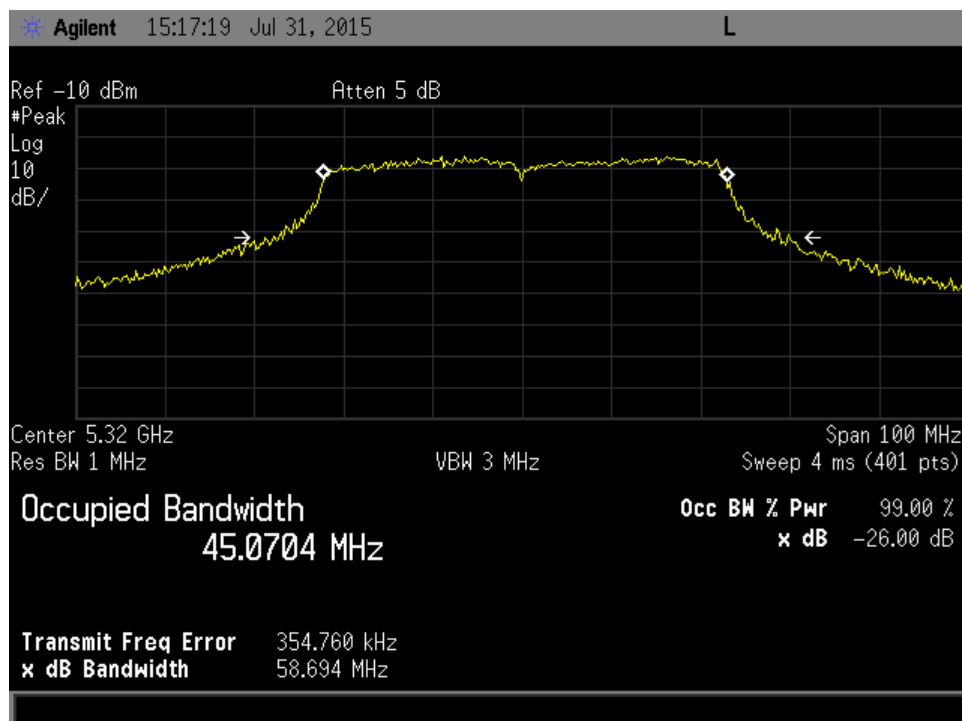




### 40MHz mid ch\_J7

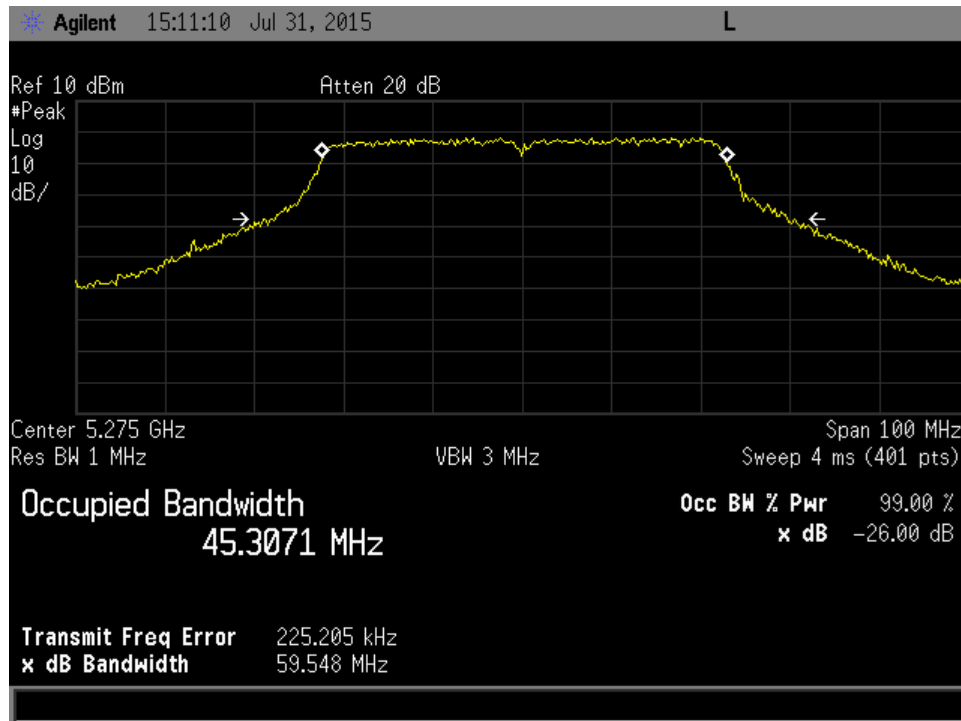


### 50MHz high ch\_J7

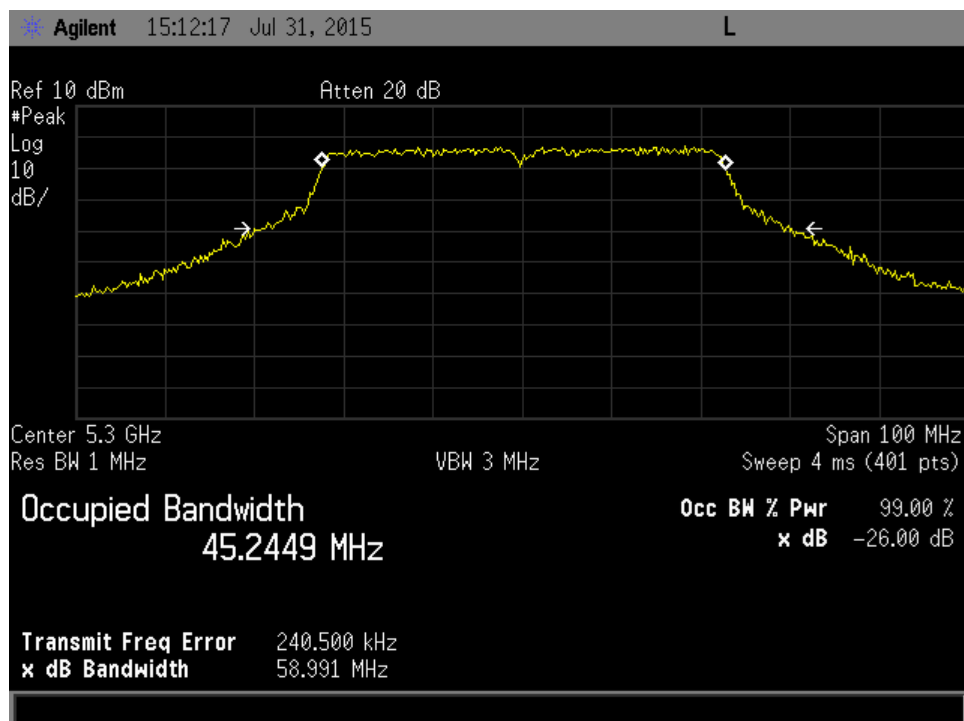




### 50MHz low ch\_J7

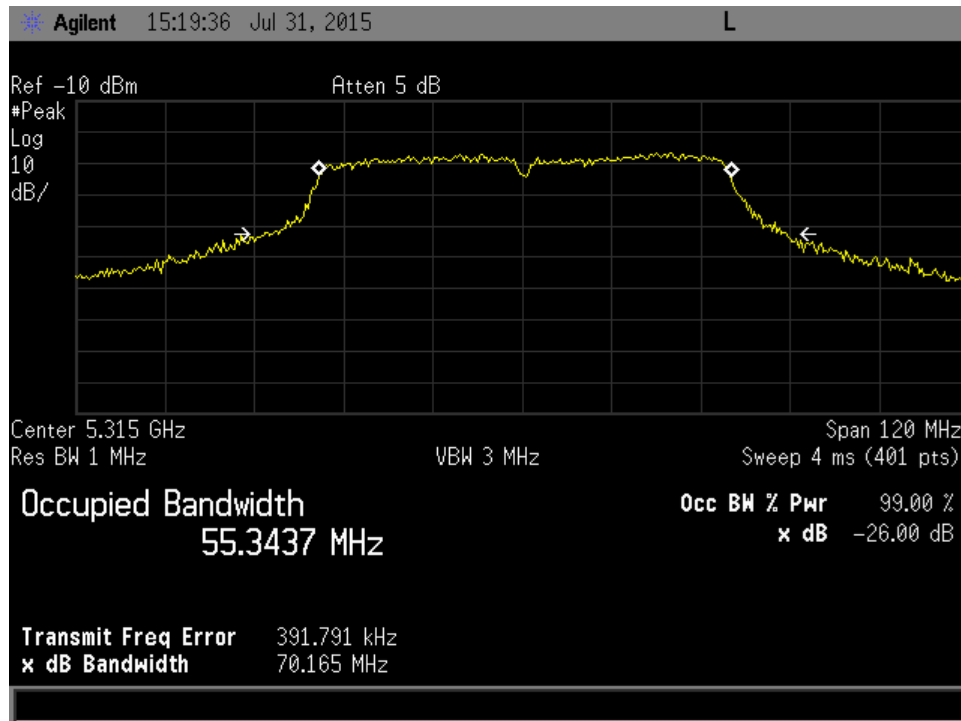


### 50MHz mid ch\_J7

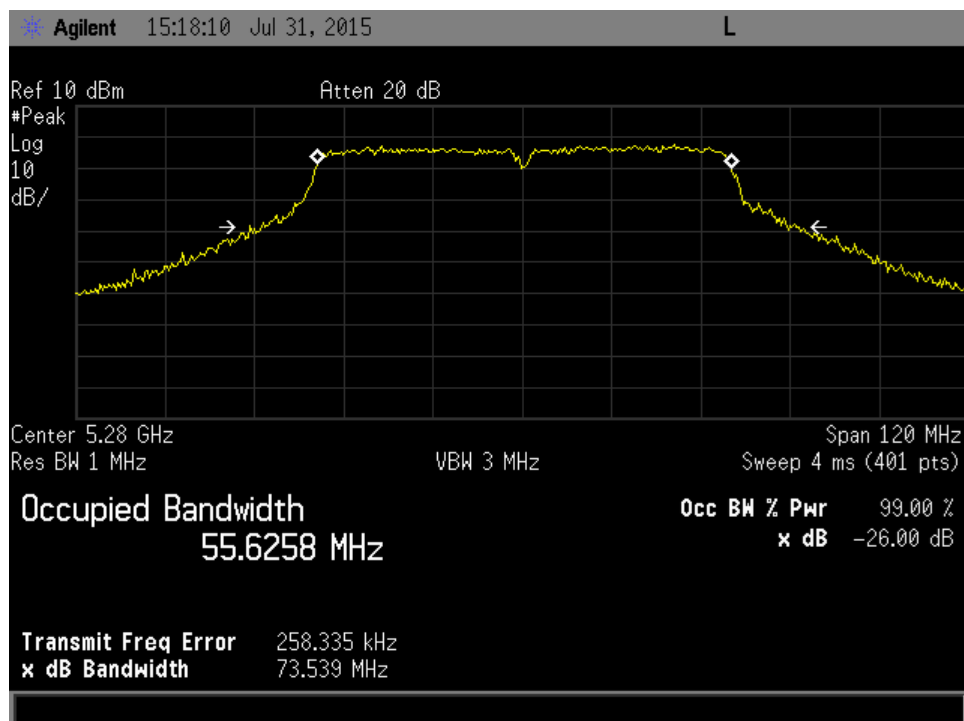




### 60MHz high ch\_J7

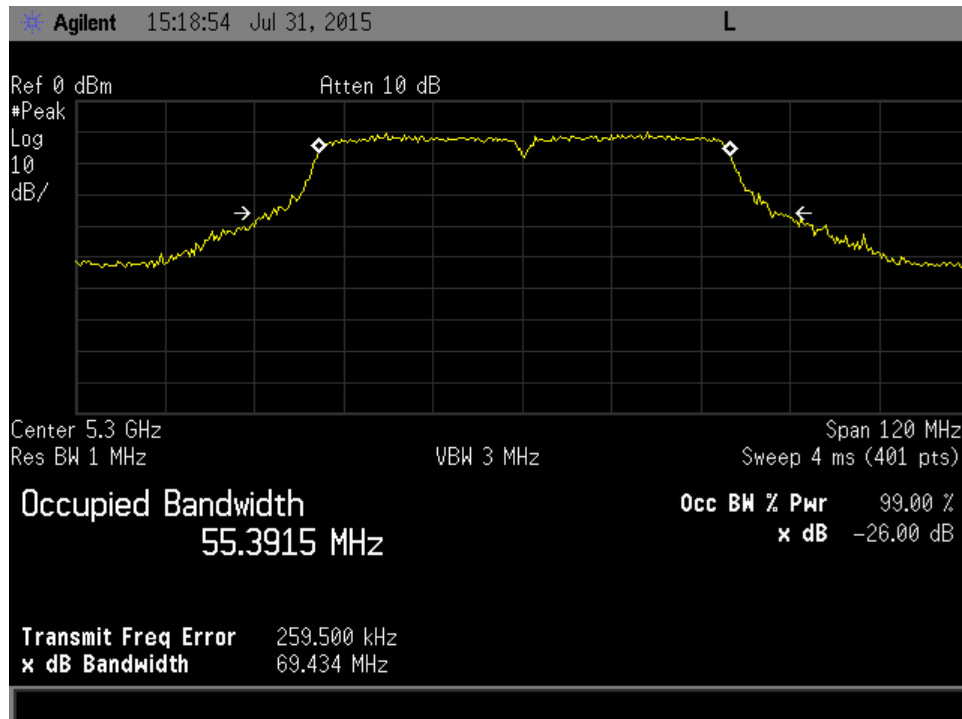


### 60MHz low ch\_J7

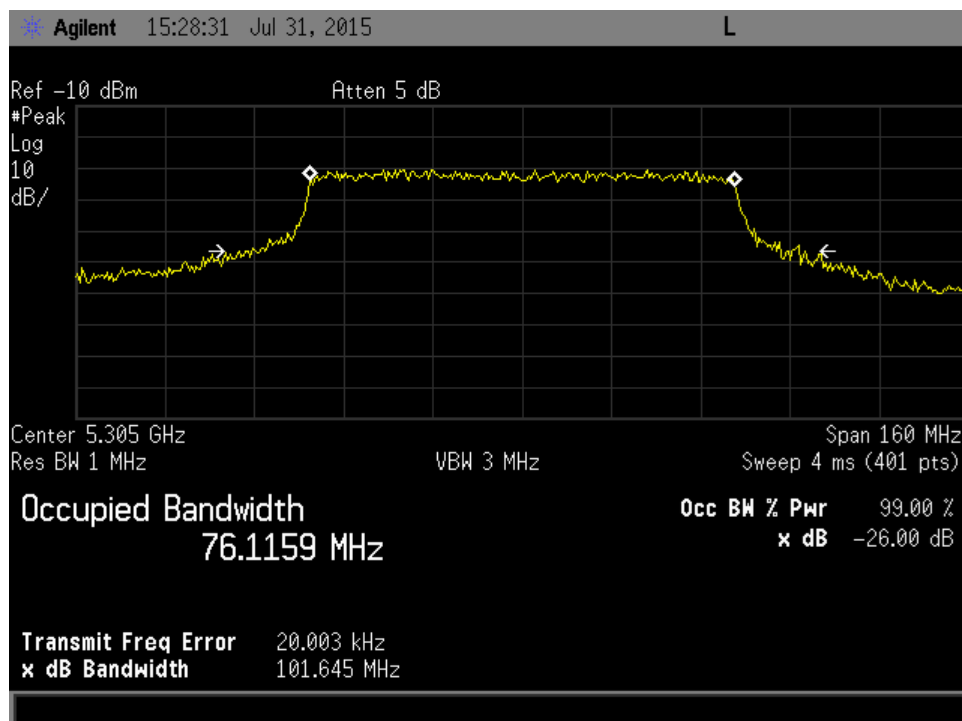




### 60MHz mid ch\_J7

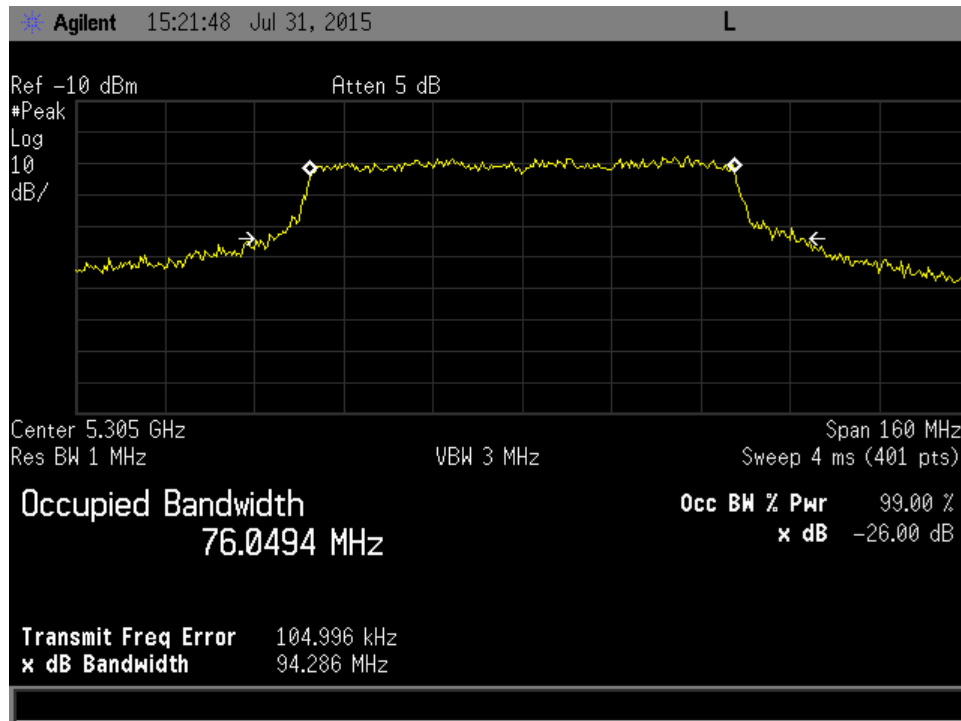


### 80MHz high ch\_J13

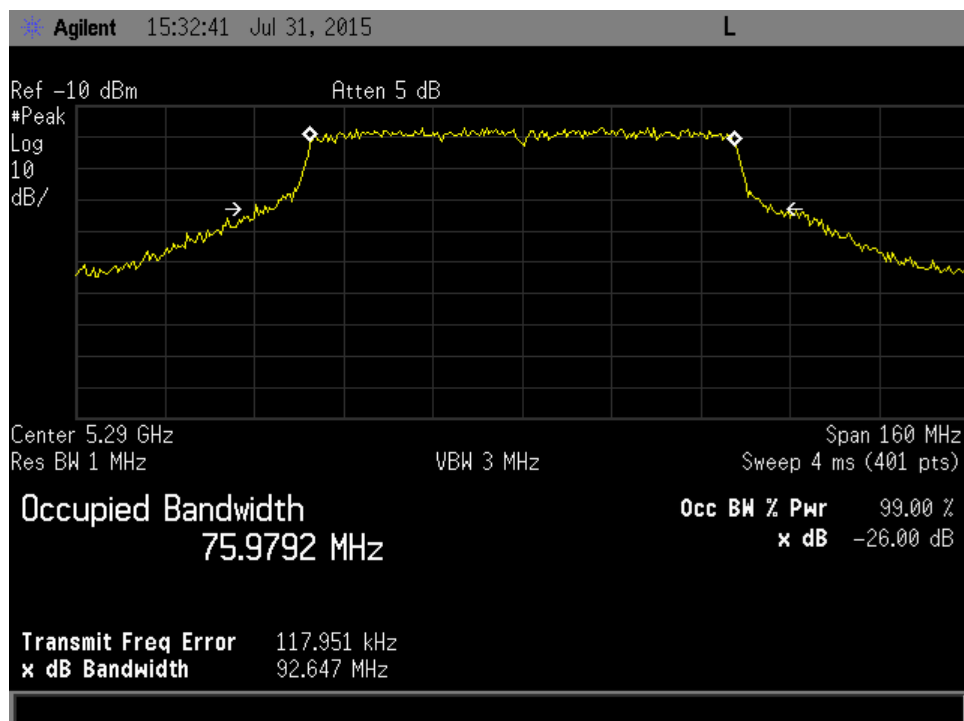




### 80MHz high ch\_J7

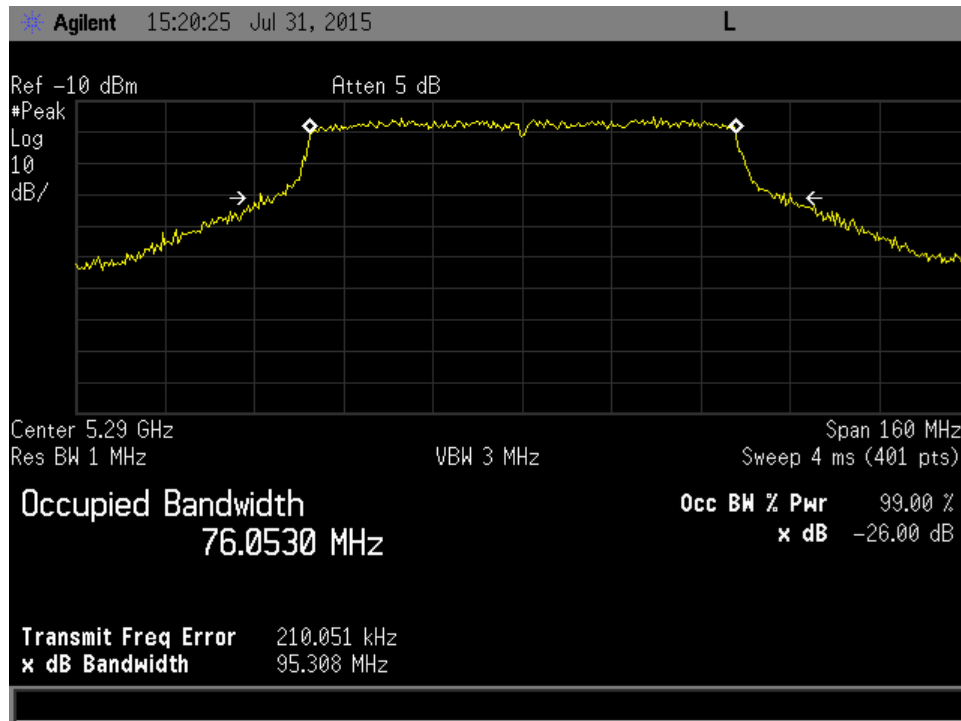


### 80MHz low ch\_J13

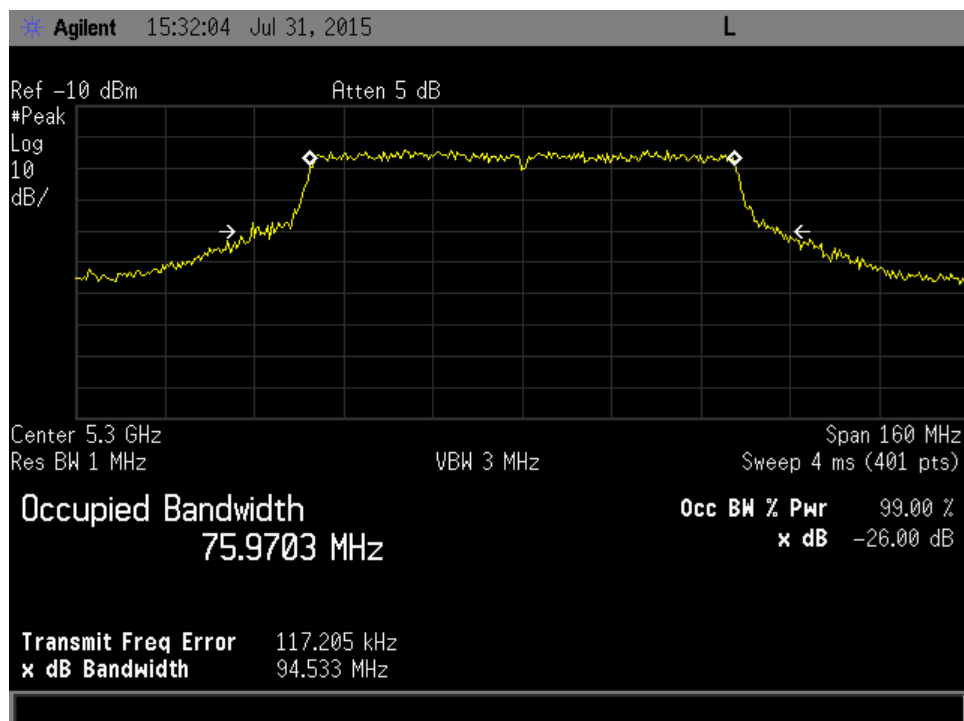




### 80MHz low ch\_J7

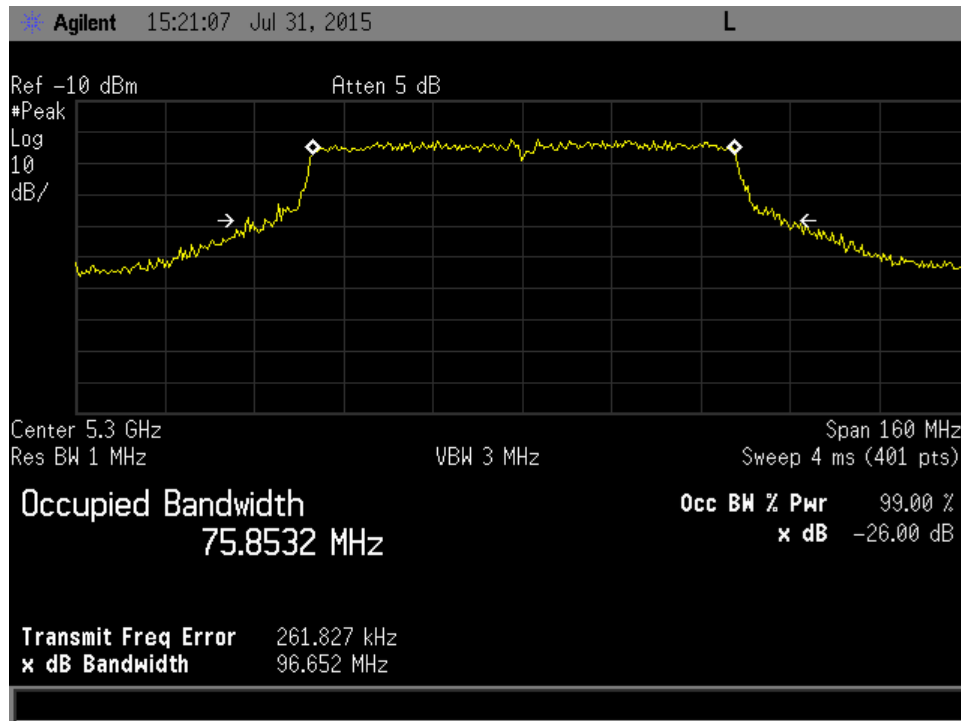


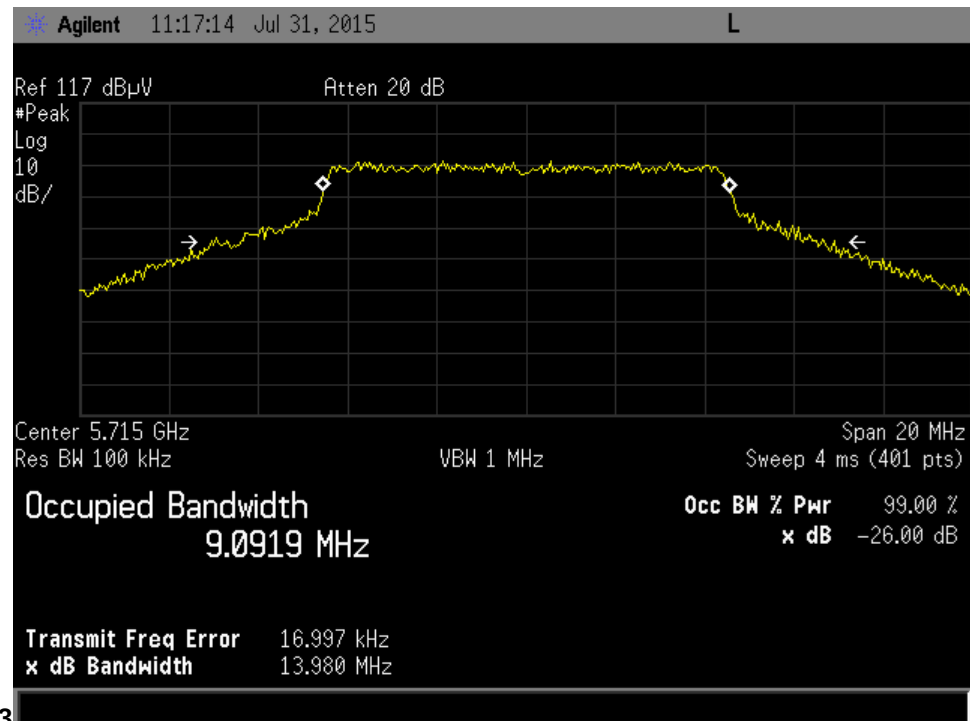
### 80MHz mid ch\_J13





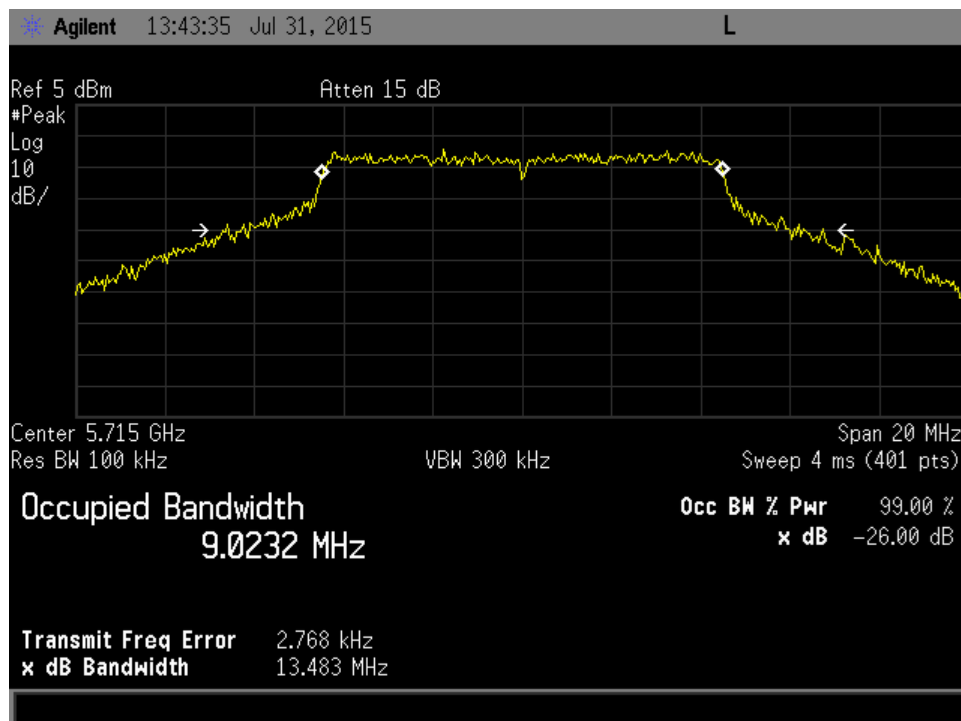
### 80MHz mid ch\_J7





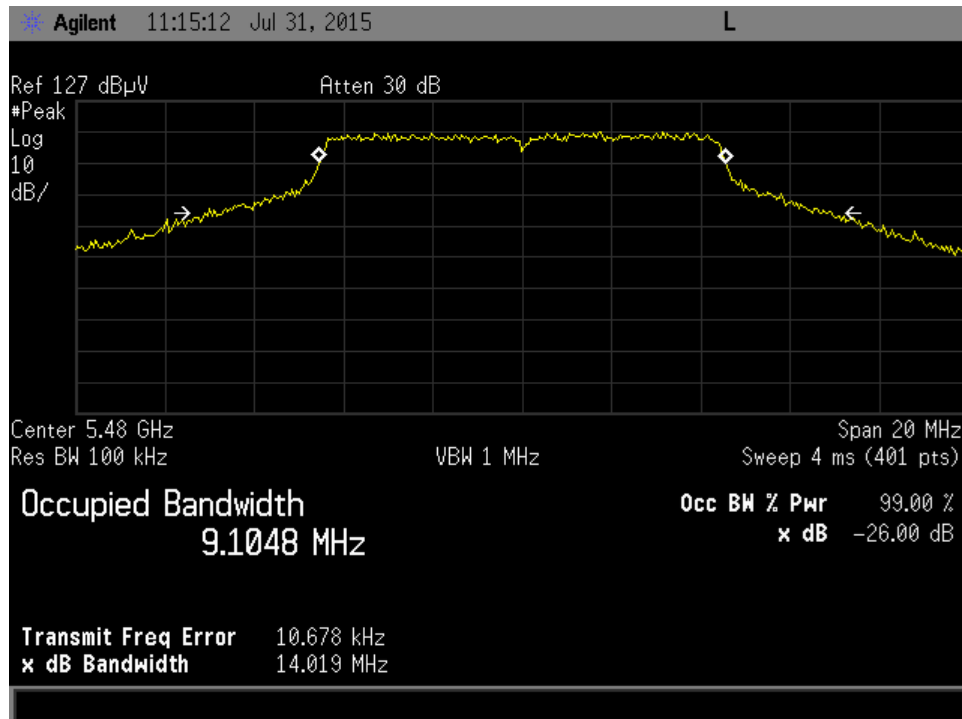
10MHz high ch\_J13

10MHz high ch\_J7

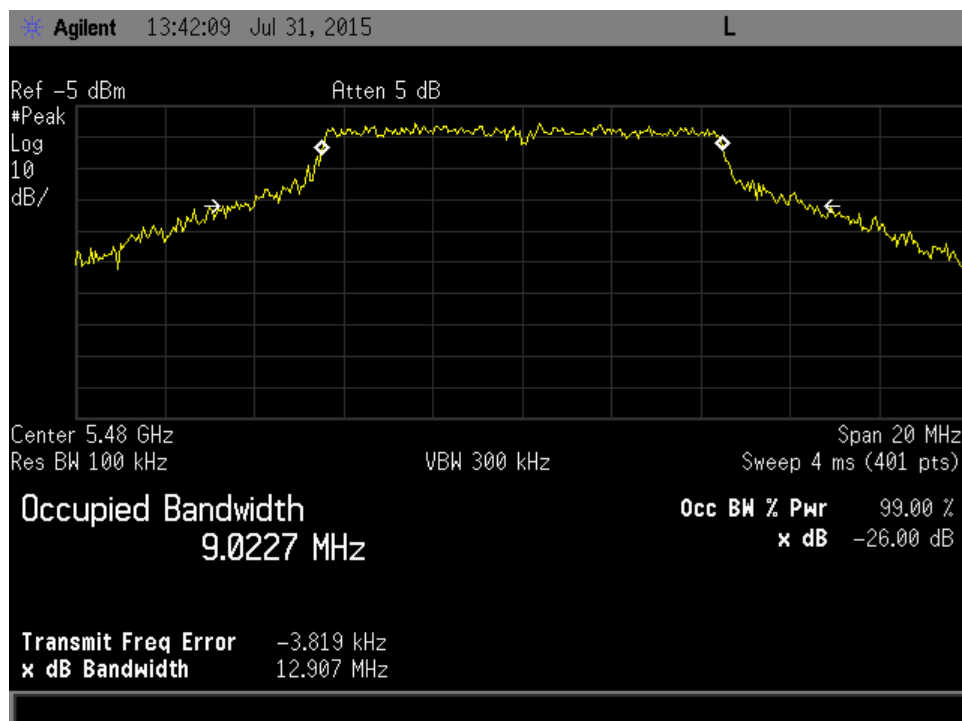




### 10MHz low ch\_J13

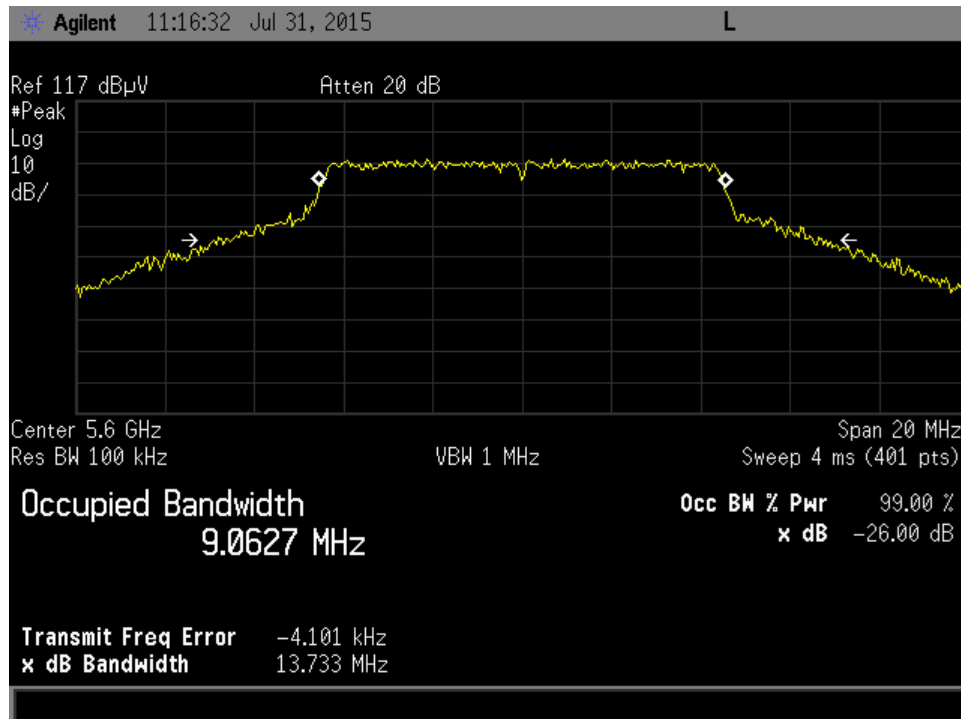


### 10MHz low ch\_J7

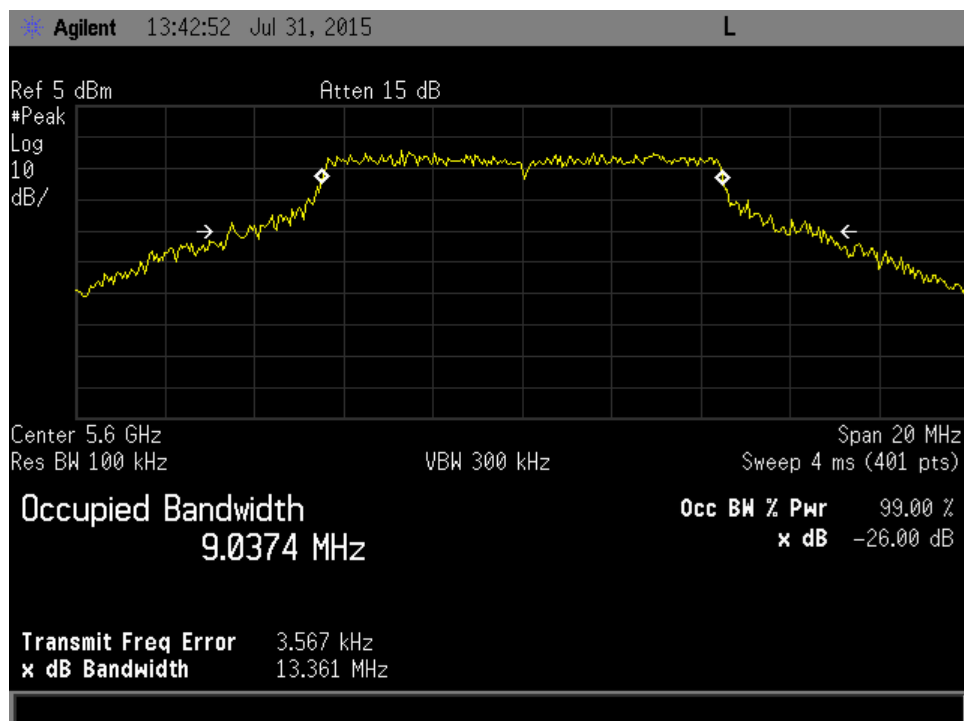




### 10MHz mid ch\_J13

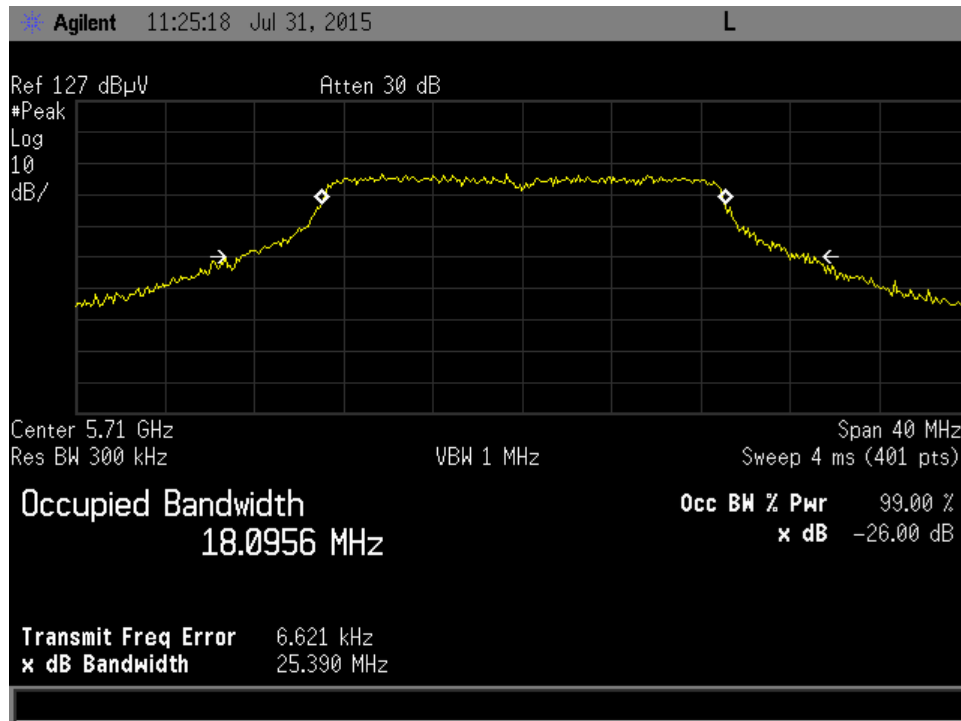


### 10MHz mid ch\_J7

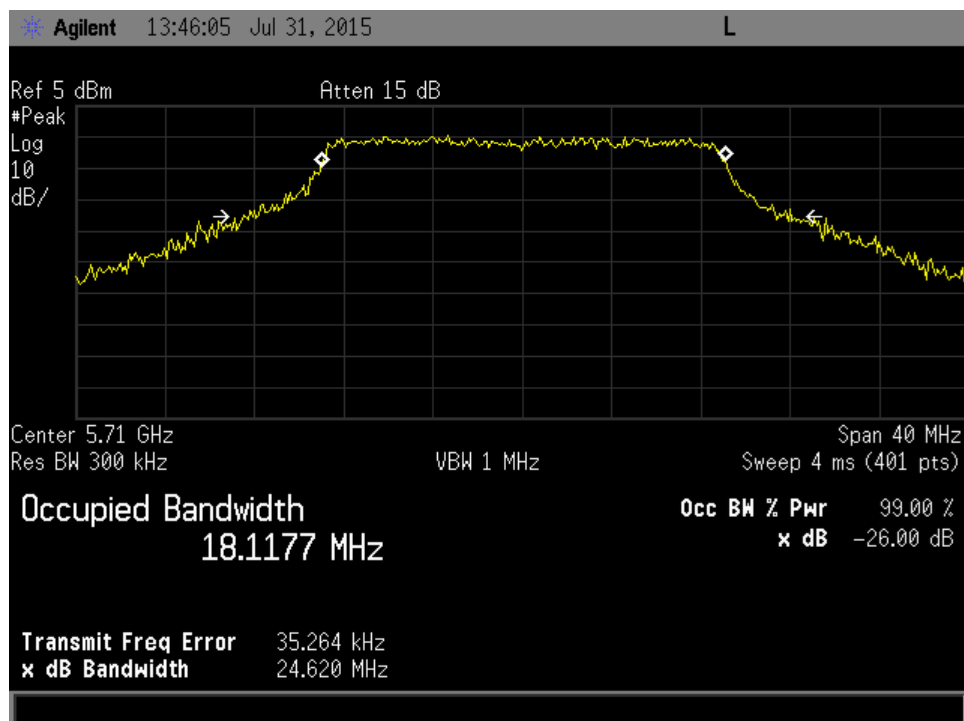




### 20MHz high ch\_J13

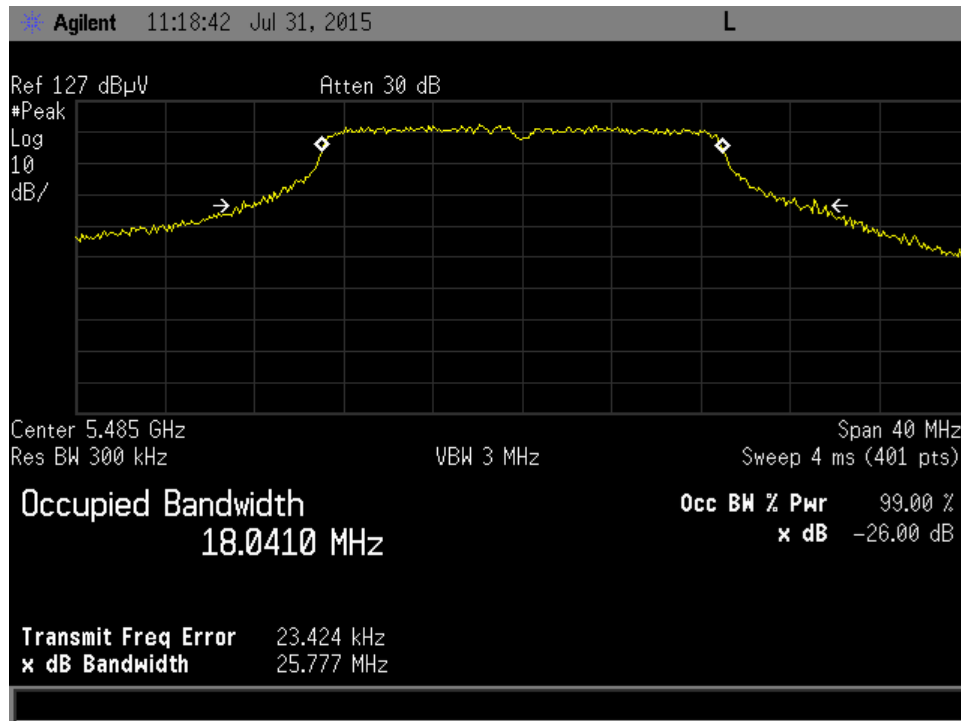


### 20MHz high ch\_J7

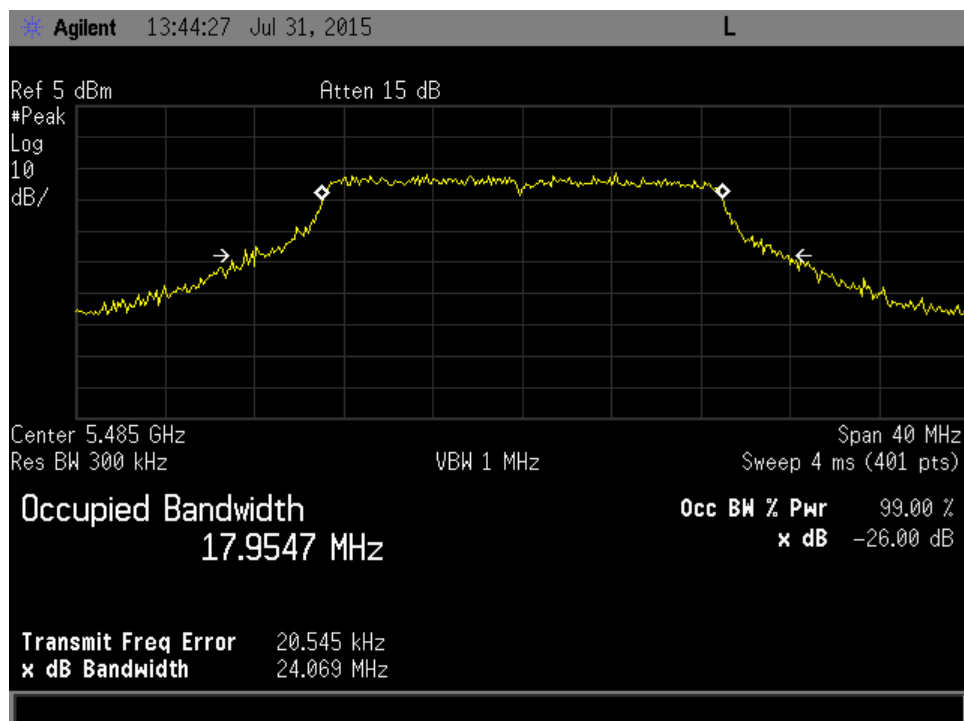




### 20MHz low ch\_J13

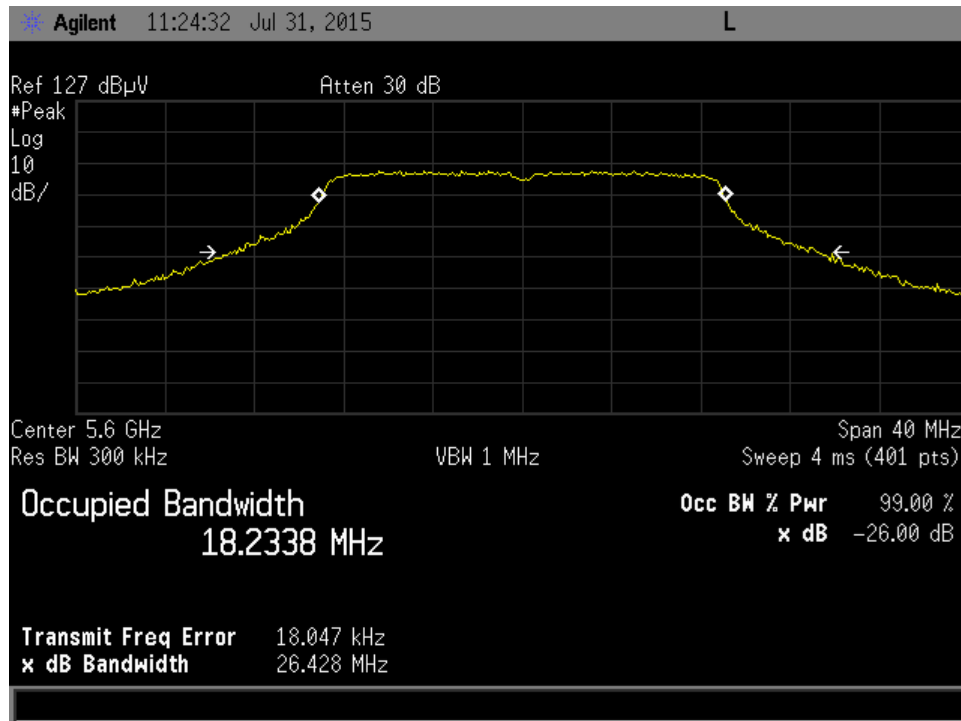


### 20MHz low ch\_J7

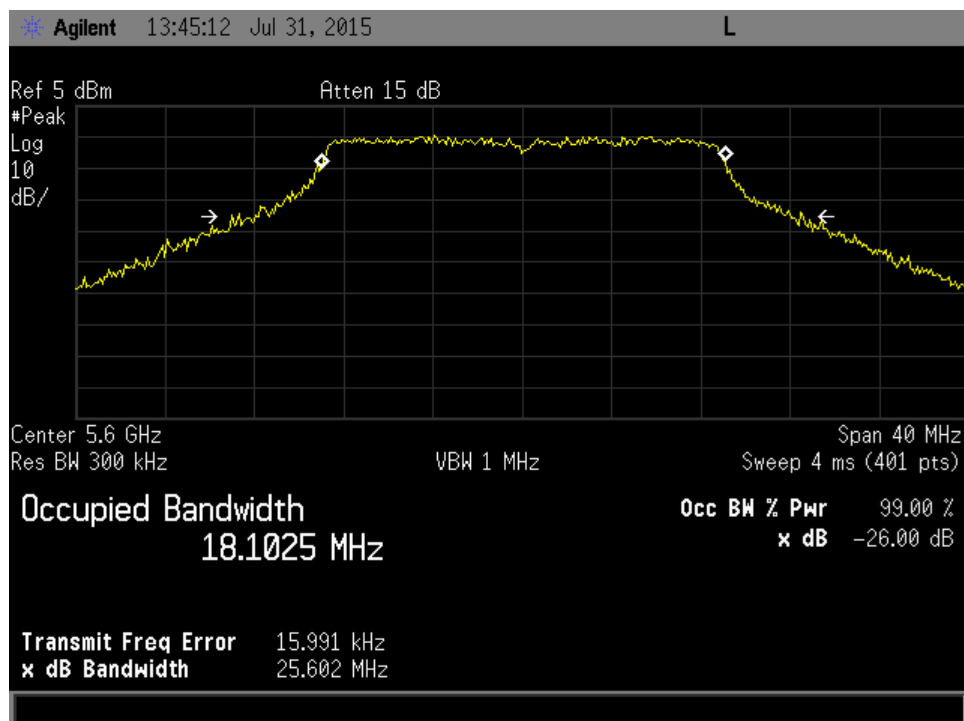




### 20MHz mid ch\_J13

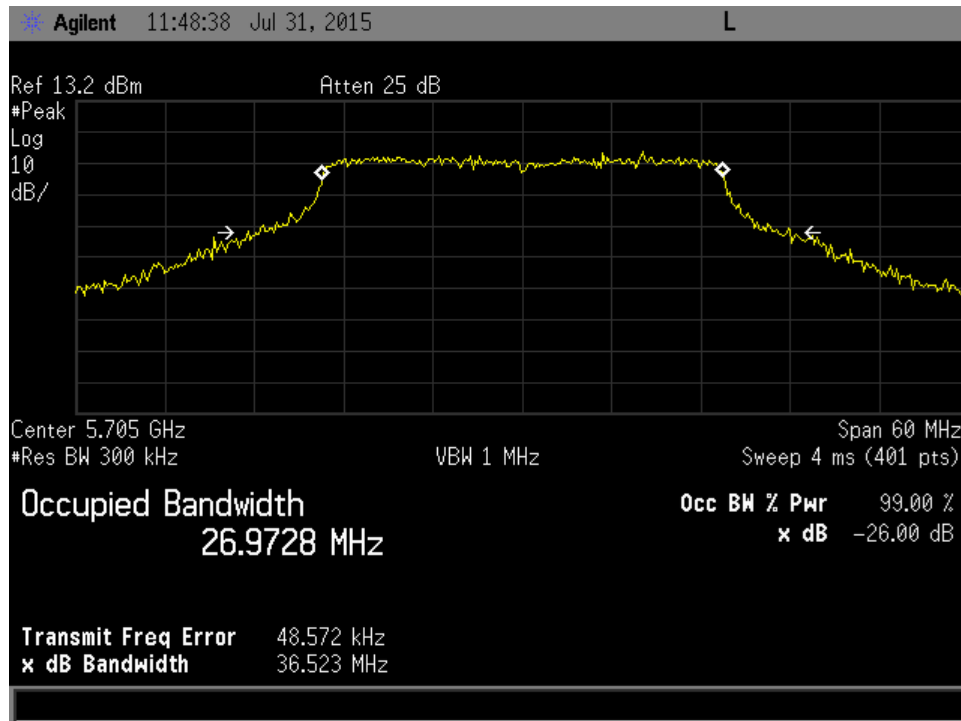


### 20MHz mid ch\_J7

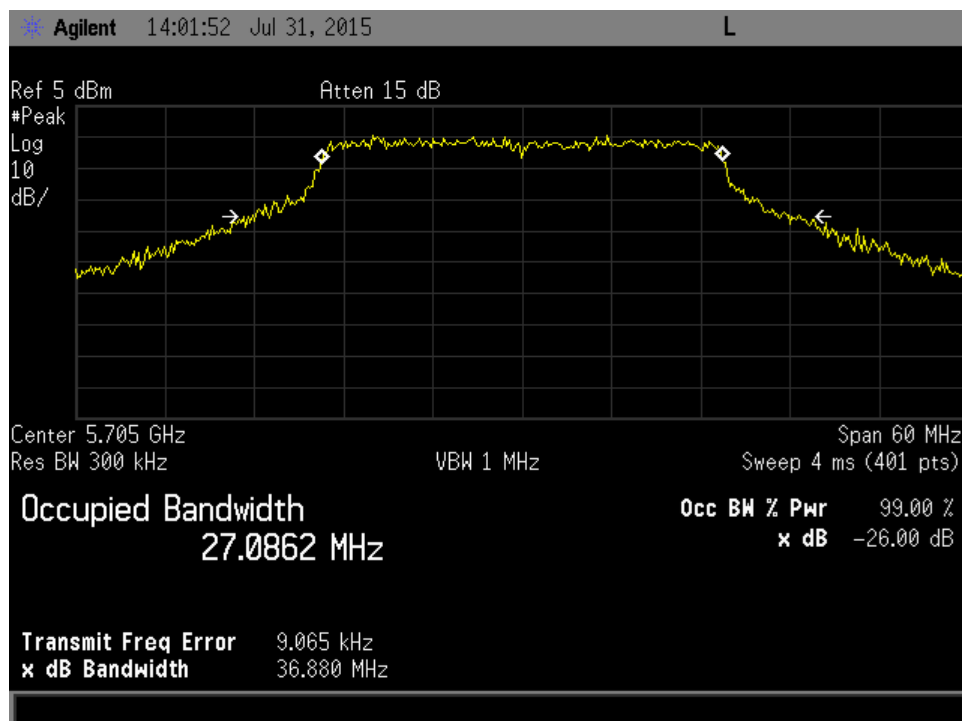




### 30MHz high ch\_J13

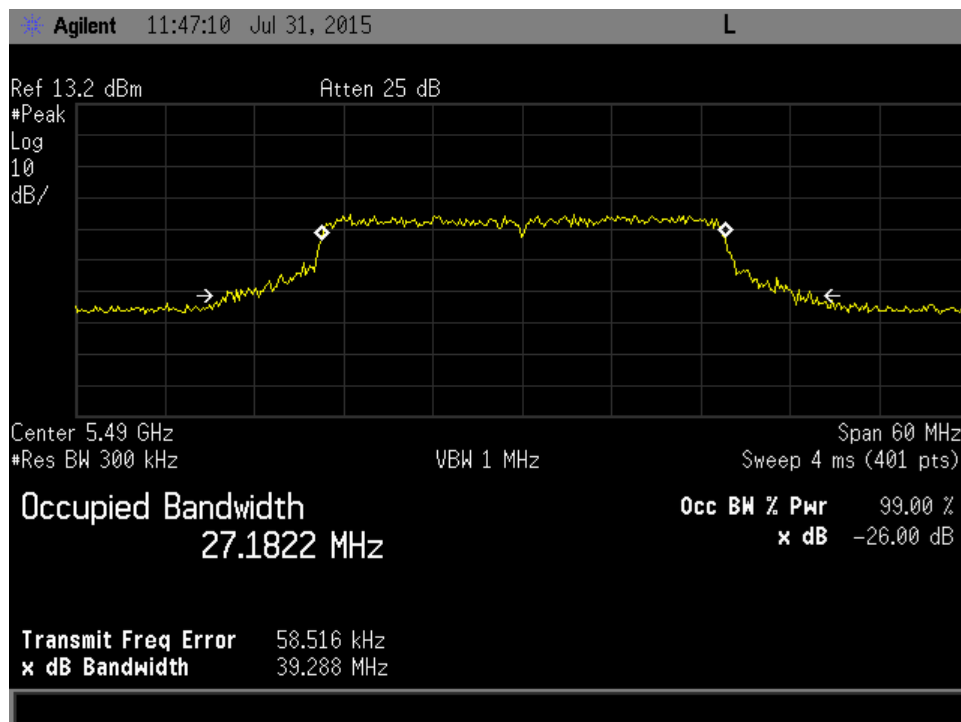


### 30MHz high ch\_J7

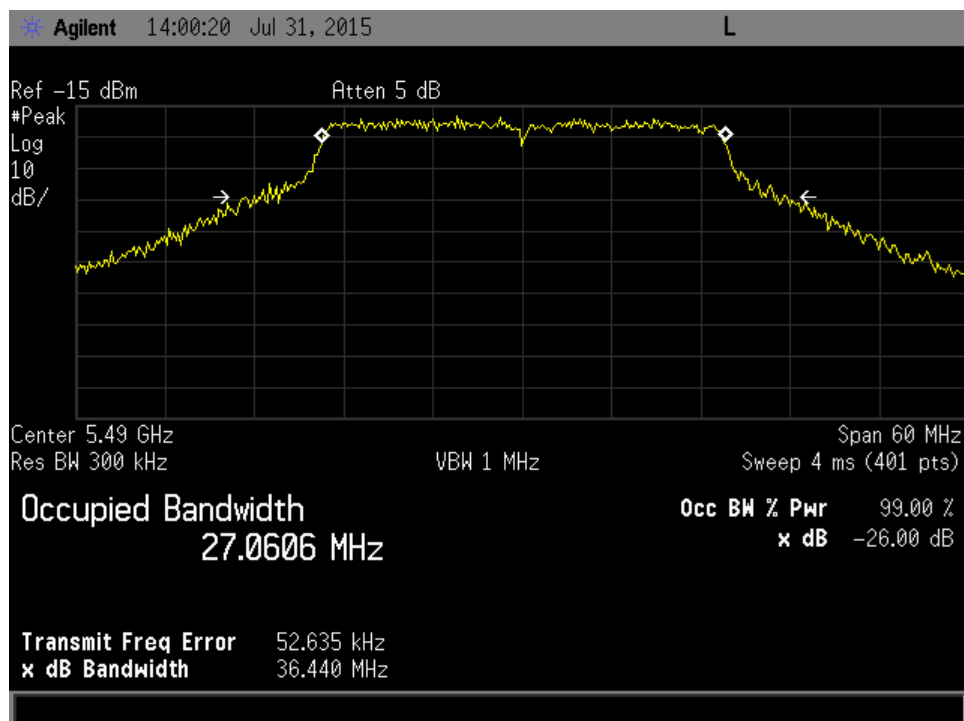




### 30MHz low ch\_J13

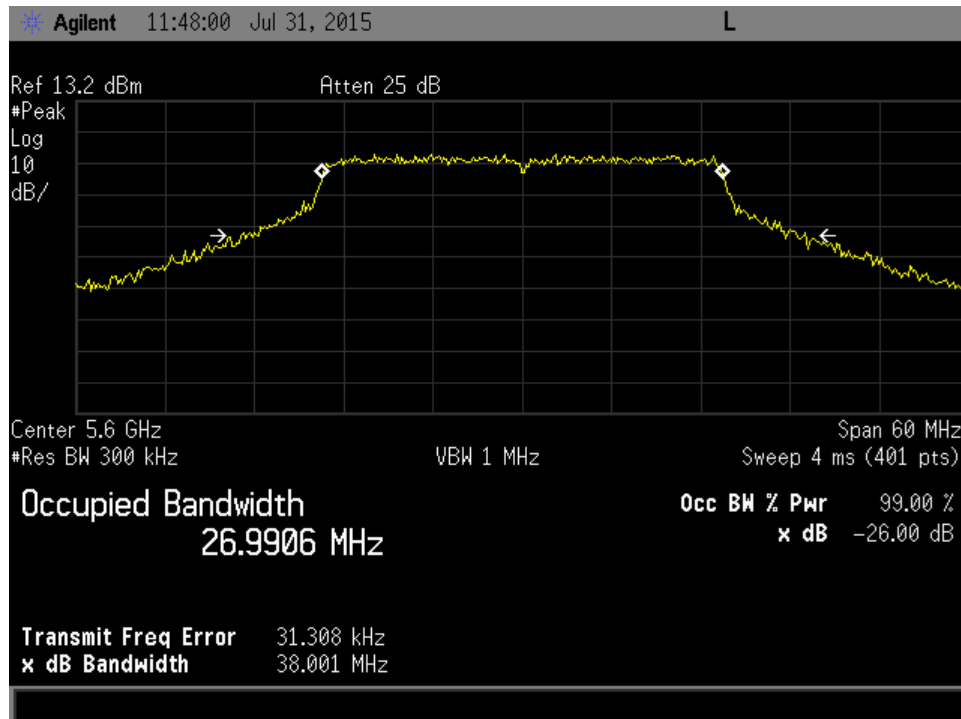


### 30MHz low ch\_J7

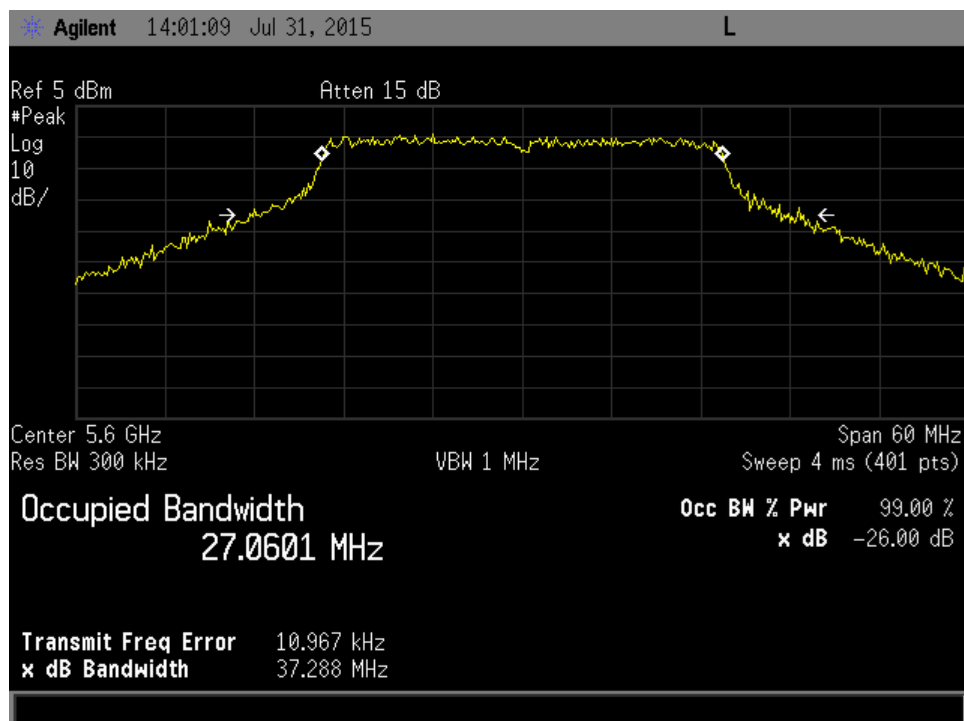




### 30MHz mid ch\_J13

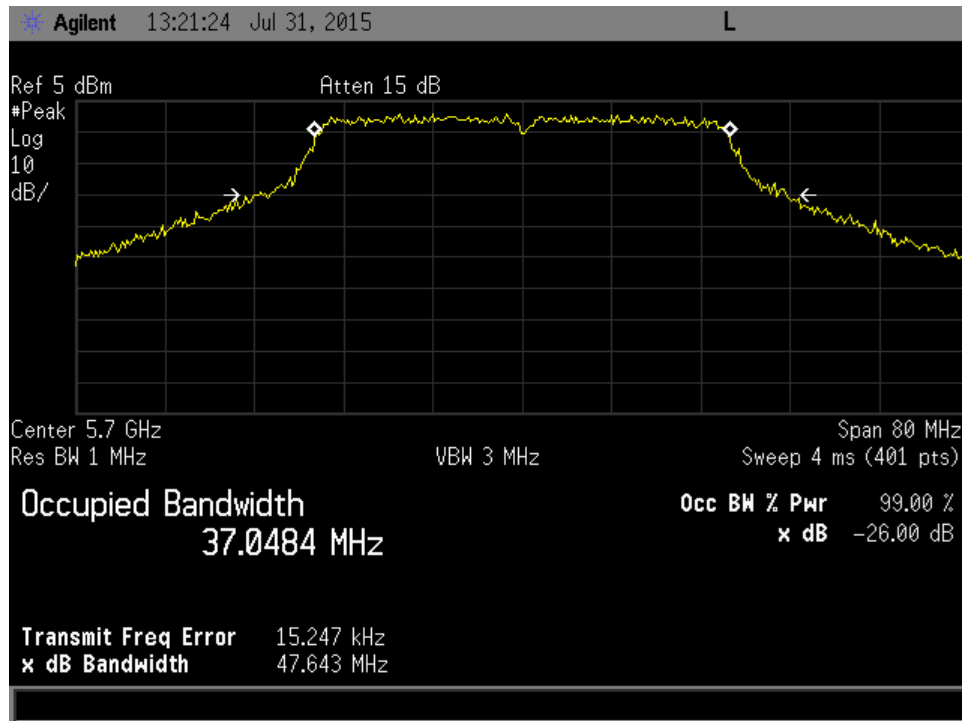


### 30MHz mid ch\_J7

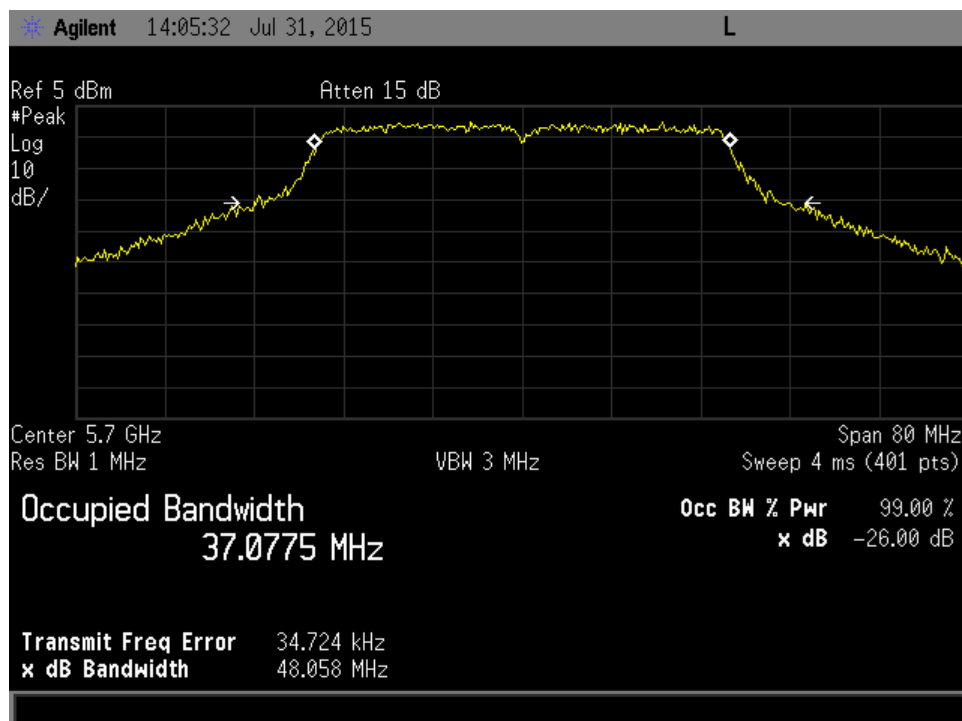




### 40MHz high ch\_J13

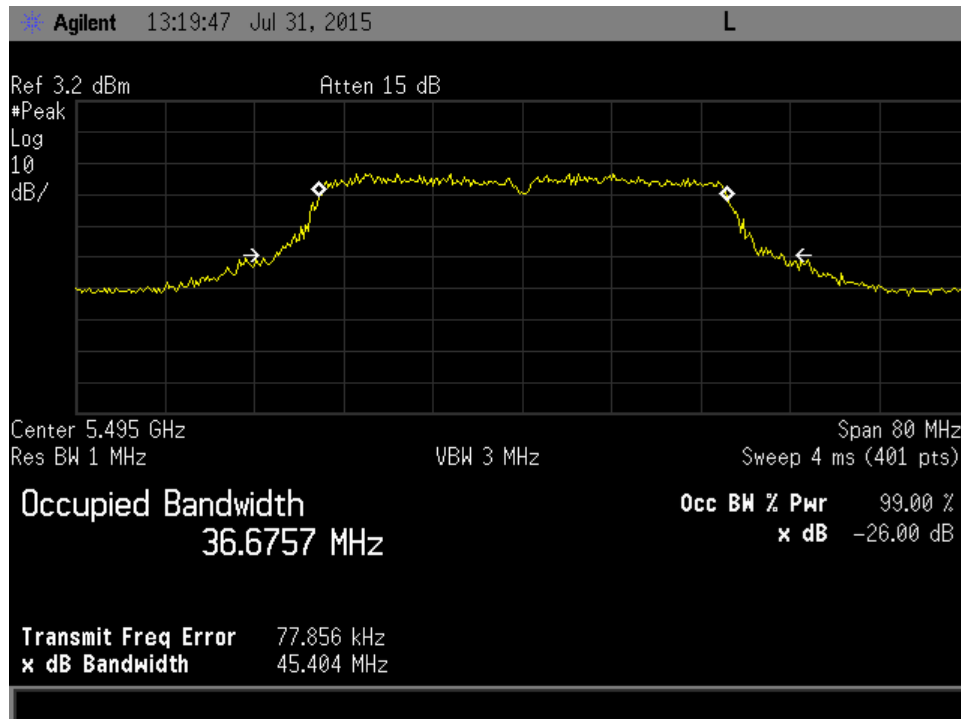


### 40MHz high ch\_J7

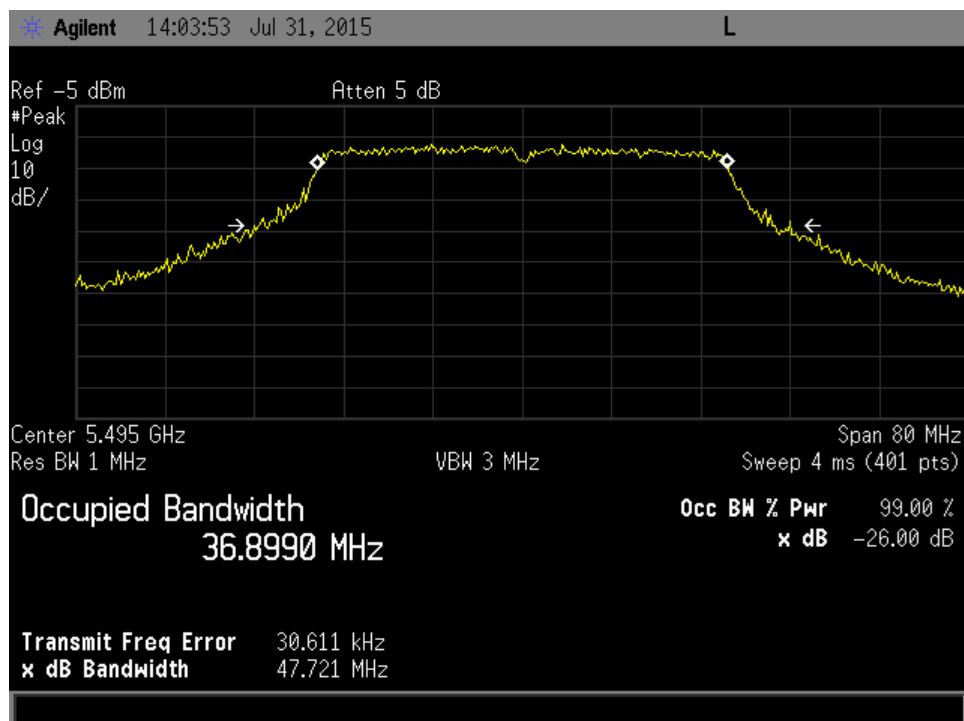




### 40MHz low ch\_J13

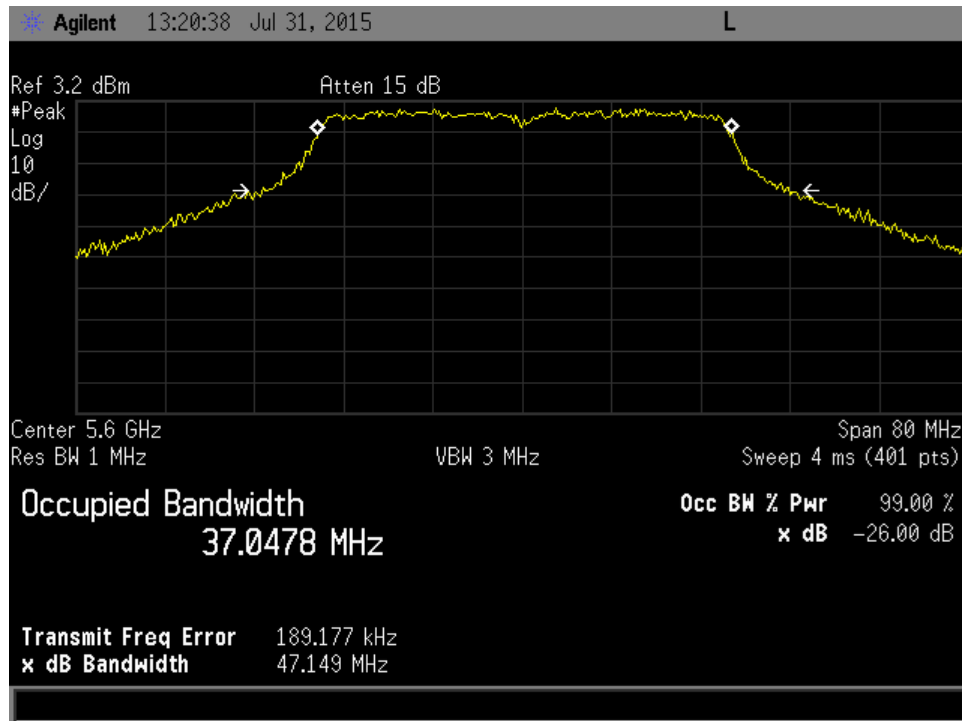


### 40MHz low ch\_J7

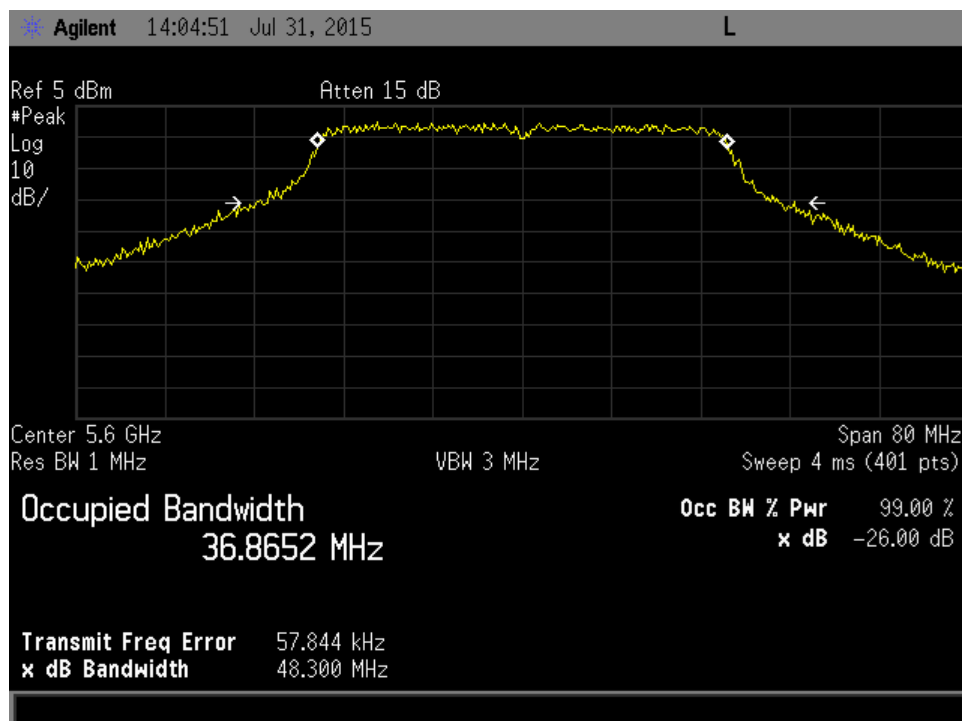




### 40MHz mid ch\_J13

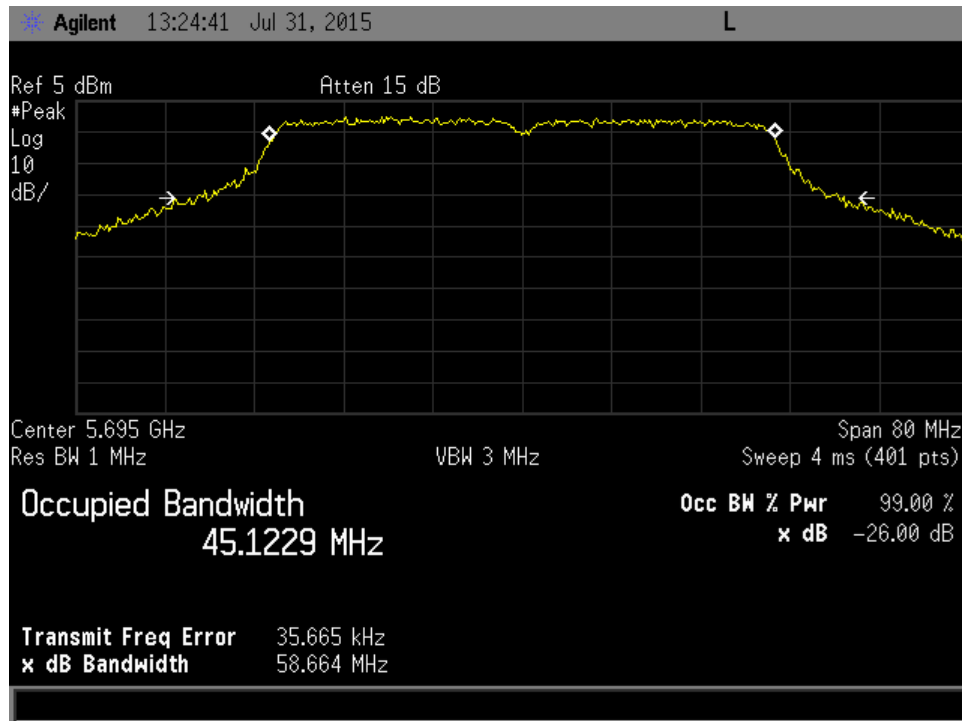


### 40MHz mid ch\_J7

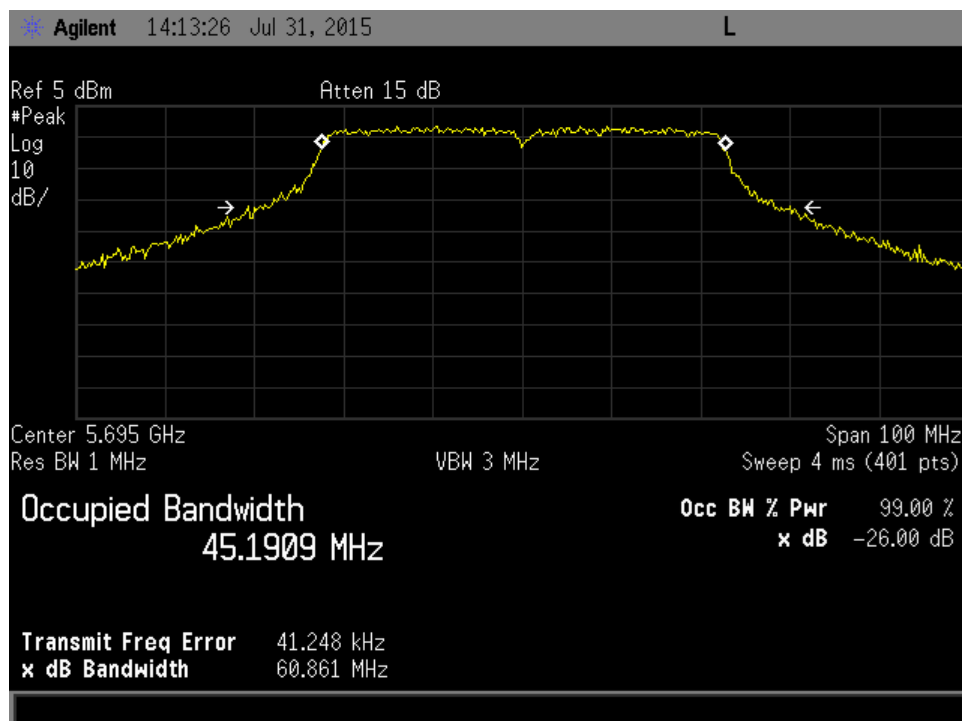




### 50MHz high ch\_J13

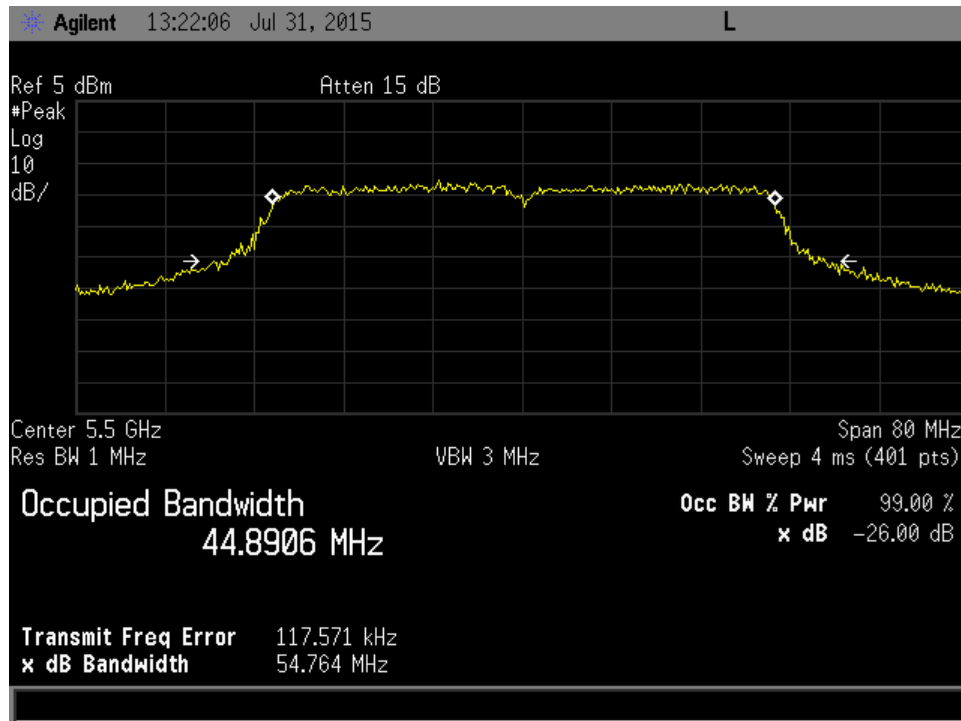


### 50MHz high ch\_J7

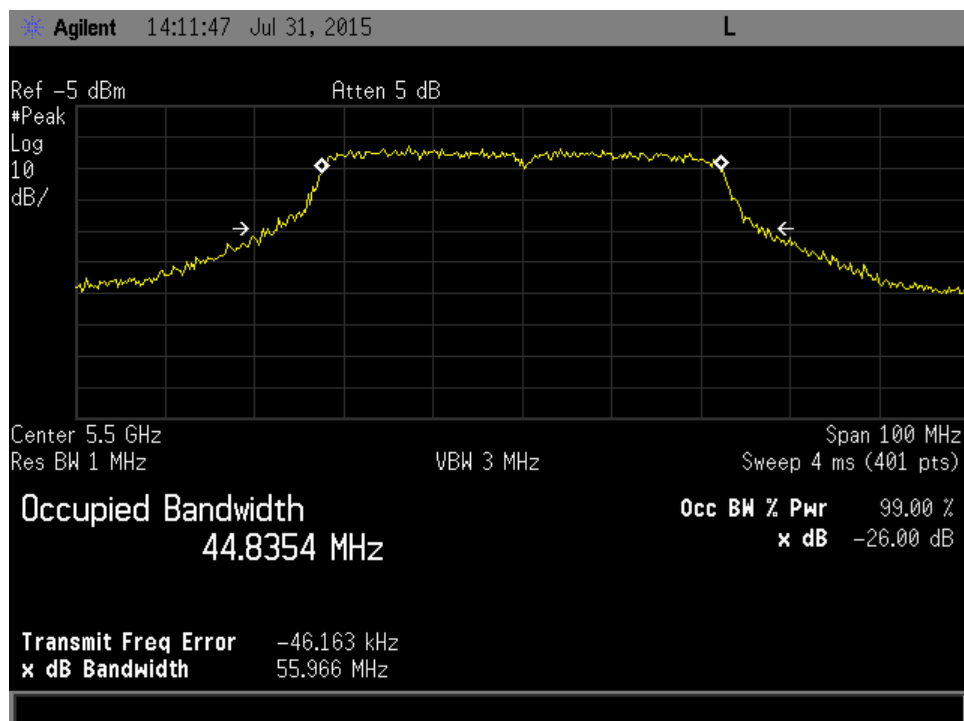




### 50MHz low ch\_J13

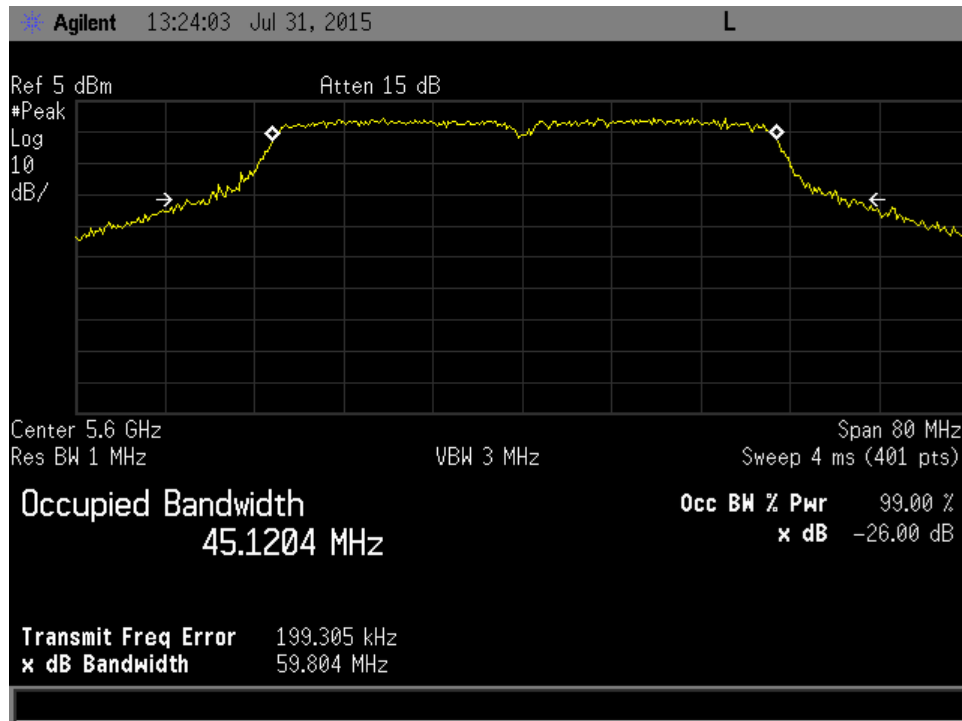


### 50MHz low ch\_J7

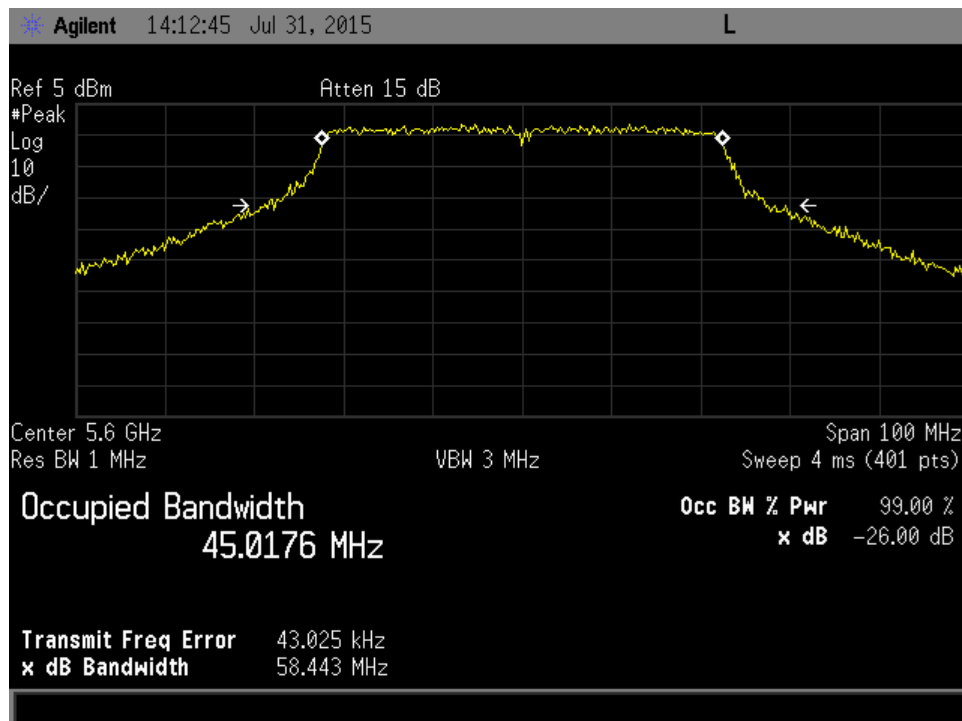




### 50MHz mid ch\_J13

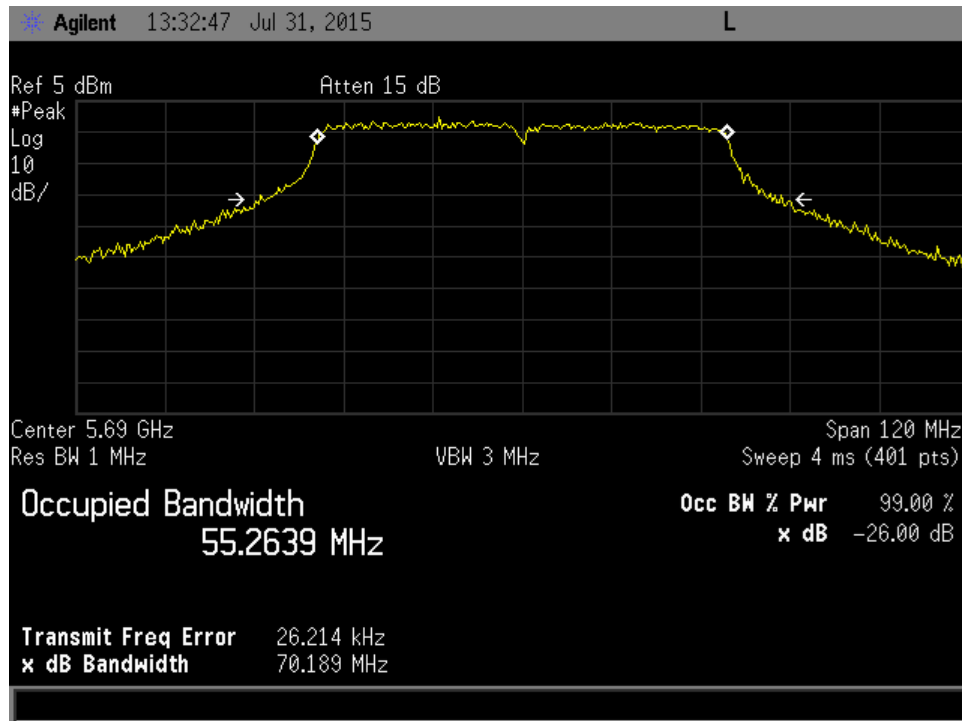


### 50MHz mid ch\_J7

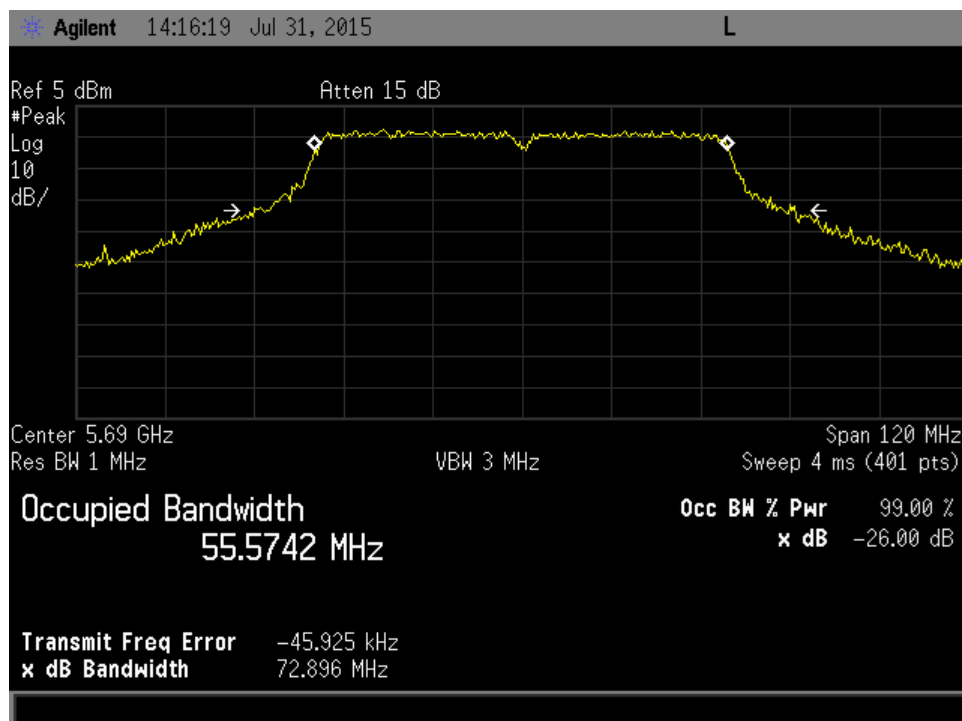




### 60MHz high ch\_J13

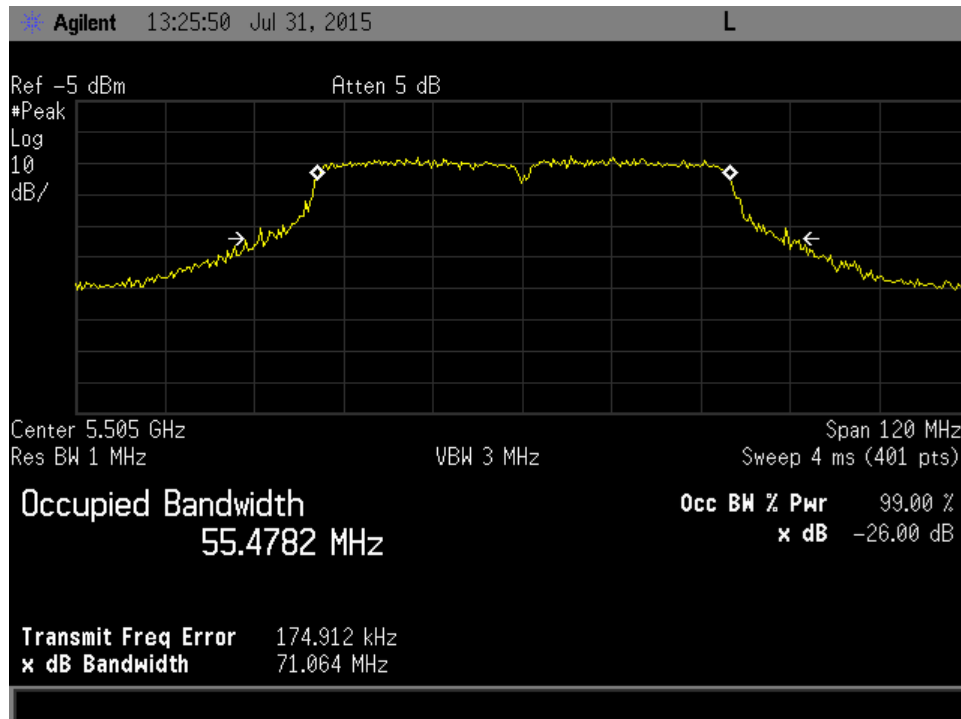


### 60MHz high ch\_J7

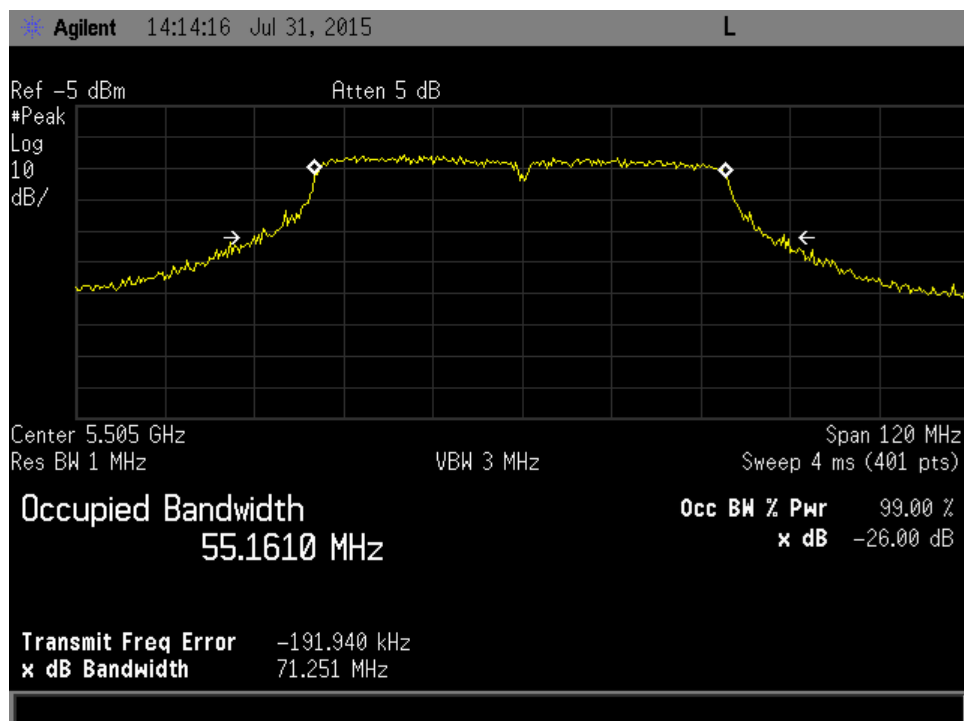




### 60MHz low ch\_J13

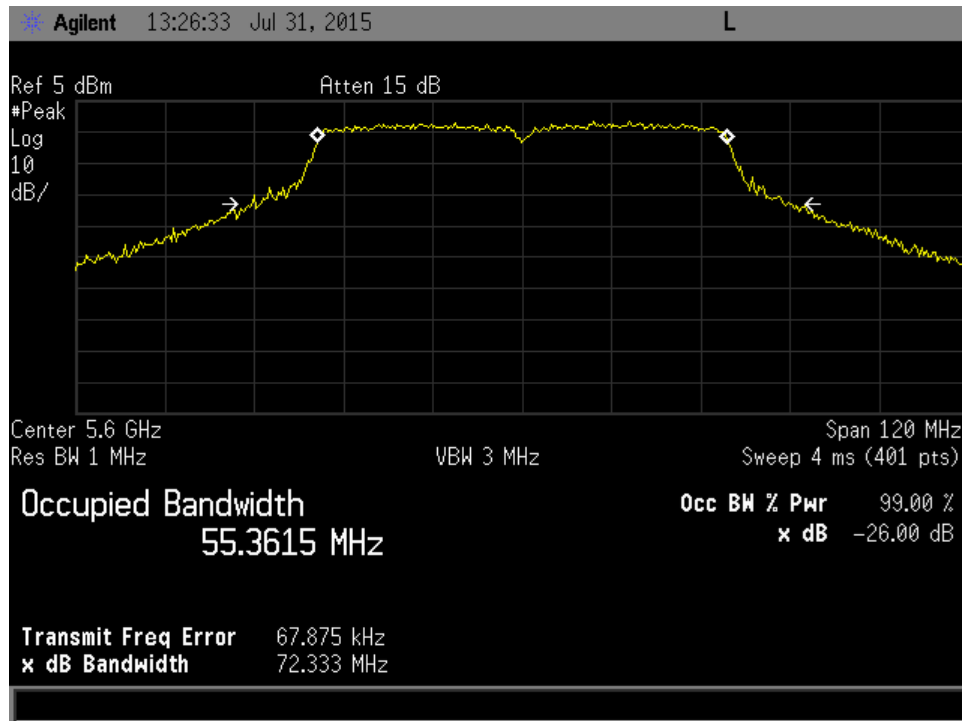


### 60MHz low ch\_J7

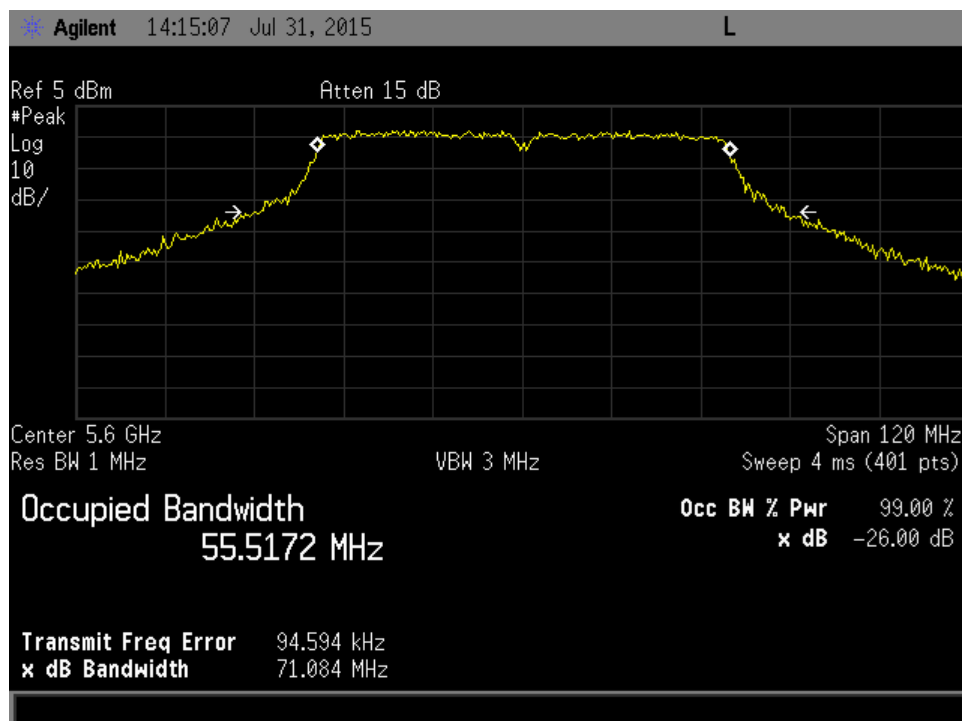




### 60MHz mid ch\_J13

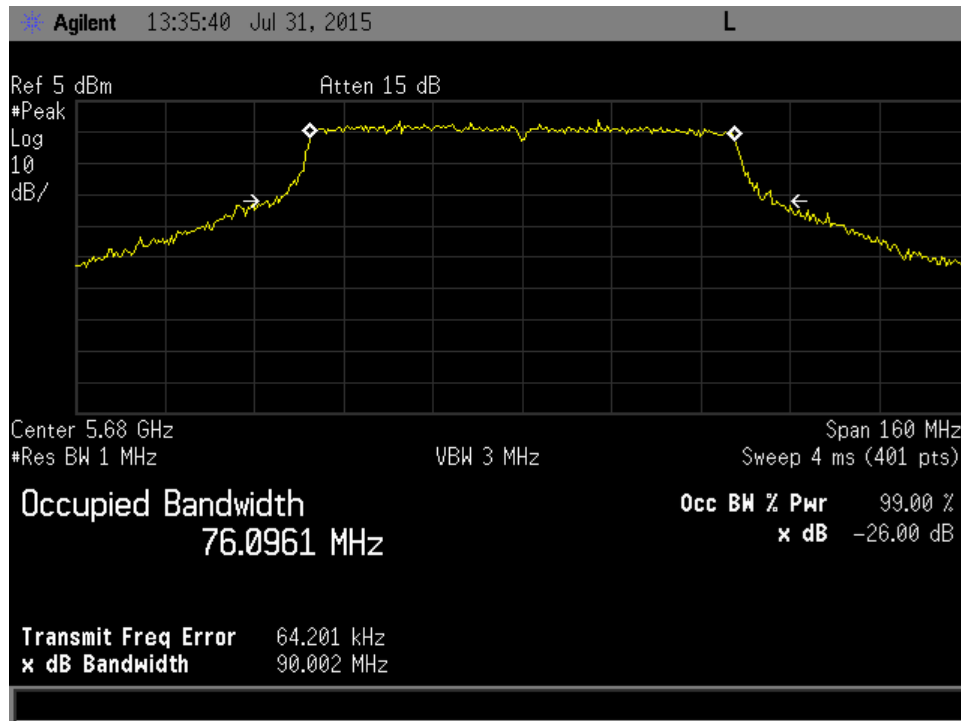


### 60MHz mid ch\_J7

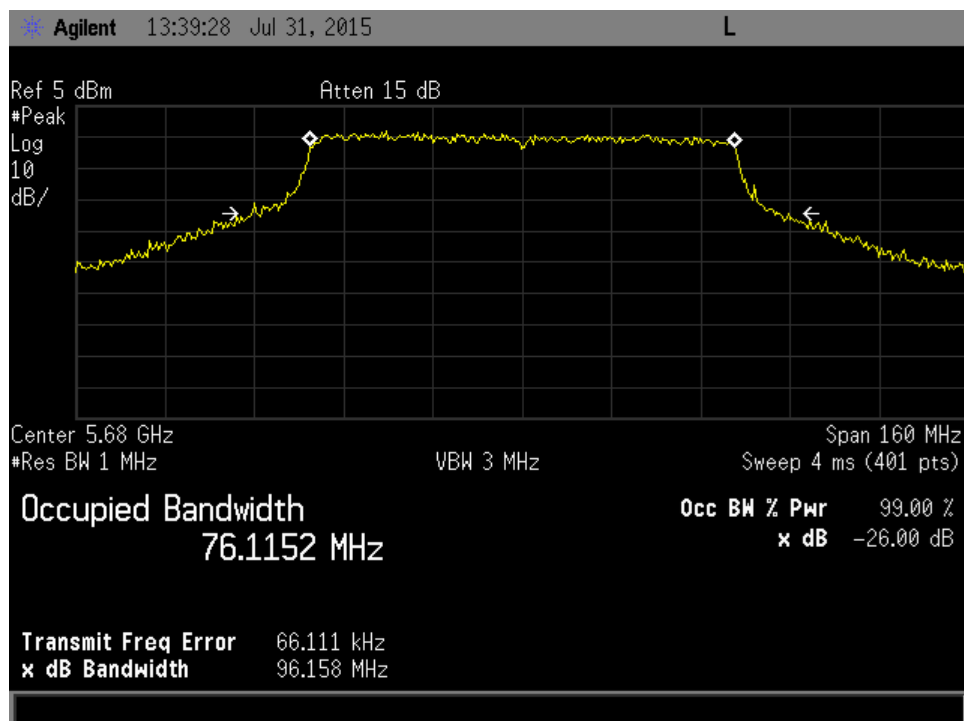




### 80MHz high ch\_J13

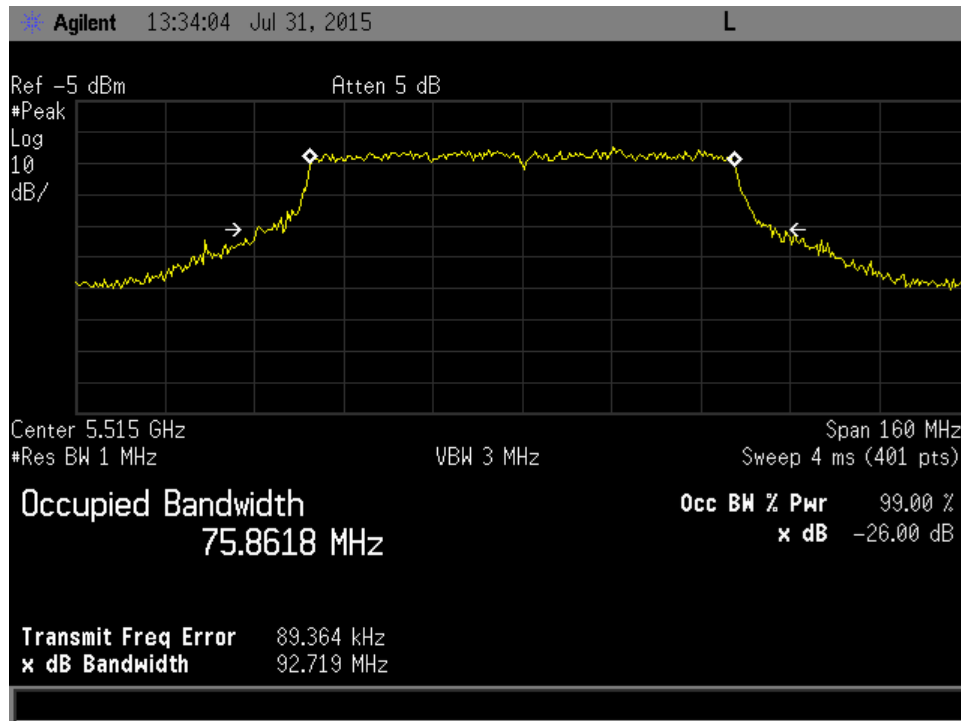


### 80MHz high ch\_J7

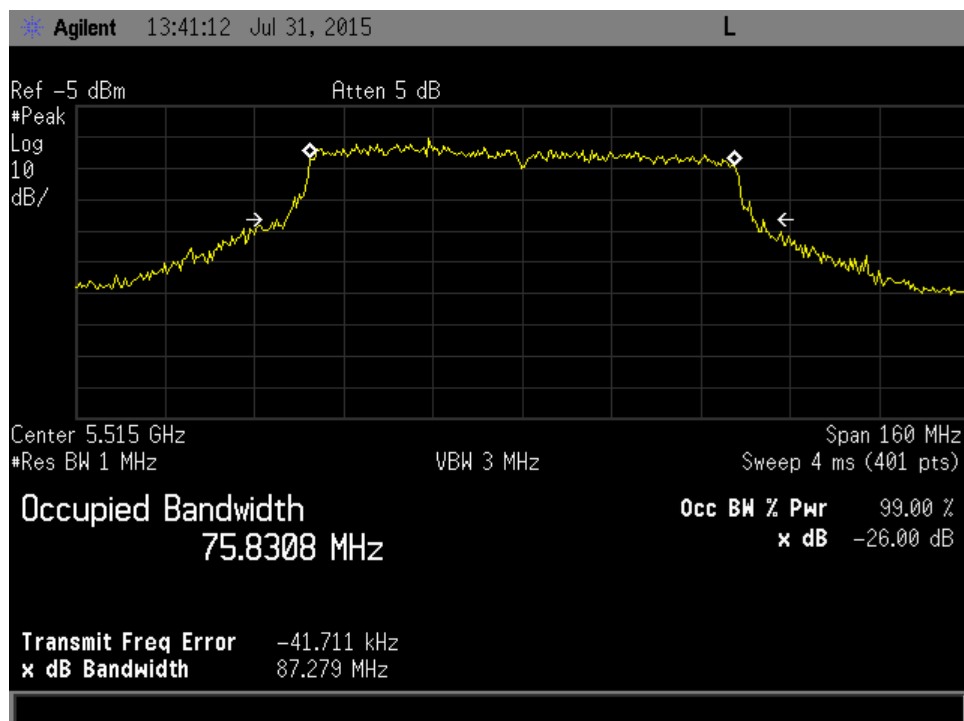




### 80MHz low ch\_J13

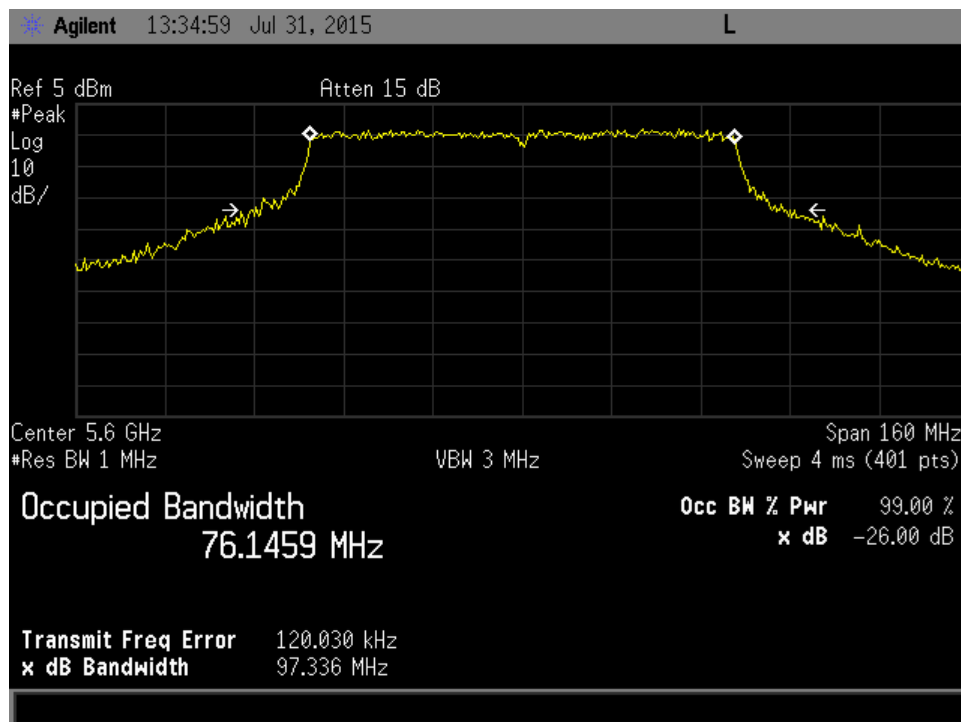


### 80MHz low ch\_J7





### 80MHz mid ch\_J13



### 80MHz mid ch\_J7

