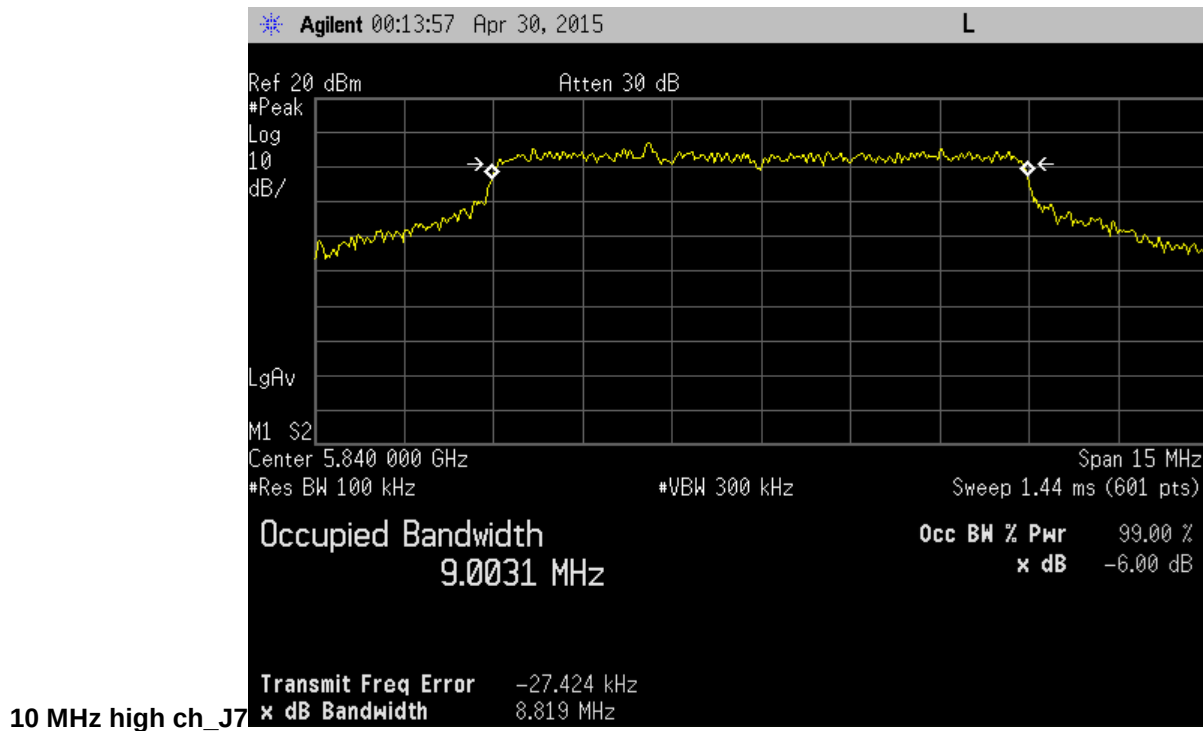




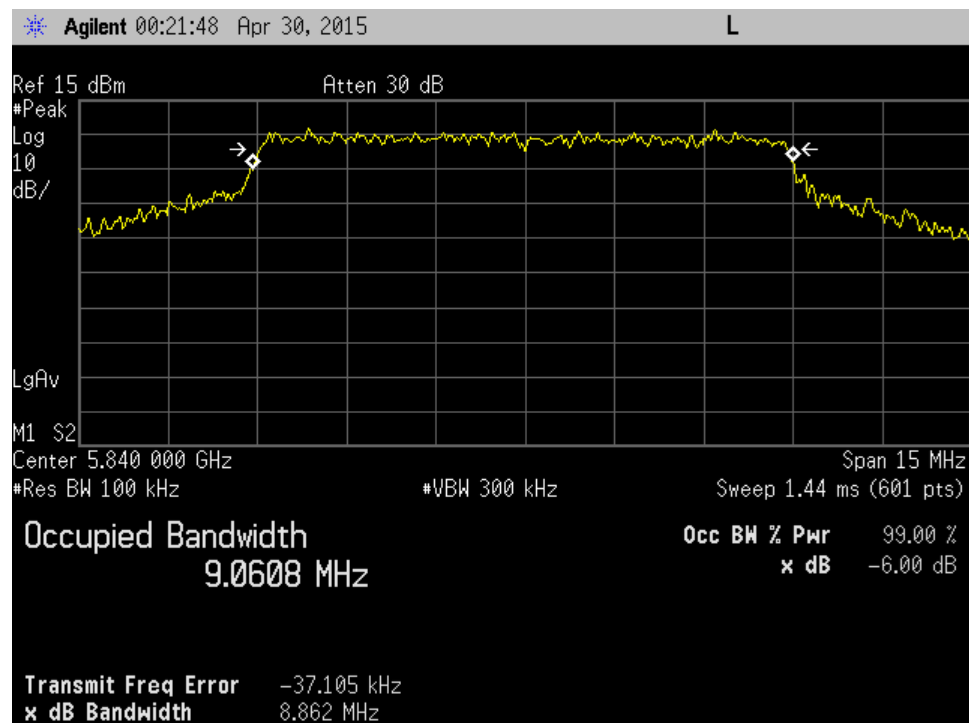
Annex D

Occupied Band Width

Ports J7 and J8

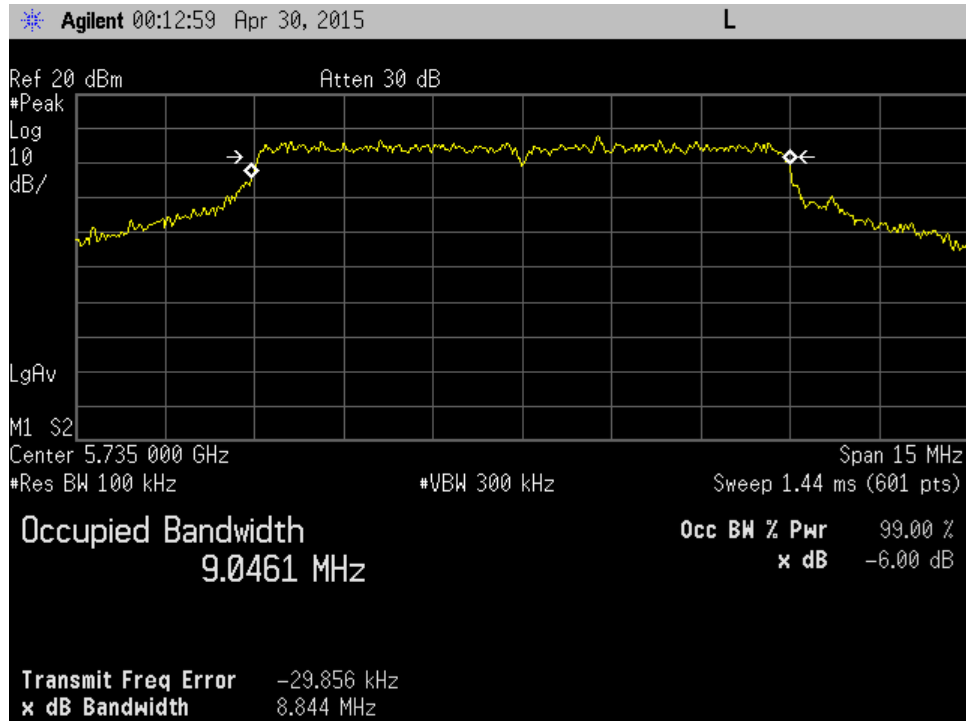


10 MHz high ch_J8

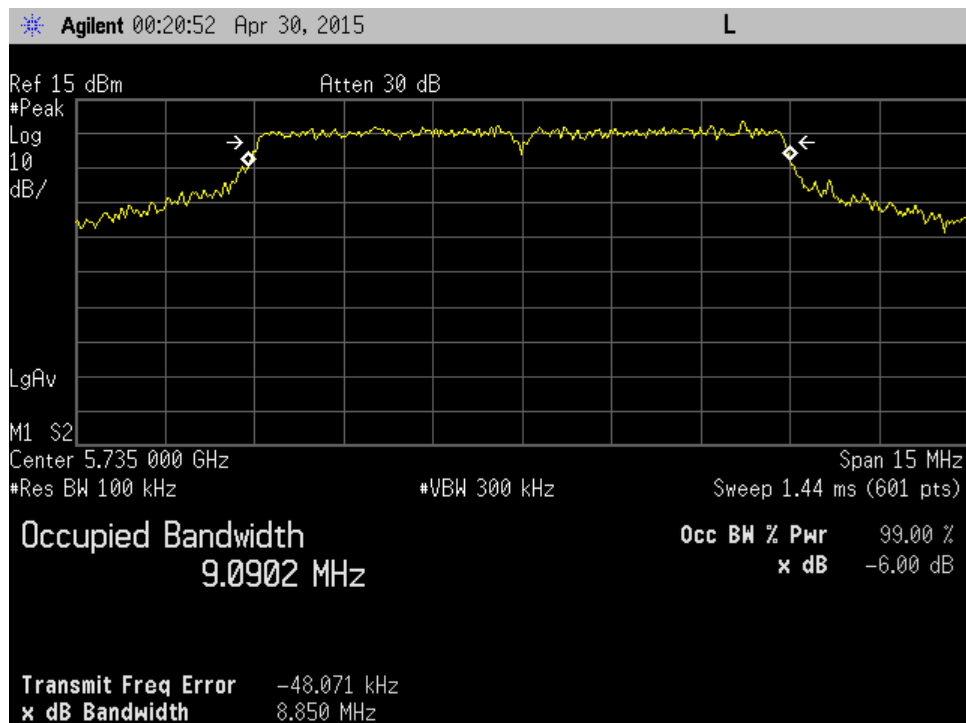




10 MHz low ch_J7

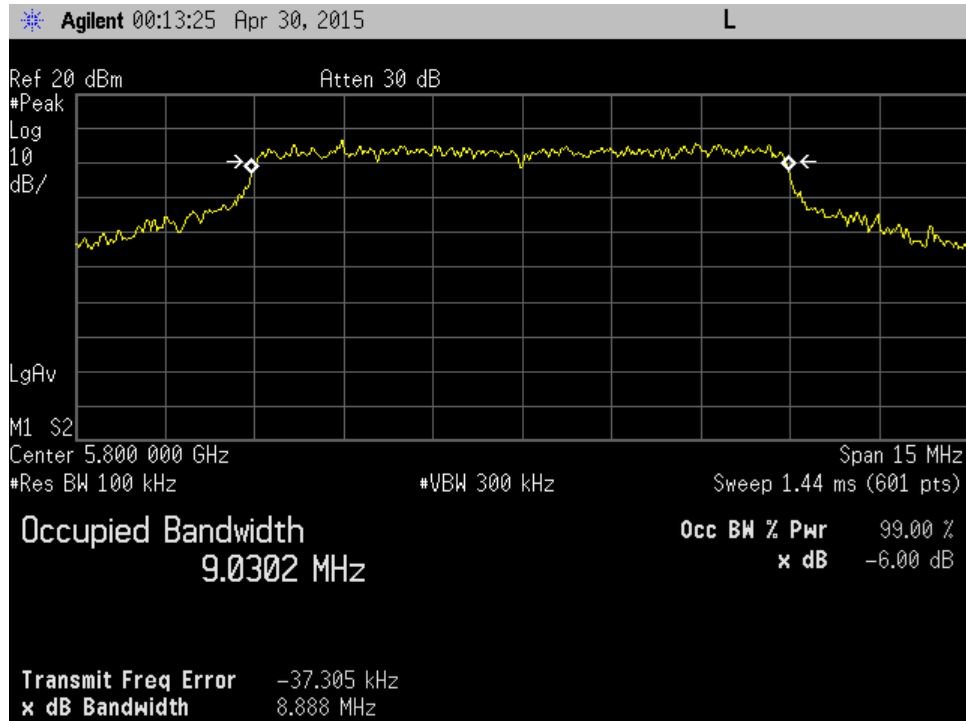


10 MHz low ch_J8

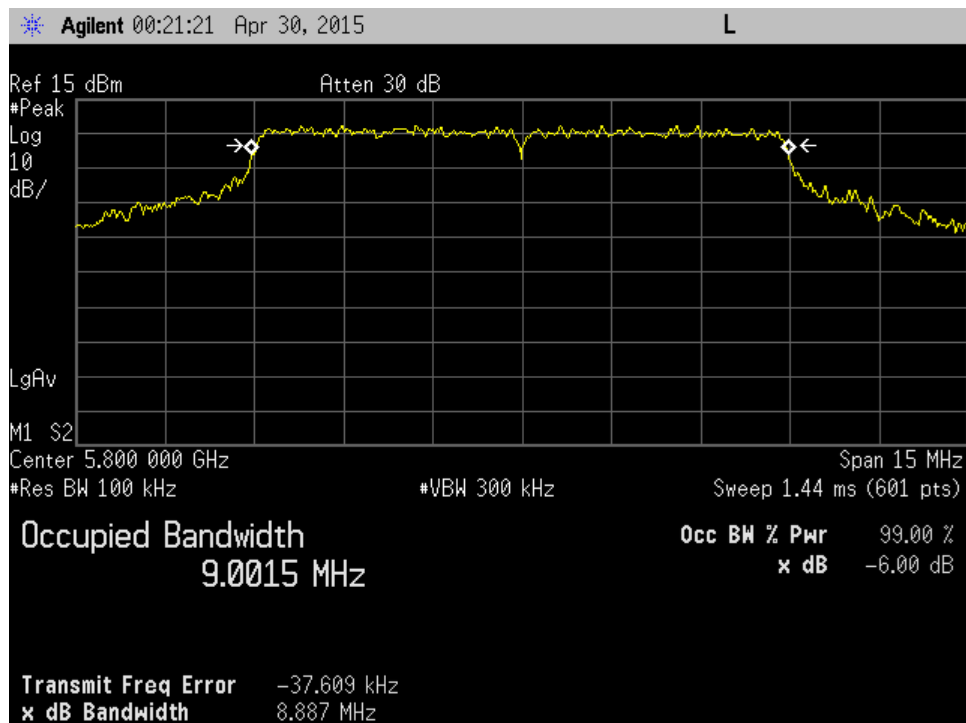




10 MHz mid ch_J7

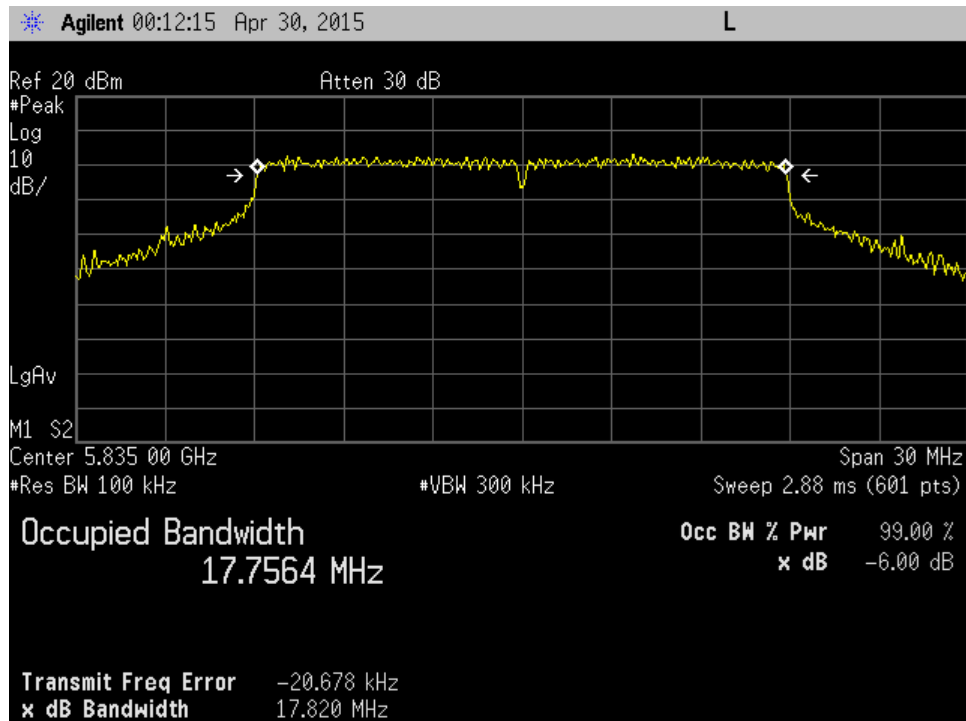


10 MHz mid ch_J8

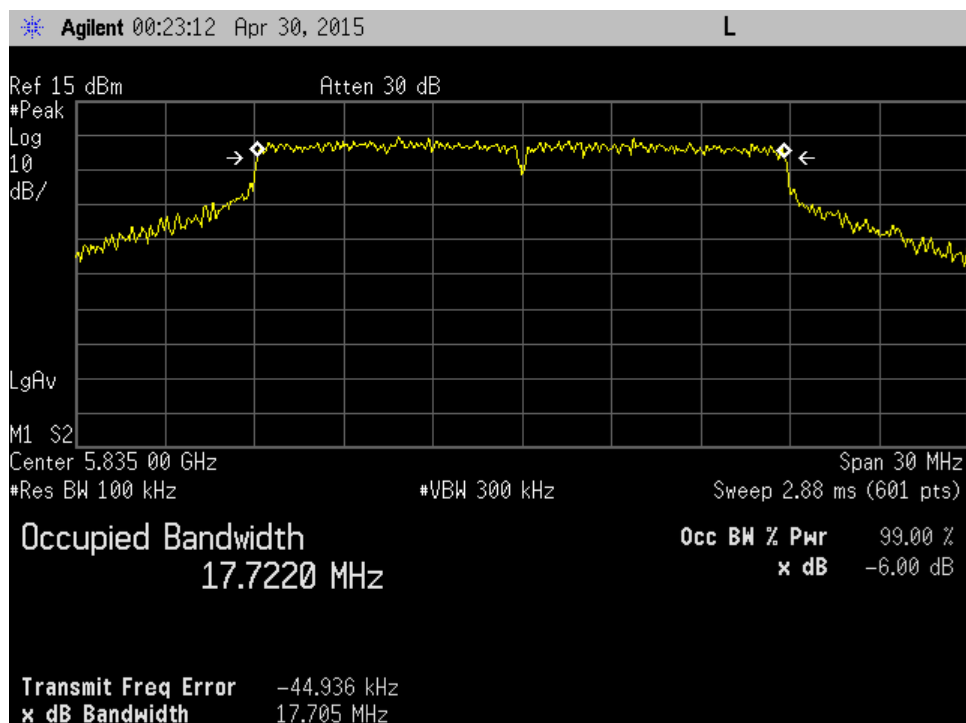




20 MHz high ch_J7

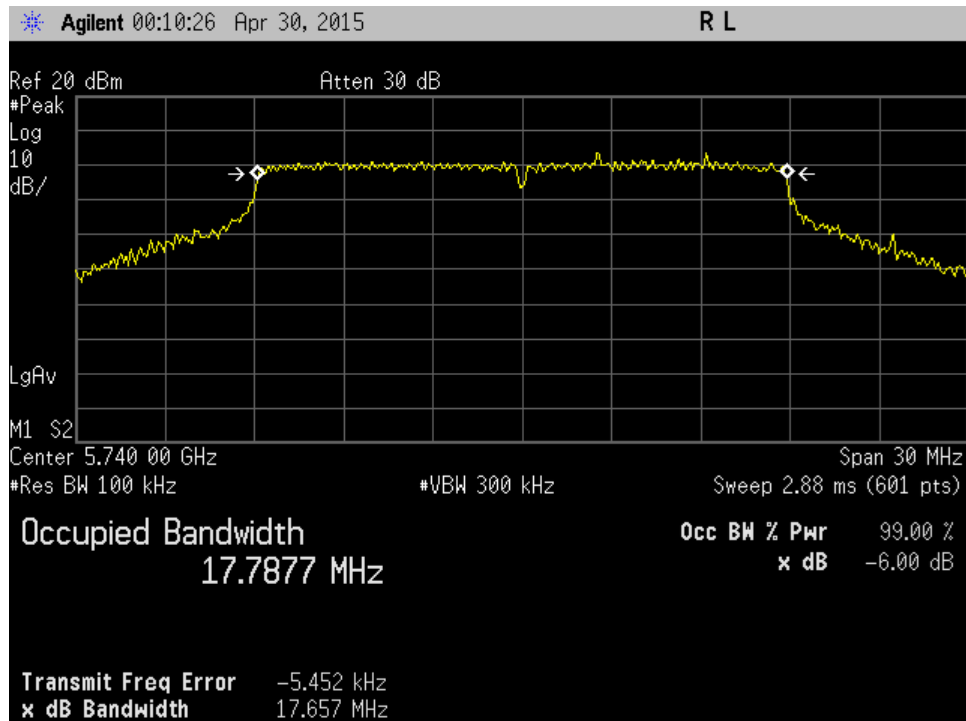


20 MHz high ch_J8

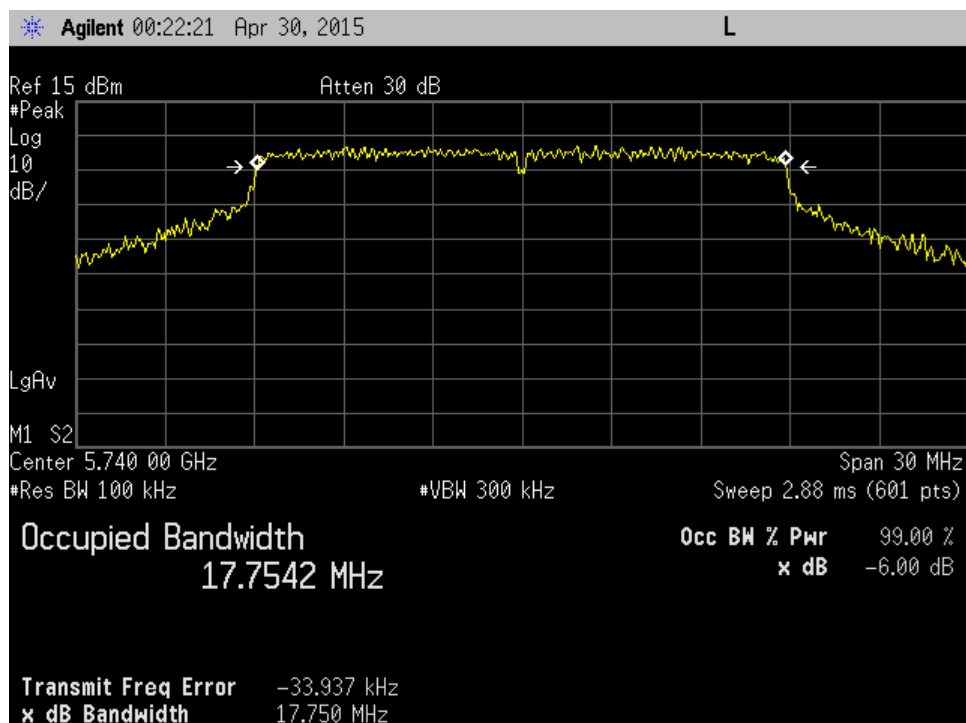




20 MHz low ch_J7

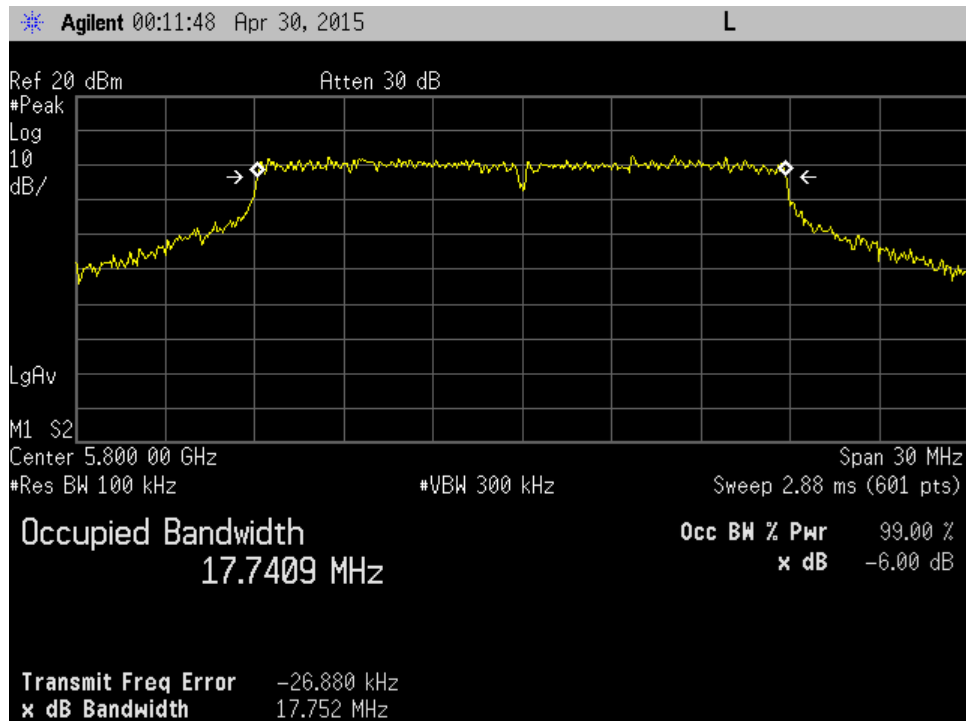


20 MHz low ch_J8

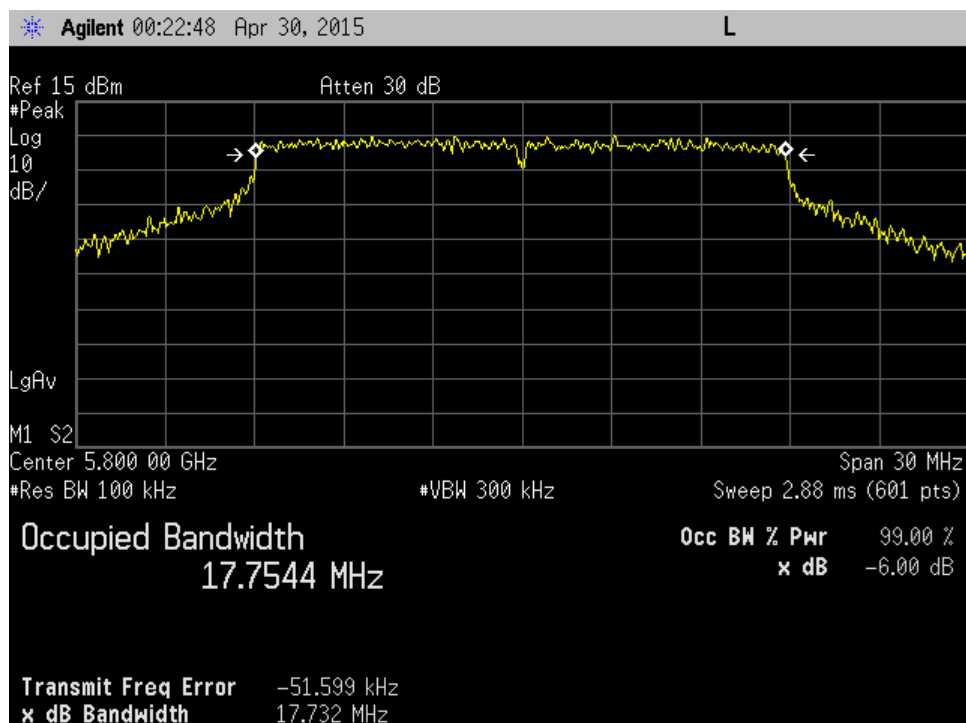




20 MHz mid ch_J7

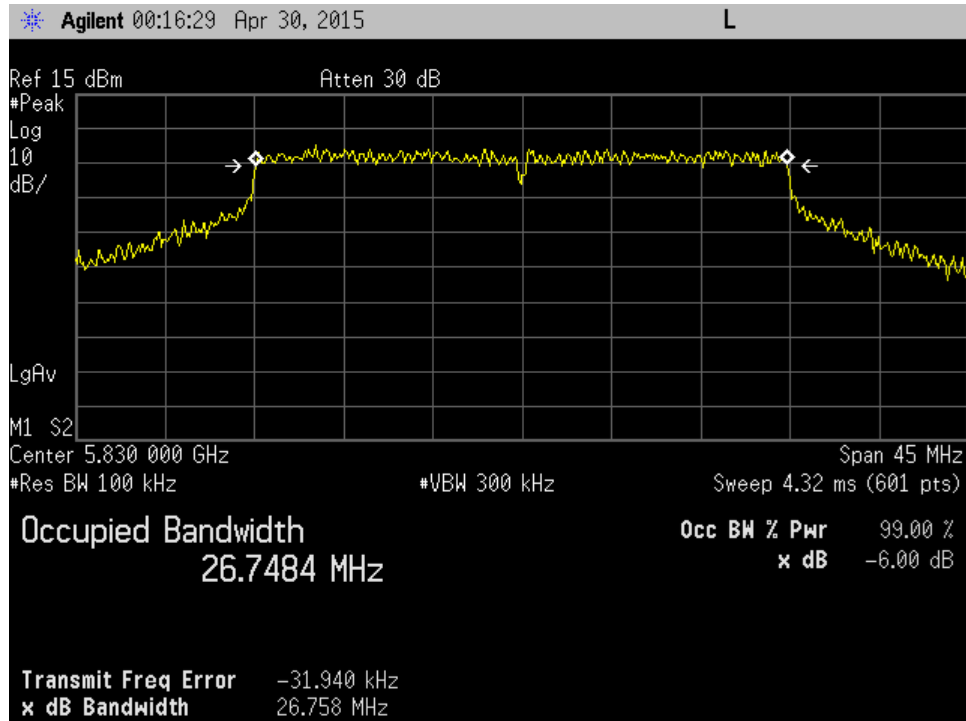


20 MHz mid ch_J8

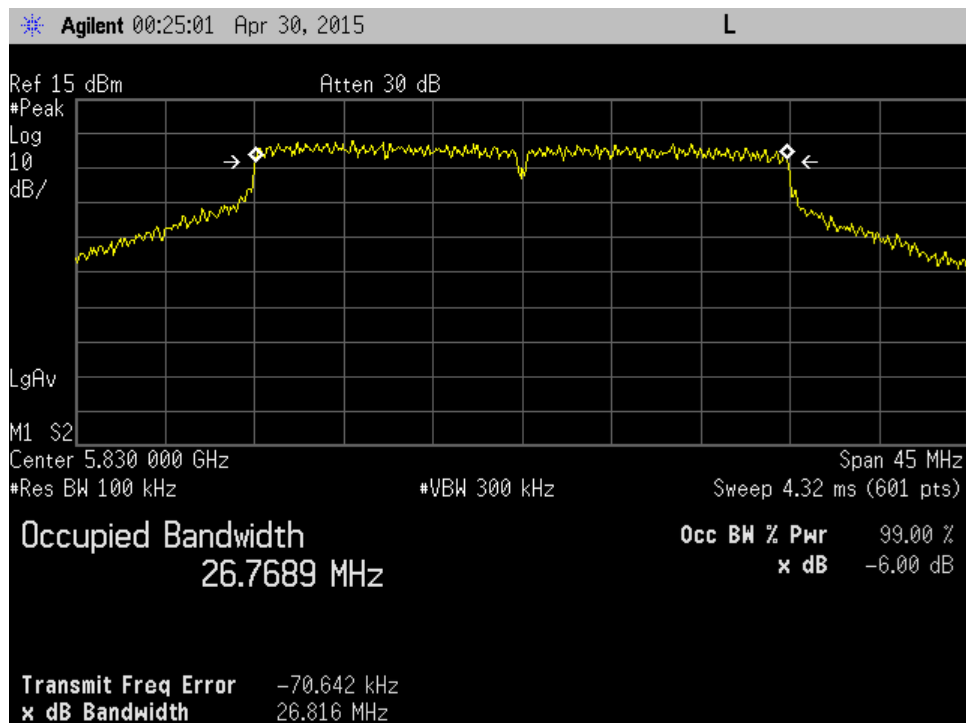




30 MHz high ch_J7

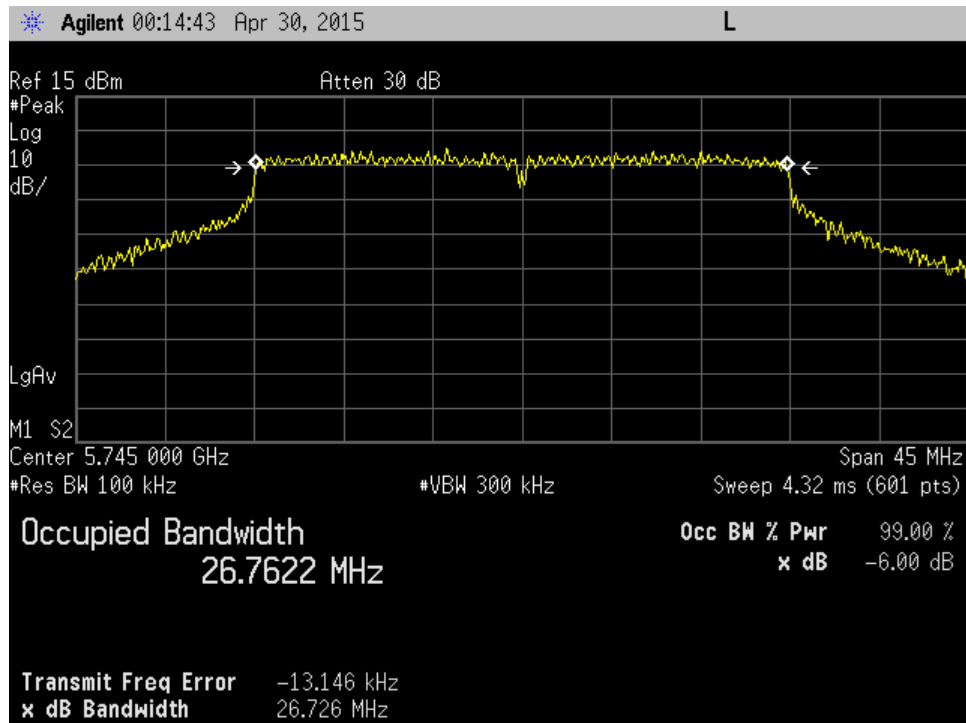


30 MHz high ch_J8

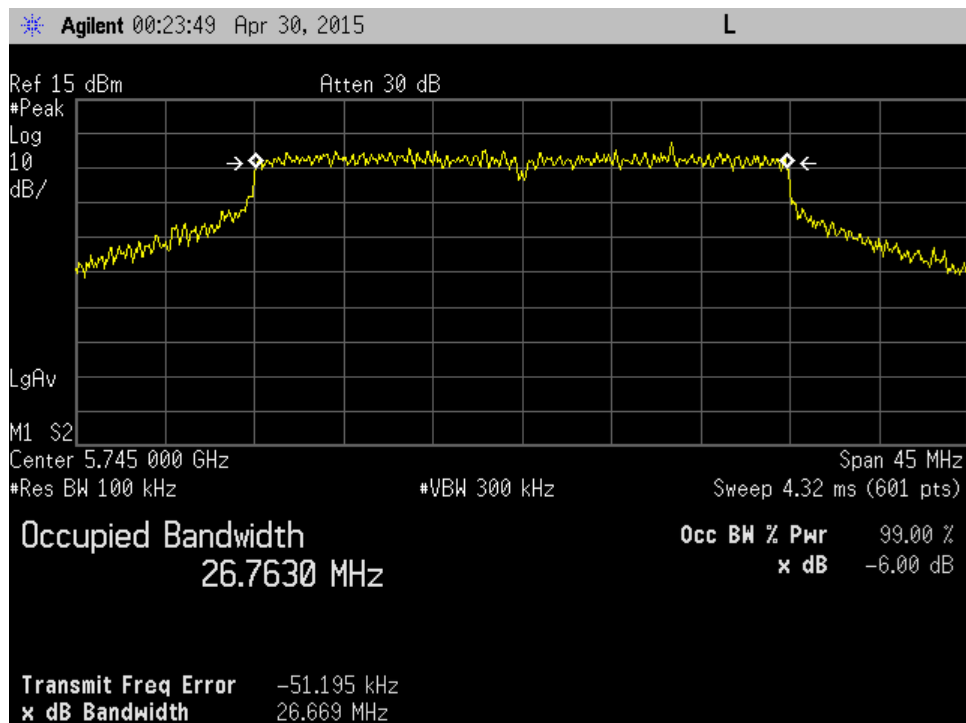




30 MHz low ch_J7

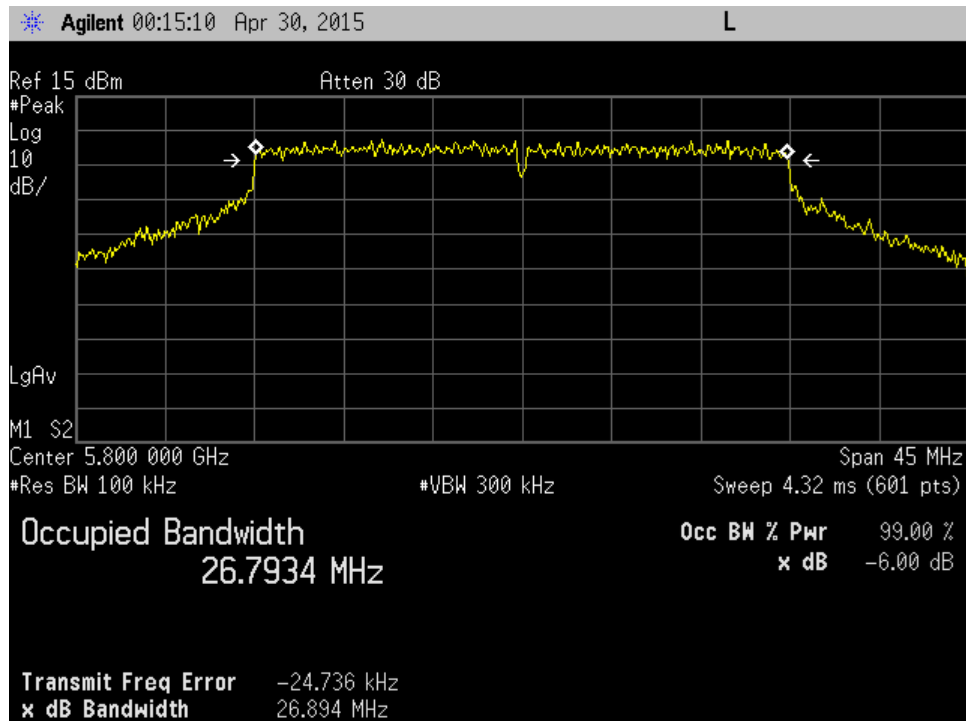


30 MHz low ch_J8

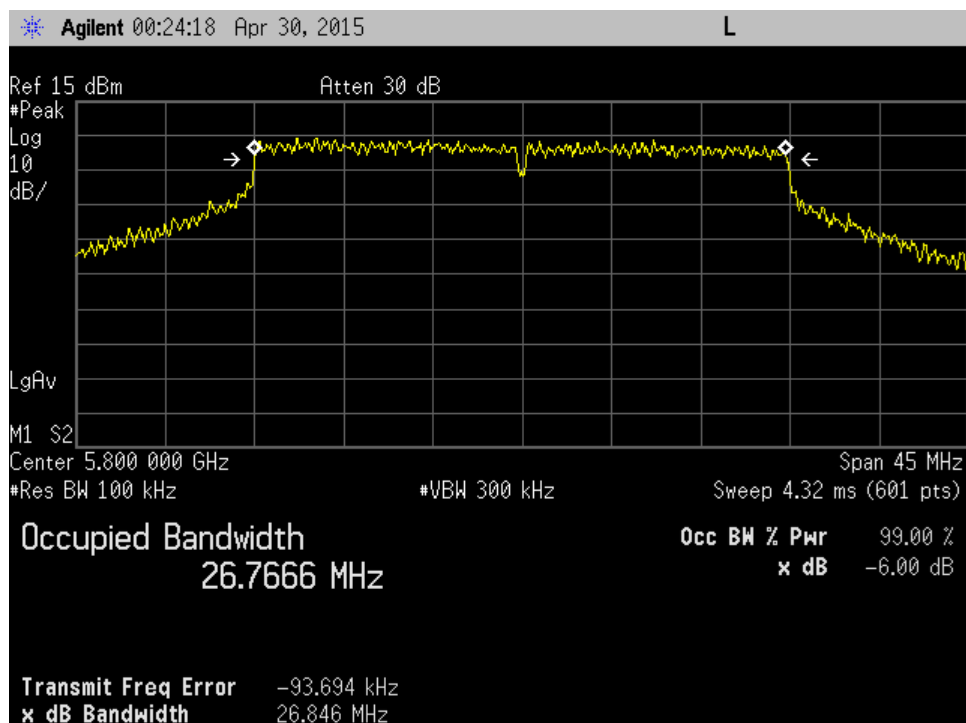




30 MHz mid ch_J7

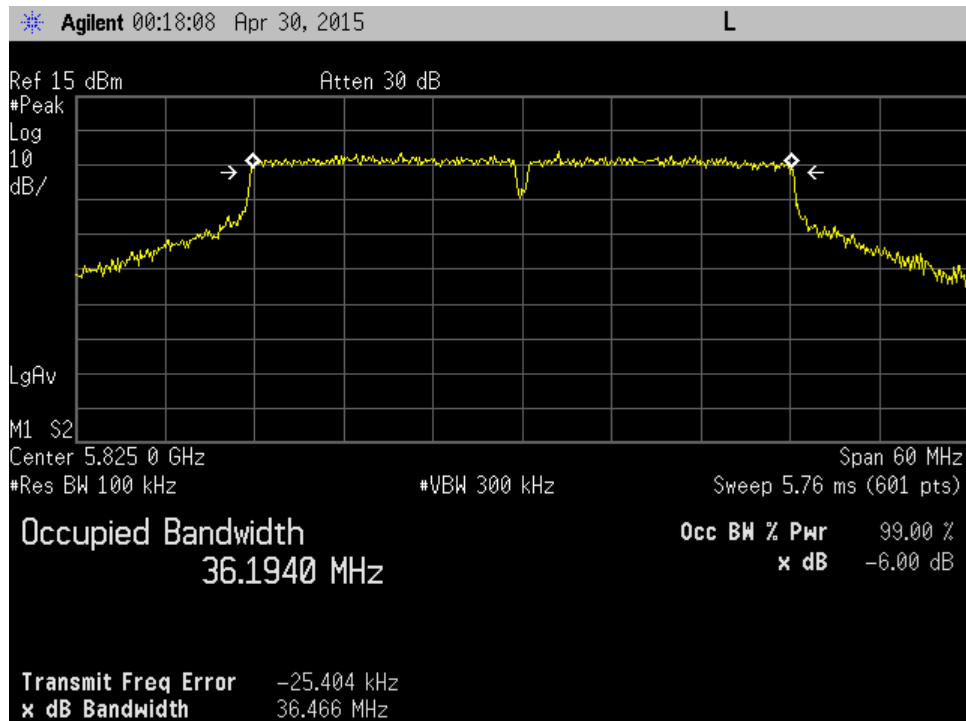


30 MHz mid ch_J8

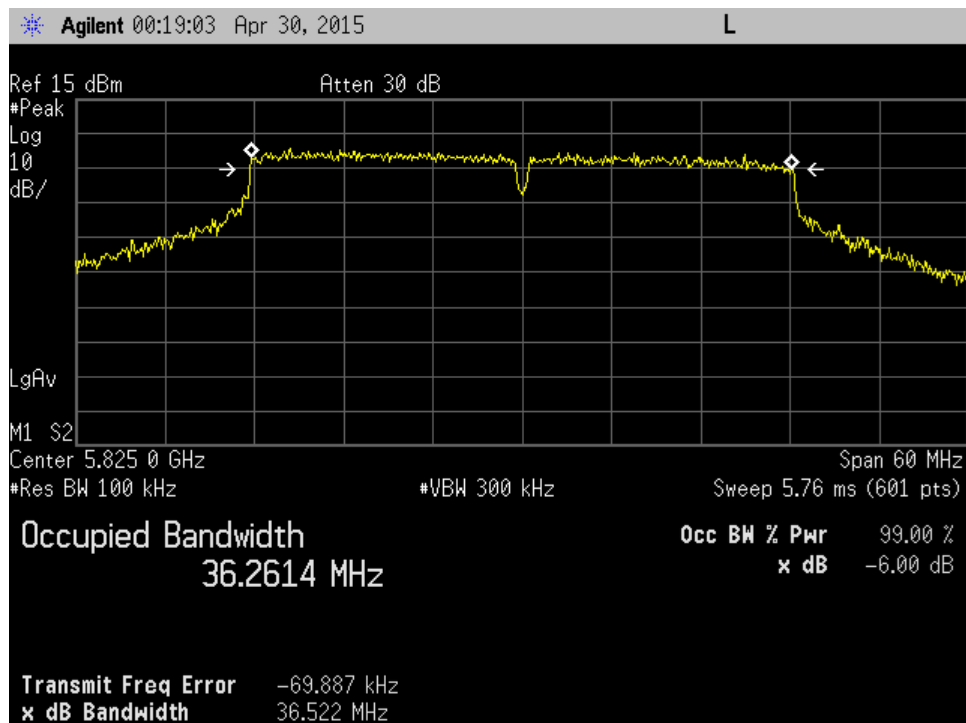




40 MHz high ch_J7

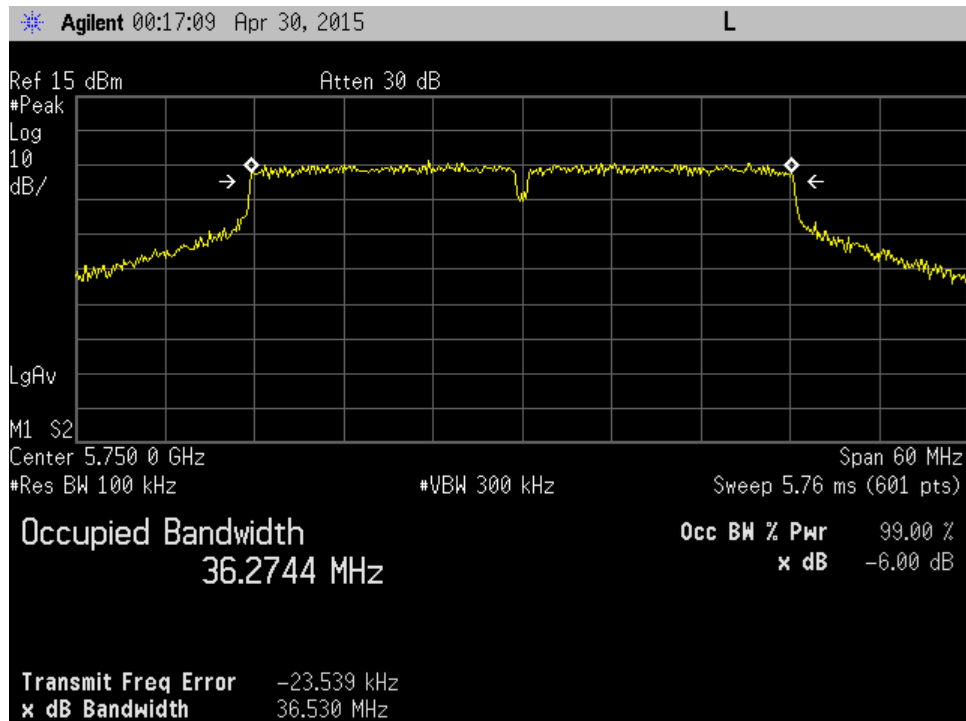


40 MHz high ch_J8

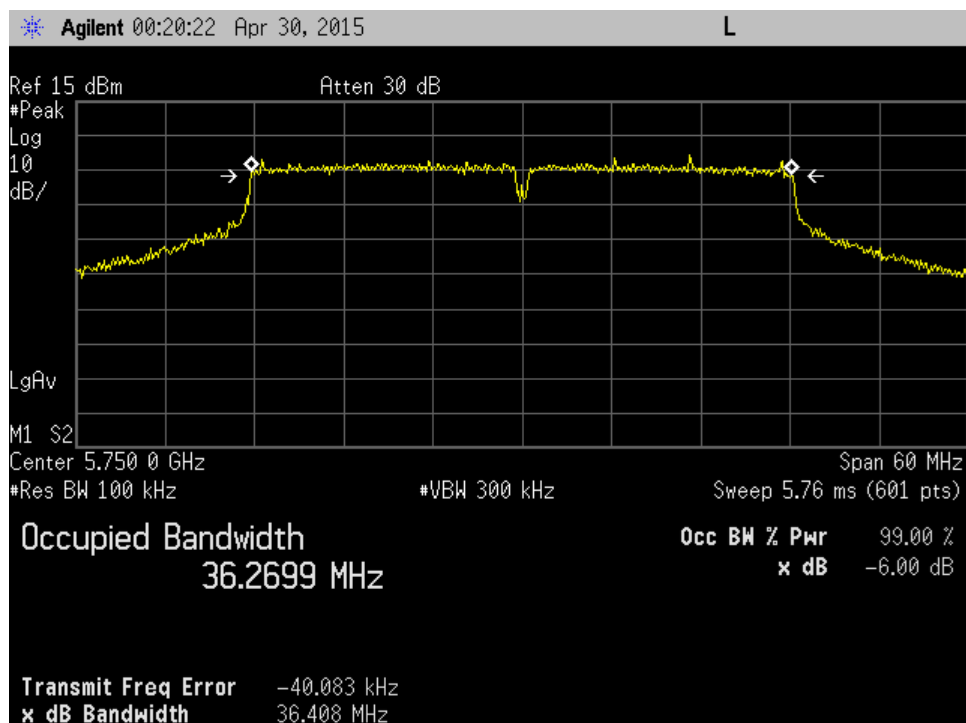




40 MHz low ch_J7

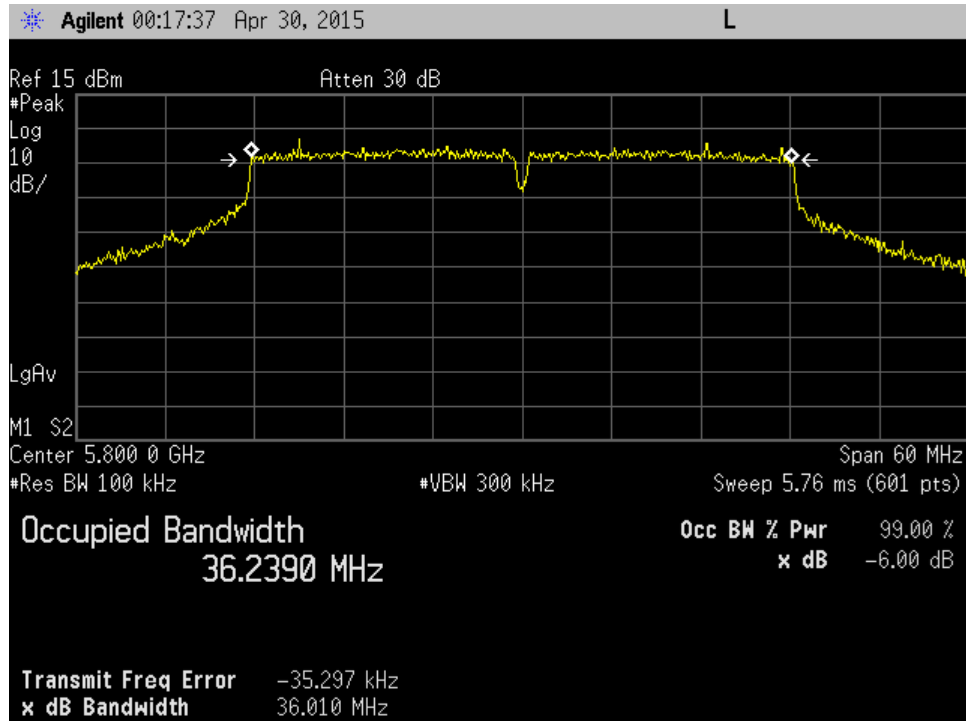


40 MHz low ch_J8





40 MHz mid ch_J7



40 MHz mid ch_J8

