



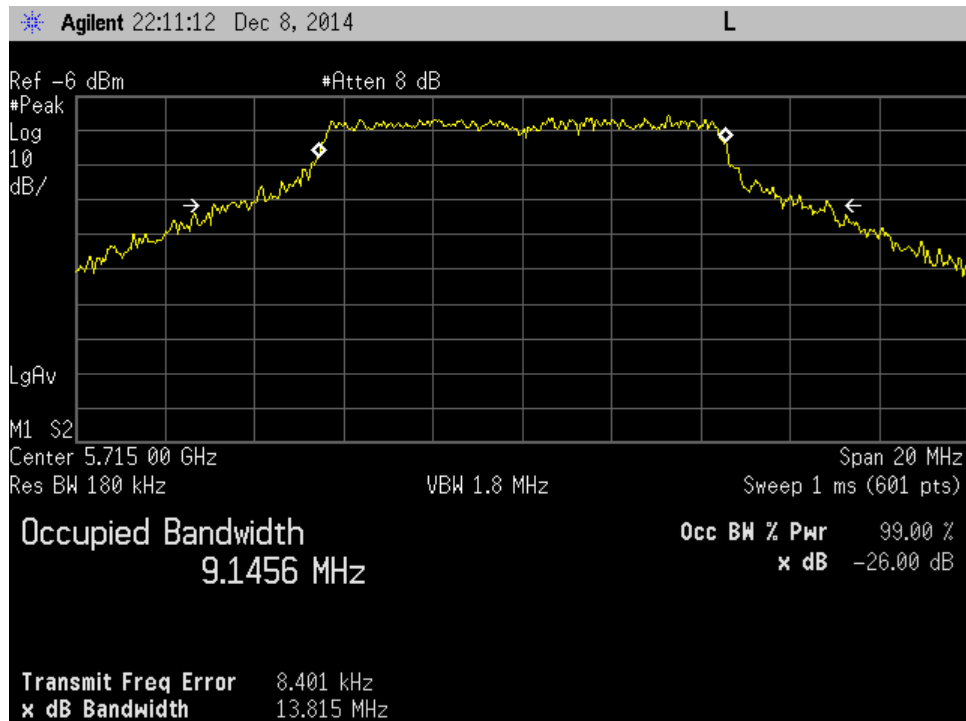
Annex D

Occupied Band Width

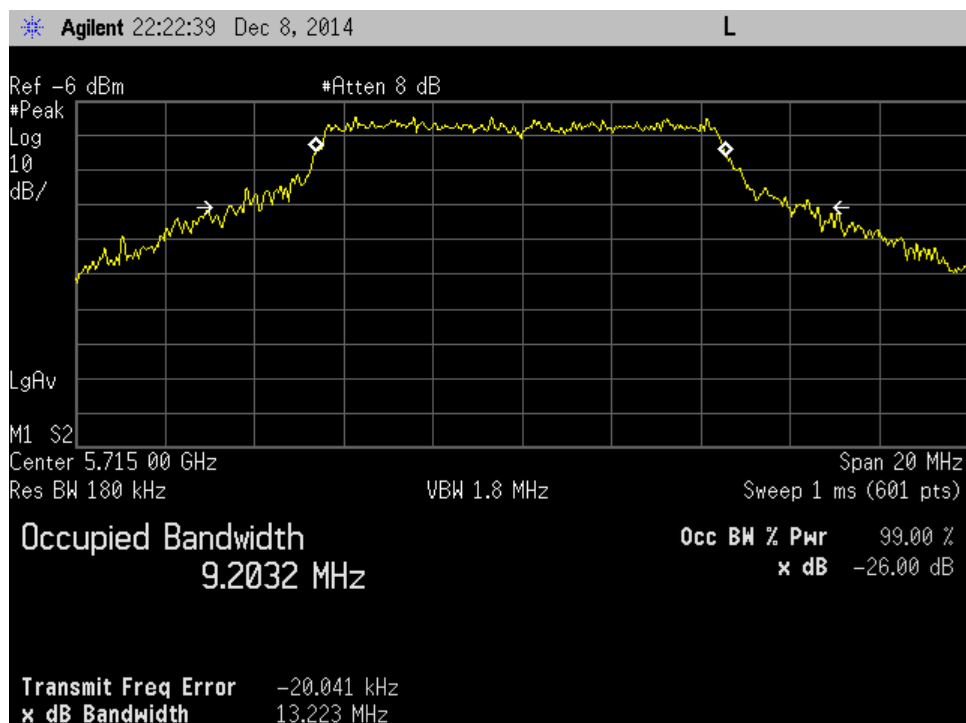
Ports J7 and J8



10MHz high ch J7

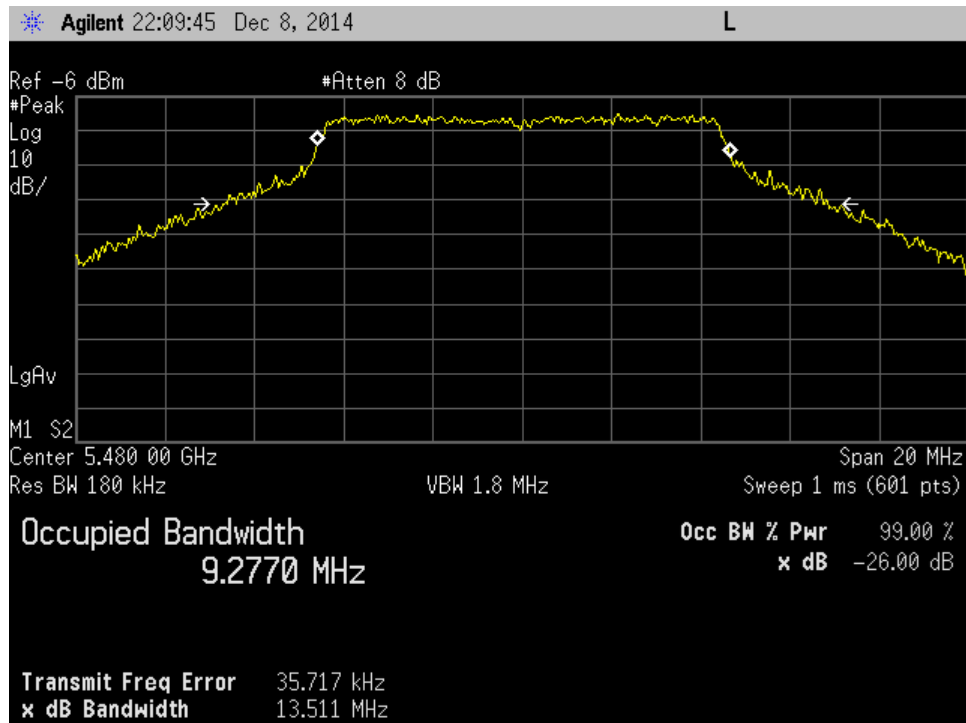


10MHz high ch J8

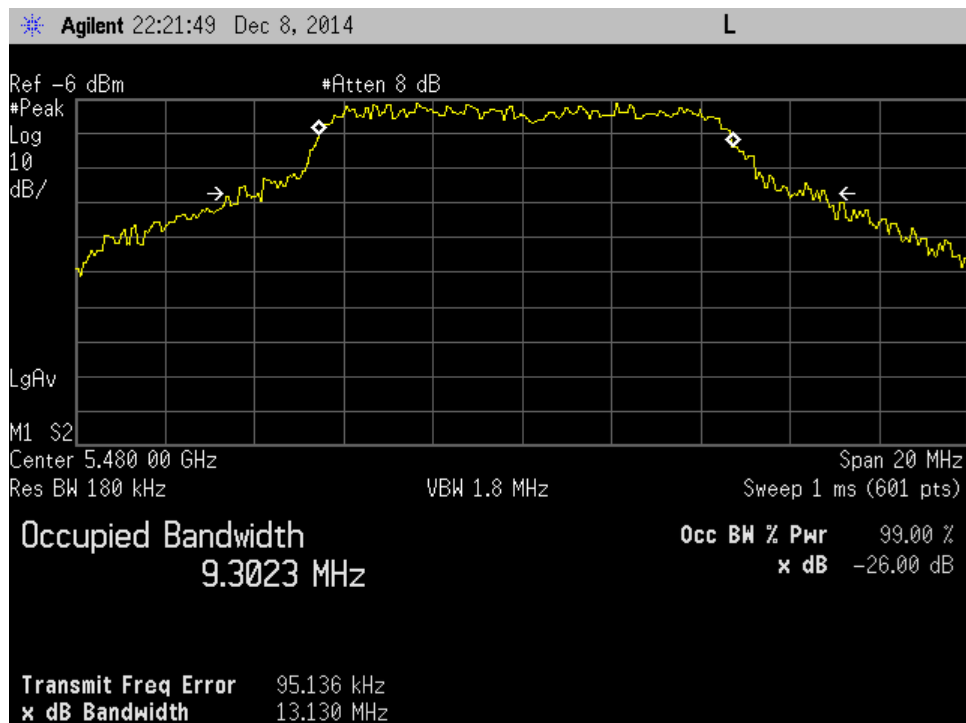




10MHz low ch J7

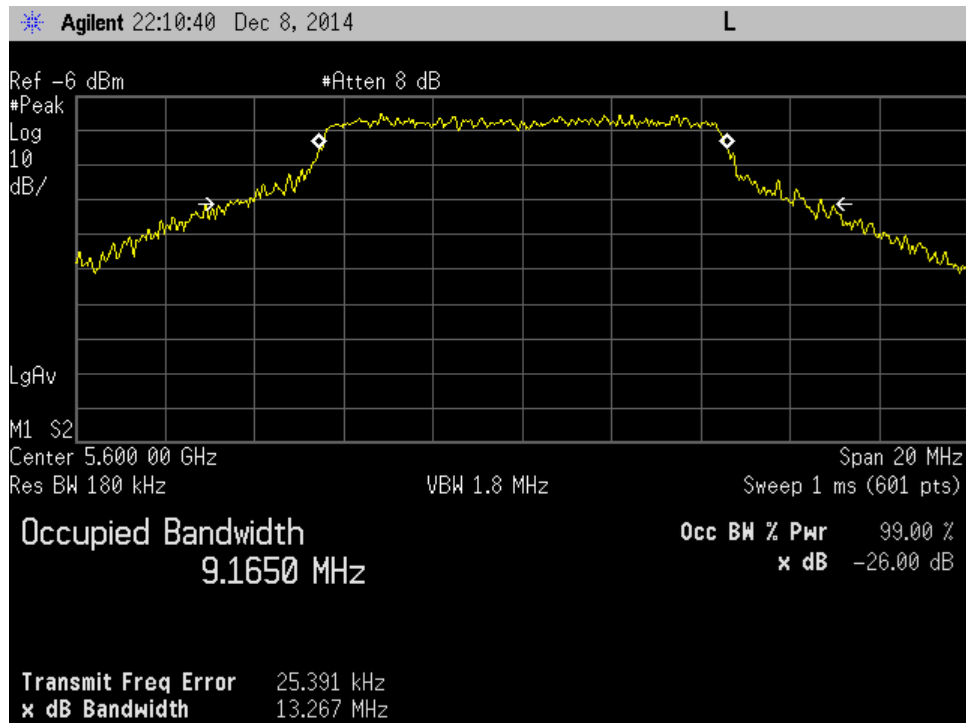


10MHz low ch J8

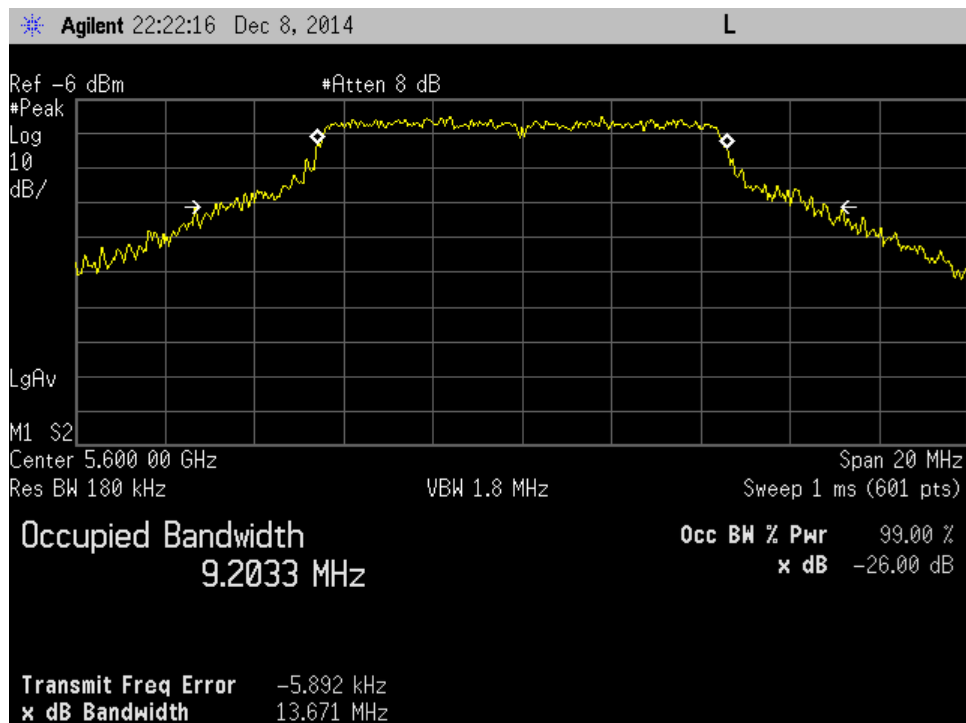




10MHz mid ch J7

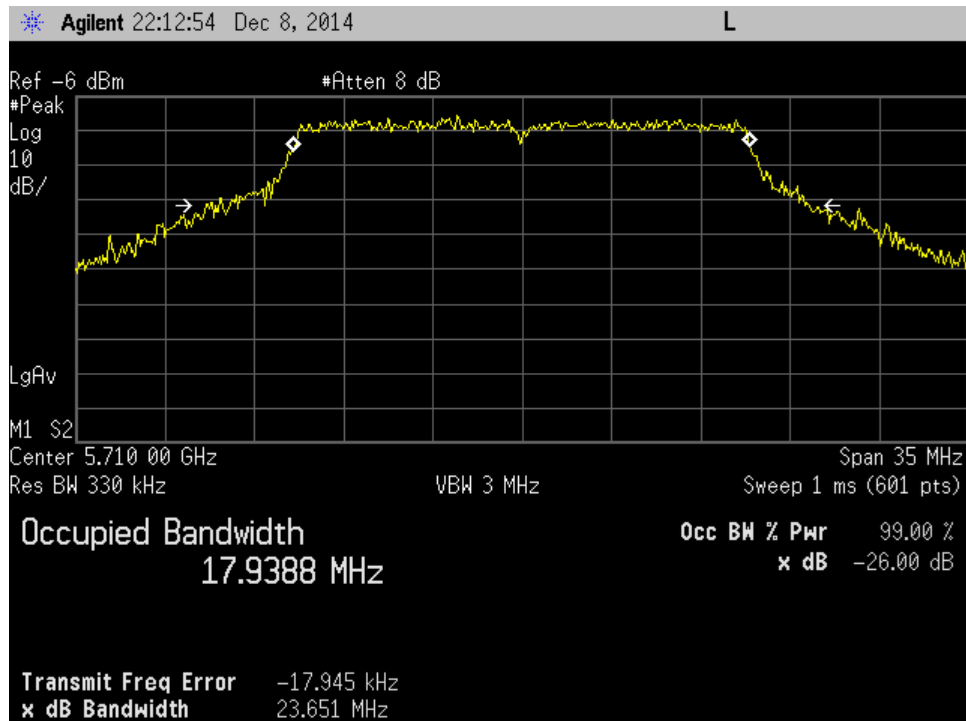


10MHz mid ch J8

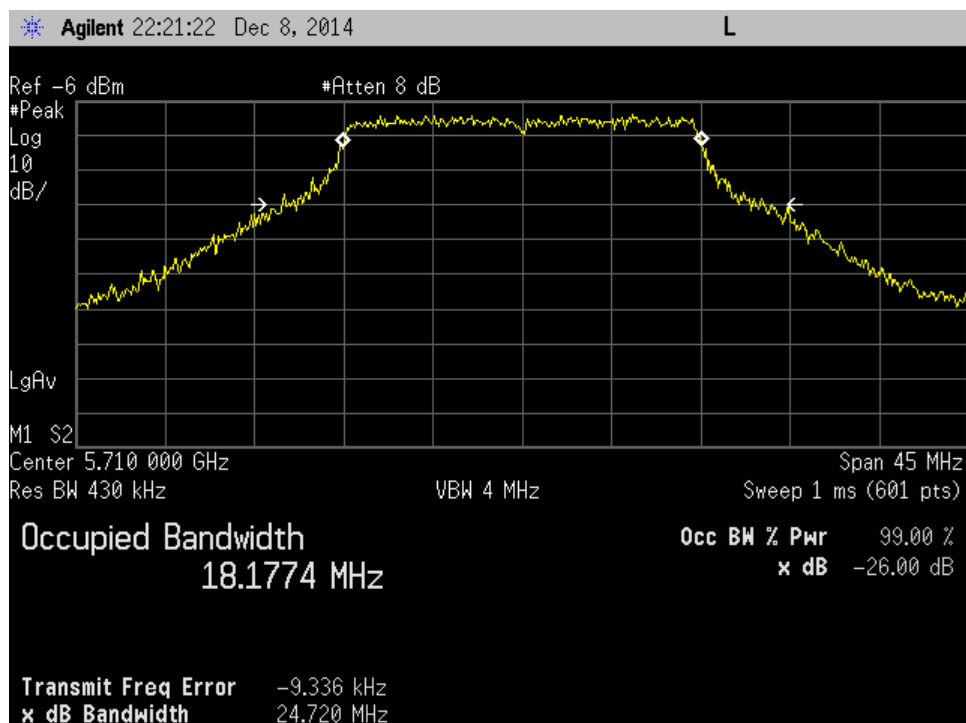




20MHz high ch J7

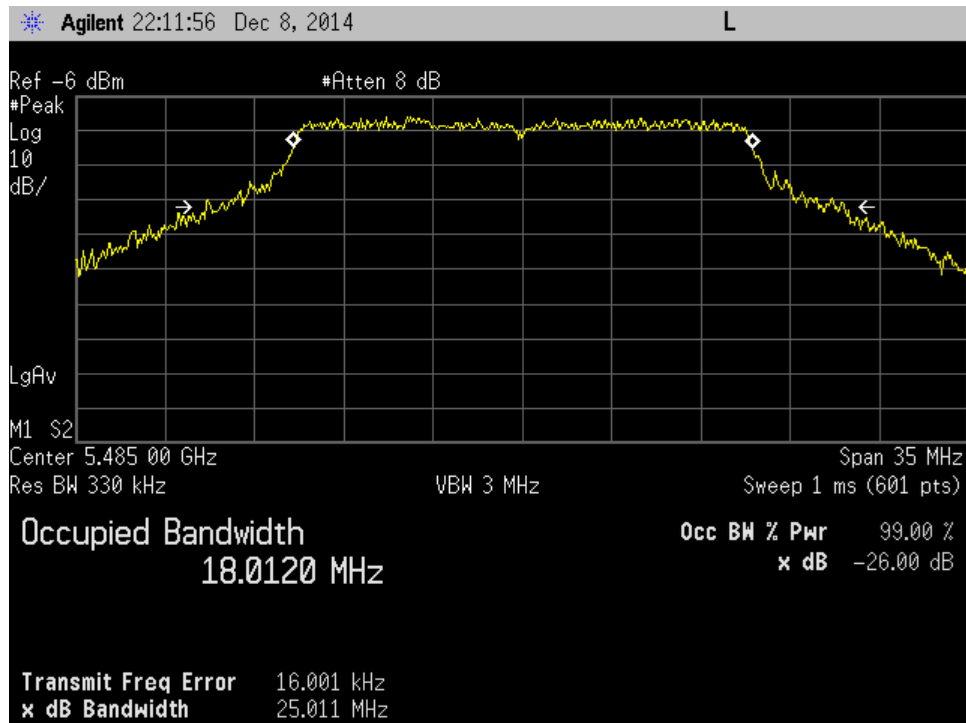


20MHz high ch J8

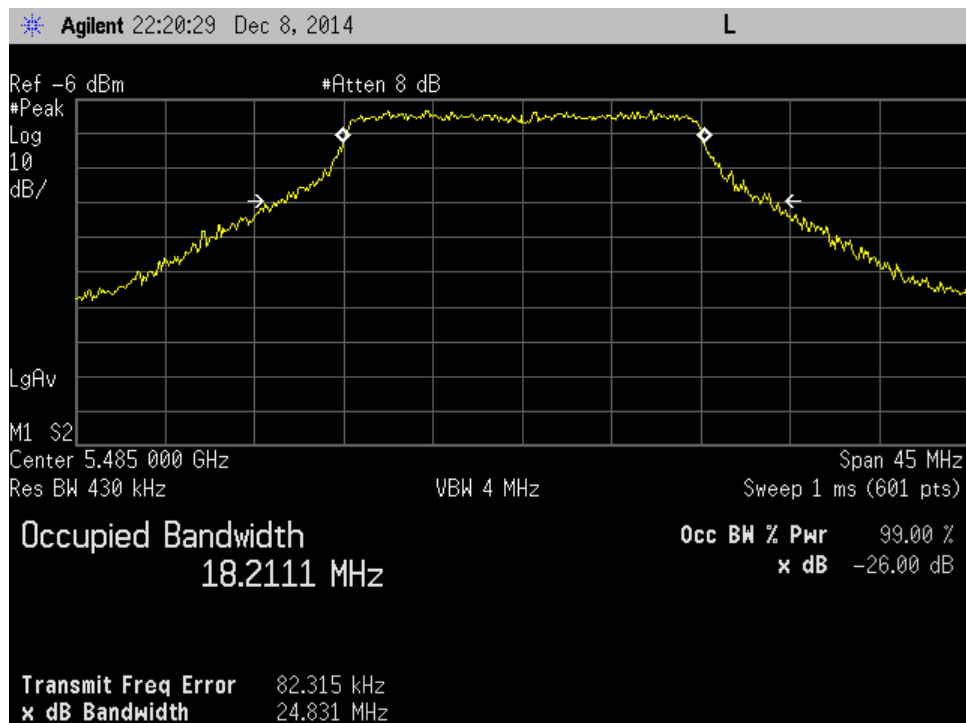




20MHz low ch J7

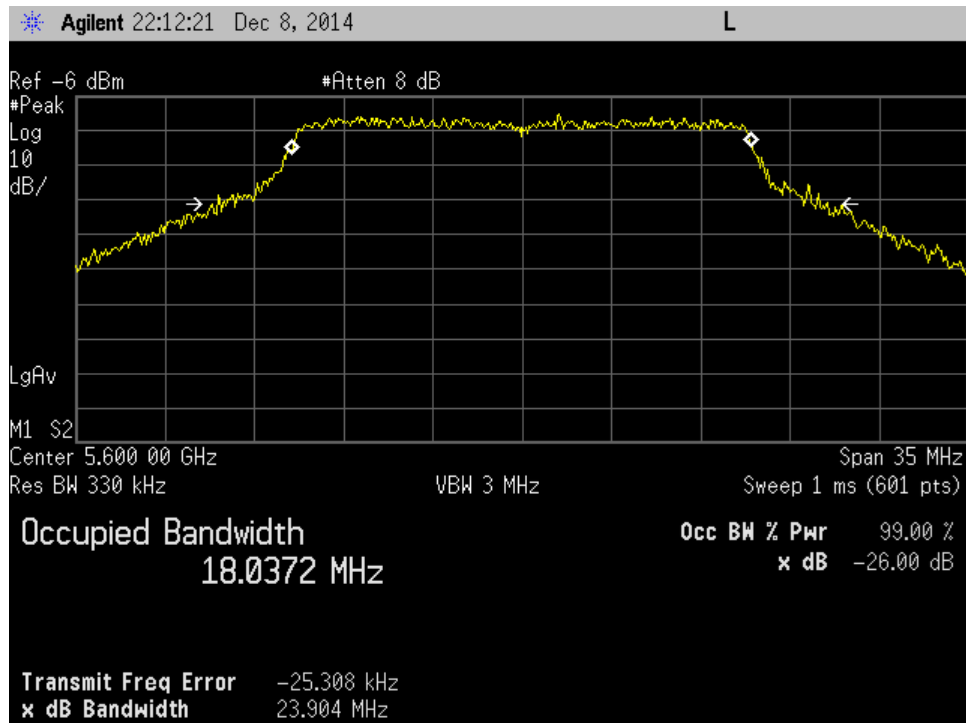


20MHz low ch J8

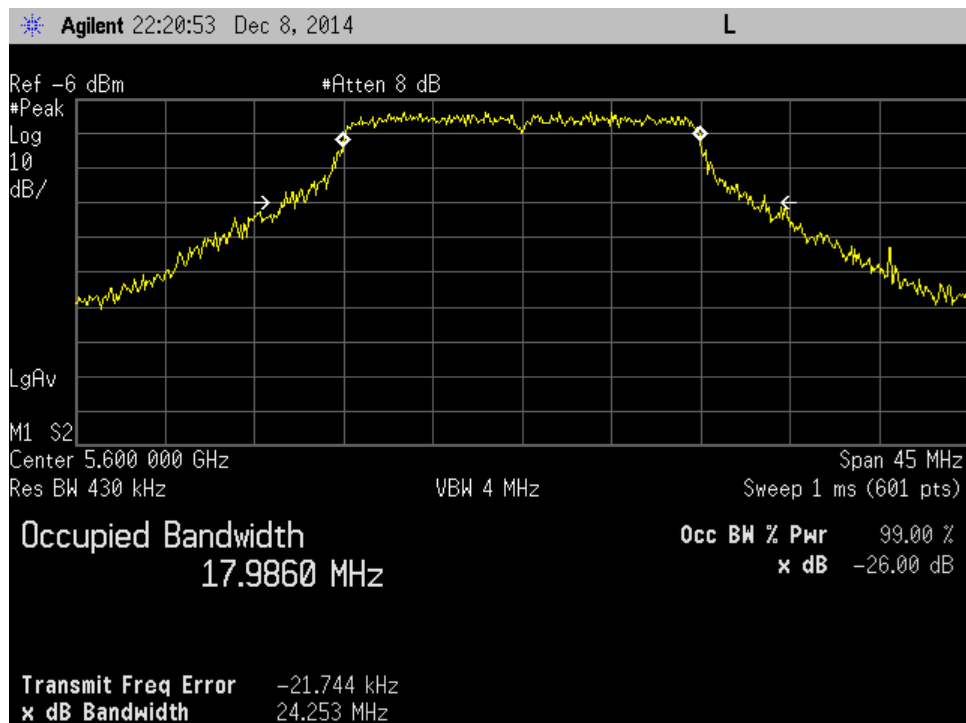




20MHz mid ch J7

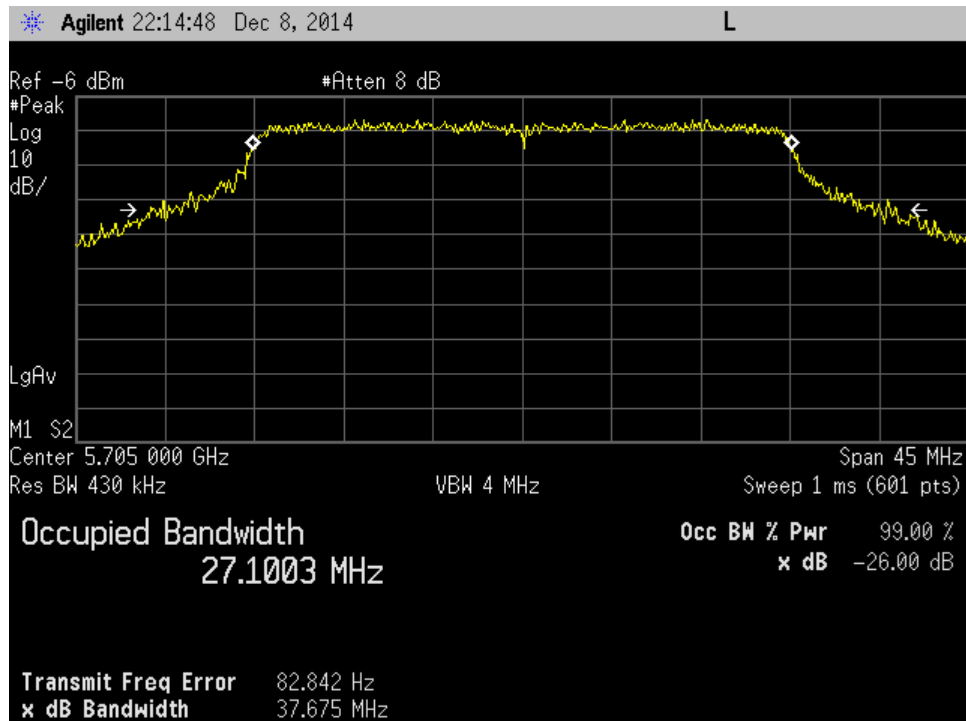


20MHz mid ch J8

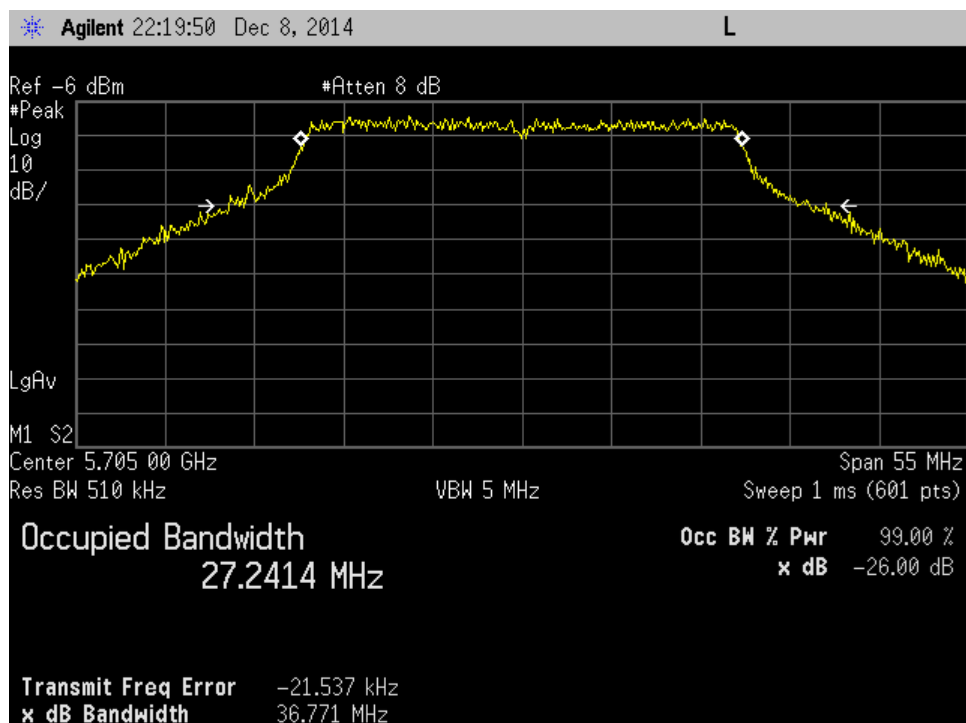




30MHz high ch J7

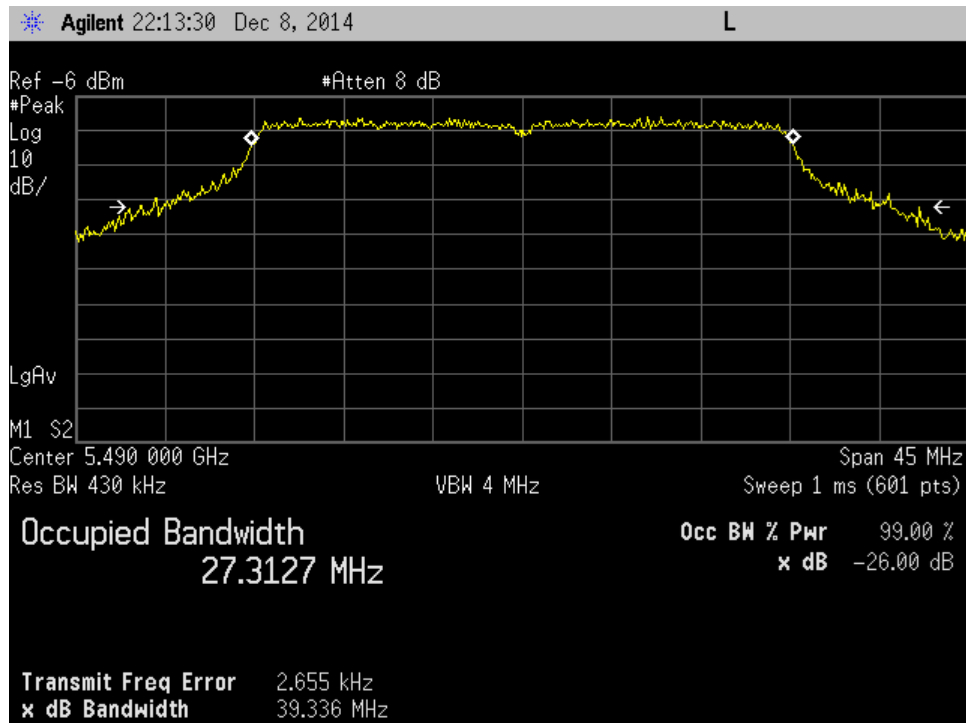


30MHz high ch J8

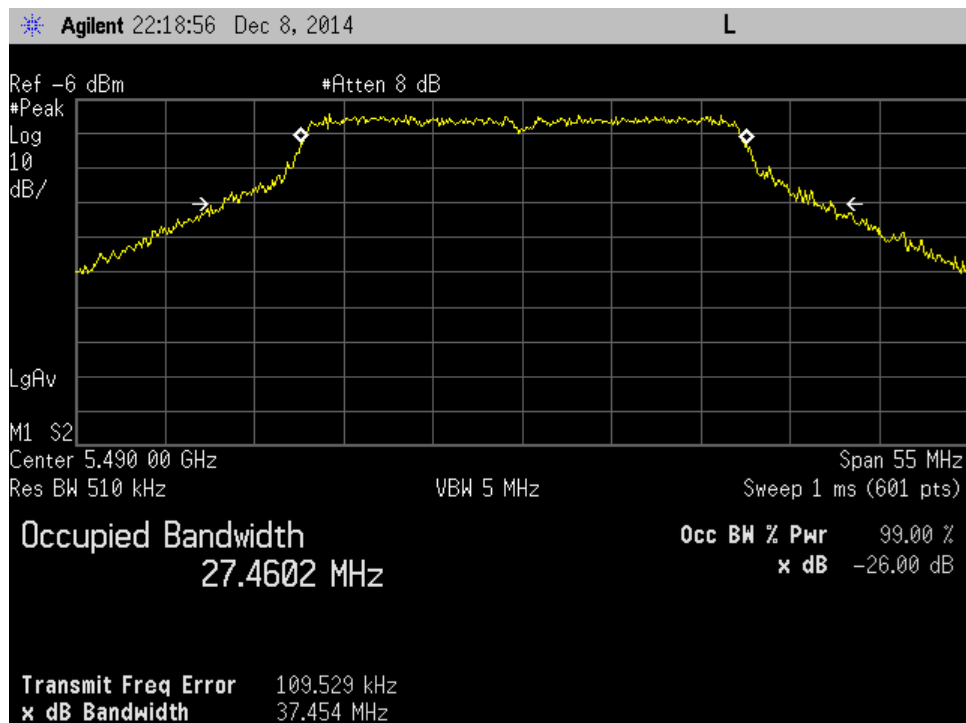




30MHz low ch J7

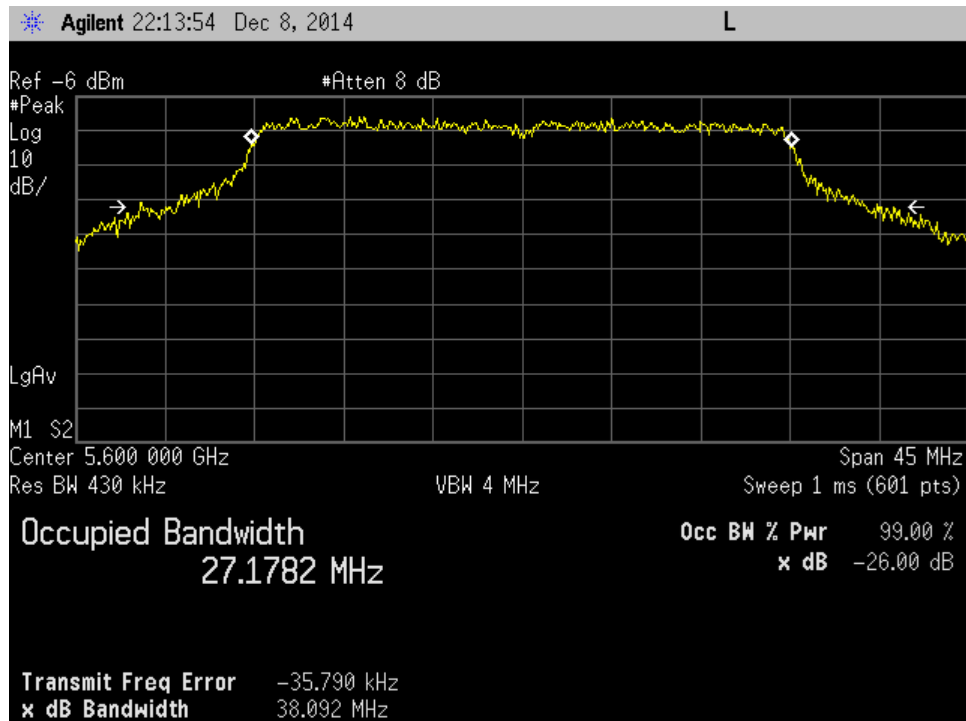


30MHz low ch J8

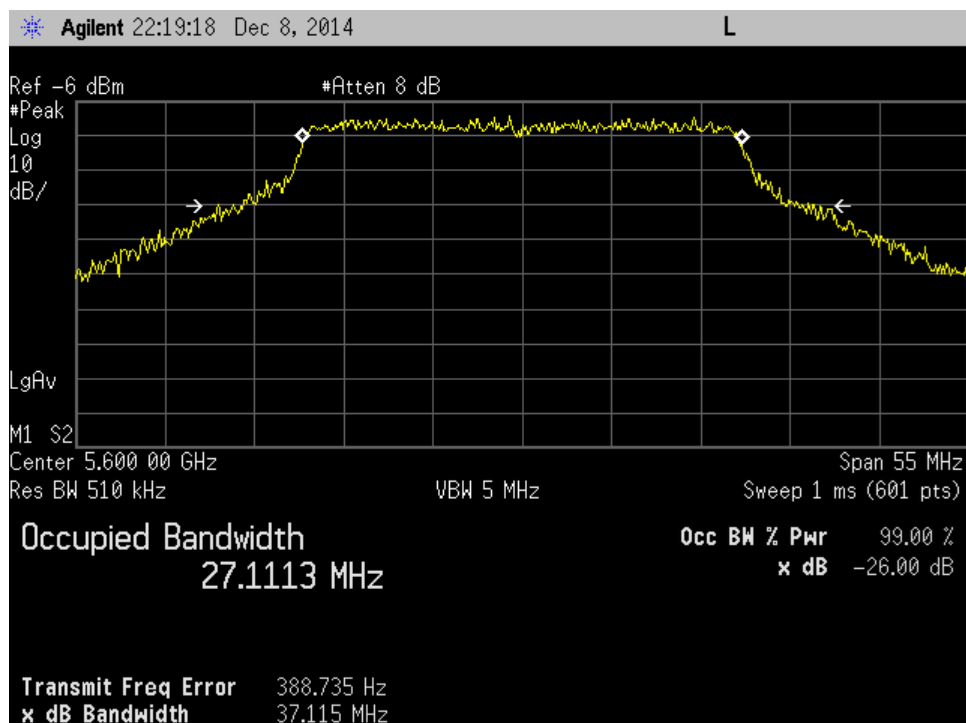




30MHz mid ch J7

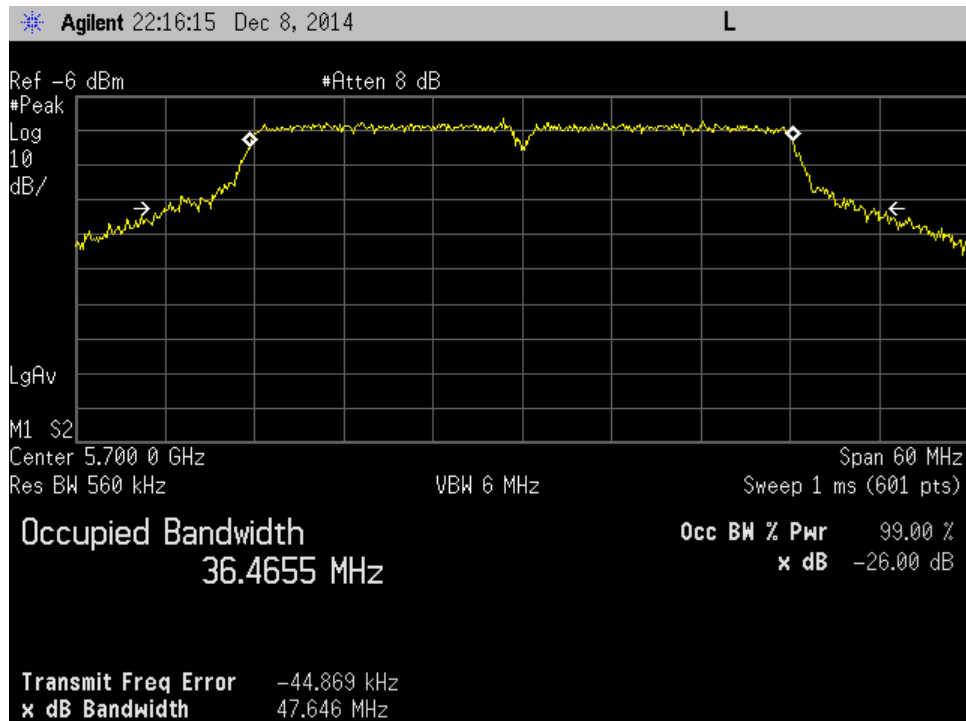


30MHz mid ch J8

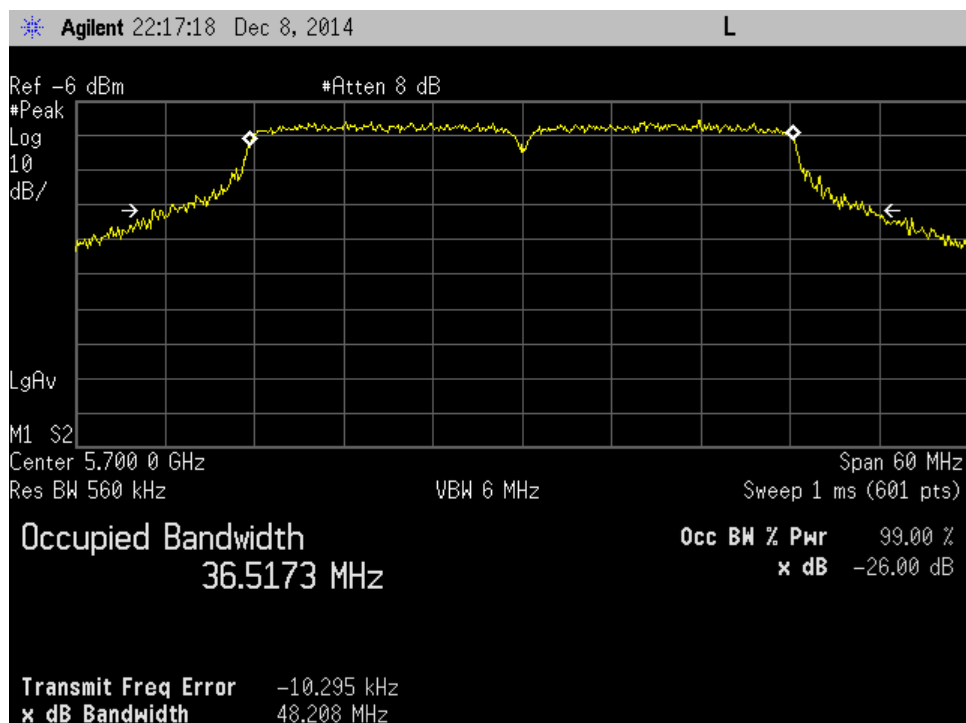




40MHz high ch J7

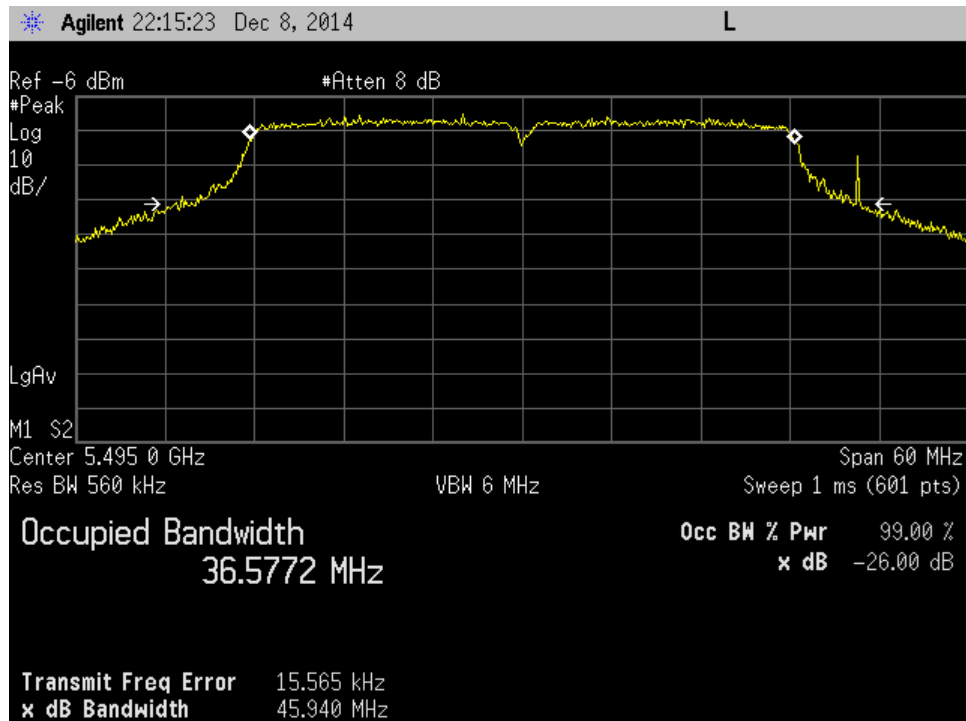


40MHz high ch J8

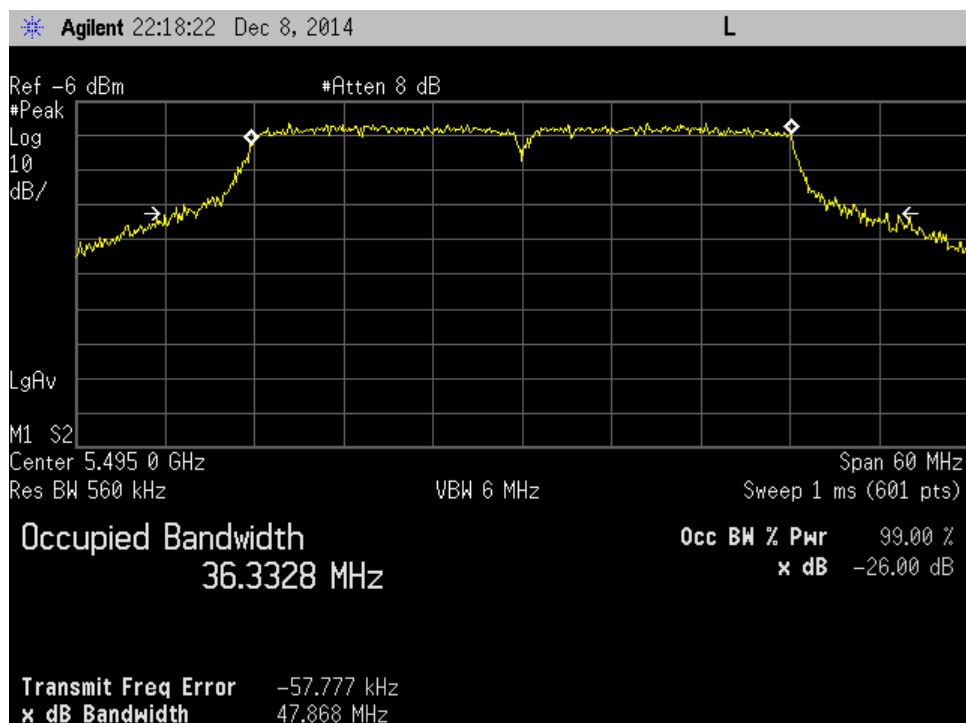




40MHz low ch J7

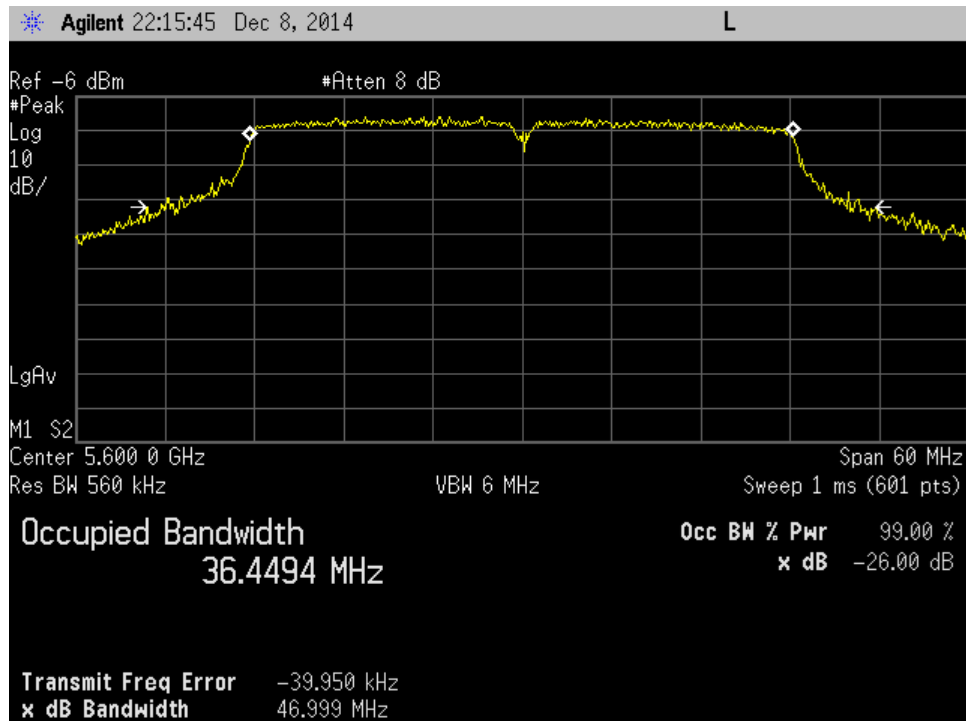


40MHz low ch J8





40MHz mid ch J7



40MHz mid ch J8

