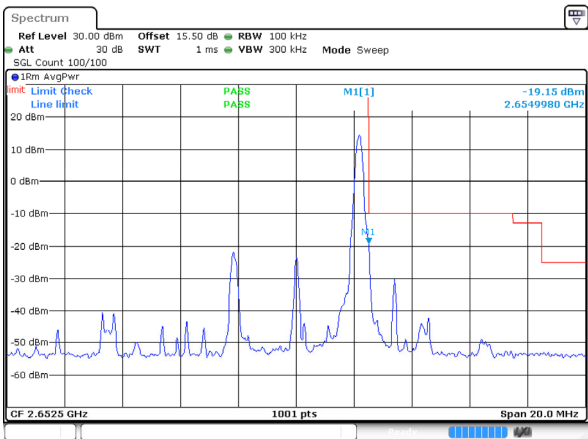
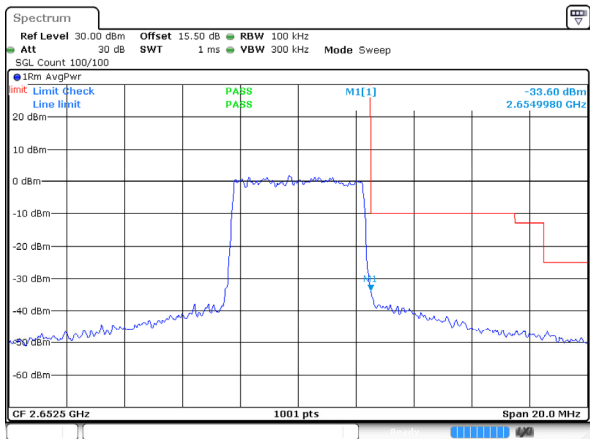


5MHz_High_16QAM_1@24



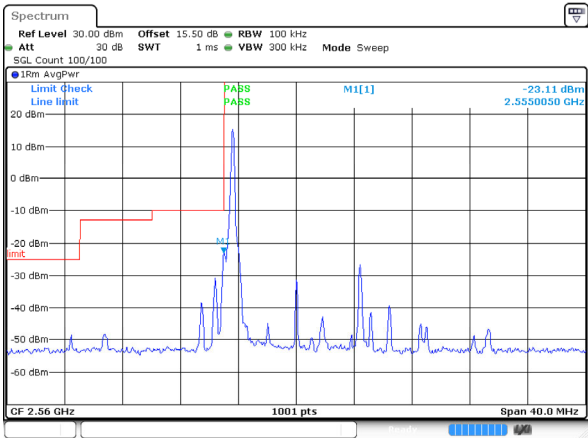
ProjectNo.:RSHA240520001 Tester:Jason Lu
Date: 16 JUL 2024 17:19:09

5MHz_High_16QAM_25@0



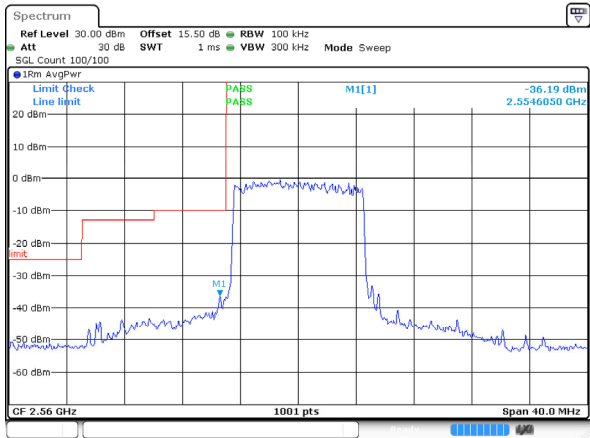
ProjectNo.:RSHA240520001 Tester:Jason Lu
Date: 16 JUL 2024 17:19:01

10MHz_Low_QPSK_1@0



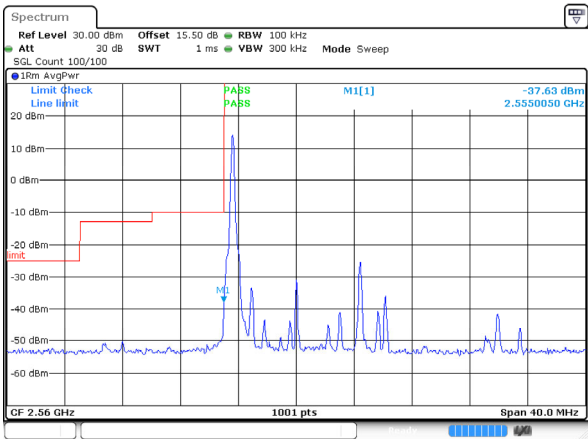
ProjectNo.:RSHA240520001 Tester:Jason Lu
Date: 16 JUL 2024 17:20:02

10MHz_Low_QPSK_50@0



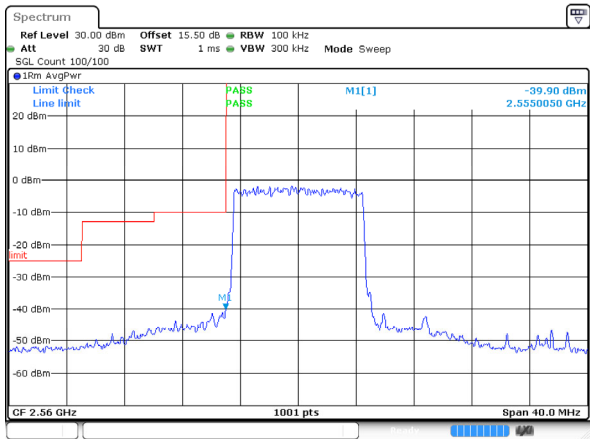
ProjectNo.:RSHA240520001 Tester:Jason Lu
Date: 16 JUL 2024 17:19:54

10MHz_Low_16QAM_1@0



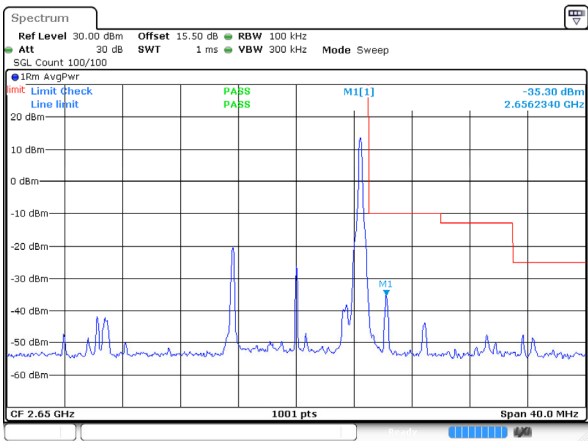
ProjectNo.:RSHA240520001 Tester:Jason Lu
Date: 16 JUL 2024 17:20:17

10MHz_Low_16QAM_50@0



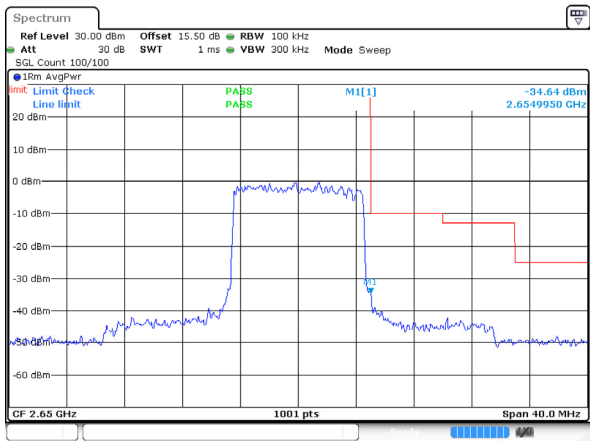
ProjectNo.:RSHA240520001 Tester:Jason Lu
Date: 16 JUL 2024 17:20:09

10MHz_High_QPSK_1@49



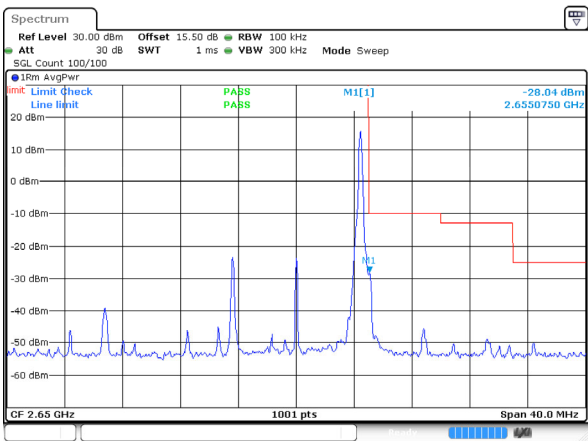
ProjectNo.:RSHA240520001 Tester:Jason Lu
Date: 16 JUL 2024 17:20:43

10MHz_High_QPSK_50@0



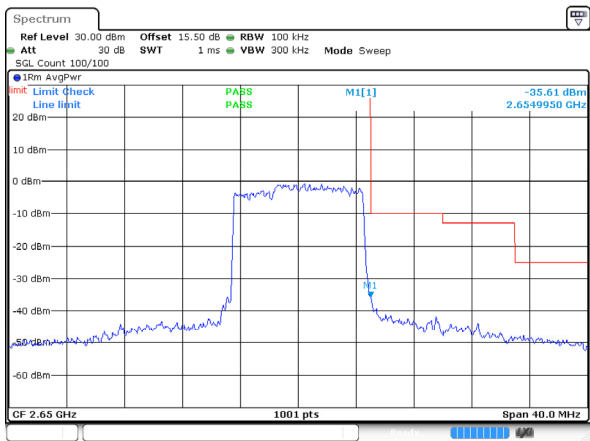
ProjectNo.:RSHA240520001 Tester:Jason Lu
Date: 16 JUL 2024 17:20:36

10MHz_High_16QAM_1@49



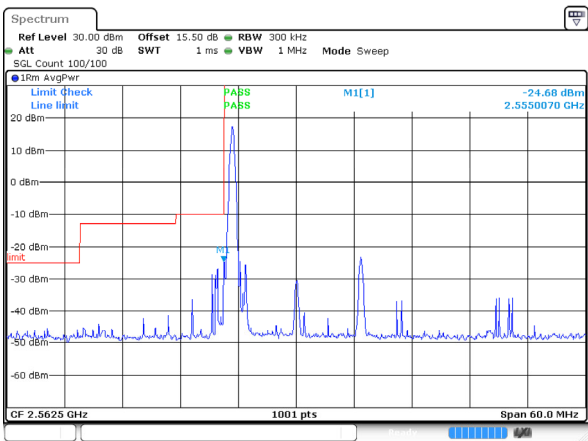
ProjectNo.:RSHA240520001 Tester:Jason Lu
Date: 16 JUL 2024 17:20:59

10MHz_High_16QAM_50@0



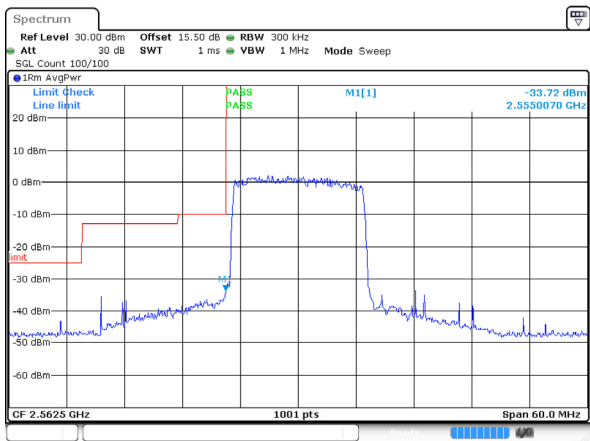
ProjectNo.:RSHA240520001 Tester:Jason Lu
Date: 16 JUL 2024 17:20:51

15MHz_Low_QPSK_1@0



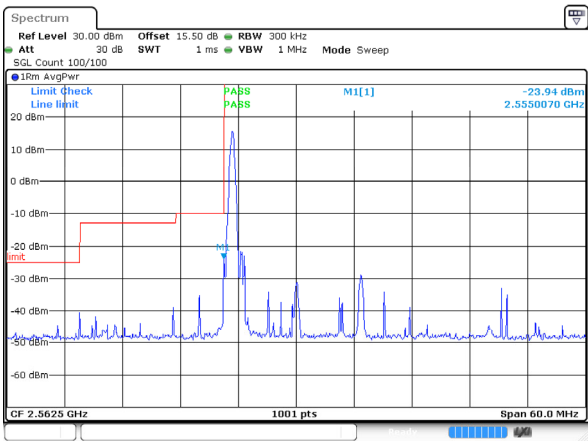
ProjectNo.:RSHA240520001 Tester:Jason Lu
Date: 16 JUL 2024 17:21:49

15MHz_Low_QPSK_75@0



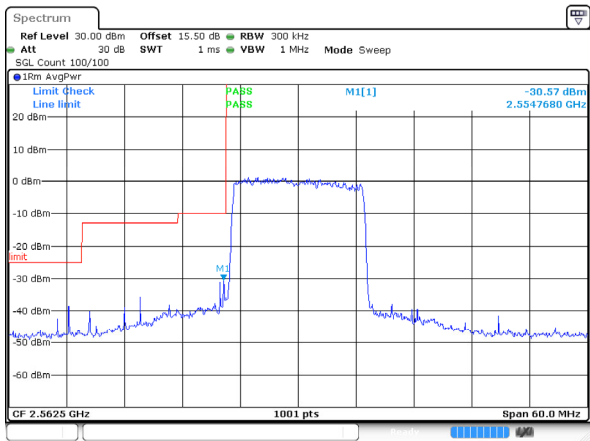
ProjectNo.:RSHA240520001 Tester:Jason Lu
Date: 16 JUL 2024 17:21:43

15MHz_Low_16QAM_1@0



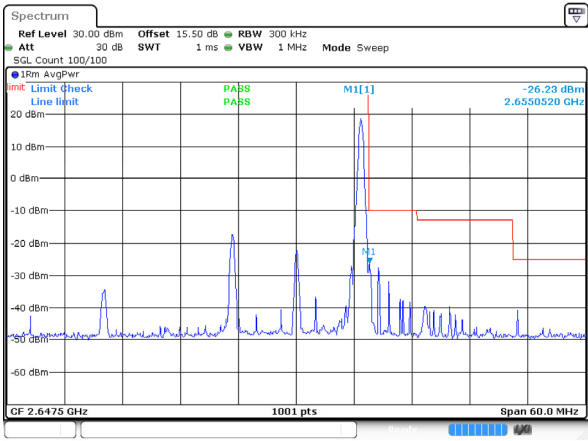
ProjectNo.:RSHA240520001 Tester:Jason Lu
Date: 16 JUL 2024 17:22:01

15MHz_Low_16QAM_75@0



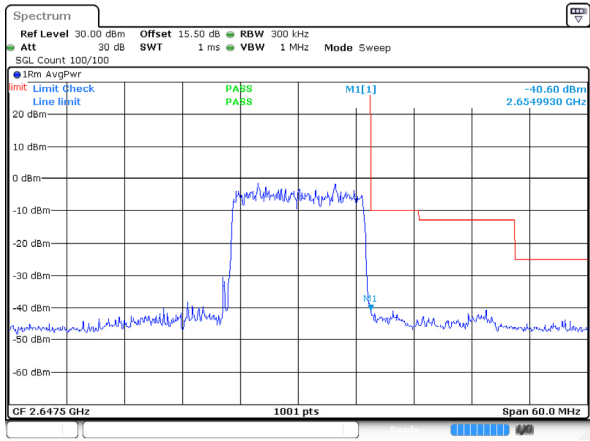
ProjectNo.:RSHA240520001 Tester:Jason Lu
Date: 16 JUL 2024 17:21:55

15MHz_High_QPSK_1@74



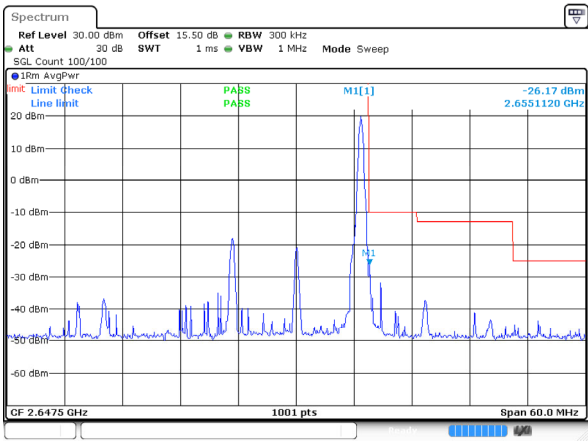
ProjectNo.:RSHA240520001 Tester:Jason Lu
Date: 16 JUL 2024 17:22:24

15MHz_High_QPSK_75@0



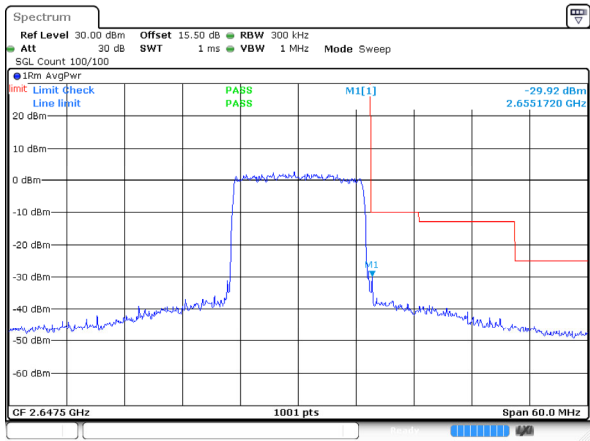
ProjectNo.:RSHA240520001 Tester:Jason Lu
Date: 16 JUL 2024 17:22:18

15MHz_High_16QAM_1@74



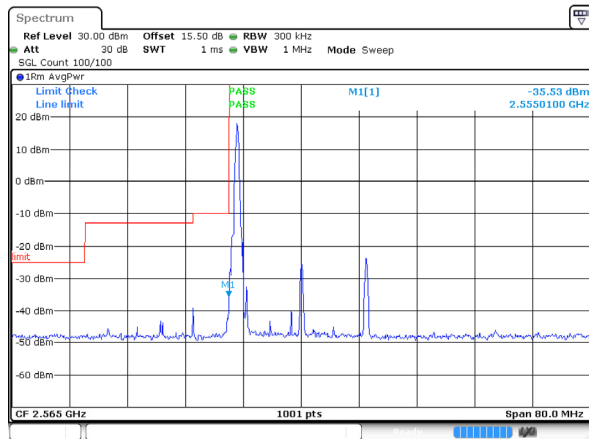
ProjectNo.:RSHA240520001 Tester:Jason Lu
Date: 16 JUL 2024 17:22:37

15MHz_High_16QAM_75@0



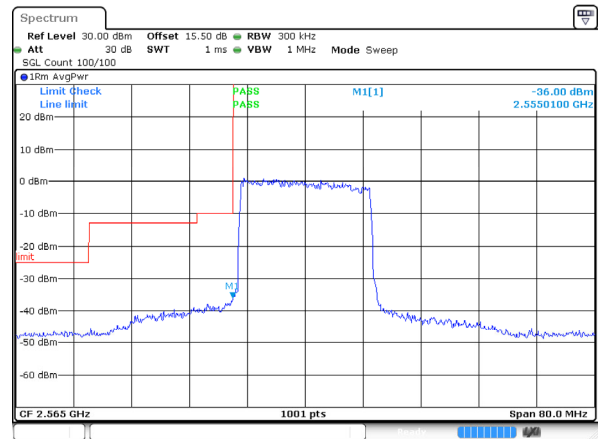
ProjectNo.:RSHA240520001 Tester:Jason Lu
Date: 16 JUL 2024 17:22:31

20MHz_Low_QPSK_1@0



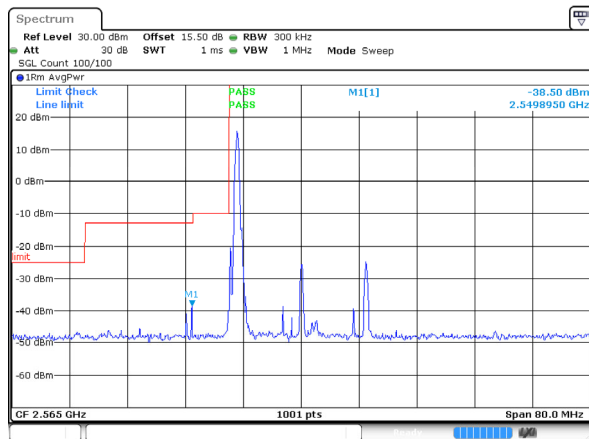
ProjectNo.:RSHA240520001 Tester:Jason Lu
Date: 16 JUL 2024 17:23:30

20MHz_Low_QPSK_100@0



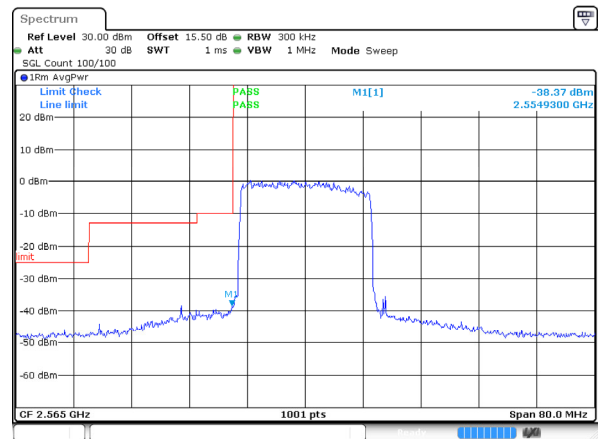
ProjectNo.:RSHA240520001 Tester:Jason Lu
Date: 16 JUL 2024 17:23:24

20MHz_Low_16QAM_1@0



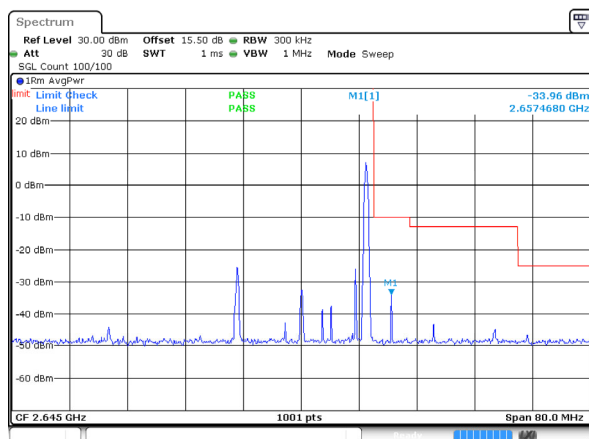
ProjectNo.:RSHA240520001 Tester:Jason Lu
Date: 16 JUL 2024 17:23:43

20MHz_Low_16QAM_100@0



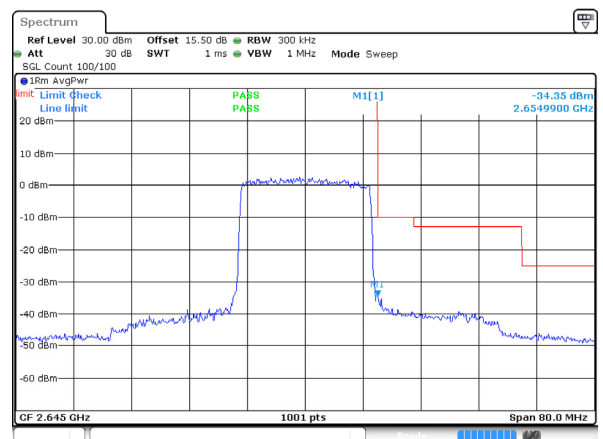
ProjectNo.:RSHA240520001 Tester:Jason Lu
Date: 16 JUL 2024 17:23:38

20MHz_High_QPSK_1@99



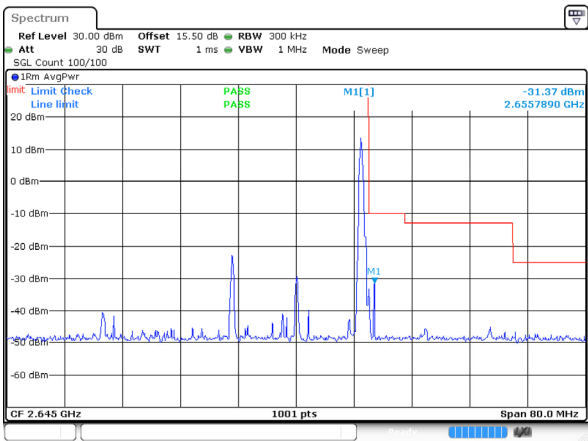
ProjectNo.:RSHA240520001 Tester:Jason Lu
Date: 16 JUL 2024 17:24:06

20MHz_High_QPSK_100@0



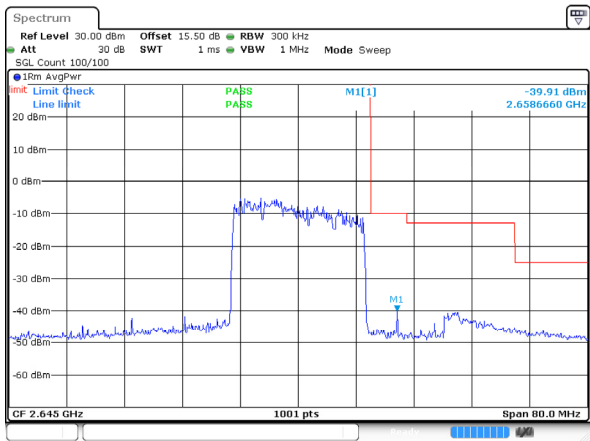
ProjectNo.:RSHA240520001 Tester:Jason Lu
Date: 16 JUL 2024 17:23:59

20MHz_High_16QAM_1@99



ProjectNo.:RSHA240520001 Tester:Jason Lu
Date: 16 JUL 2024 17:24:19

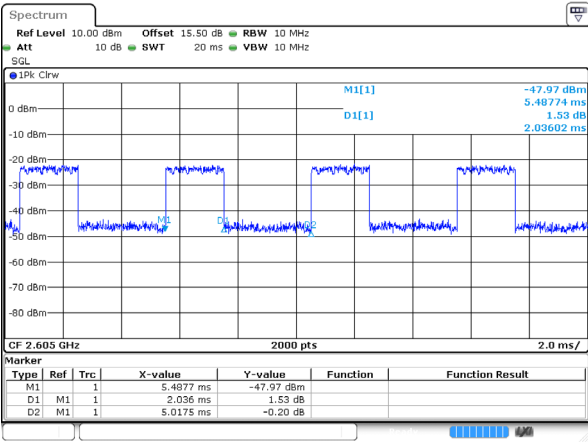
20MHz_High_16QAM_100@0



ProjectNo.:RSHA240520001 Tester:Jason Lu
Date: 16 JUL 2024 17:24:12

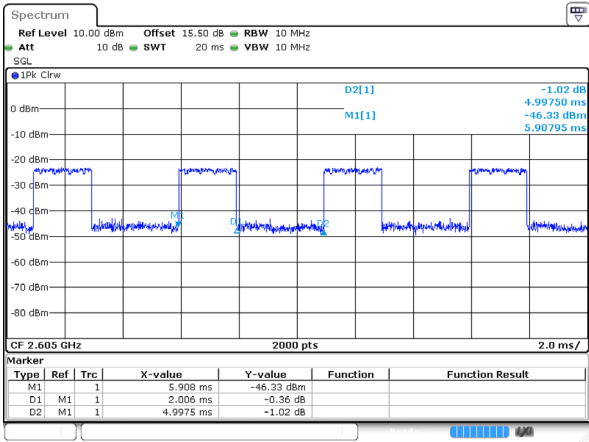
Band 41 Duty cycle

5MHz_16QAM



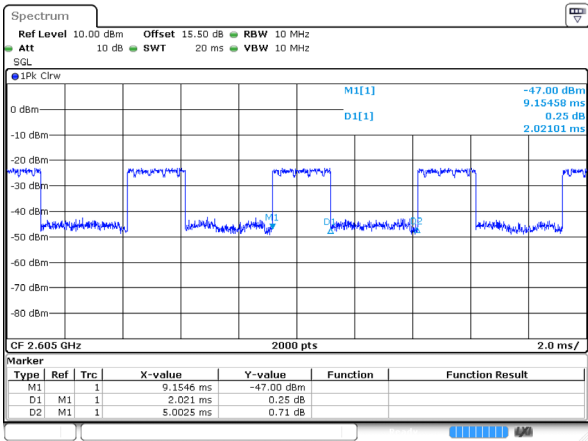
ProjectNo.:RSHA240520001 Tester:Jason Lu
Date: 24.SEP.2024 10:19:37

5MHz_QPSK



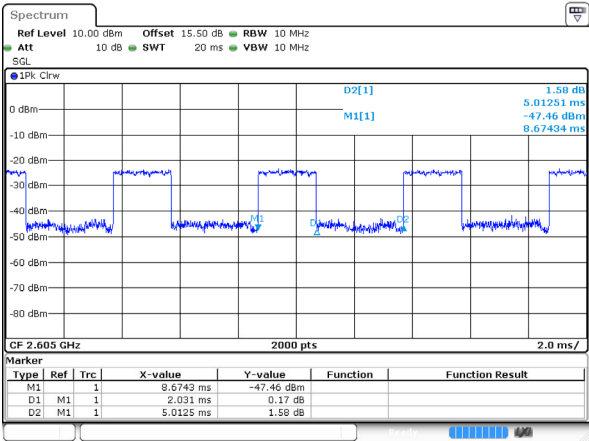
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Date: 24.SEP.2024 10:17:59

10MHz_16QAM



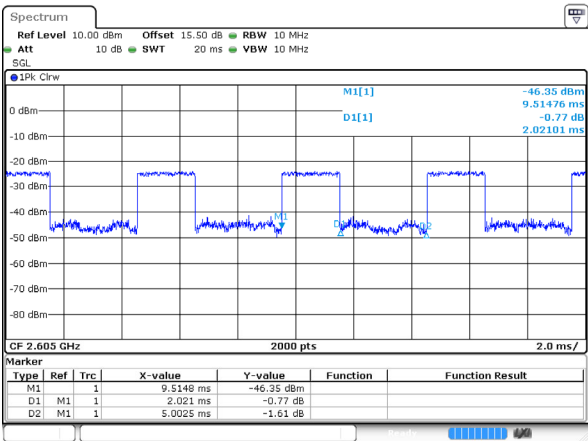
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Date: 24.SEP.2024 10:23:05

10MHz_QPSK



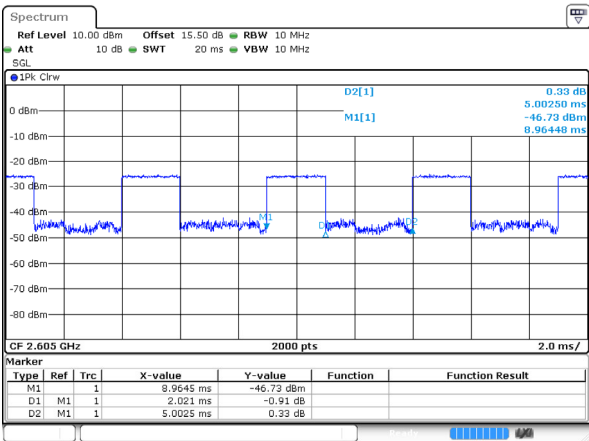
ProjectNo.:RSHA240520001 Tester:Jason Lu
Date: 24.SEP.2024 10:22:02

15MHz_16QAM



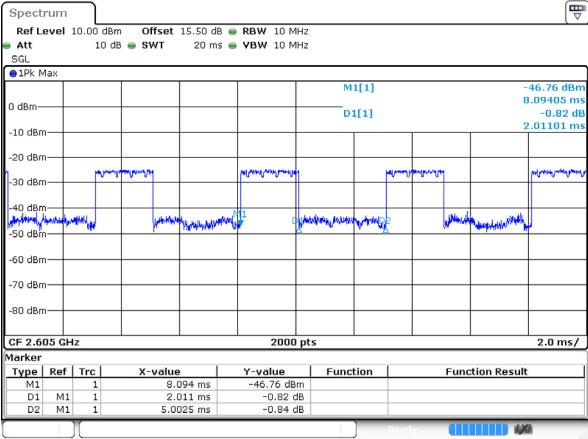
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15MHz_QPSK



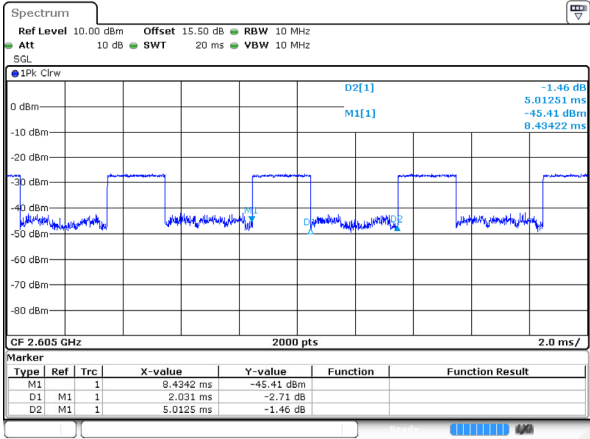
ProjectNo.:RSHA240520001 Tester:Jason Lu
Date: 24.SEP.2024 10:23:57

20MHz_16QAM



ProjectNo.:RSHA240520001 Tester:Jason Lu
Date: 24.SEP.2024 10:27:02

20MHz_QPSK



ProjectNo.:RSHA240520001 Tester:Jason Lu
Date: 24.SEP.2024 10:28:05