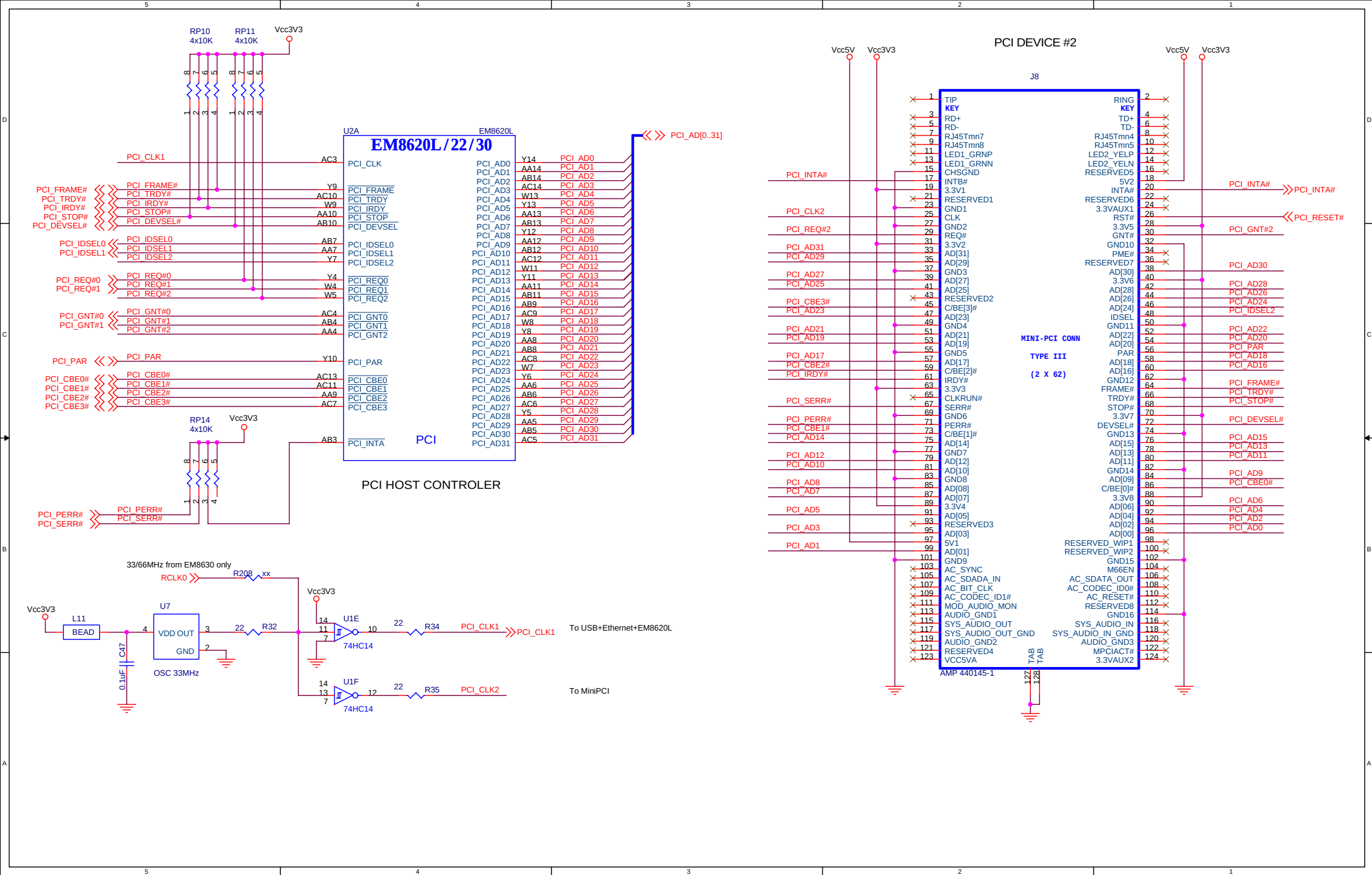
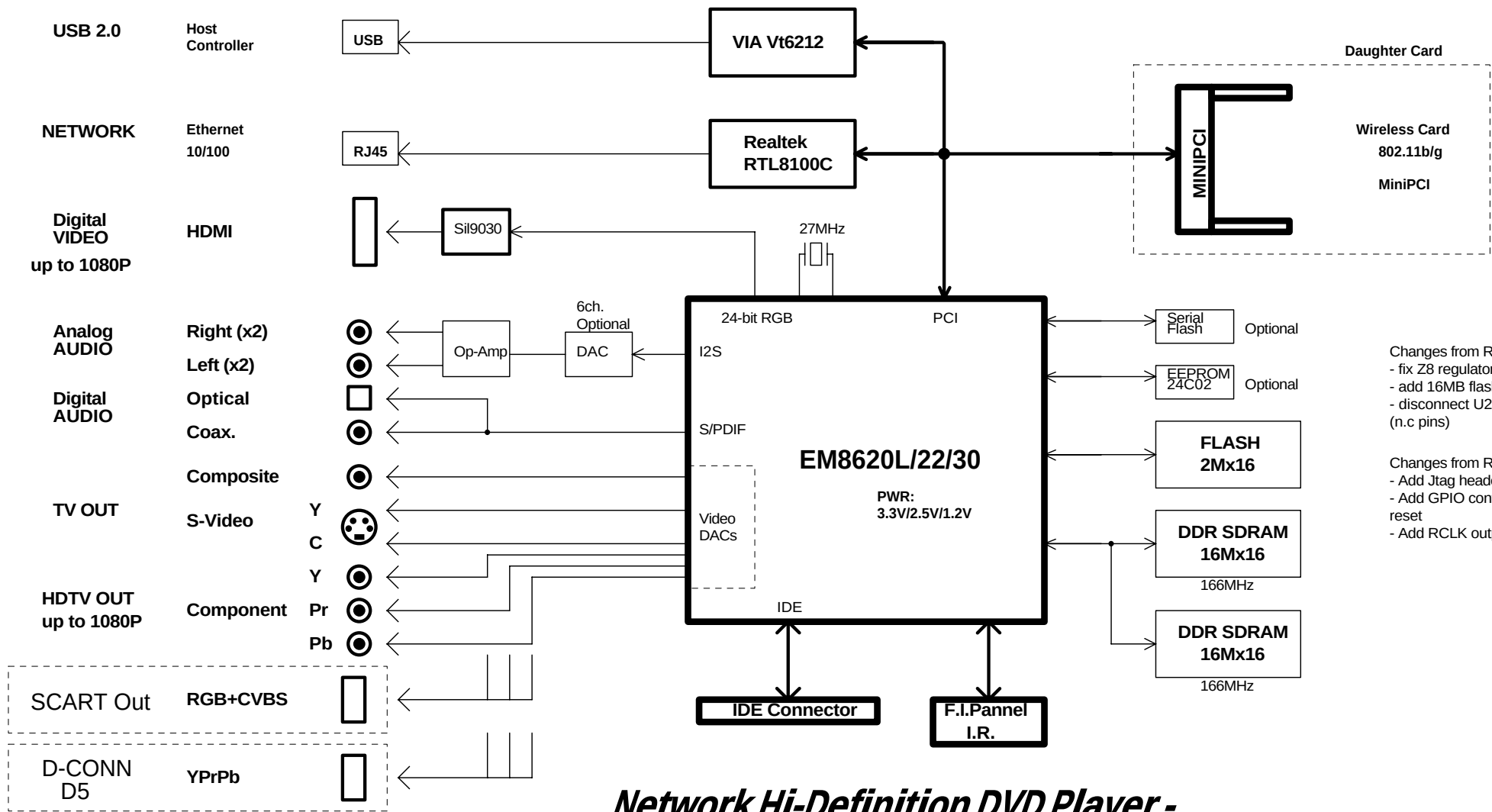






Changes from Rev A1 to Rev A2:
- fix Z8 regulator issue
- add 16MB flash support
- disconnect U2 pin W2 & W3 (n.c pins)

Changes from Rev A2 to Rev A3:
- Add Jtag header for 8630
- Add GPIO control for PCI/IDE reset
- Add RCLK outputs for 8630

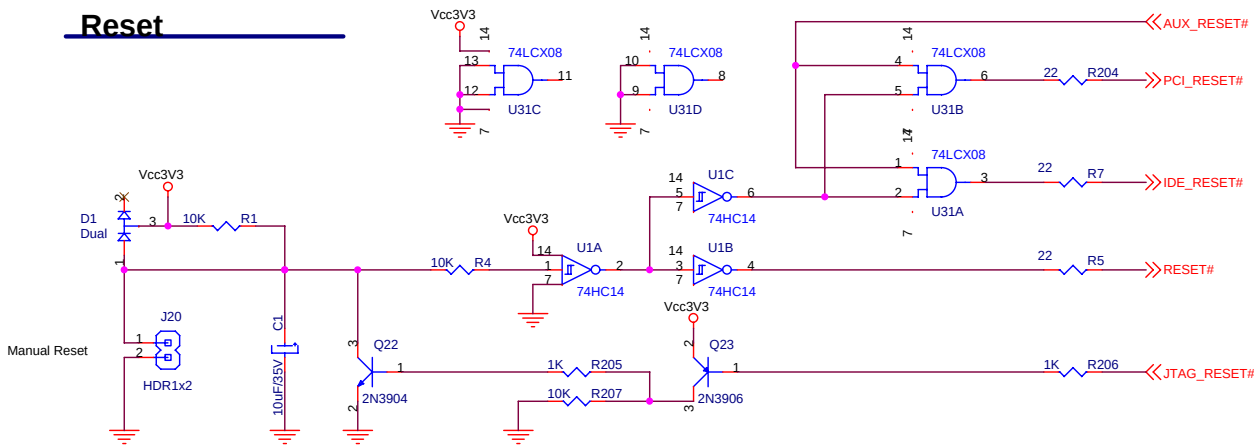
Network Hi-Definition DVD Player - EZ500/22/30 HDMI

MPEG1-2-4/WMV9/H.264 HD DVD Player
Optional:
- Network
- USB Host
- Wireless

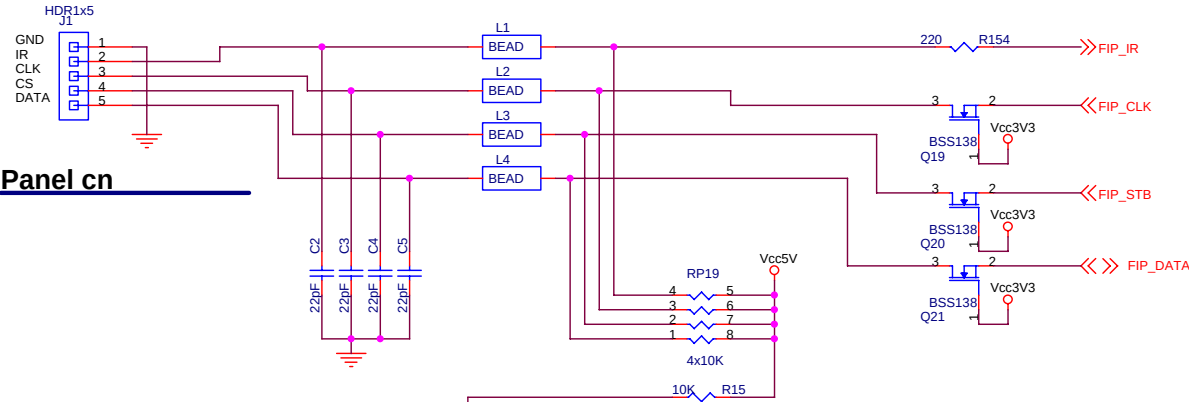
825 rev A3

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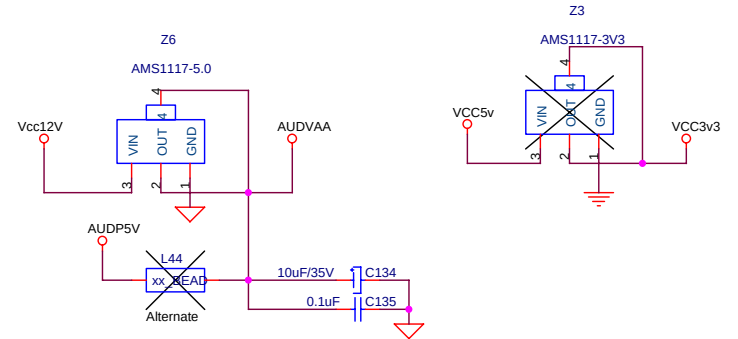
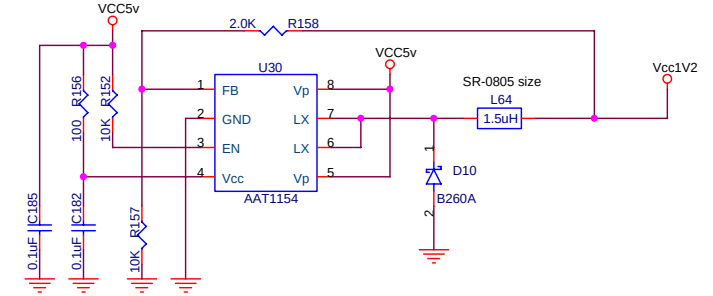
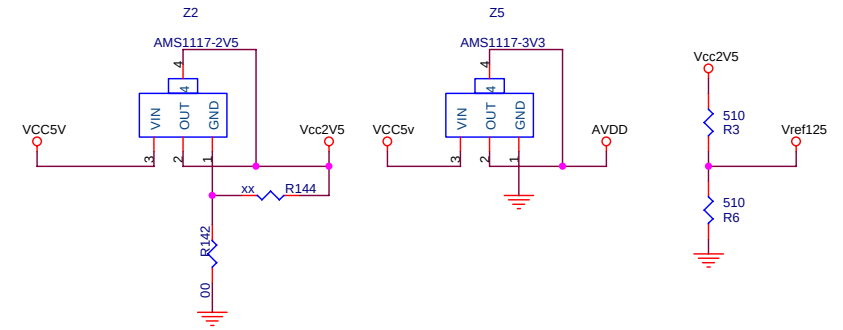
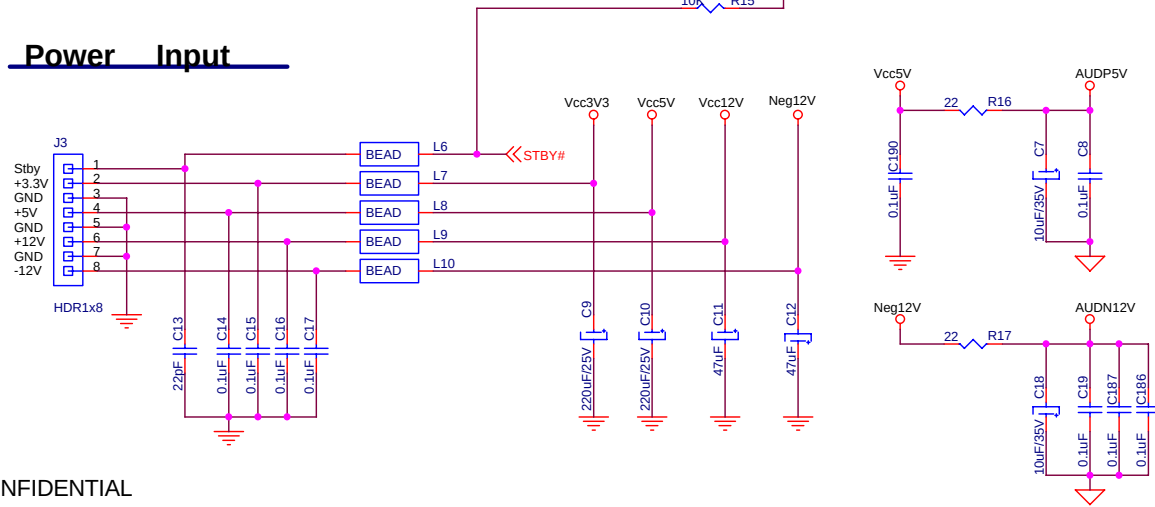
Reset



Panel cn



Power Input



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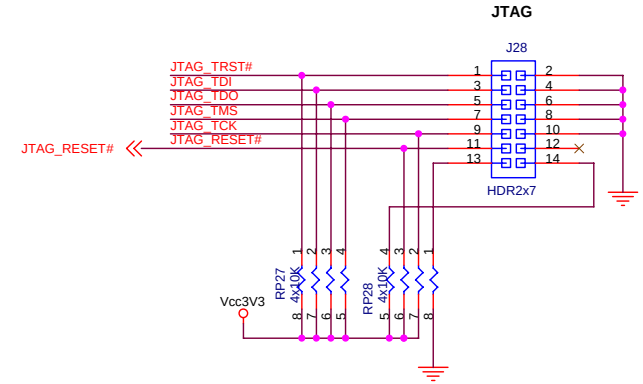
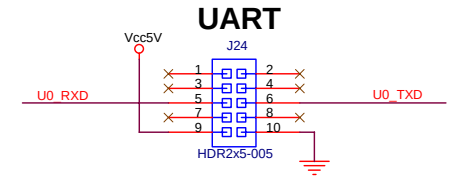
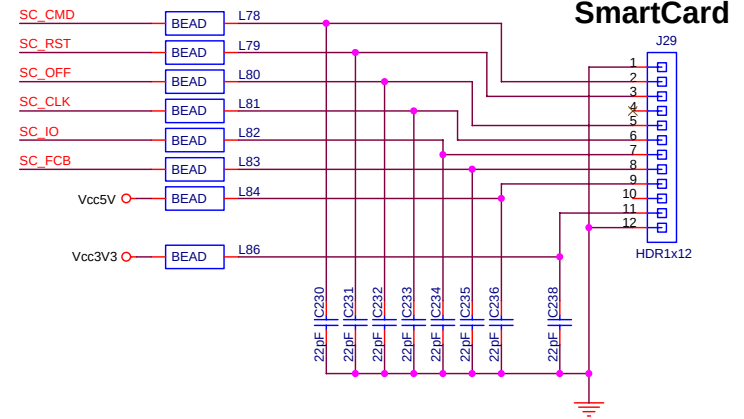
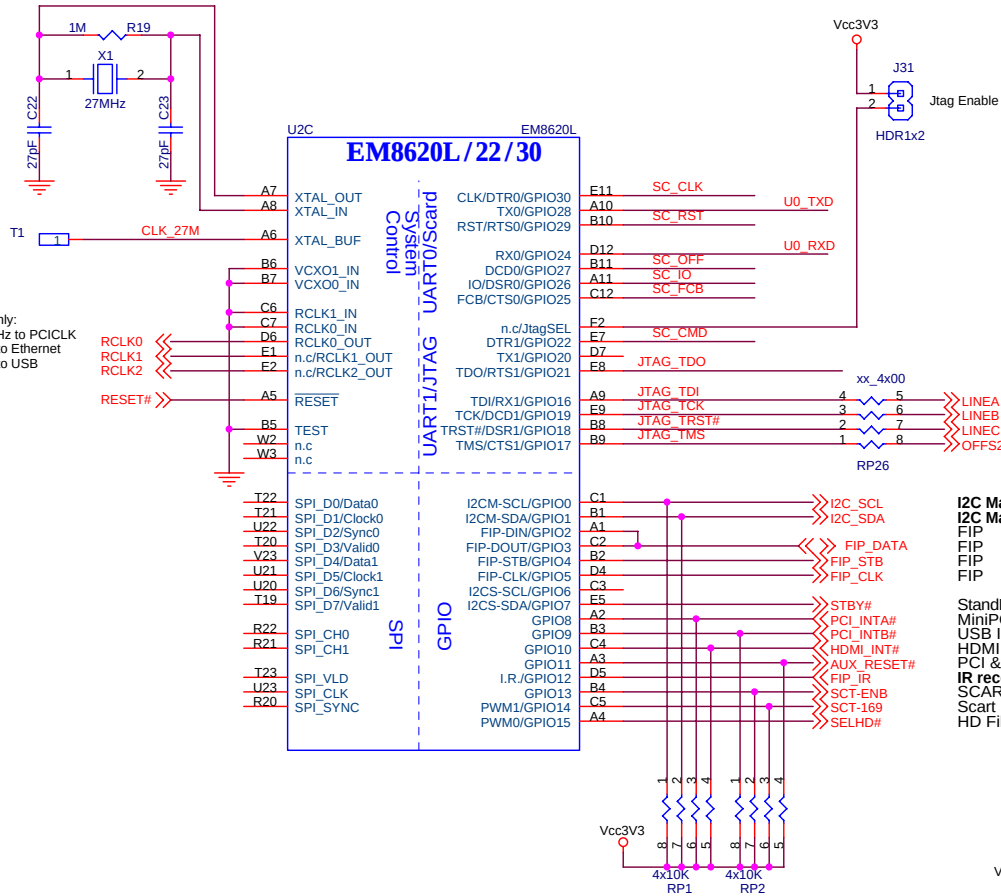
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DVD Player - Z500

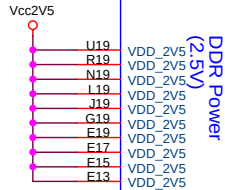
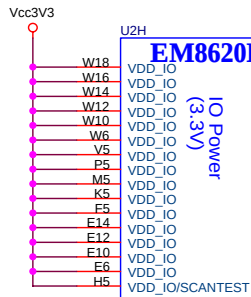
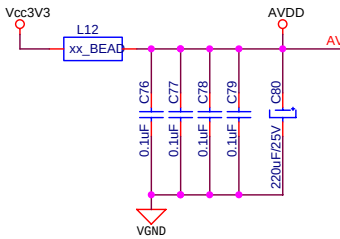
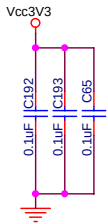
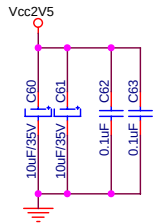
Title Clocks / Reset / Tooling		
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EM8630 only:
33/66MHz to PCICLK
25MHz to Ethernet
24MHz to USB



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Title Main I/Os			
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IO
Ground

Video DACs

EM8620L / 22 / 30

IO Power
(3.3V)

DDR Power
(2.5V)

Video DACs

IO
Ground

Video DACs

EM8620L / 22 / 30

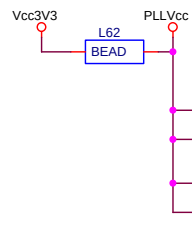
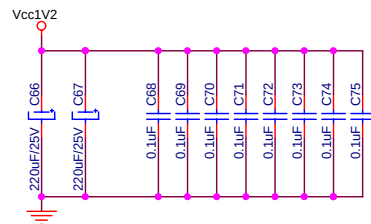
IO Power
(3.3V)

DDR Power
(2.5V)

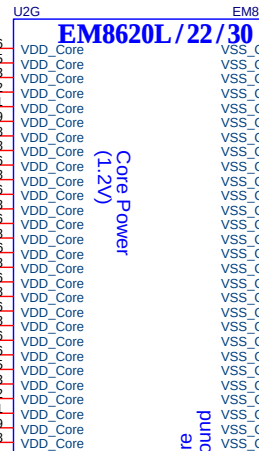
Video DACs

IO
Ground

Video DACs



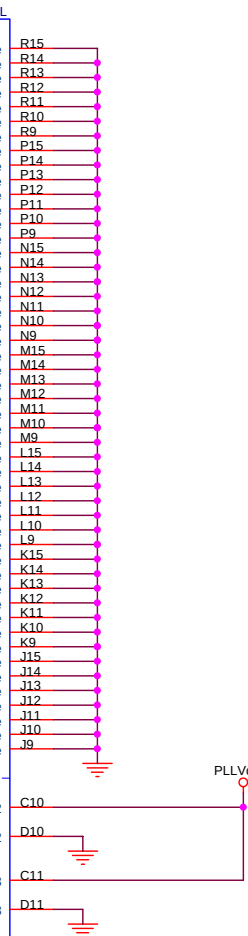
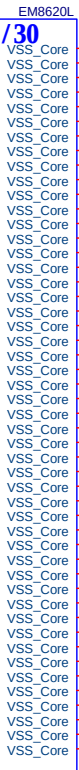
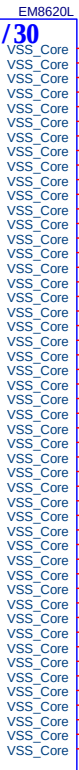
Vcc1V2



Core Power
(1.2V)

PLL Section

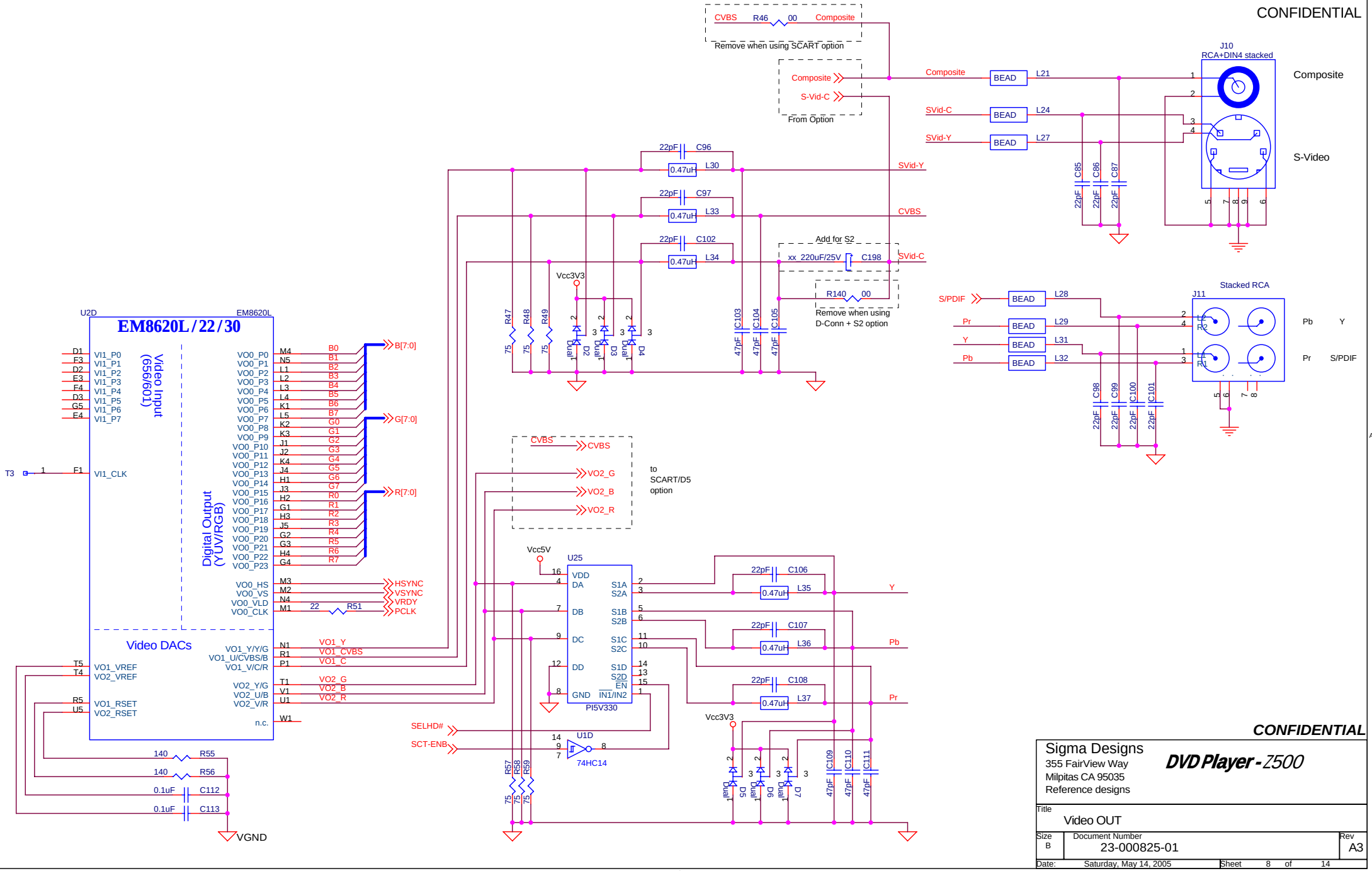
Core
Ground



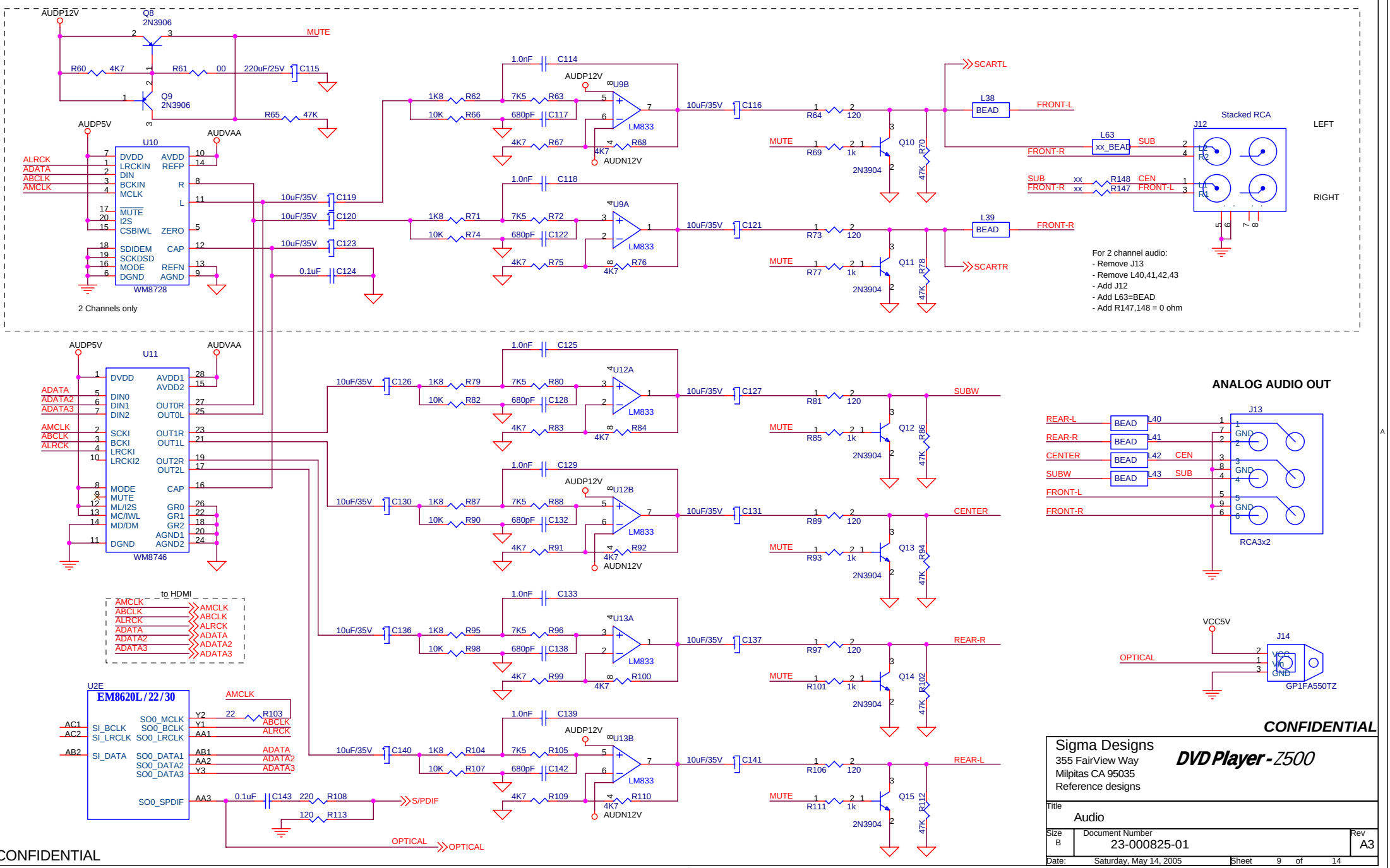
PLL Vcc

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Sigma Designs 355 FairView Way Milpitas CA 95035 Reference designs		DVD Player - Z500	
Title Power			
Size B	Document Number 23-000825-01		Rev A3
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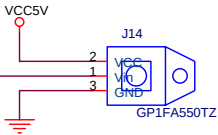
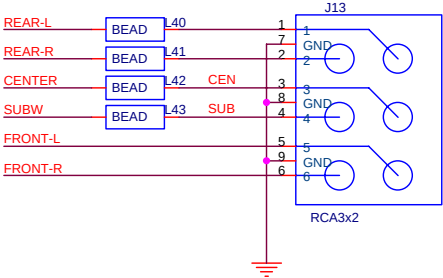


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Milpitas CA 95035		
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Title		
Video OUT		
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For 2 channel audio:
- Remove J13
- Remove L40,41,42,43
- Add J12
- Add L63=BEAD
- Add R147,148 = 0 ohm

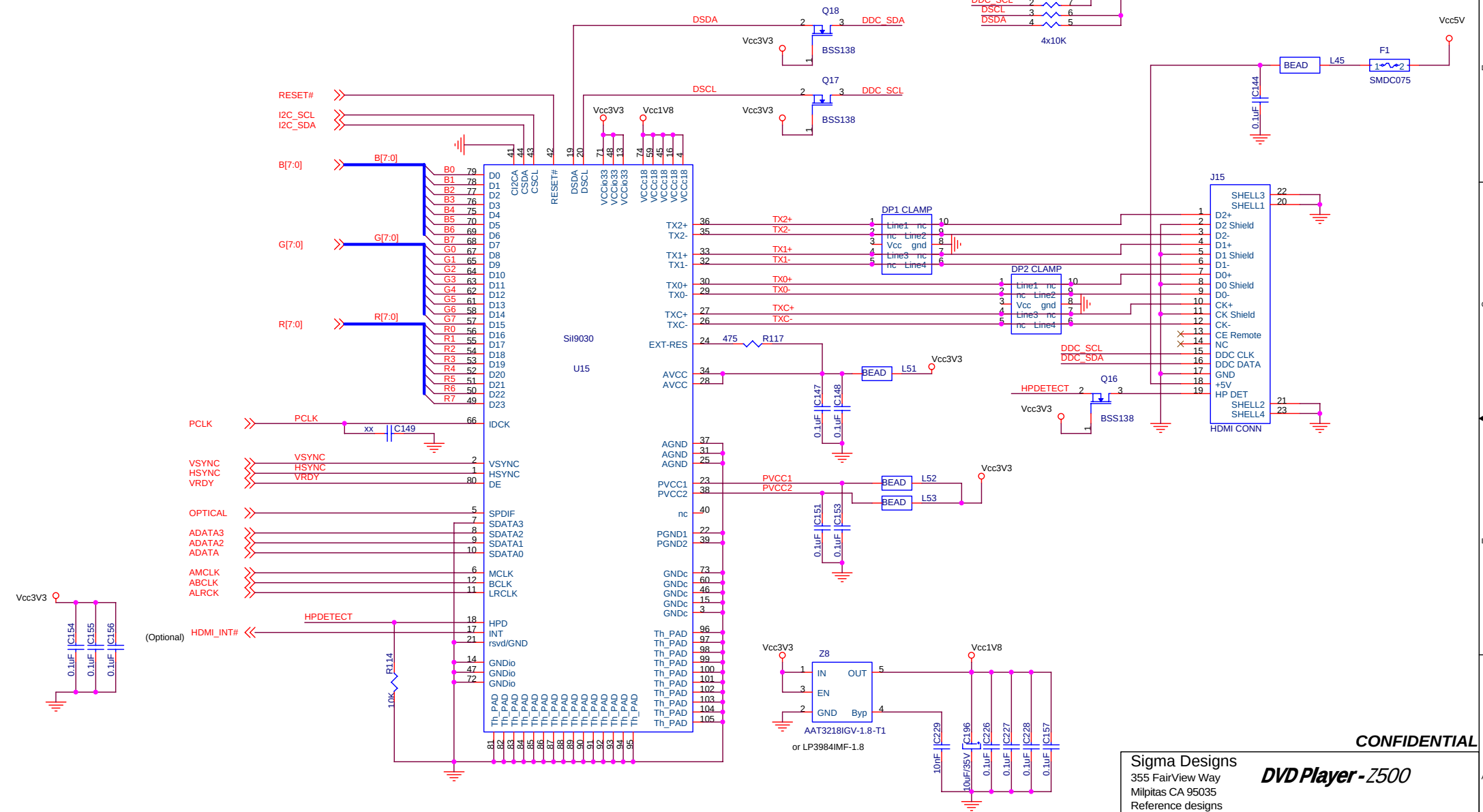
ANALOG AUDIO OUT



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Title			Audio
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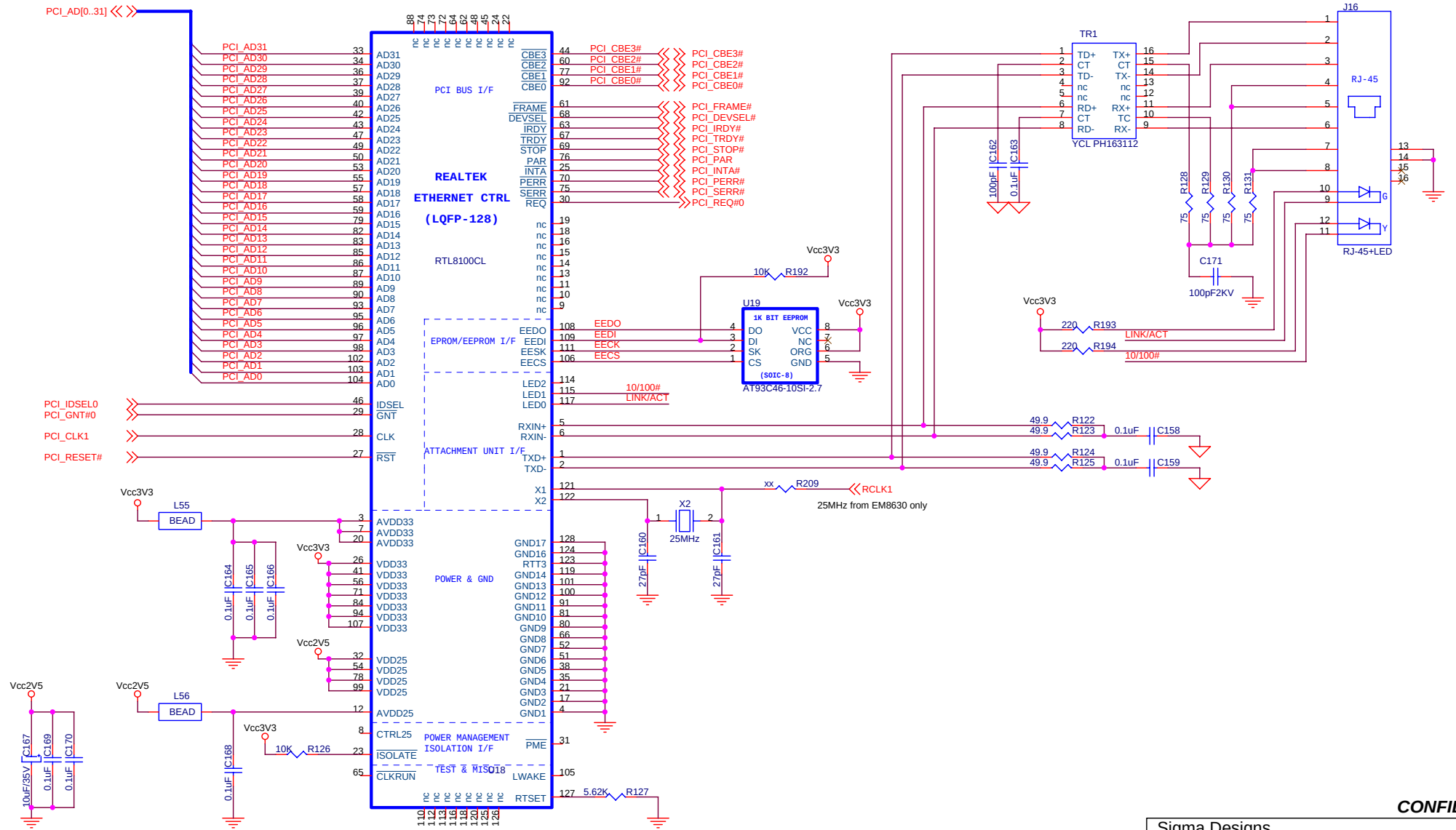


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DVD Player - Z500

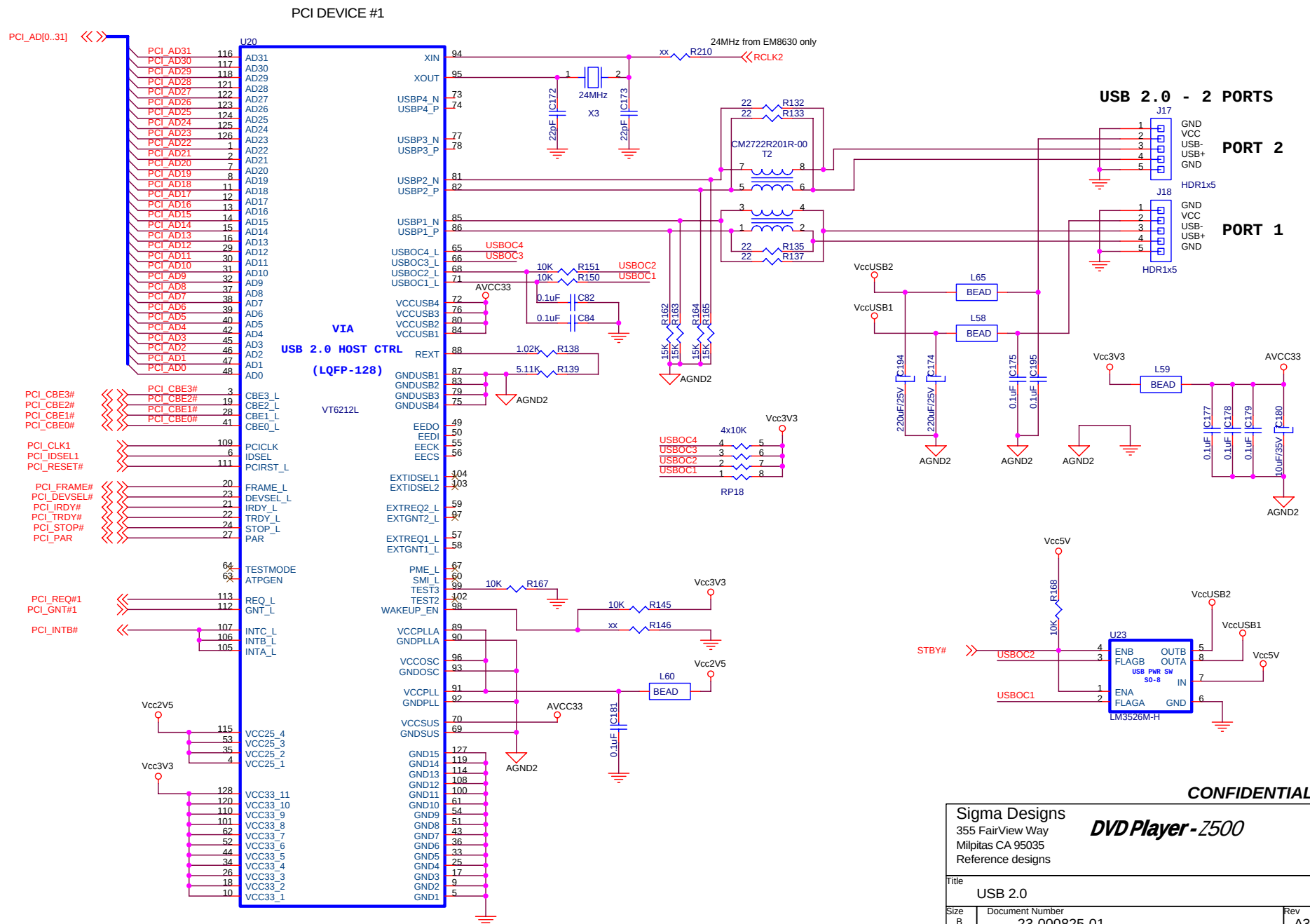
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HDMI				
Size B	Document Number			Rev
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PCI DEVICE #0



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DVD Player - EZ500		
Title Ethernet		
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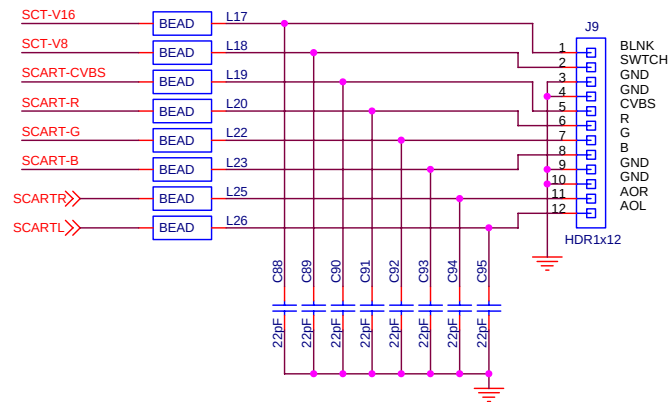
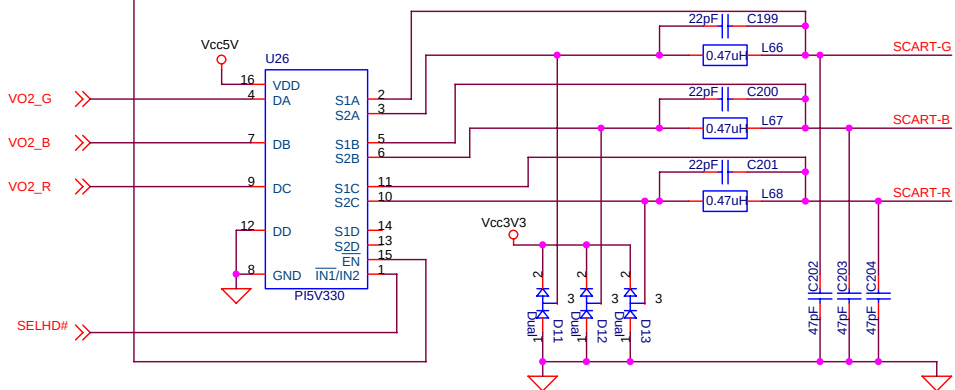
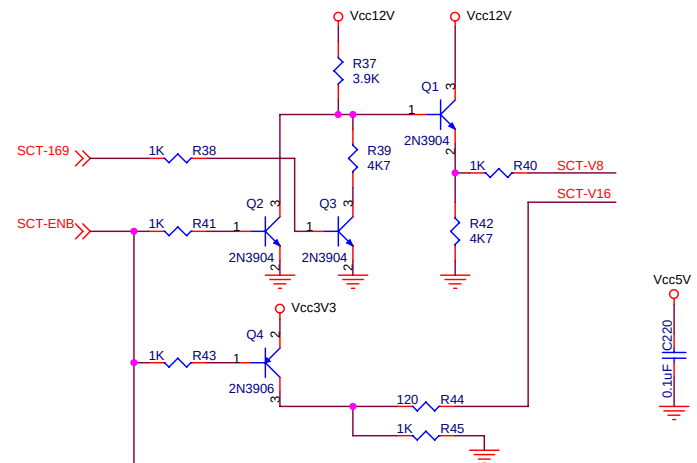
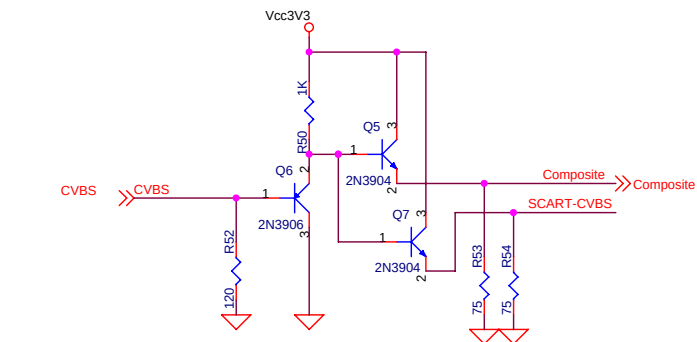


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Title			
USB 2.0			
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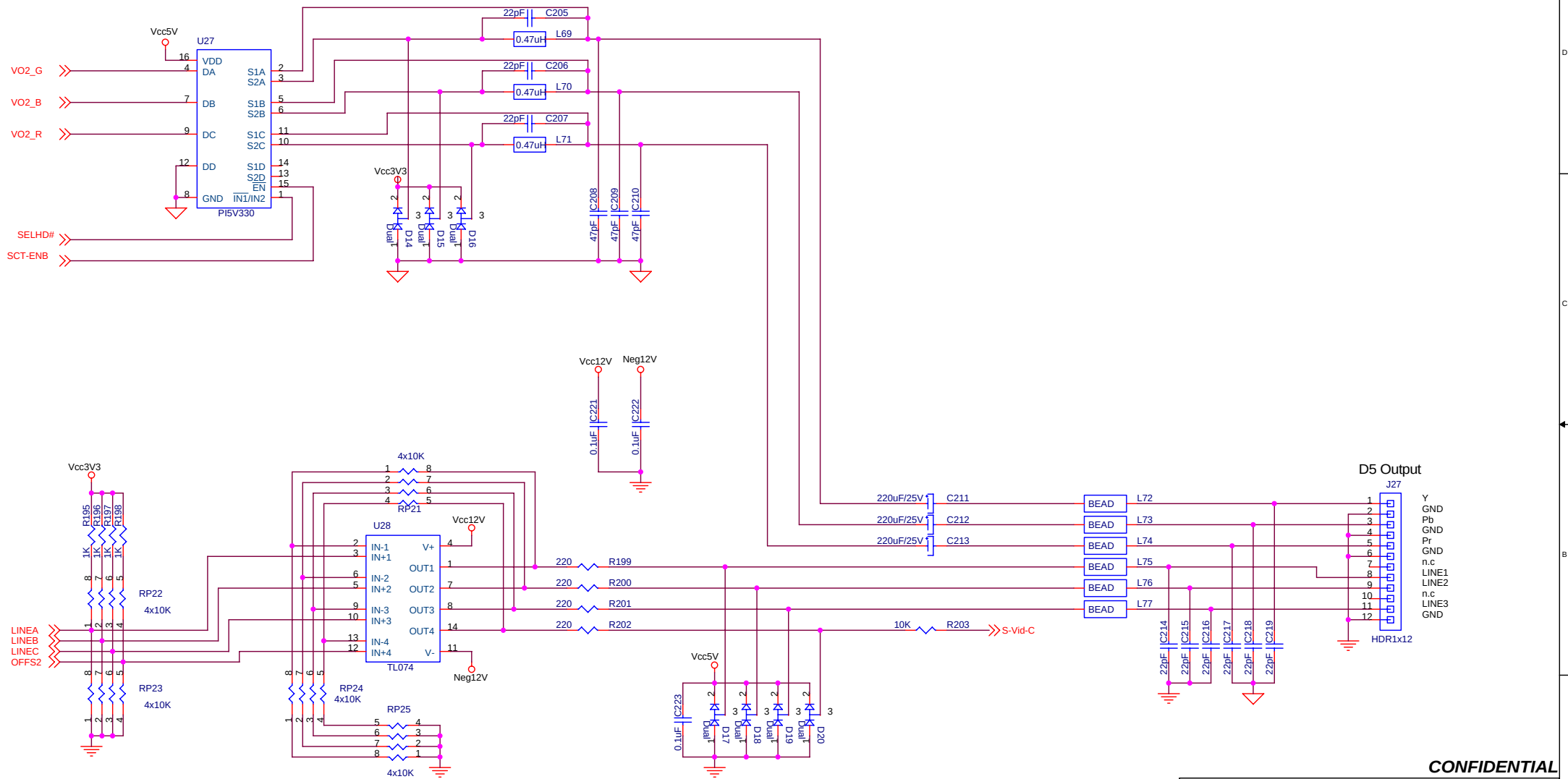
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Title: SCART Option

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