

Data Transfer Mode

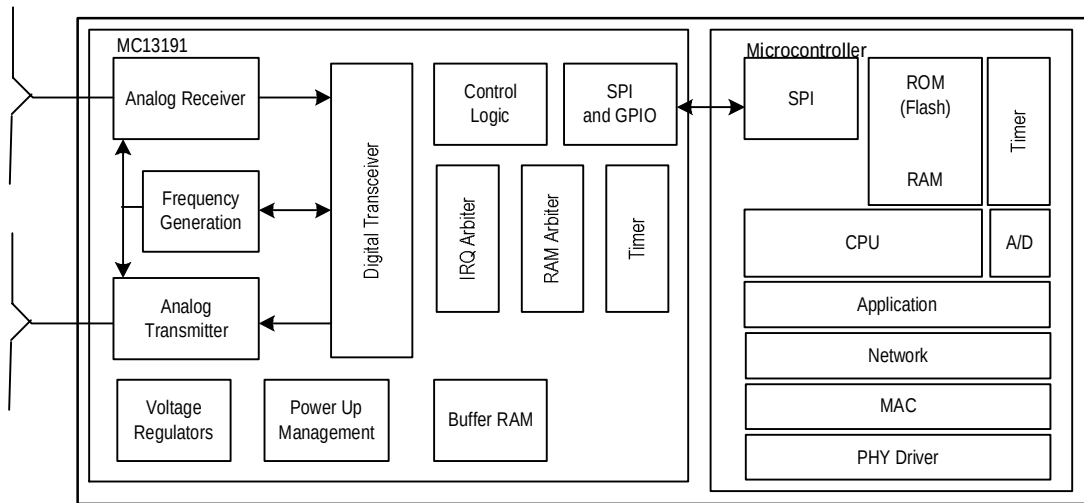


Figure 3. System Level Block Diagram

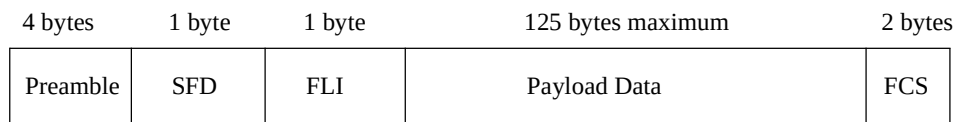


Figure 4. MC13191 Packet Structure

4.3 Transmit Path Description

For the transmit path, the TX data that was previously stored in TX PAcKet RAM is retrieved, formed into packets, spread, and then up-converted to the transmit frequency.

Because the MC13191 is used in packet mode, data is processed as an entire packet. The data is first loaded into the TX buffer. The MCU then requests that the MC13191 transmit the data. The MCU is notified via an interrupt when the whole packet has successfully been transmitted.

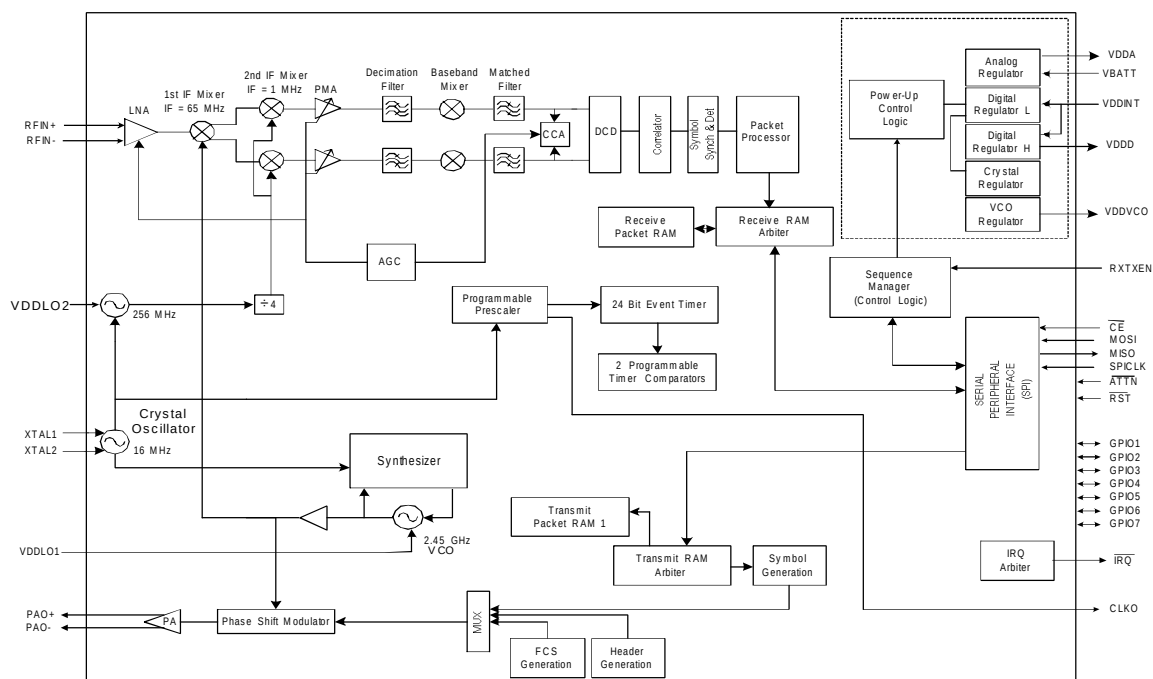


Figure 2. MC13191 Simplified Block Diagram