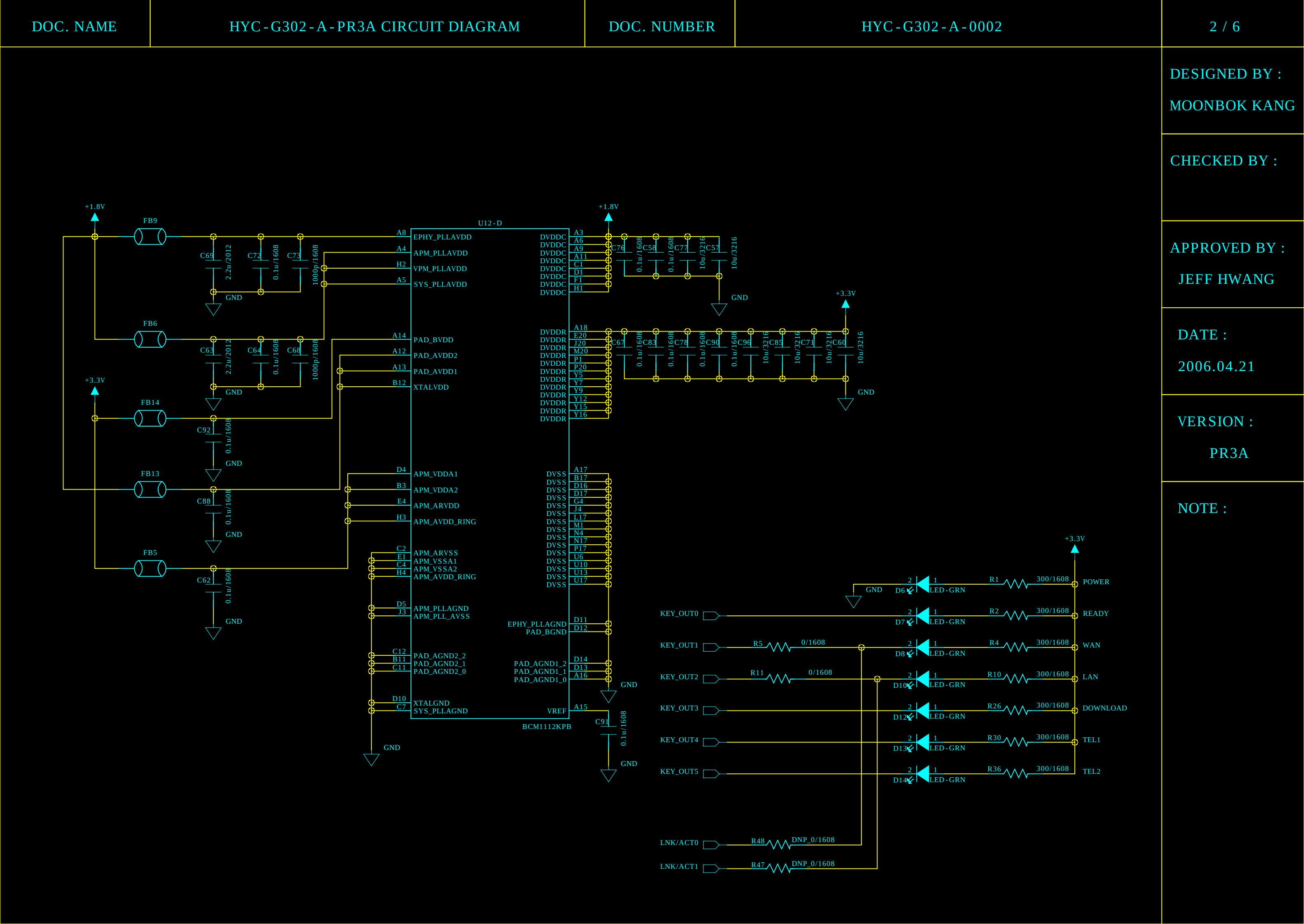


|                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |                                    |             |                    |                               |
|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|------------------------------------|-------------|--------------------|-------------------------------|
| DOC. NAME                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            | HYC -G302 -A -PR3A CIRCUIT DIAGRAM | DOC. NUMBER | HYC -G302 -A -0002 | 1 / 6                         |
| <p>The circuit diagram illustrates the power management section of the HYC-G302-A-PR3A board. It features three voltage regulators: U7 (LD1117A-3.3_TO252) for +3.3V, U11 (LD1117-1.8_SOT223) for +1.8V, and U15 (DNP_FRAME_GND) for a frame ground. The +5V input is connected to J5 and regulated by FB3 (CNH41F224Z-BP) and FB2 (HH-1H3216-500JT). The +3.3V output is regulated by FB4 (HH-1H3216-500JT). The +1.8V output is regulated by FB7 (HH-1H3216-500JT). Various capacitors (C24, C26, C25, C37, C34, C35, C23, C51, C50, C75, C70, C74, C66, C61) and a diode (D11) are used for filtering and protection. Test points TP_1, TP_2, TP_3, and TP_GND are indicated.</p> |                                    |             |                    | DESIGNED BY :<br>MOONBOK KANG |
|                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |                                    |             |                    | CHECKED BY :                  |
|                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |                                    |             |                    | APPROVED BY :<br>JEFF HWANG   |
|                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |                                    |             |                    | DATE :<br>2006.04.21          |
|                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |                                    |             |                    | VERSION :<br>PR3A             |
|                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |                                    |             |                    | NOTE :                        |



DESIGNED BY :  
MOONBOK KANG

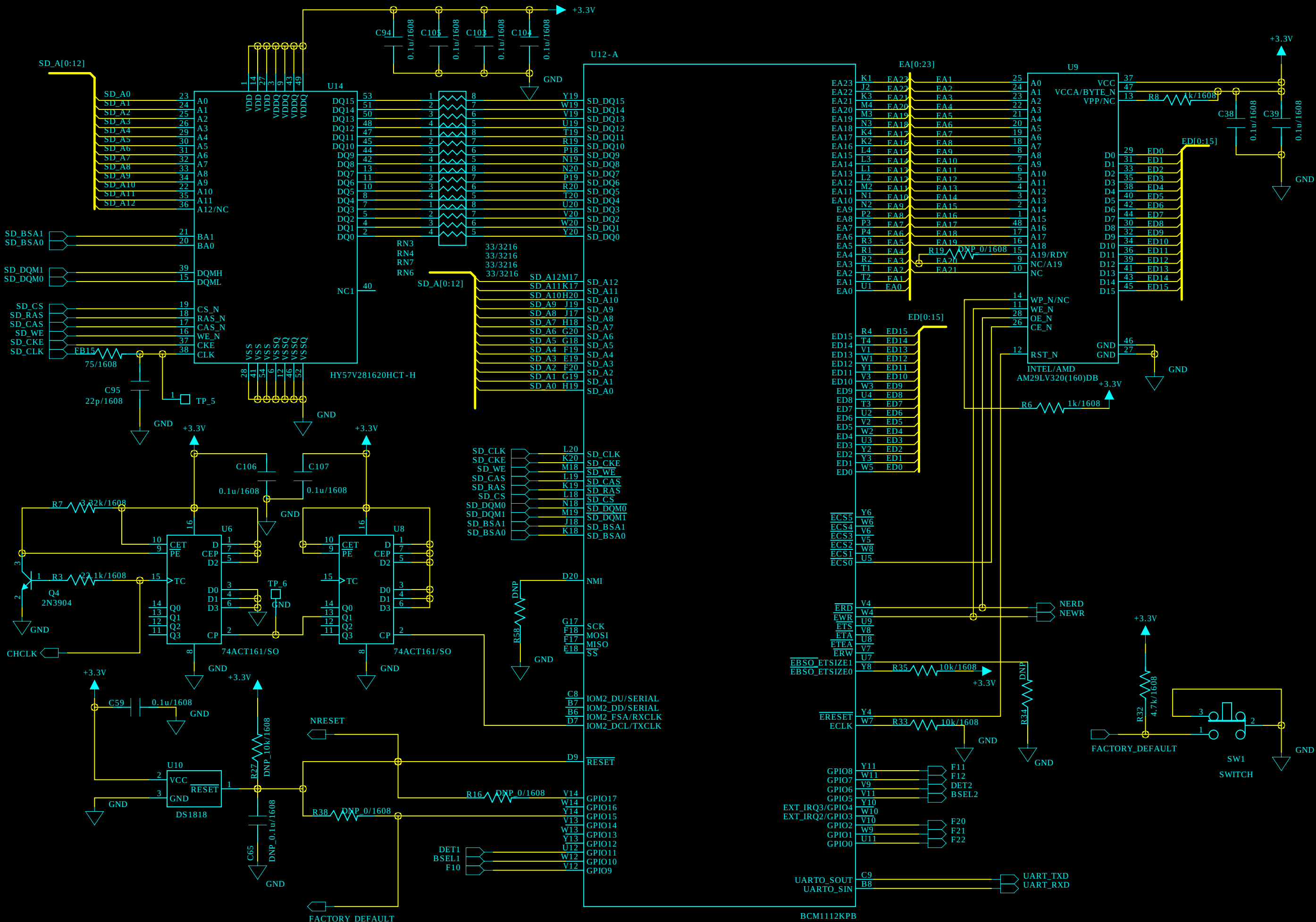
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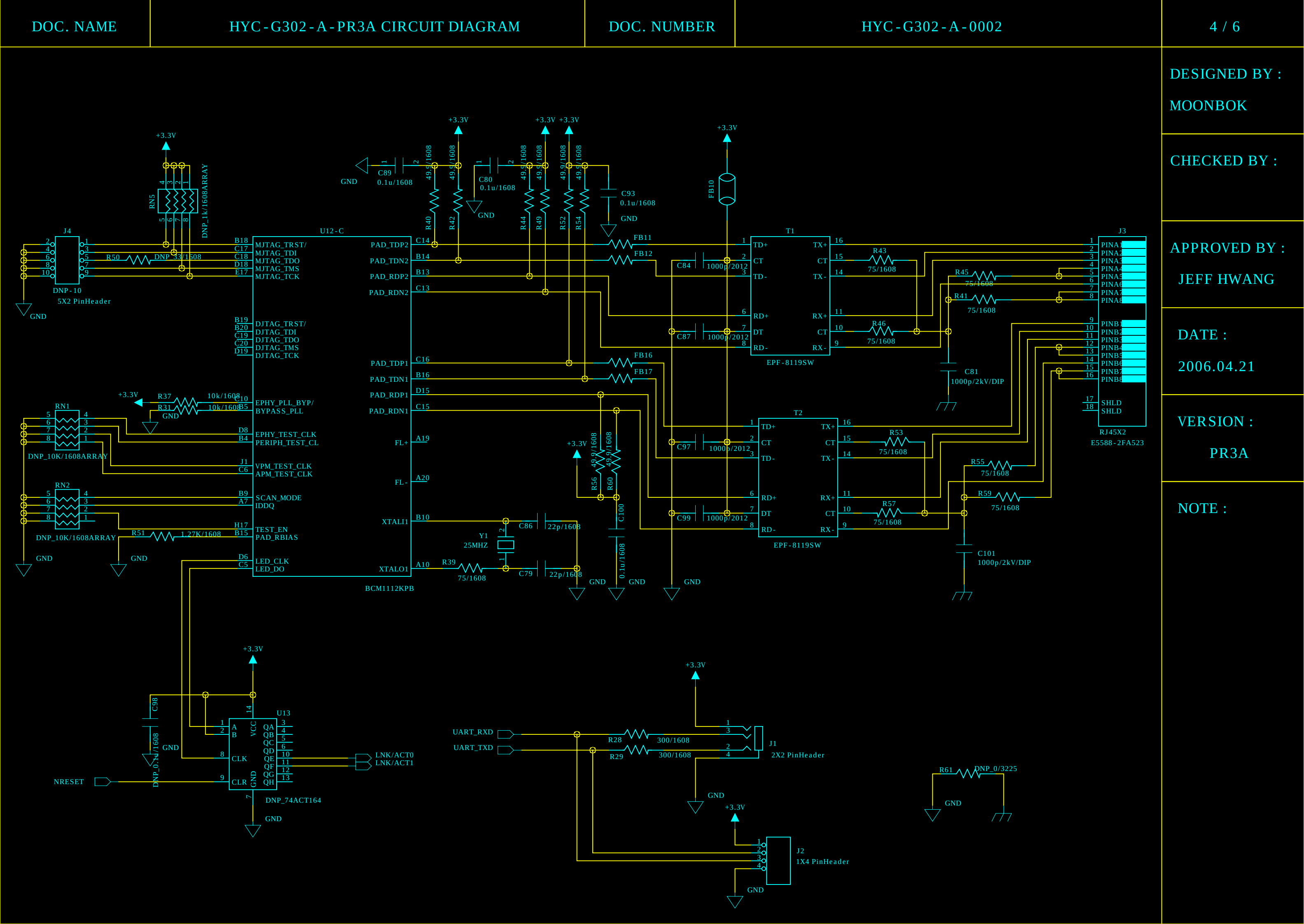
APPROVED BY :  
JEFF HWANG

DATE :  
2006.04.21

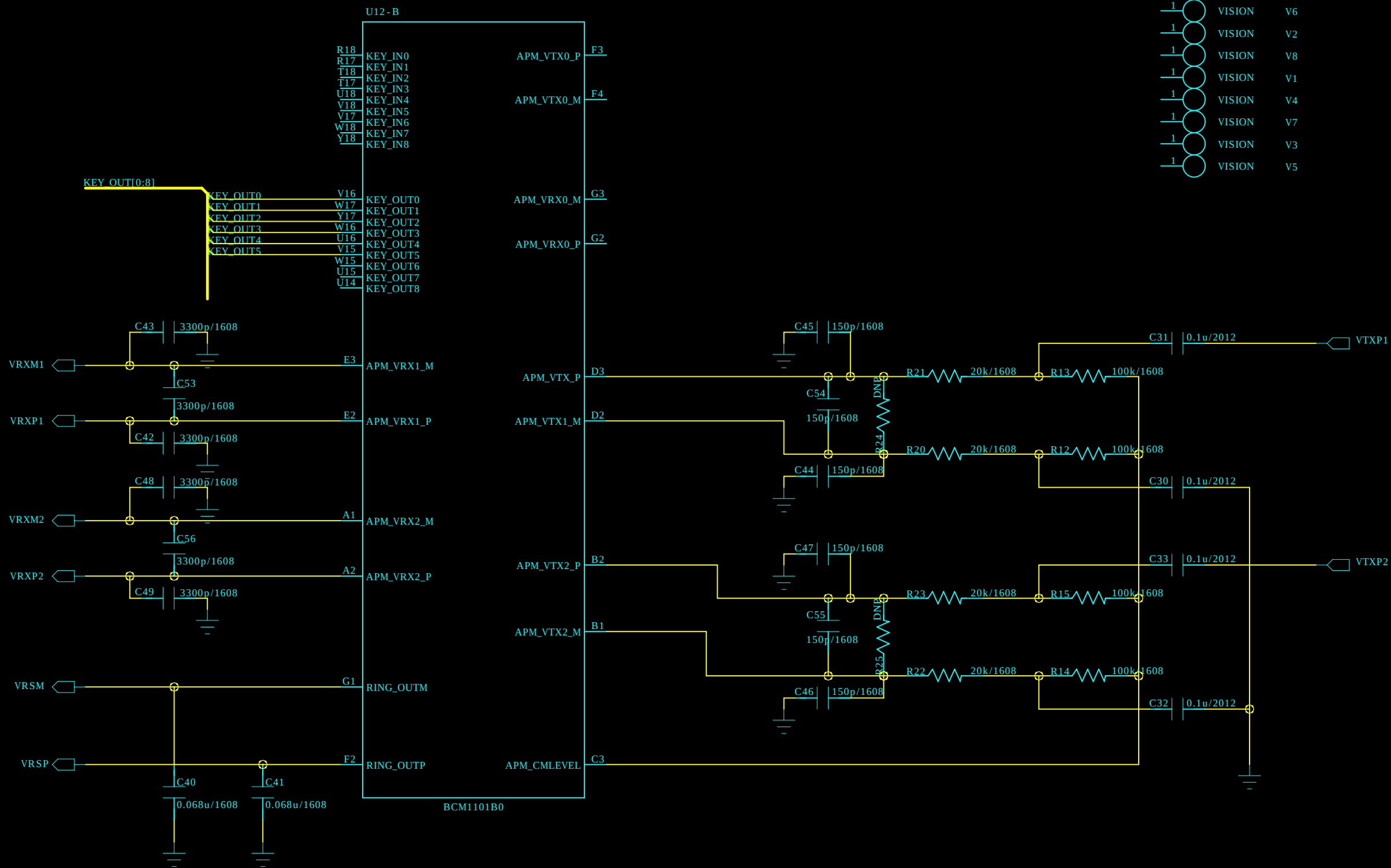
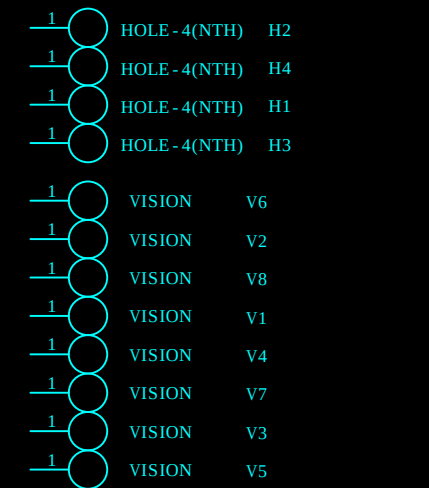
VERSION :  
PR3A

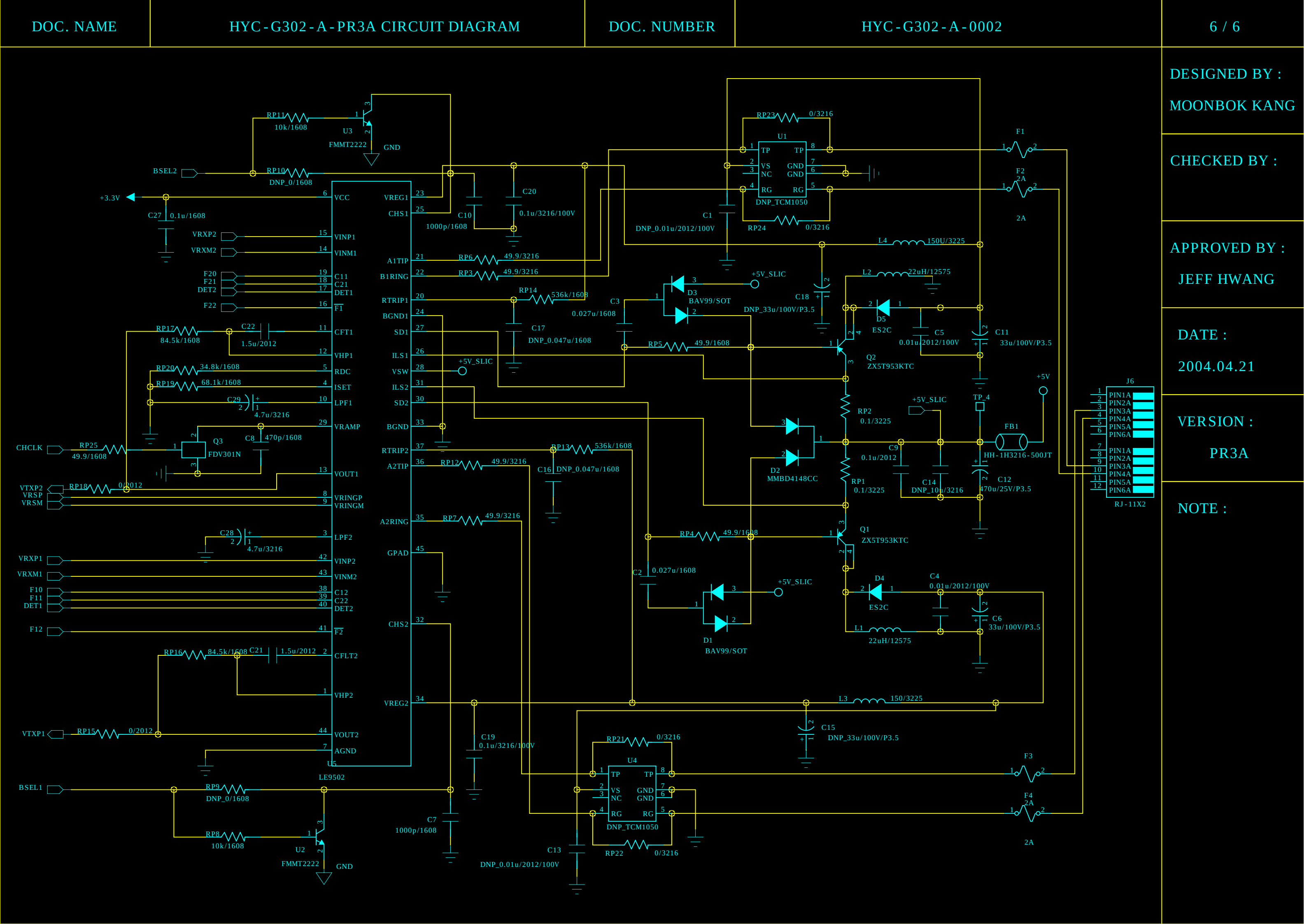
NOTE :





VERSION :  
PR3A





Schematic

|             |              |           |      |
|-------------|--------------|-----------|------|
|             | NAME         | SIGNATURE | DATE |
| DESIGNED BY | MOONBOK KANG |           |      |
| CHECKED BY  |              |           |      |
| APPROVED BY | JEFF HWANG   |           |      |

|             |                       |      |            |             |      |
|-------------|-----------------------|------|------------|-------------|------|
| PROJECT     | HYC - G302 - A        |      |            | REVISION    | PR3A |
| DOC. NUMBER | HYC - G302 - A - 0002 | DATE | 2006.04.21 | TOTAL PAGES | 6    |

|                   |                                   |         |          |          |     |
|-------------------|-----------------------------------|---------|----------|----------|-----|
| DEVELOPMENT LEVEL | FEATURES                          | PL Rev. | PCB Rev. | S/W Rev. | ECN |
| DPP               | PCB size : 130(L) x 130(W) [mm]   | PR1A    | PR1A     |          |     |
| DPP               | PCB size : 130(L) x 120(W) [mm]   | PR2A    | PR2A     |          |     |
| DPP               | PCB size : CHANGE LEFT SIDE ROUND | PR2B    | PR2B     |          |     |
| DPP               | PCB size : ADD A HALL             | PR2C    | PR2C     |          |     |
| DPP               | 5[V]/2[A] Input Power Solution    | PR3A    | PR3A     |          |     |
|                   |                                   |         |          |          |     |
|                   |                                   |         |          |          |     |

|                                                                                    |  |  |           |  |  |
|------------------------------------------------------------------------------------|--|--|-----------|--|--|
| EACH SHEET                                                                         |  |  | REFERENCE |  |  |
| P1 : POWER<br>P2 : LED<br>P3 : CPU/MEMORY<br>P4 : WAN/LAN<br>P5 : APM<br>P6 : SLIC |  |  |           |  |  |

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