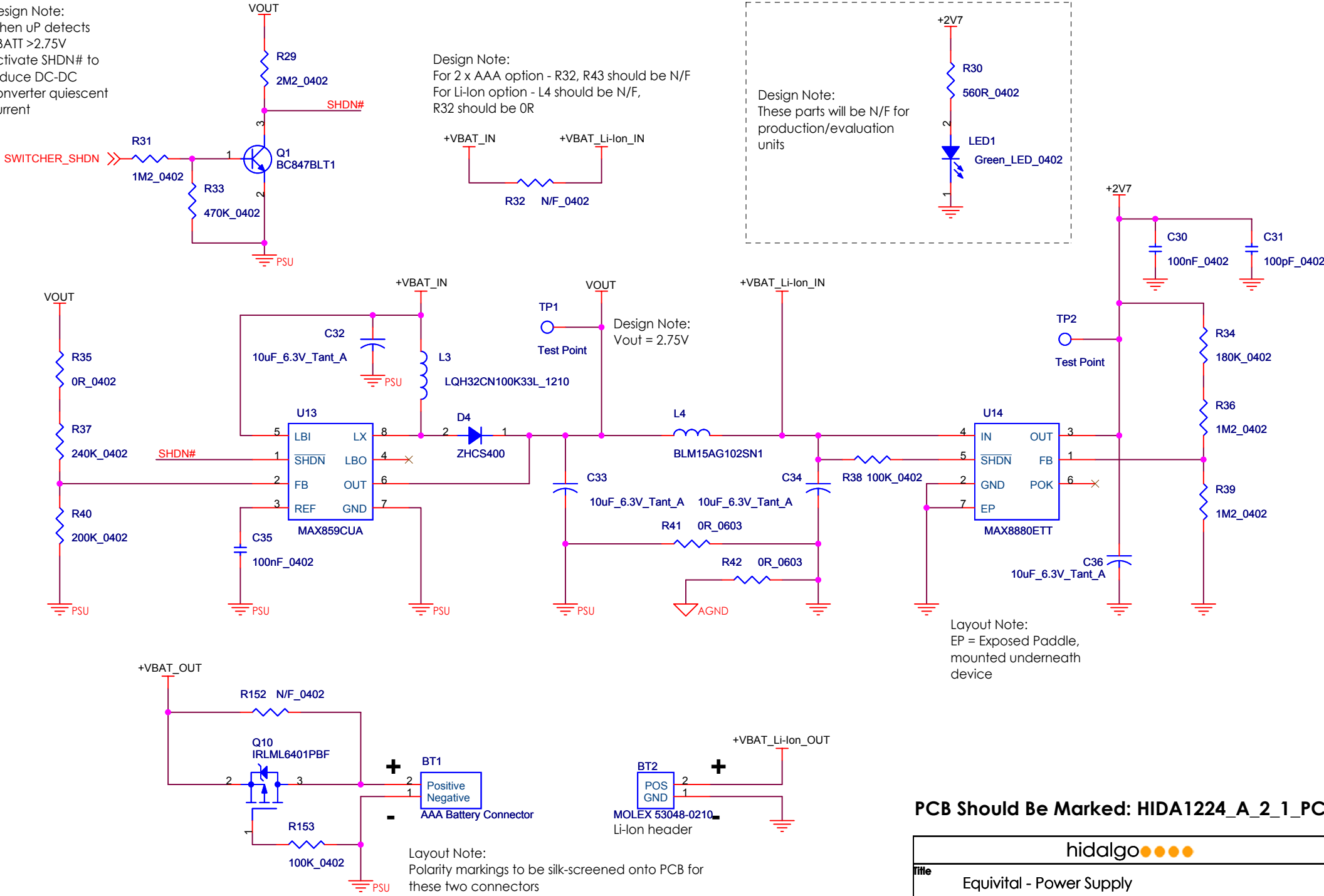


Design Note:
When uP detects
VBATT >2.75V
activate SHDN# to
reduce DC-DC
converter quiescent
current

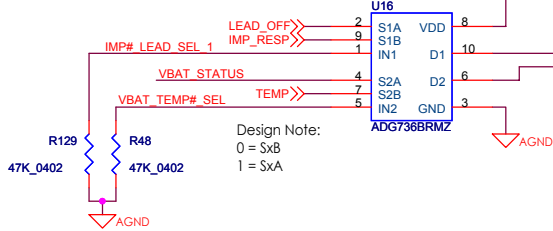
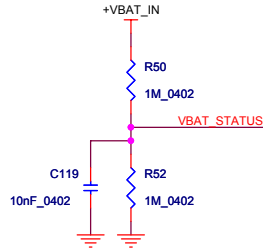
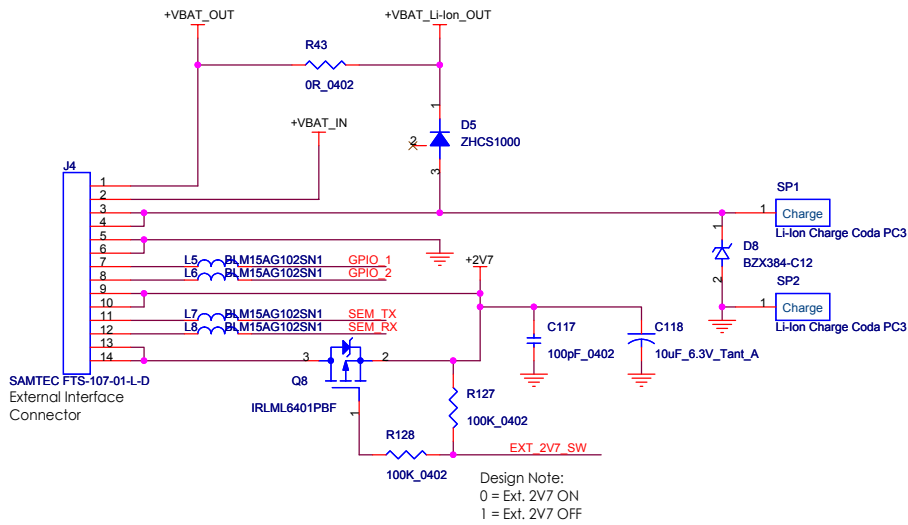
Design Note:
For 2 x AAA option - R32, R43 should be N/F
For Li-Ion option - L4 should be N/F,
R32 should be 0R

Design Note:
These parts will be N/F for
production/evaluation
units

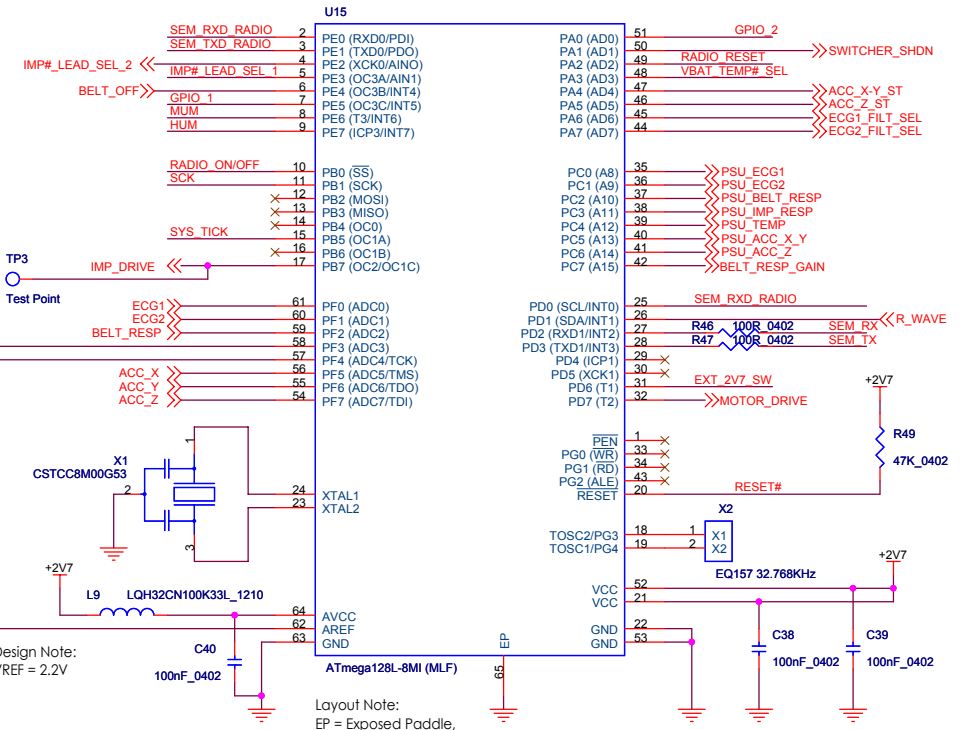
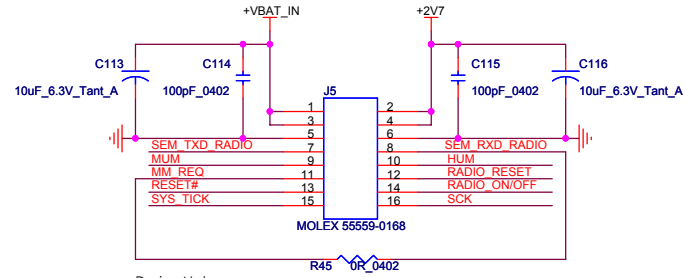
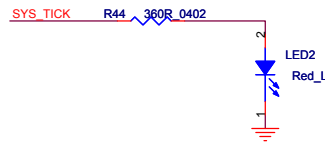


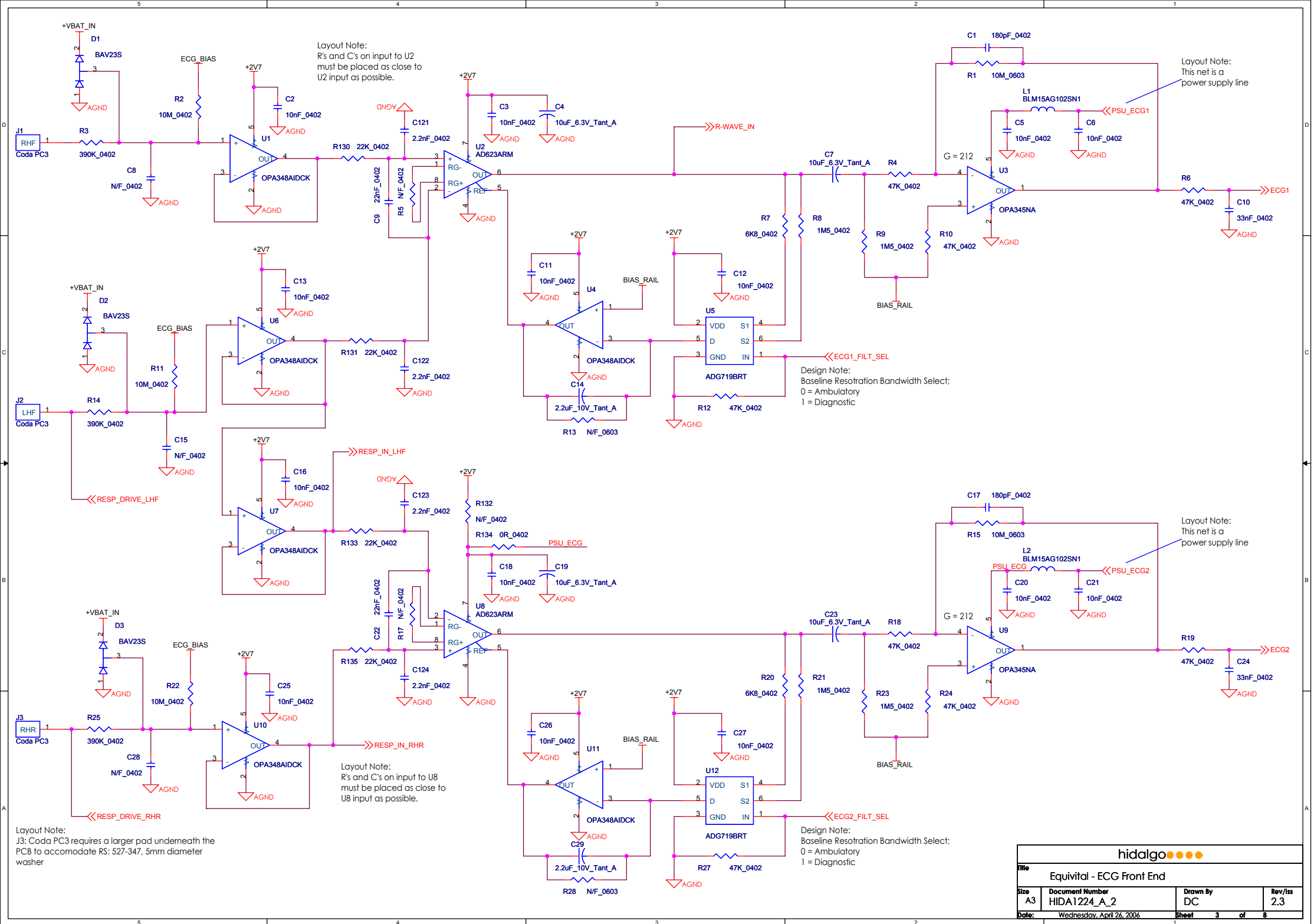
PCB Should Be Marked: HIDA1224_A_2_1_PCB

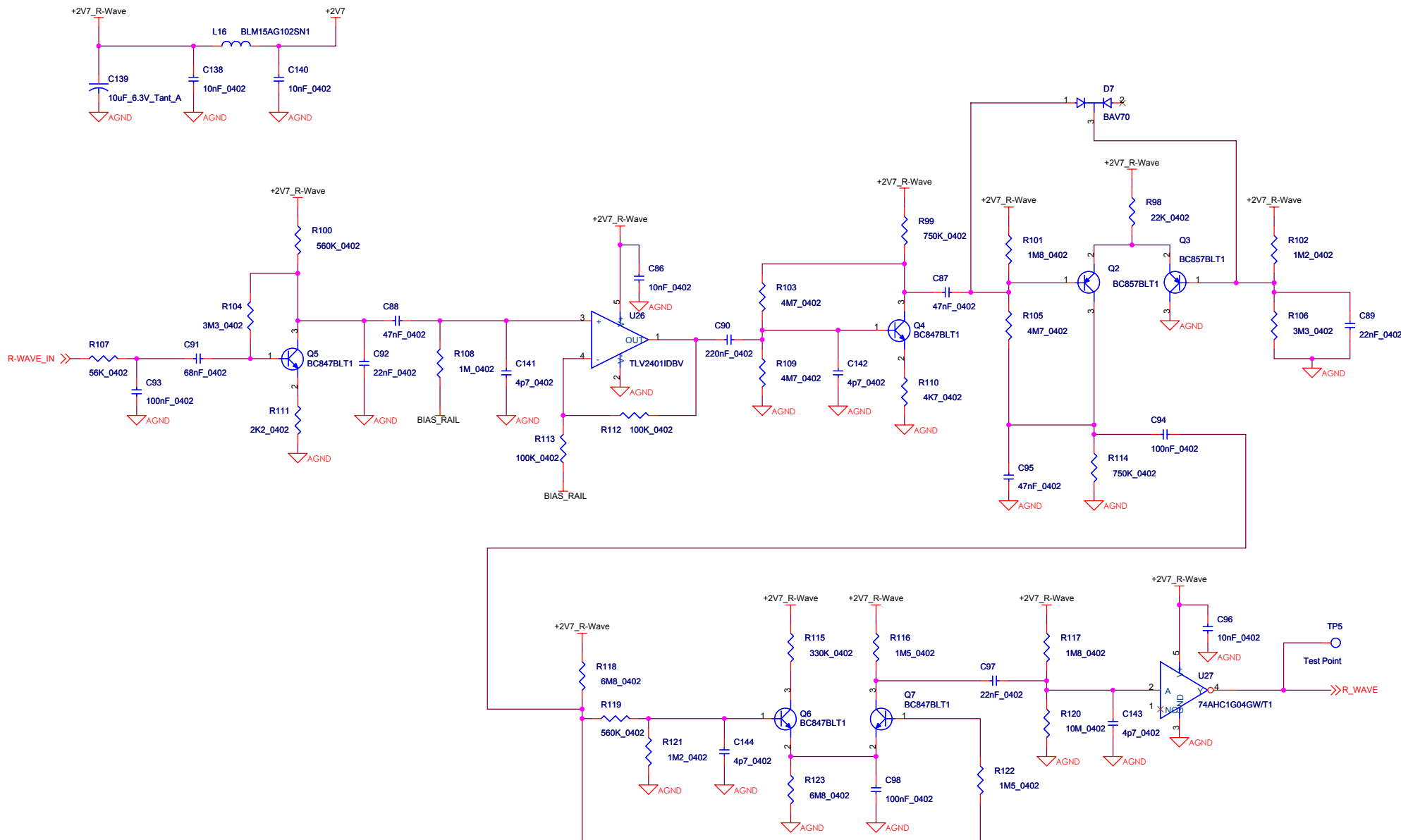
hidalgo			
Title Equival - Power Supply			
Size A4	Document Number HIDA1224_A_2	Drawn By DC	Rev/Iss 2.3
Date: Wednesday, April 26, 2006	Sheet 1 of 8		

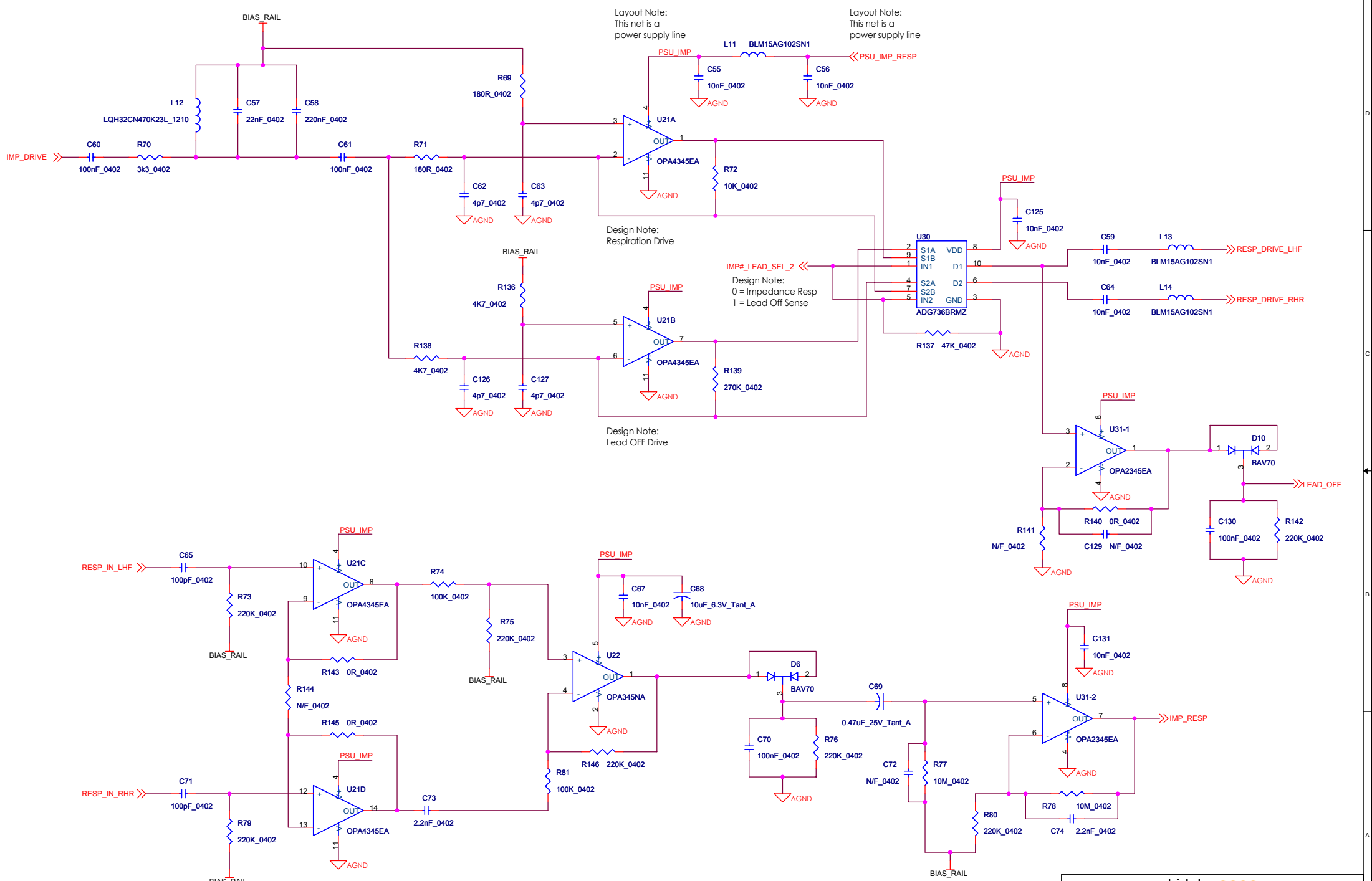


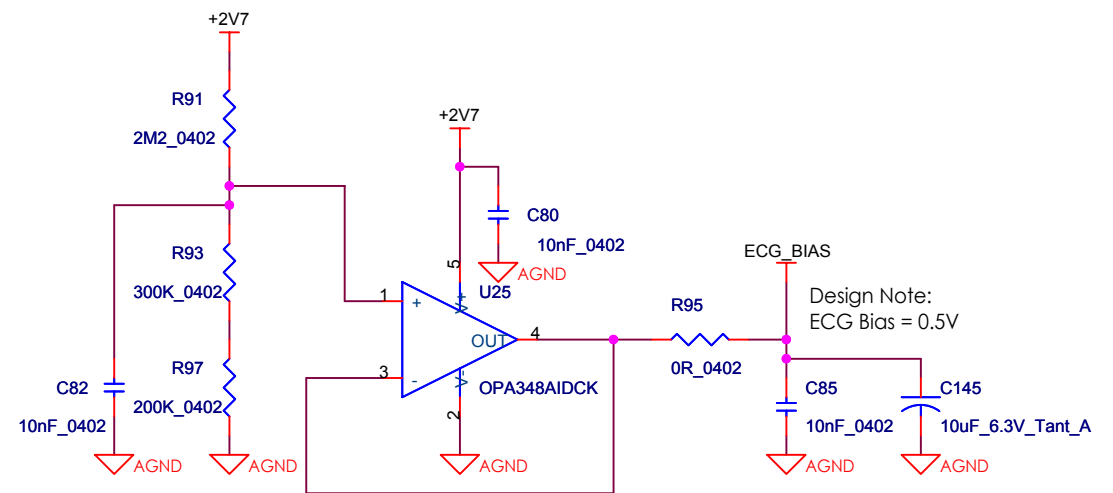
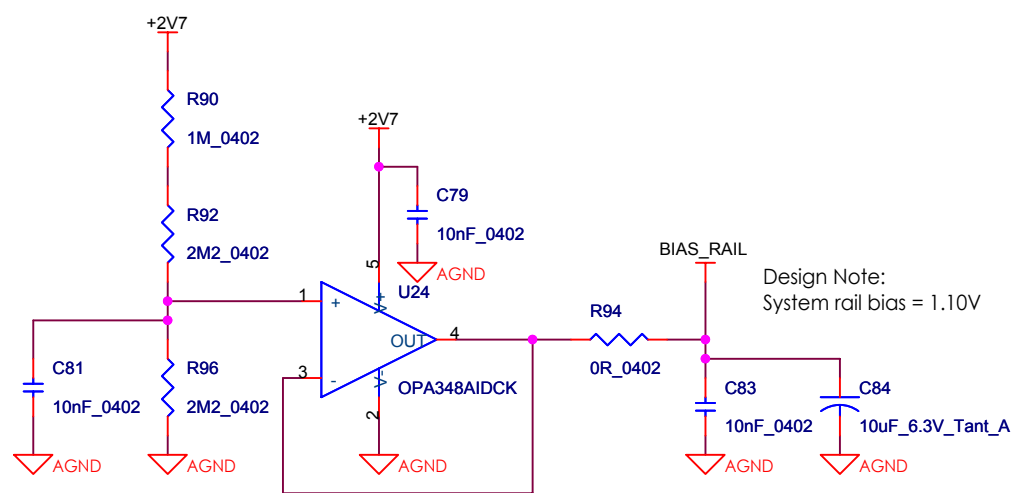
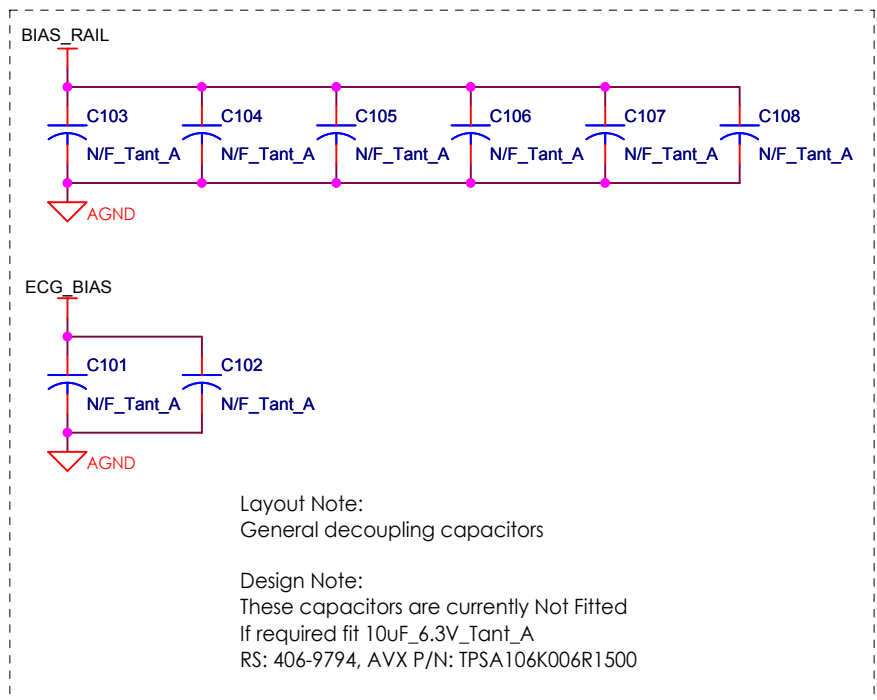
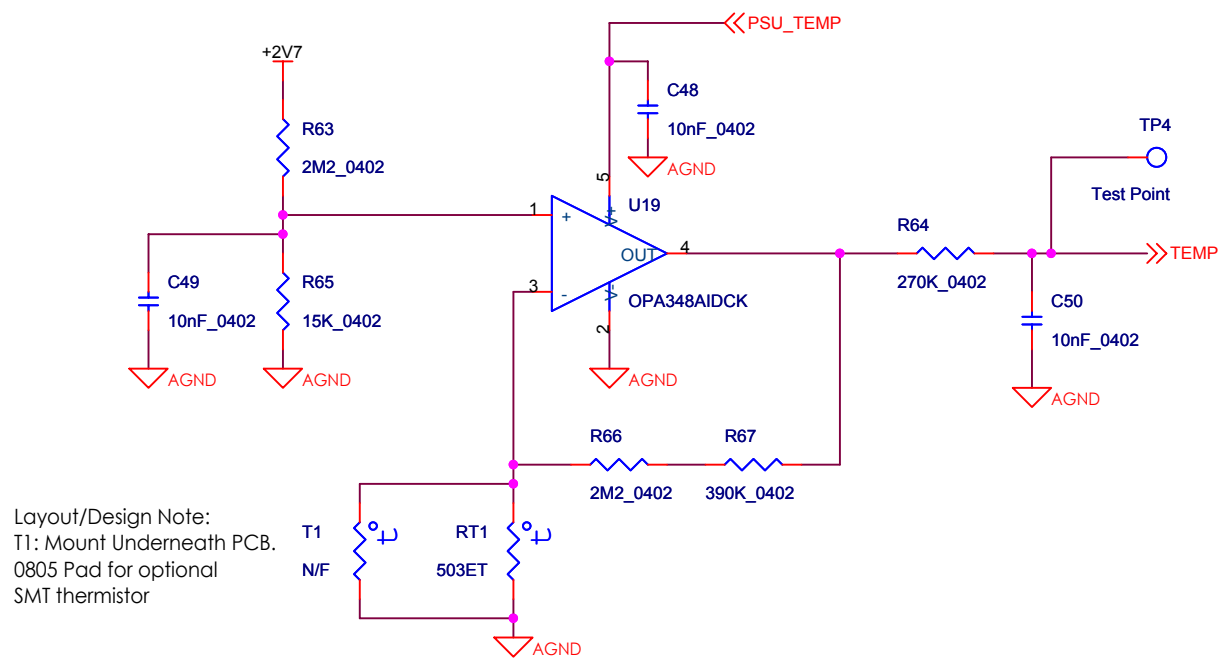
Design Note:
Flashing 'processor active' indicator
These parts will be N/F for
production/evaluation units

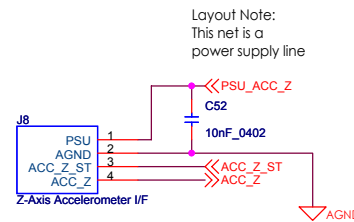
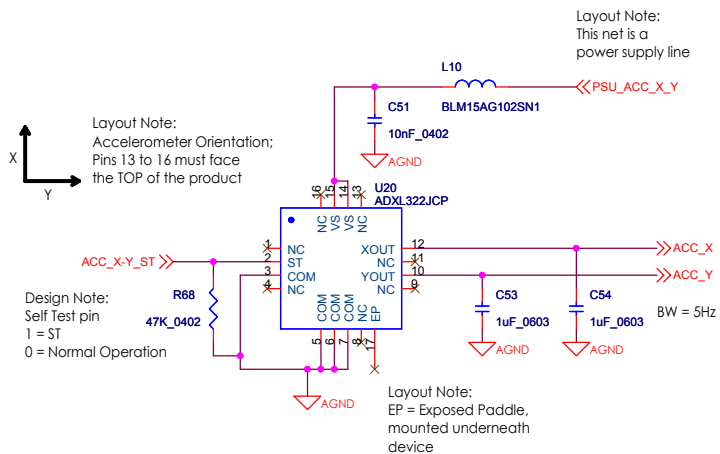
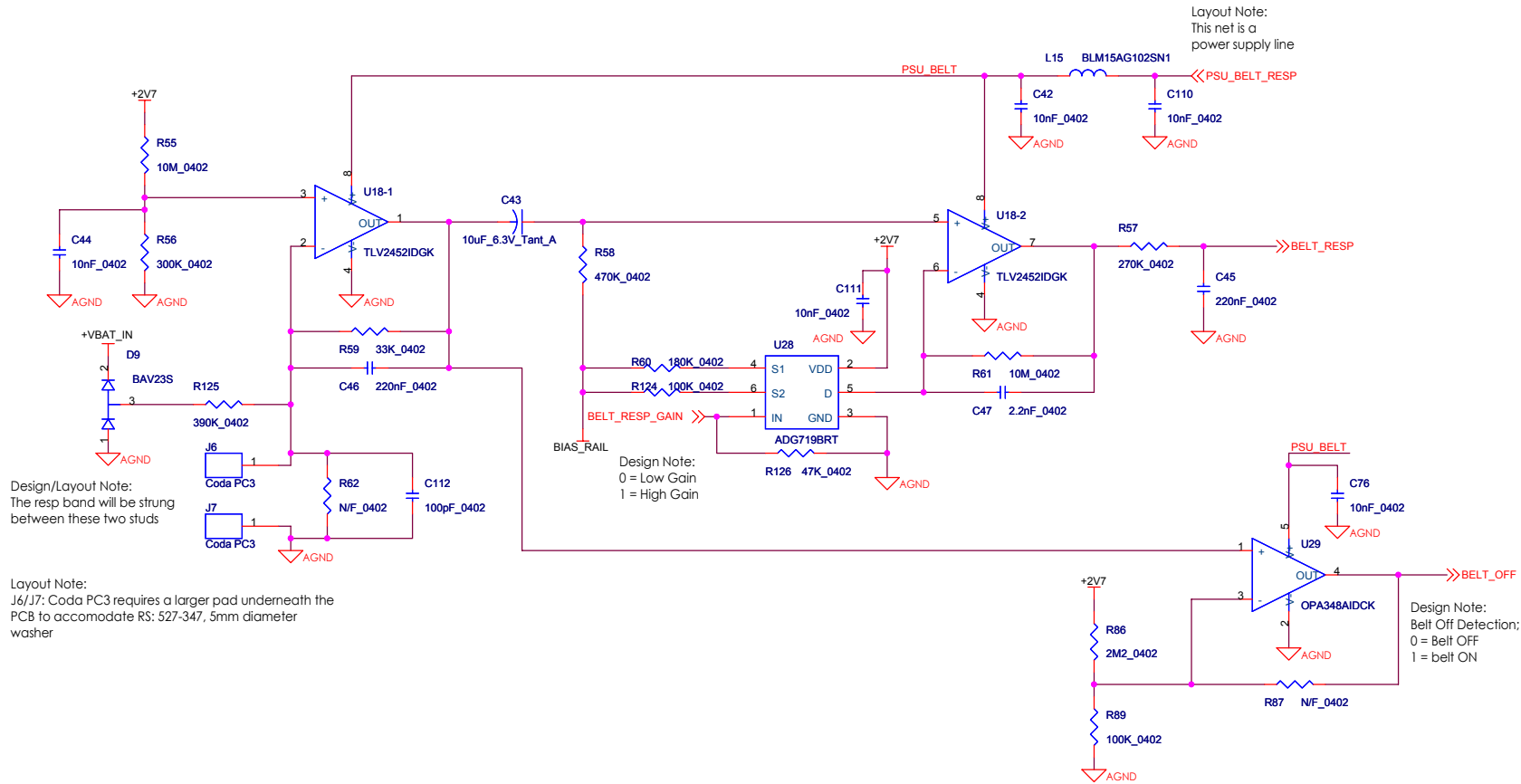


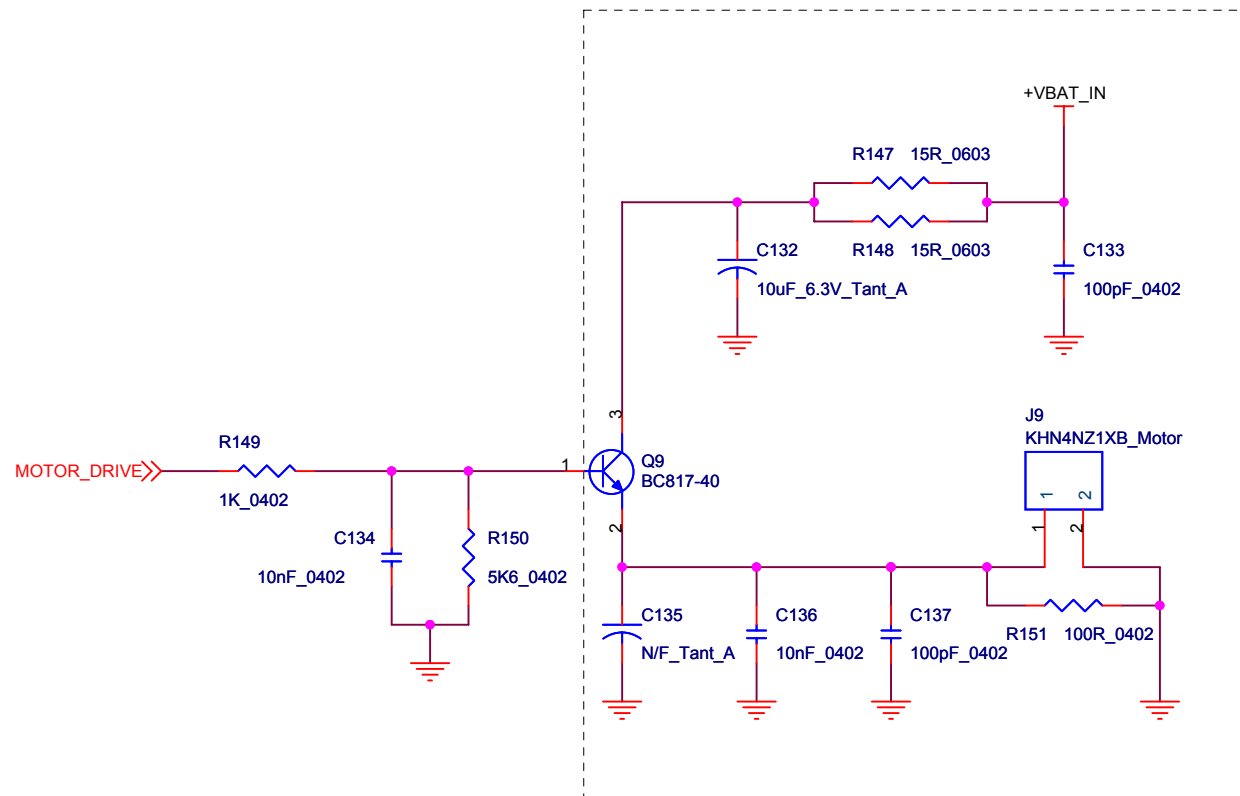












Layout Note:
At least 700mA (ideal 1Amp) tracking required in this area
The pads for the motor contacts must be blanked
off the paste mask