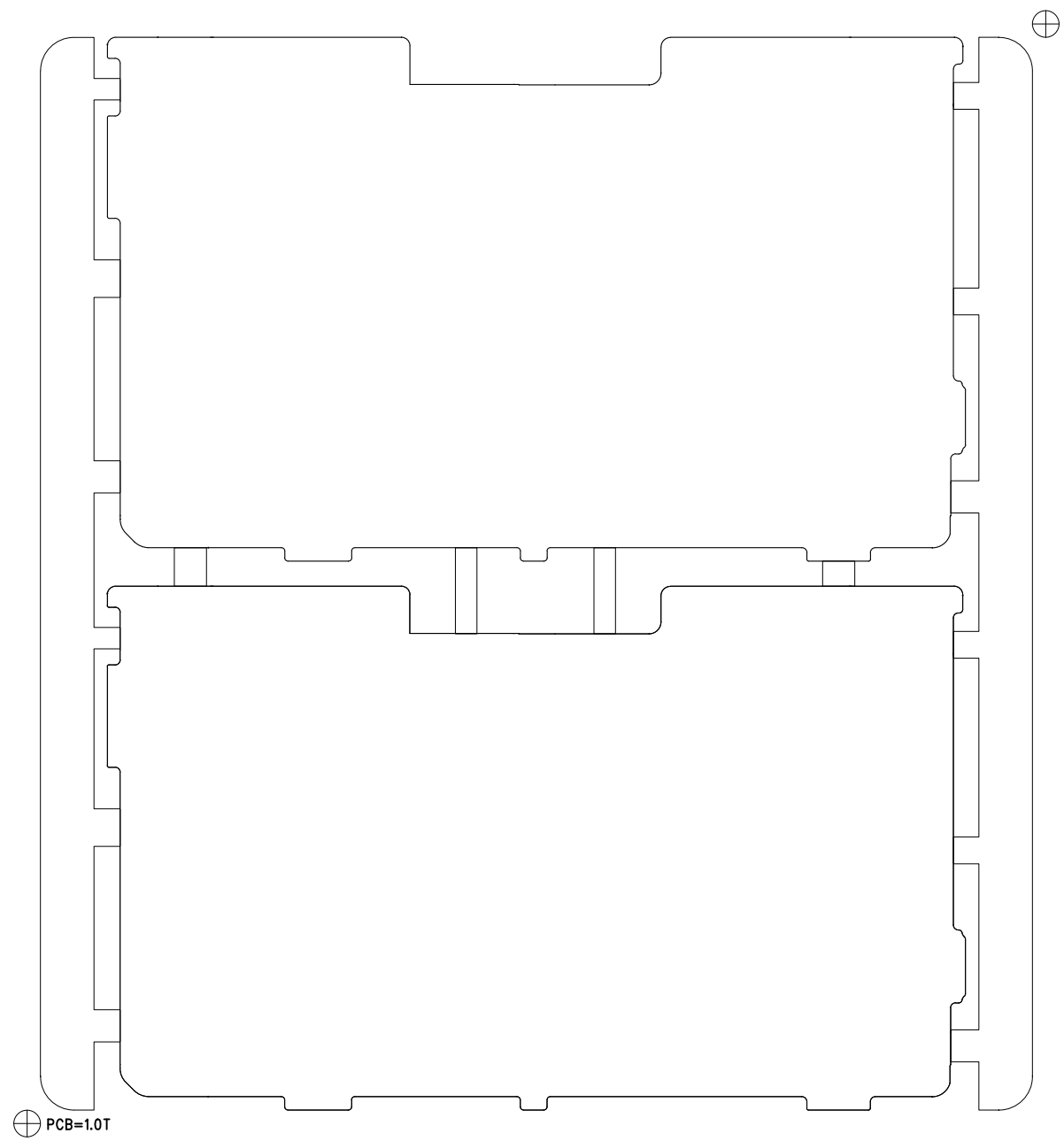
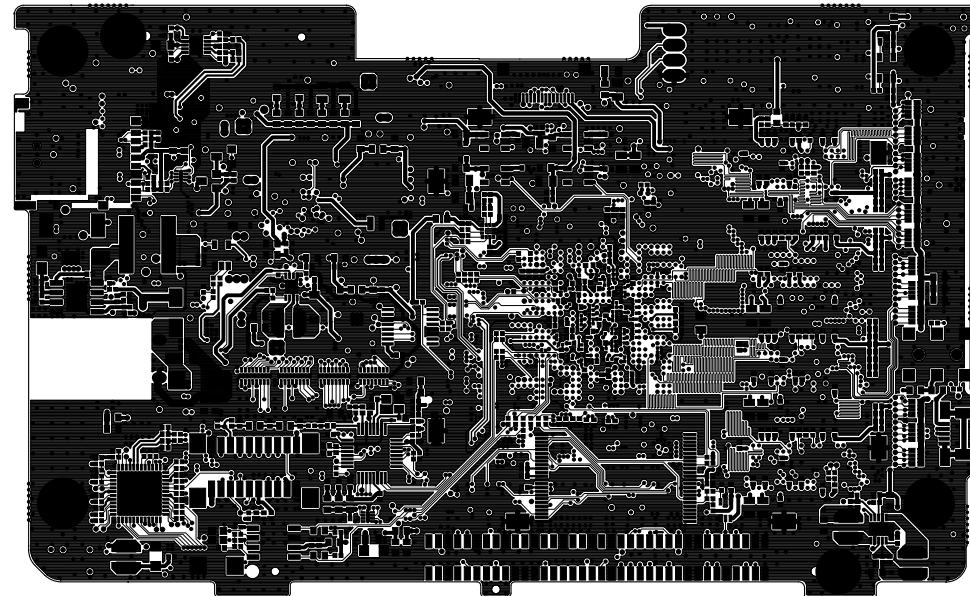




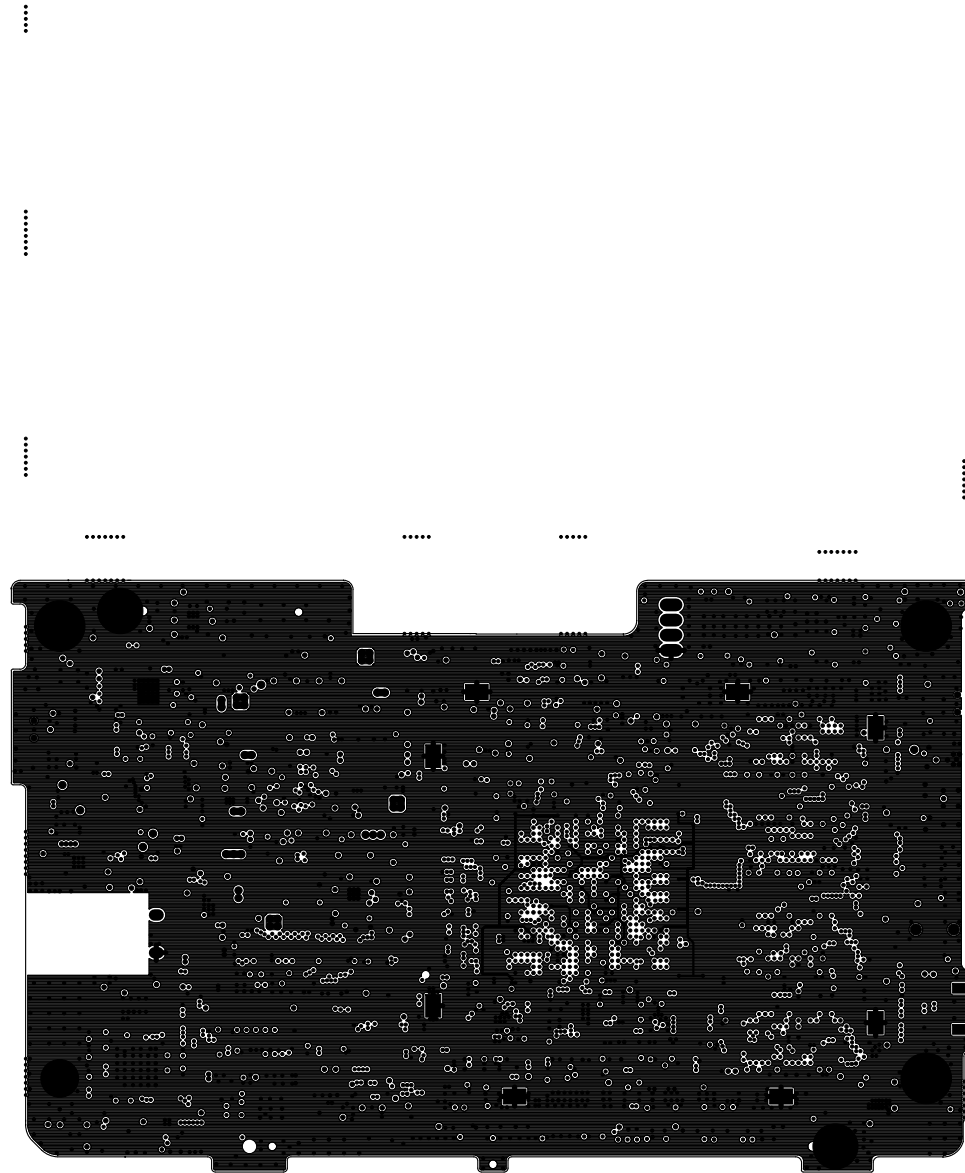


⊕ PCB=1.0T

SIZE	QTY	SW	PLATED	TOL
3	4	+	NO	+/-0.0
0.2	288	X	YES	+/-0.0
0.3	244	□	YES	+/-0.0
0.05	8	◇	NO	+/-0.0
1.6	1	X	NO	+/-0.0
1	2	□	NO	+/-0.0
3.2	2	+	YES	+/-0.0
0.4	128	+	NO	+/-0.0
3.3	4	+	YES	+/-0.0
1 x 2	8	+	YES	+/-0.0
0.6 x 1.3	2	+	YES	+/-0.0
0.8	6	+	YES	+/-0.0
0.5	38	+	YES	+/-0.0
1.5	2	+	NO	+/-0.0

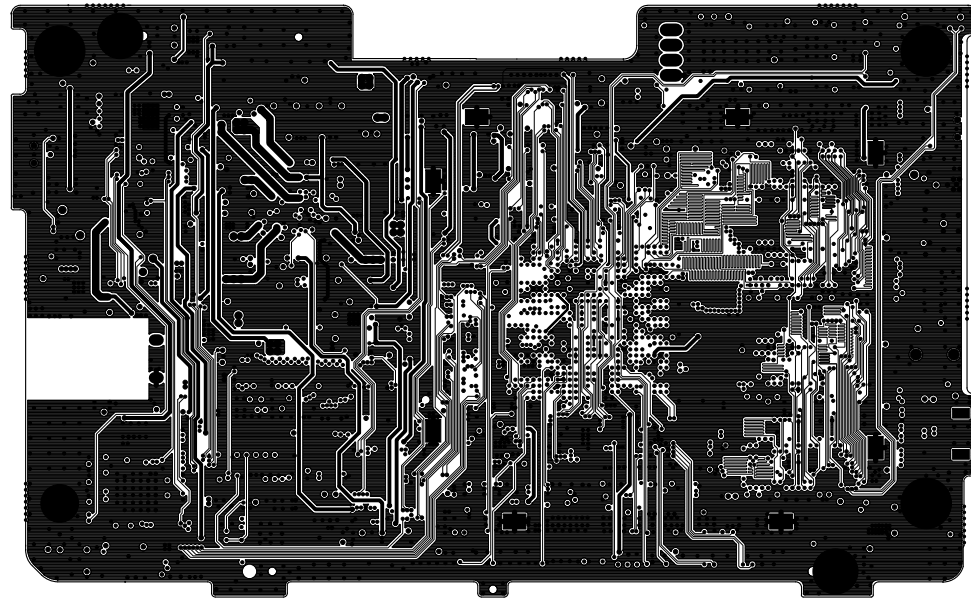
⊕ PCB=1.0T



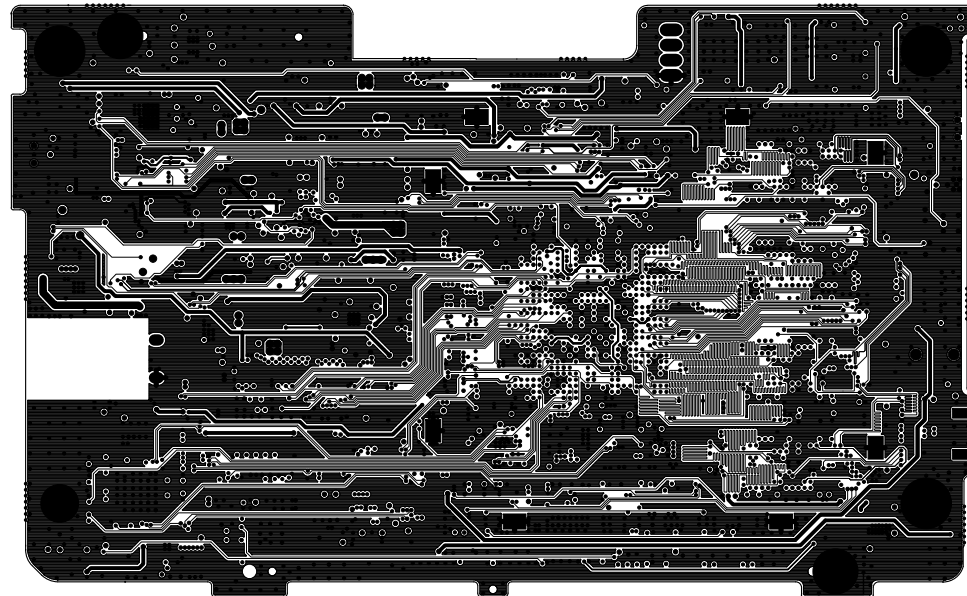


⊕ PCB=1.0T

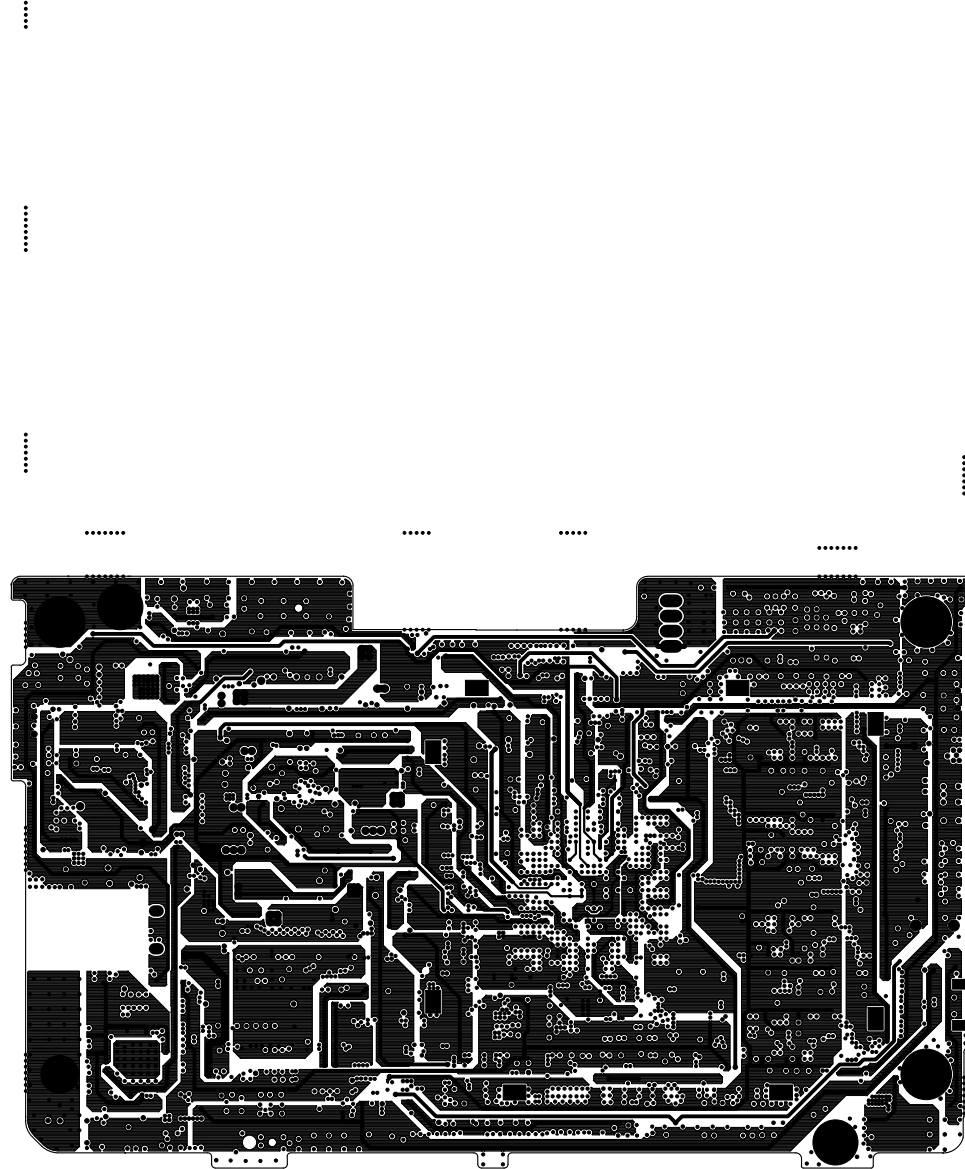




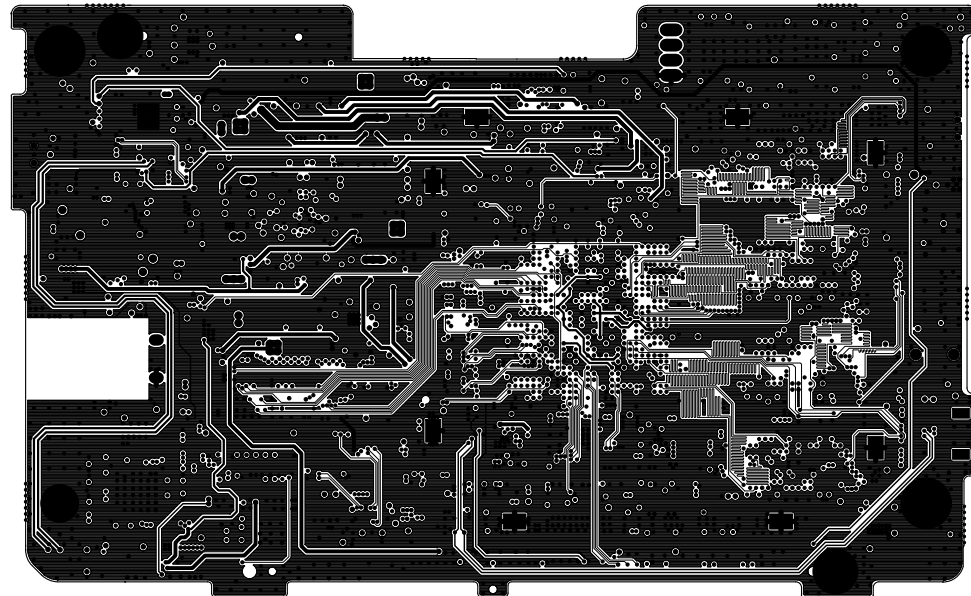
⊕ PCB=1.0T



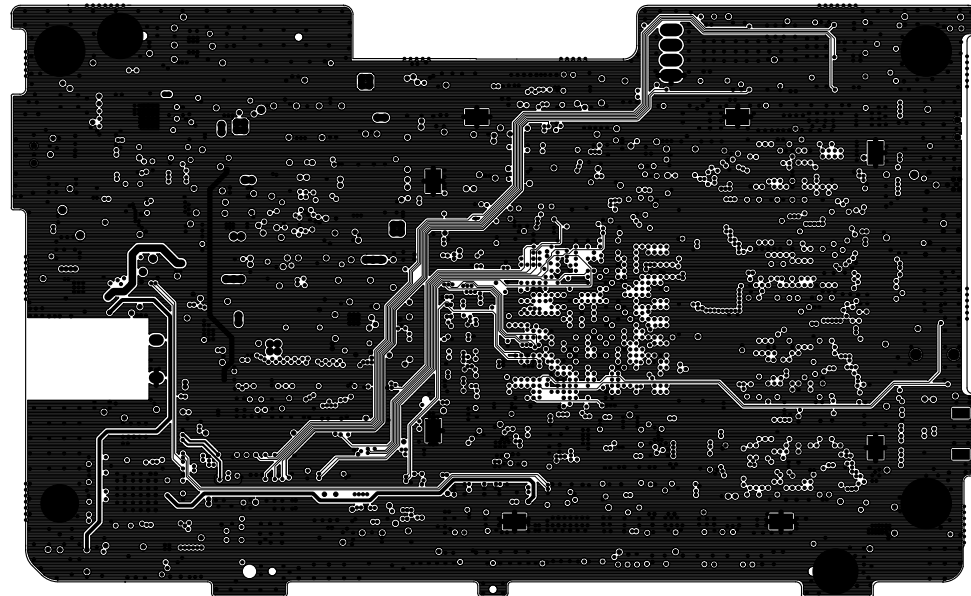
⊕ PCB=1.0T

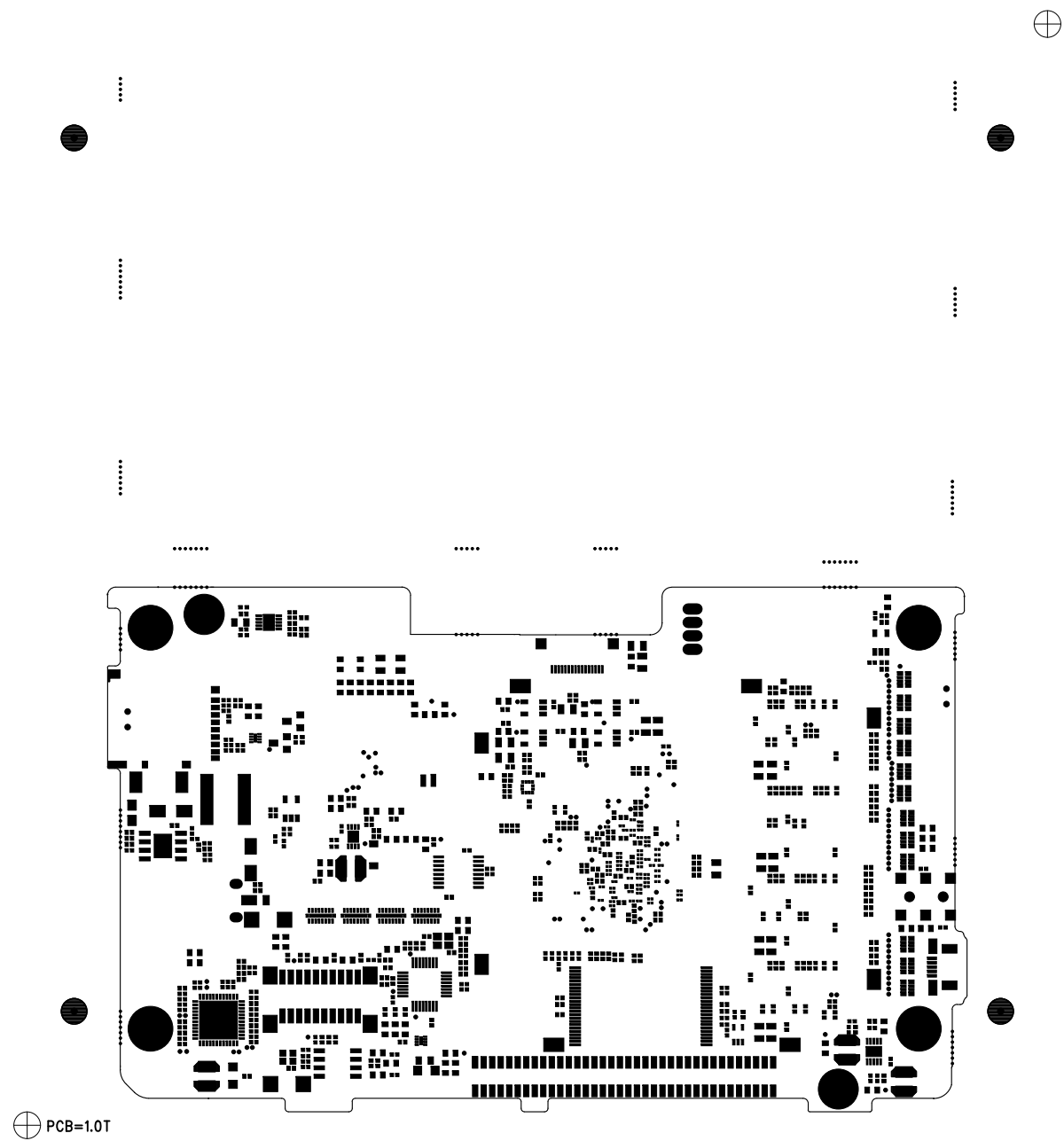


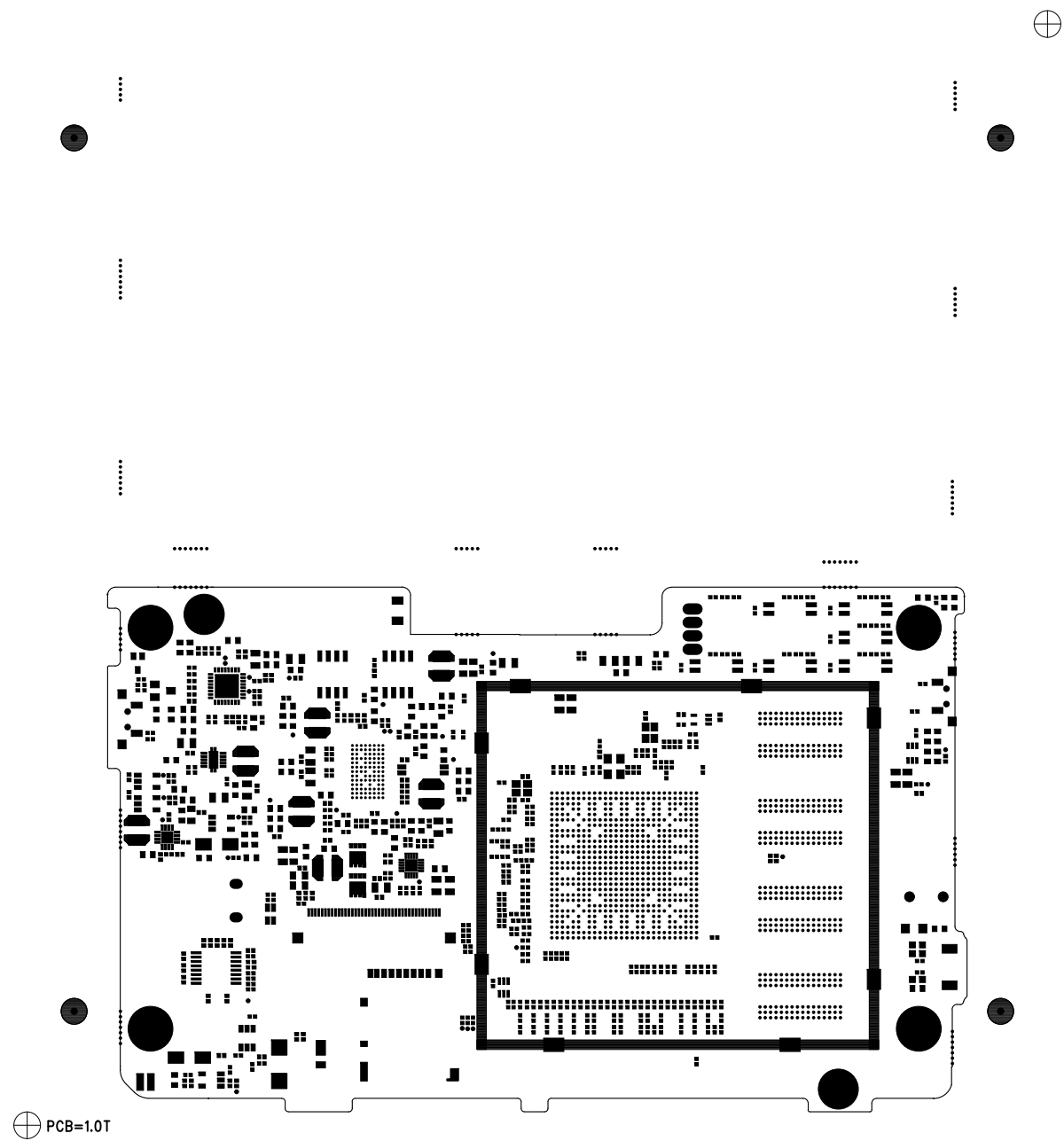
⊕ PCB=1.0T



⊕ PCB=1.0T

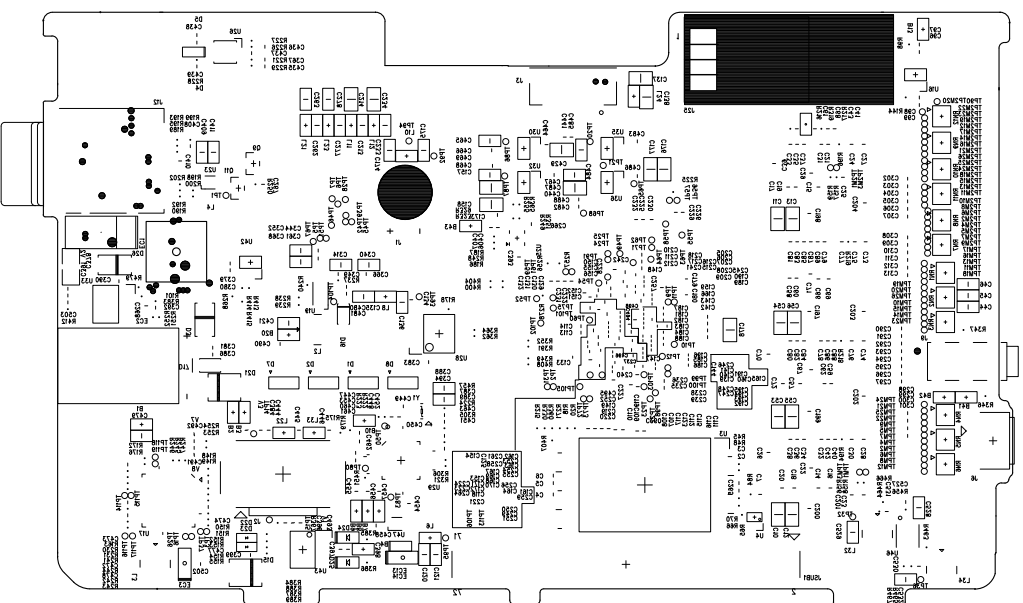






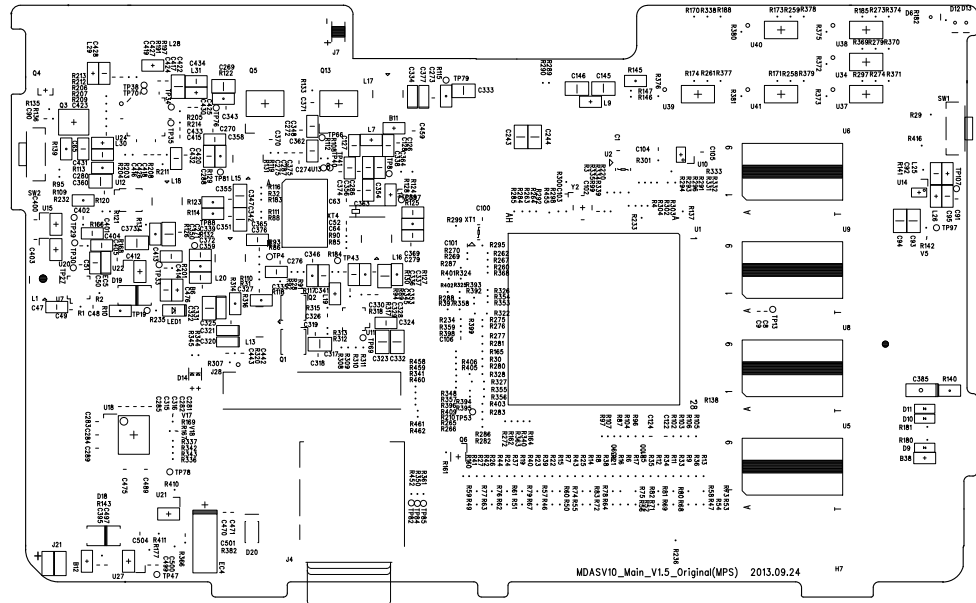


4S.E0.ET0S (29M)Ipnig10_3.IV_nioM_0IV2ADM





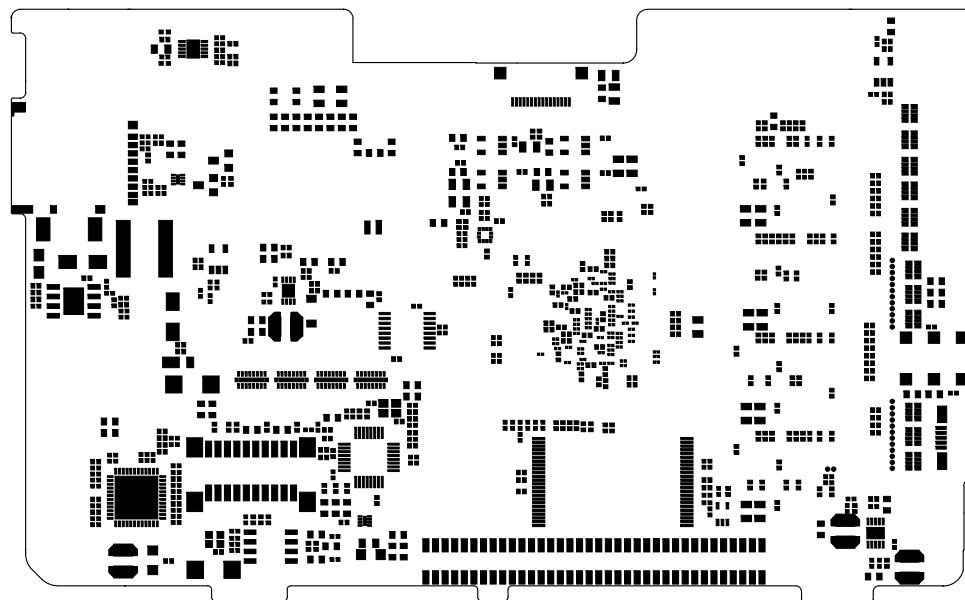
MDASV10_Main_V1.5_Original(MPS) 2013.09.24



PCB=1.0T

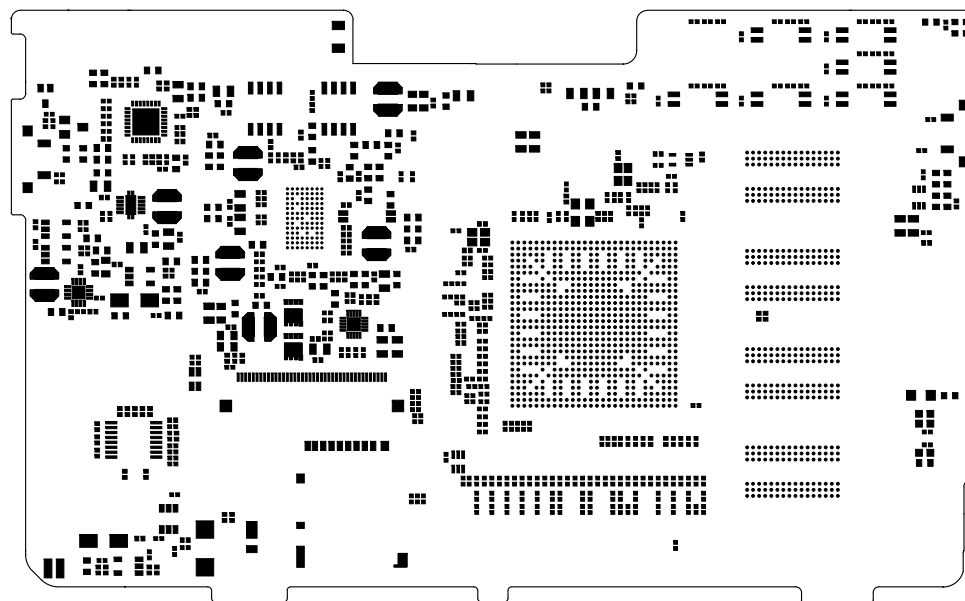


PCB=1.0T





PCB=1.0T



⊕ PCB=1.0T

