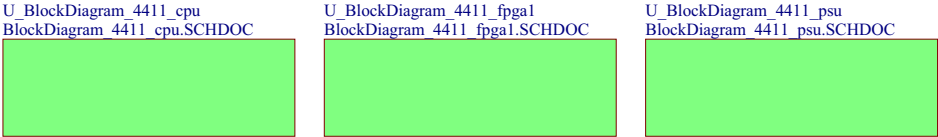


These components are located in their own RF shielded compartment

Bottom compartment of enclosure houses RFD4411

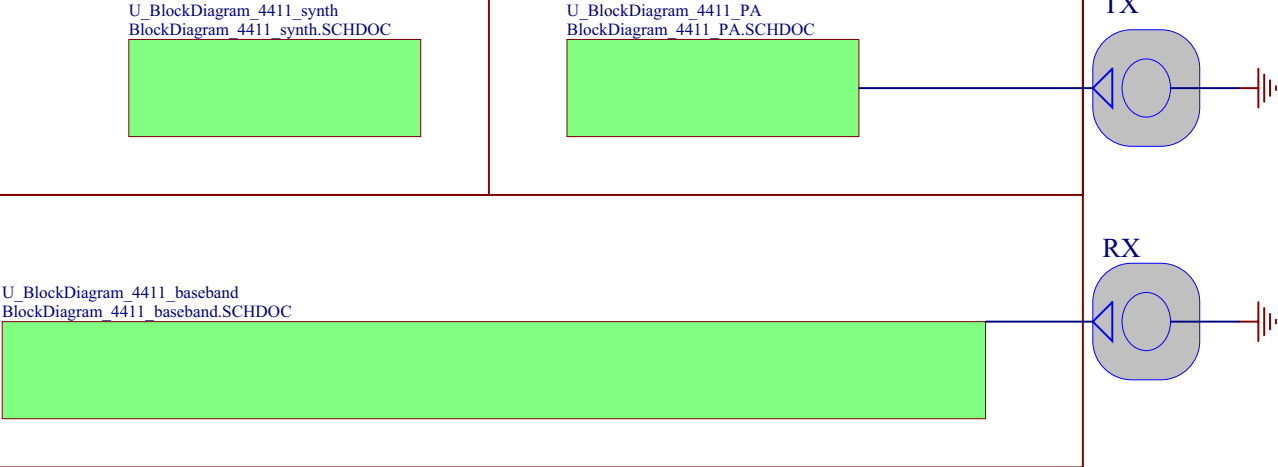


The MaxID RFID reader consist of the following PCBs:
RFD4411 - Digital section, Power supply, Digital decoder (FPGA), User Interface
RFM4410 - RF synthesizer, RF power amp. , Base Band signal conditioning

The Digital and RF PBCs are physically seperated from each other via an aluminium section

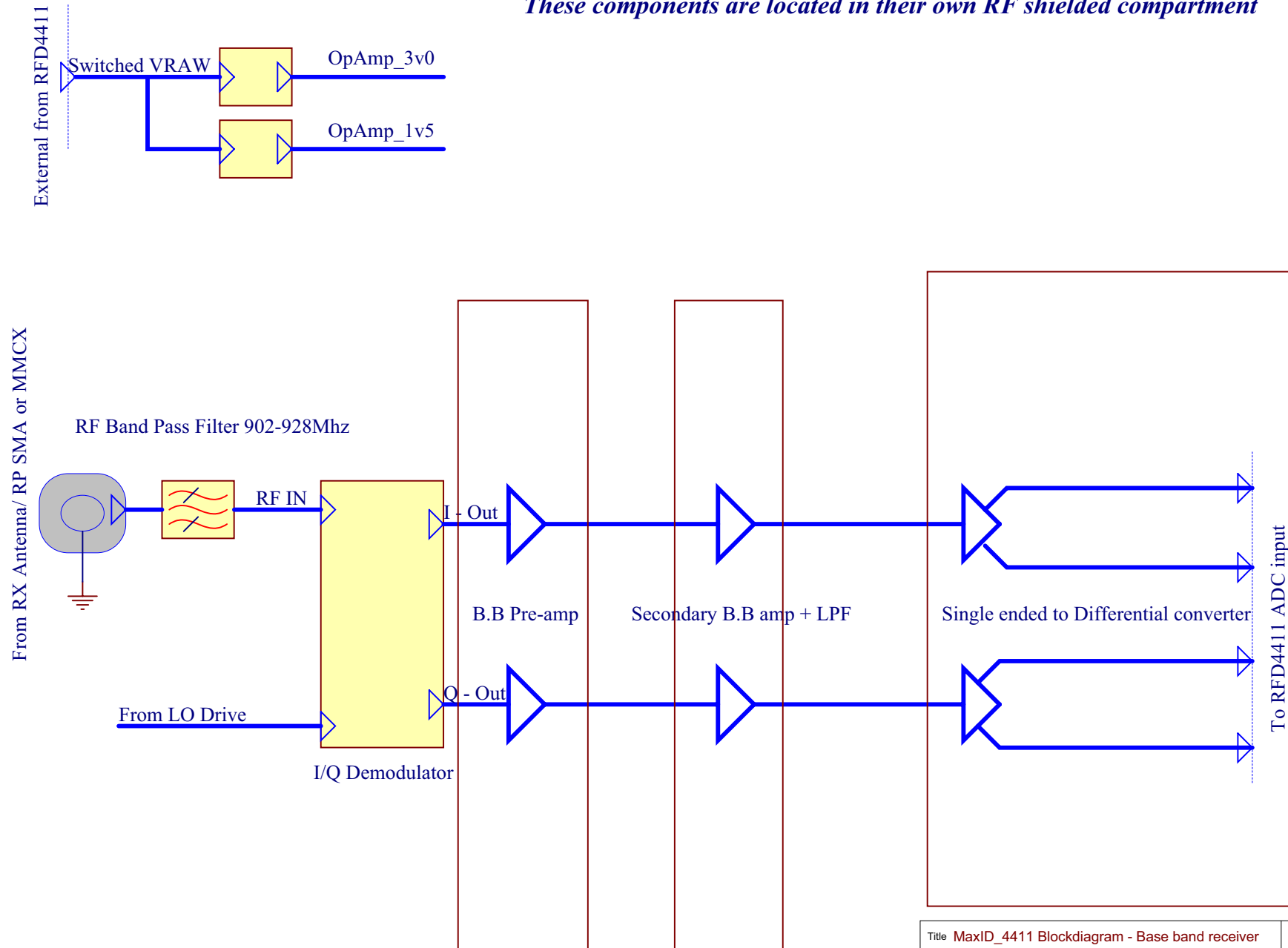
The RF section is also further seperated from each other via aluminium compartments.

Top compartment of enclosure houses RFM4410

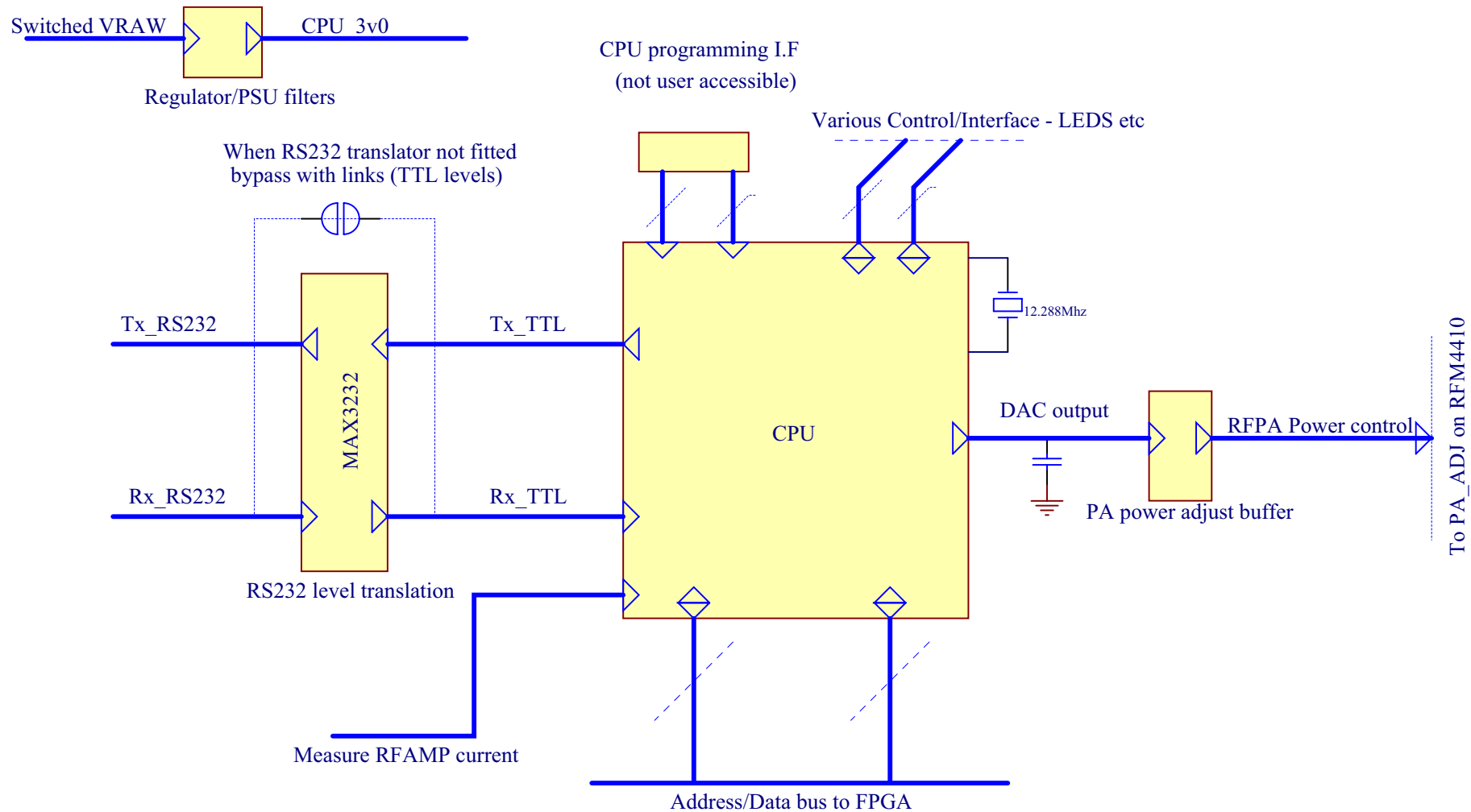


Title MaxID_4411 Blockdiagram			MaxID Limited Quatro House, Lyon Way Frimley Surrey GU16 7ER United Kingdom 
Size: A3	Number: RM100_4411	Revision: 00	
Date: 7/20/2005	Time: 2:14:19 PM	Sheet 1 of 7	
File: C:\work\Protel\Projects\MAXID_DXP\maxid_4411\Documents\PROJ\BlockDiagram_4411.SCHDOC			

These components are located in their own RF shielded compartment

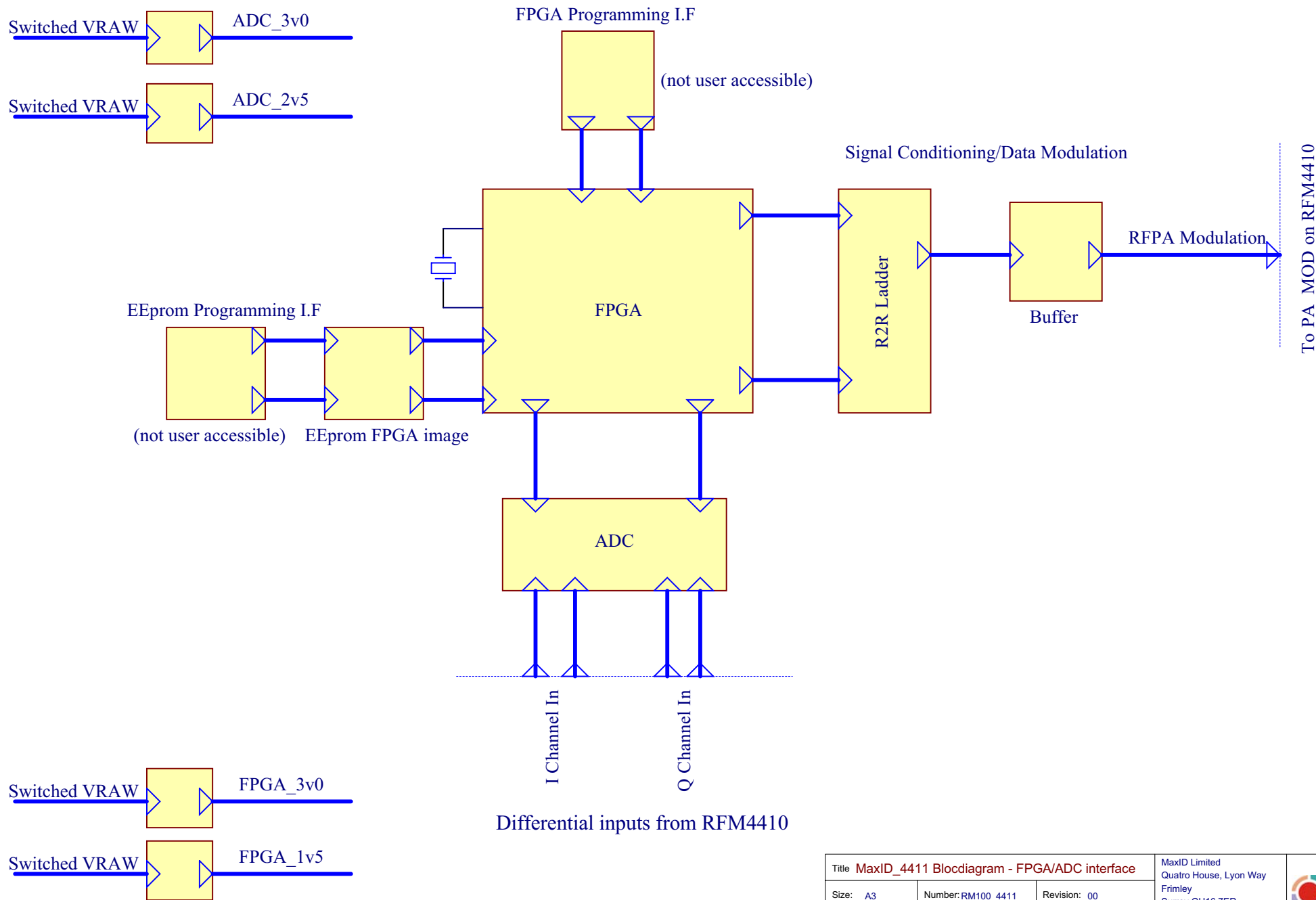


Title: MaxID_4411 Blockdiagram - Base band receiver			MaxID Limited Quatro House, Lyon Way Frimley Surrey GU16 7ER United Kingdom 
Size: A3	Number: RM100_4411	Revision: 00	
Date: 7/20/2005	Time: 2:14:19 PM	Sheet 6 of 7	
File: C:\work\Protel\Projects\MAXID_DXP\maxid_4411\Documents\PROJ\BlockDiagram_4411_baseband.SCHDOC			



Title MaxID_4411 Blockdiagram - CPU and Interface			MaxID Limited Quatro House, Lyon Way Frimley Surrey GU16 7ER United Kingdom
Size: A3	Number: RM100_4411	Revision: 00	
Date: 7/20/2005	Time: 2:14:20 PM	Sheet 2 of 7	
File: C:\work\Protel\Projects\MAXID_DXP\maxid_4411\Documents\SCH\BlockDiagram_4411 cpu.SCHDOC			





Title: MaxID_4411 Blocdiagram - FPGA/ADC interface

Size: A3

Number: RM100_4411

Revision: 00

Date: 7/20/2005

Time: 2:14:20 PM

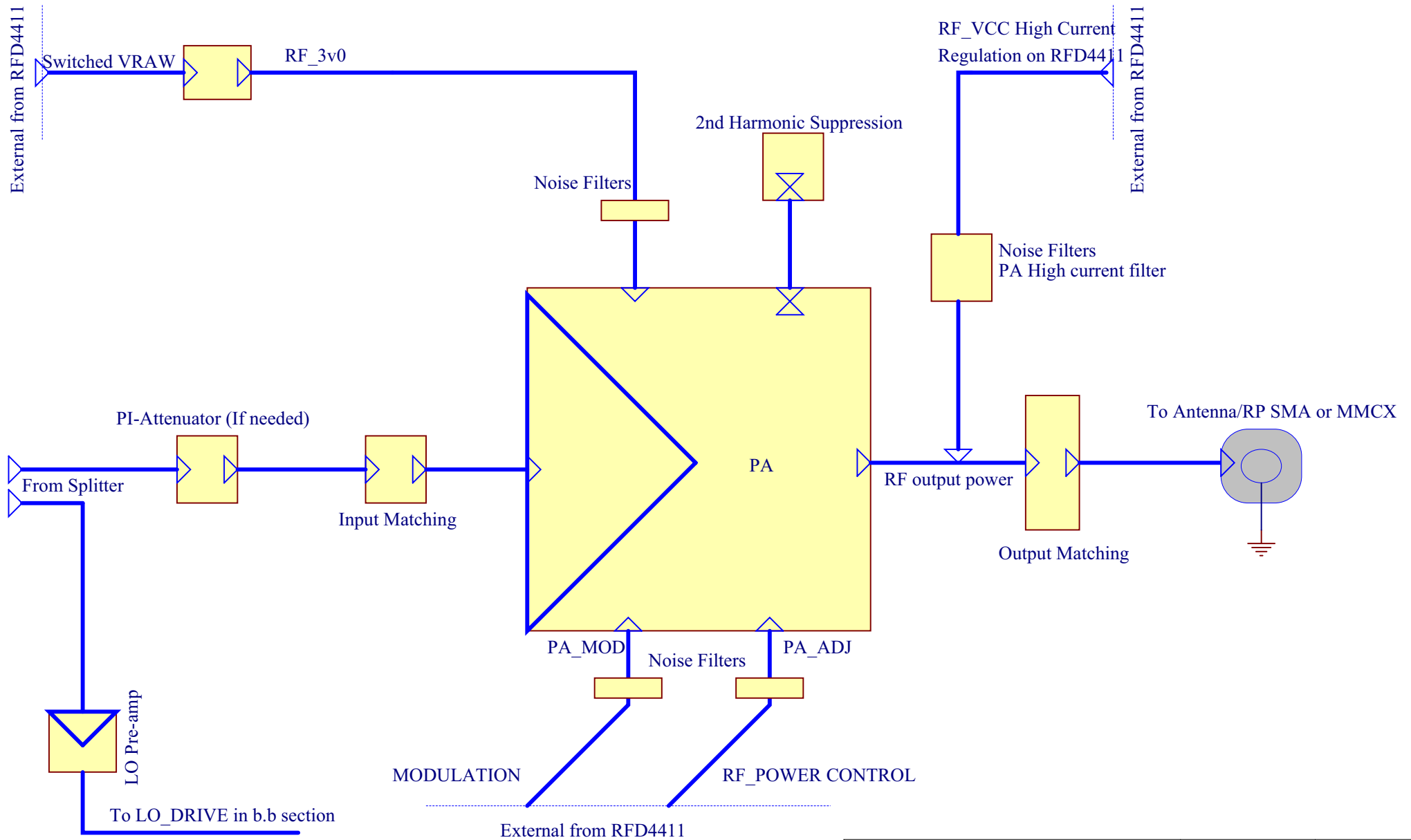
Sheet 3 of 7

File: C:\work\ProtelProjects\MAXID_DXP\maxid_4411\Documents\PROJ\BlockDiagram_4411_fpga1.SCHDOC

MaxID Limited
Quatro House, Lyon Way
Frimley
Surrey GU16 7ER
United Kingdom



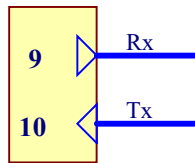
These components are located in their own RF shielded compartment



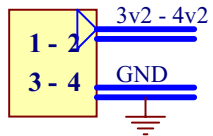
Title: MaxID_4411 Blockdiagram - RF Power Amp			MaxID Limited Quatro House, Lyon Way Frimley Surrey GU16 7ER United Kingdom 
Size: A3	Number: RM100_4411	Revision: 00	
Date: 7/20/2005	Time: 2:14:20 PM	Sheet 5 of 7	
File: C:\work\Protel\Projects\MAXID_DXP\maxid_4411\Documents\PROJ\BlockDiagram_4411_PA.SCHDOC			

External User interface

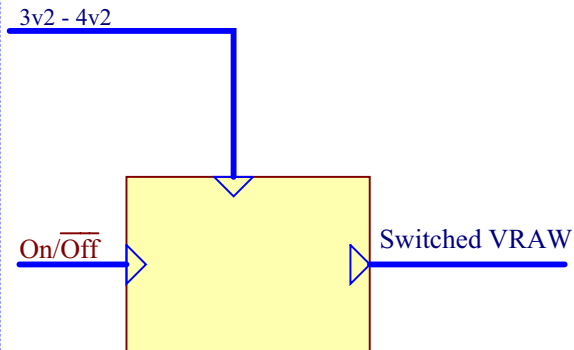
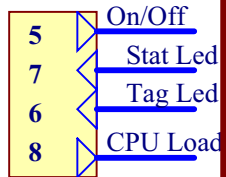
RS232 or TTL-3v0 level interface



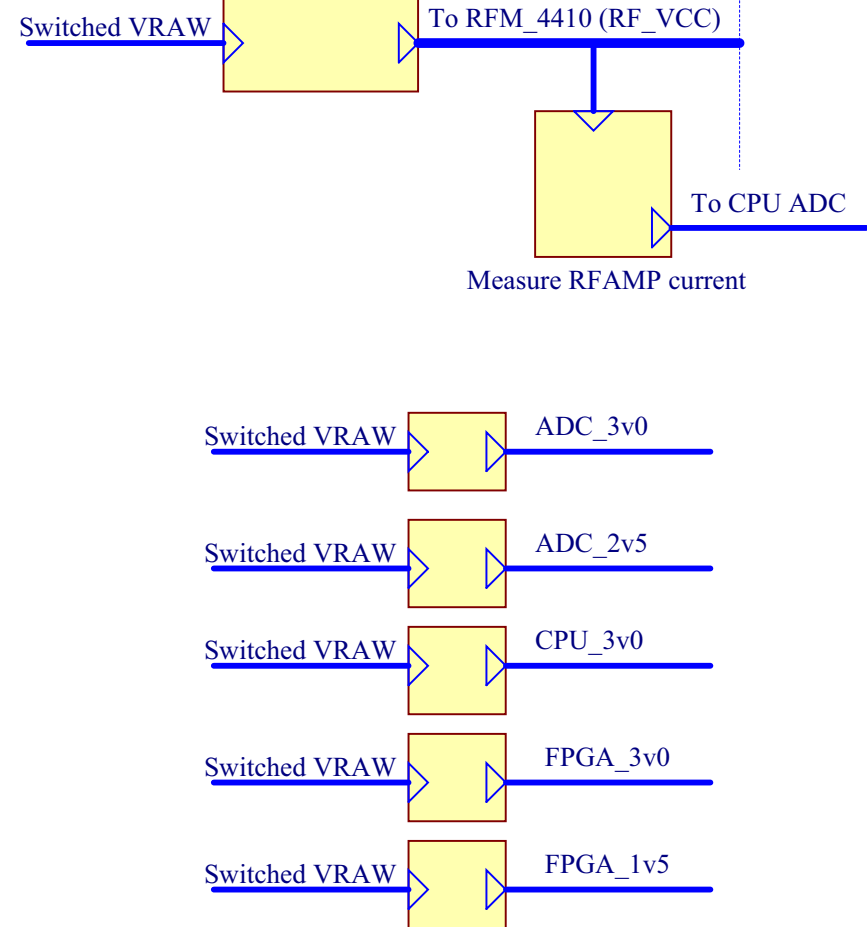
Ext. Power supply interface



Control/Status lines

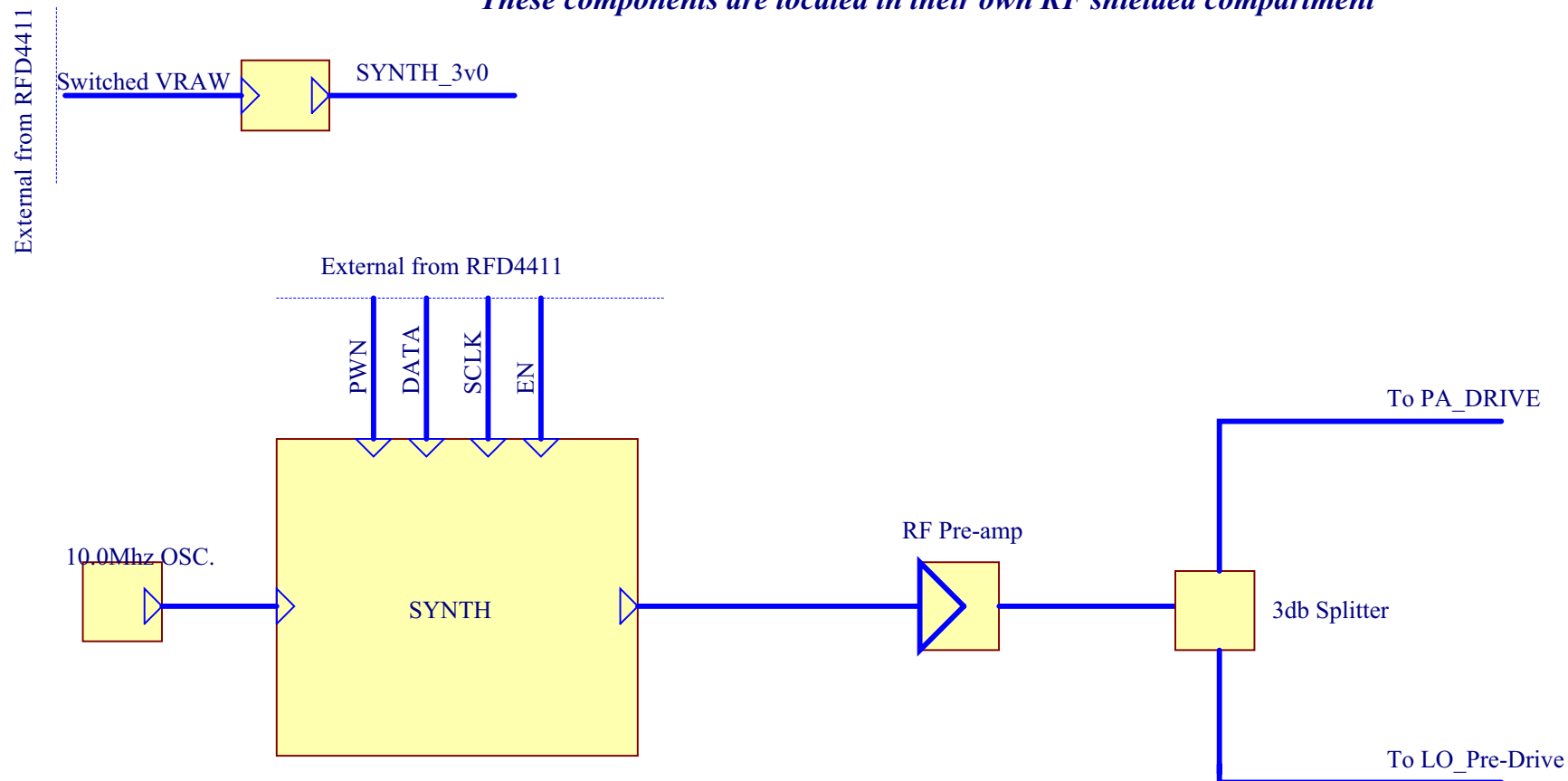


Regulators/PSU filters



Title: MaxID_4411 Blockdiagram - User interface/PSU			 MaxID Limited Quatro House, Lyon Way Frimley Surrey GU16 7ER United Kingdom
Size: A3	Number: RM100_4411	Revision: 00	
Date: 7/20/2005	Time: 2:14:21 PM	Sheet 7 of 7	
File: C:\work\Protel\Projects\MAXID_DXP\maxid_4411\Documents\PROJ\BlockDiagram_4411_psu.SCHDOC			

These components are located in their own RF shielded compartment



Title: MaxID_4411 Blockdiagram - RF Synth			 MaxID Limited Quatro House, Lyon Way Frimley Surrey GU16 7ER United Kingdom
Size: A3	Number: RM100_4411	Revision: 00	
Date: 7/20/2005	Time: 2:14:21 PM	Sheet 4 of 7	
File: C:\work\ProtelProjects\MAXID_DXP\maxid_4411\Documents\PROJ\BlockDiagram_4411_synth.SCHDOC			