

This device complies with
Part 15 of the FCC Rules.

MAC XXXXXXXXXXXXXXXX
Ver. 1.0
FCC ID: TFY0605AXM0367

MODE	CLK0	CLK1	CLKSEL	
Y1 W/PLL ON	OUT	OUT	1 AND 2 IN	*DEFAULT
Y1 W/PLL OFF	OUT	IN	1 AND 2 IN	
INT CLK W/PLL ON	IN	OUT	3 IN ONLY	
INT CLK W/PLL OFF	IN	IN	3 IN ONLY	
EXT CLK W/PLL ON	IN	OUT	4 AND 5 IN	
EXT CLK W/PLL OFF	IN	IN	4 AND 5 IN	
X1 CLK W/PLL ON	IN	OUT	5 AND 6 IN	
X1 CLK W/PLL OFF	IN	IN	5 AND 6 IN	

CLOCK MODE SETTINGS

JP1	PULL-UP ENABLE (IN)	
1	QSPI_SCLK/SCL	*DEFAULT=
2	QSPI_CS0/SDA	ALL OUT
3	CANRX/SDA	
4	CANTX/SCI	

I2C PULL-UP TABLE

VRL=IN
VRH=IN
VDDA=IN
3.3V_EN=IN
VDD_EN=IN
VDD_PLL=IN
VSTBY=IN
VX_EN=OUT
LED_EN=IN
UART0_EN=ALL IN
UART1_EN=ALL IN
UART2_EN=ALL IN
COMM_SEL=CAN
CAN_EN=ALL IN
CAN_TERM=IN
ZIGBEE_EN=OUT

DEFAULT SETTINGS

MODE	BDM_EN	JP2	
BDM MODE	IN	1-2	*DEFAULT
JTAG MODE	OUT	2-3	

BDM PORT MODE (SEE USER MANUAL)

IN=JUMPER INSTALLED ON BOTH PINS
OUT=JUMPER OFF OR IDLE ON 1 PIN

AXM-0367 REV C

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