

IC: 6062A-52081EV8
FCC ID: TKN52081EV8
This Device complies with Part 15
of the FCC Rules.



CONFIG SWITCH (SW1) SETTINGS

SW Position	Function
OFF	166.67/83.33MHz Operation
DN	88/44MHz Operation
ON	Crystal oscillator mode
ON	Oscillator bypass mode
OFF, OFF	16-bit boot port
DN, DN	8-bit boot port
OFF, DN	32-bit boot port
DN, OFF	High output drive strength
ON	Low output drive strength
OFF	Normal PLL mode
DN	Loop mode
OFF	16MHz clock input
DN	16.62MHz clock input
OFF	AC20221 = AC20221
DN	AC20221 = *CS0541

Default: All switches OFF

SRAM

JUMPER SETTINGS

Label	Setting	Function
J1	#1-2/2-3	#Enable/Disable BDM
JP1	#1-2/2-3	Connect #BDMCLK/TCLK to BDM (CN2)
JP2	#ON/OFF	Connect 1.5V to BDM (CN2) pin 29
JP3	#ON/OFF	Boot #uCLinux /cBUG
JP4	#ON/OFF	#Disable/Enable TEST mode
JP5	#ON/OFF	#Enable/Disable reset config
JP6	#ON/OFF	#DDR/SDR mode
JP7	#ON/OFF	Connect Core, PLL to 1.5V
JP8	#ON/OFF	Connect Core to 1.5V
JP9	#ON/OFF	Connect MPU I/O rail to 3.3V
JP10	#ON/OFF	Connect MPU ext bus to 2.5V
JP11	#1-2/2-3	#Oscillator/External clock source
JP12	#1-2/2-3	#Crystal/Oscillator clock source
JP13	#ON/OFF	#Enable/Disable TOUT3 LED
JP14	#ON/OFF	#Enable/Disable TOUT2 LED
JP15	#ON/OFF	#Enable/Disable TOUT1 LED
JP16	#ON/OFF	#Enable/Disable TOUT0 LED

Default setting marked with '#'