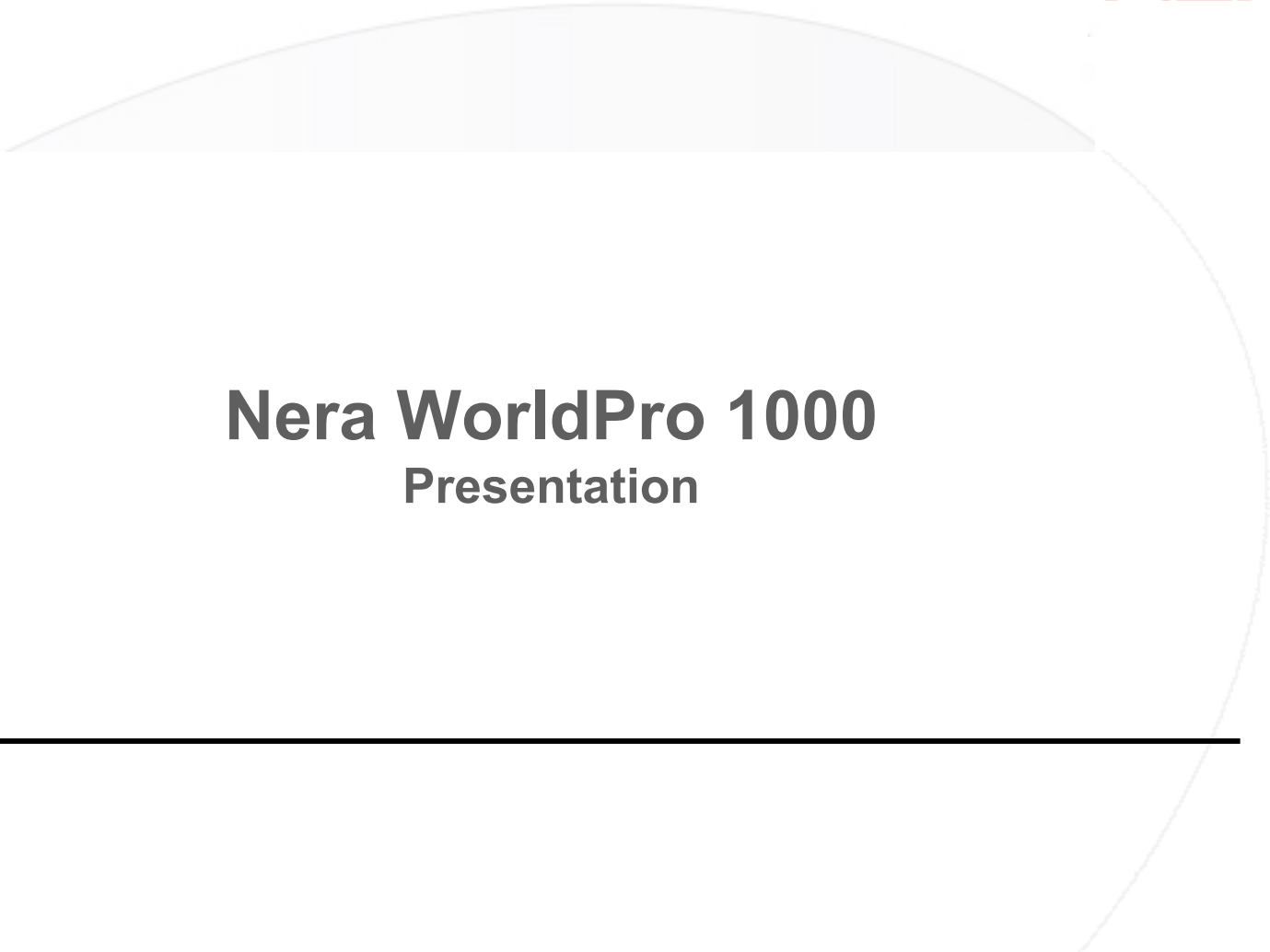


A large, light grey decorative graphic is positioned in the background. It features a semi-circular arc at the top, which transitions into a larger, more complex curve that sweeps from the top left towards the bottom right, framing the central text.

Nera WorldPro 1000

Presentation

The Nera BGAN Class3 terminal

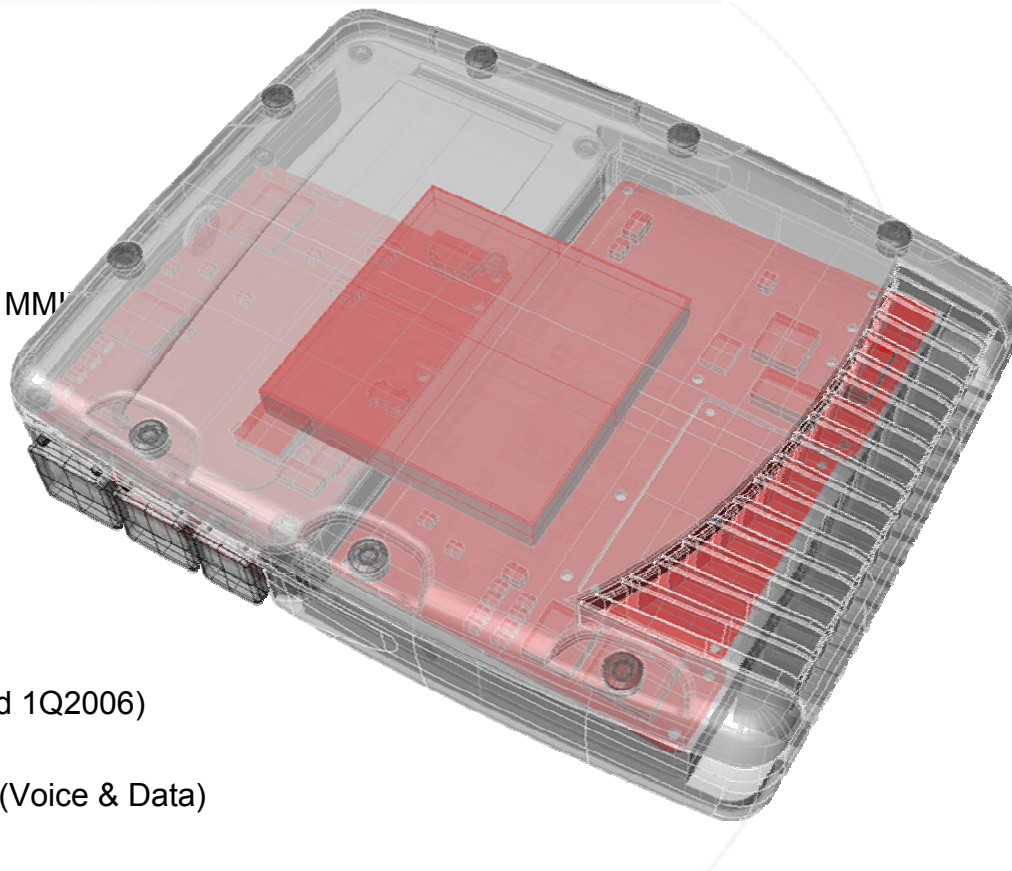


The BGAN PUT ODU consists of the following parts:

- Inmarsat I4 antenna
- GPS antenna
- RF and IF circuits
- DSP processors
- FPGA circuit
- Reference frequency oscillator
- Mechanics
- Buzzer, button and LED (On box MM)

The IDU consists of:

- CPU processor
- SIM/USIM card interface
- FPGA circuit
- Buzzer and LEDs
- Multi purpose button
- USB interface (or ;)
 - Ethernet Interface (planned 1Q2006)
- ISDN interface (for Voice)
- Bluetooth interface with antenna (Voice & Data)
- Battery with charging circuitry
- Mechanics



Hardware Building Blokcs

