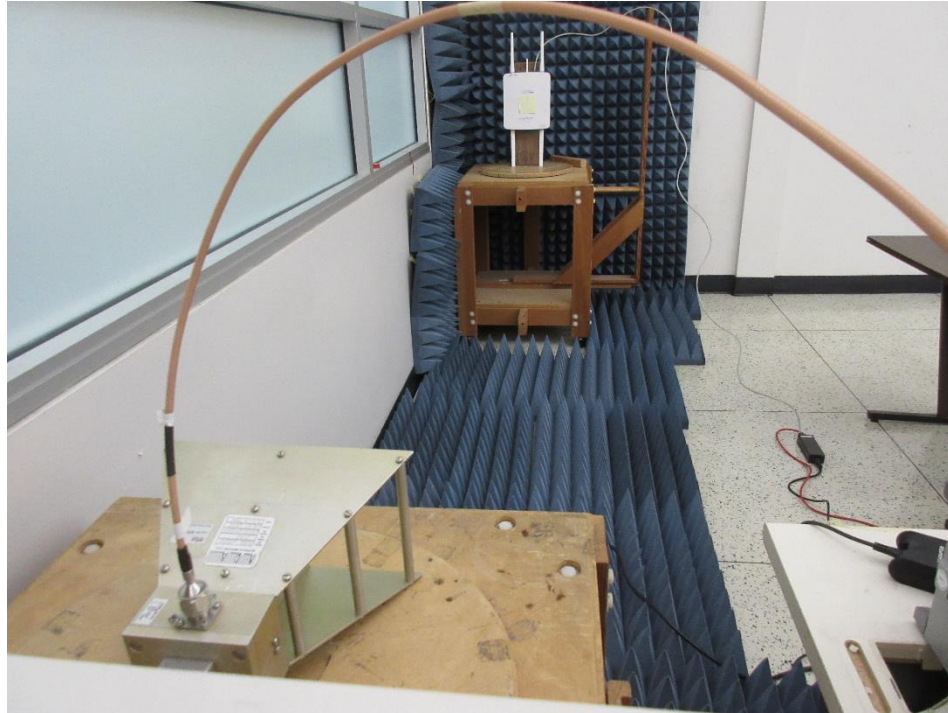


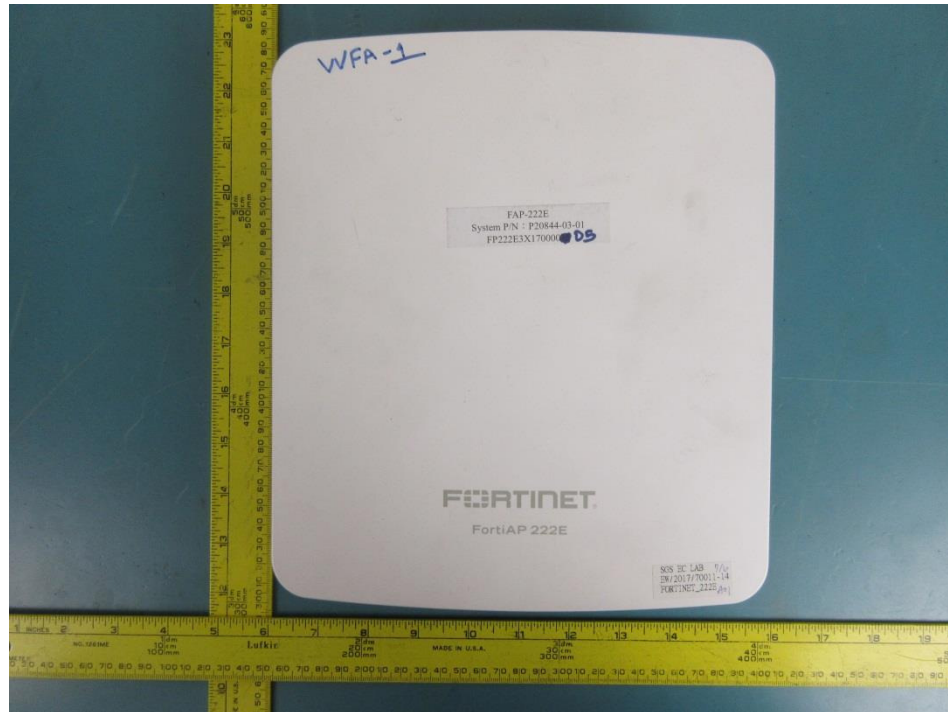
1 Annex B (Normative) – Test Setup Photographs

1.1 Setup Photo

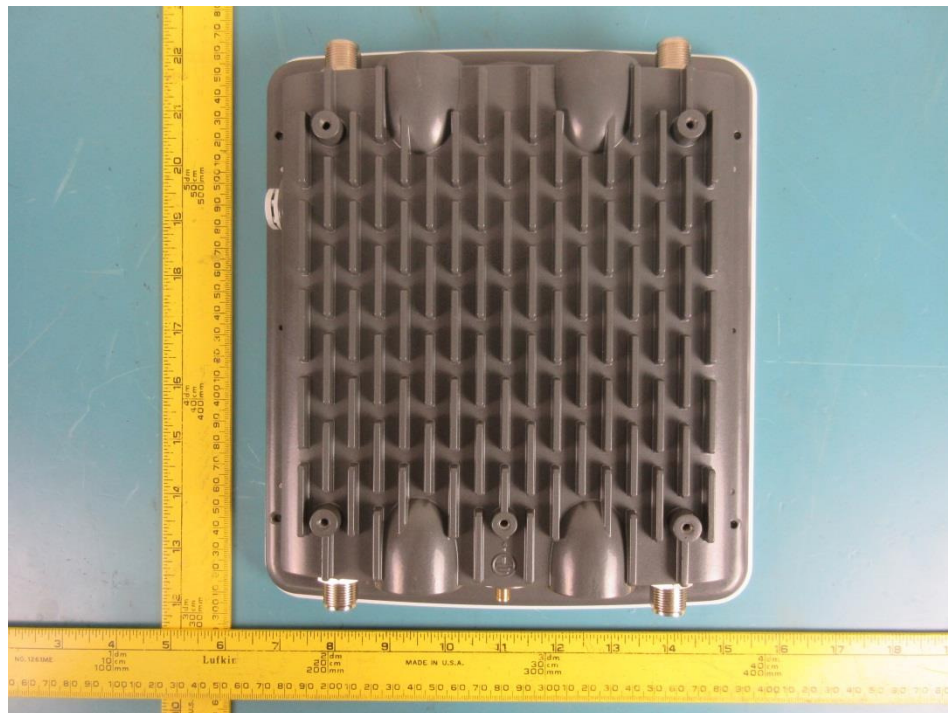


4 Annex C (Normative) – EUT Photographs

4.1 EUT – External Front View



4.2 EUT – External Back View



4.3 EUT – External Top Side View



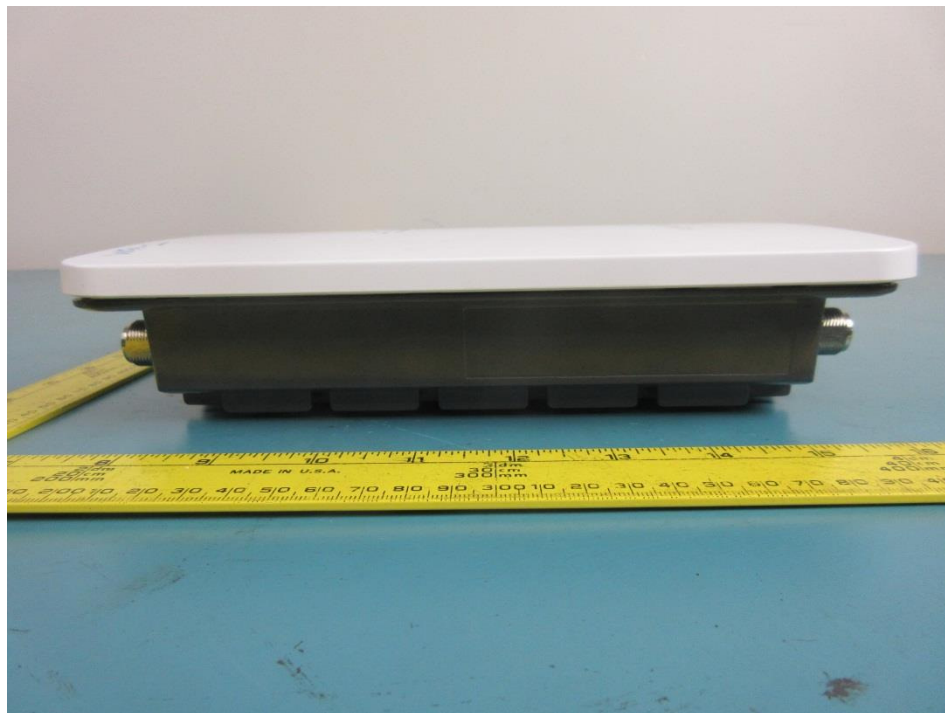
4.4 EUT – External Bottom Side View



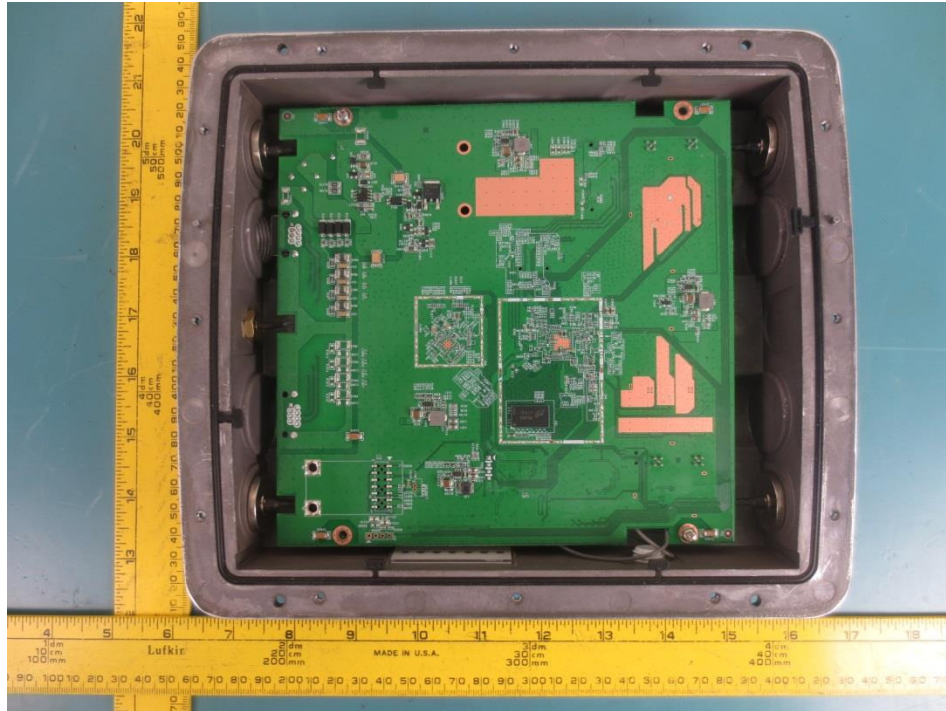
4.5 EUT – External Right Side View



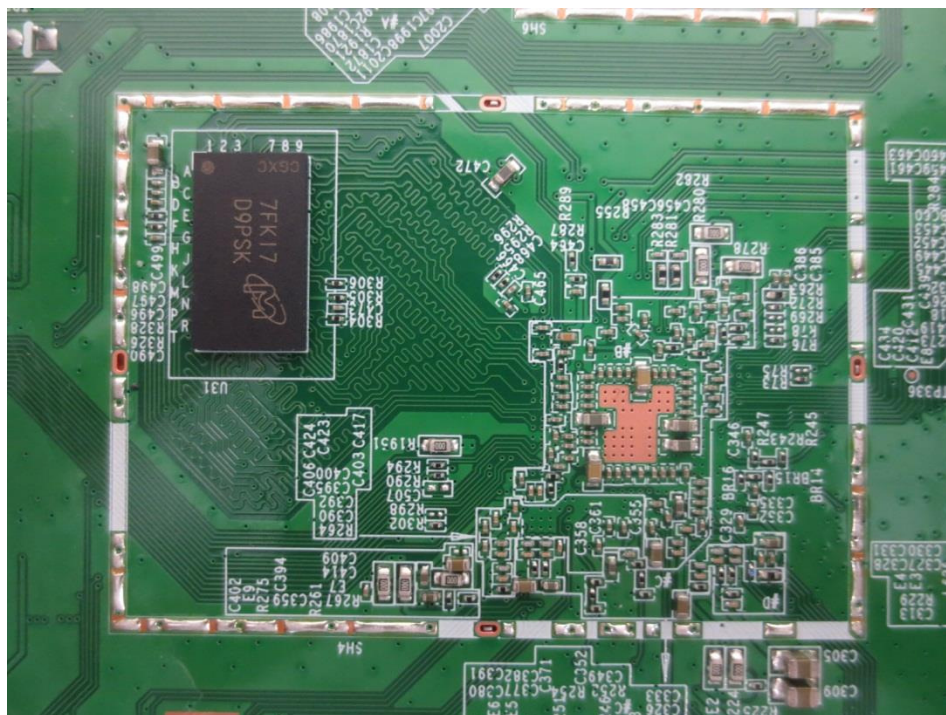
4.6 EUT – External Left Side View



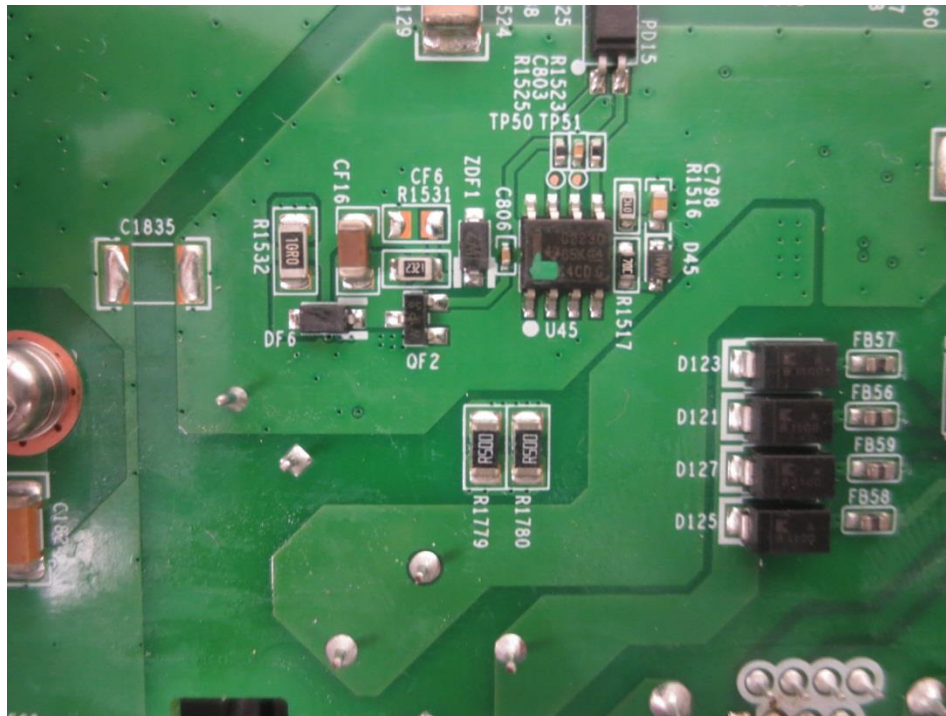
4.7 EUT – Main PCB Board Top View



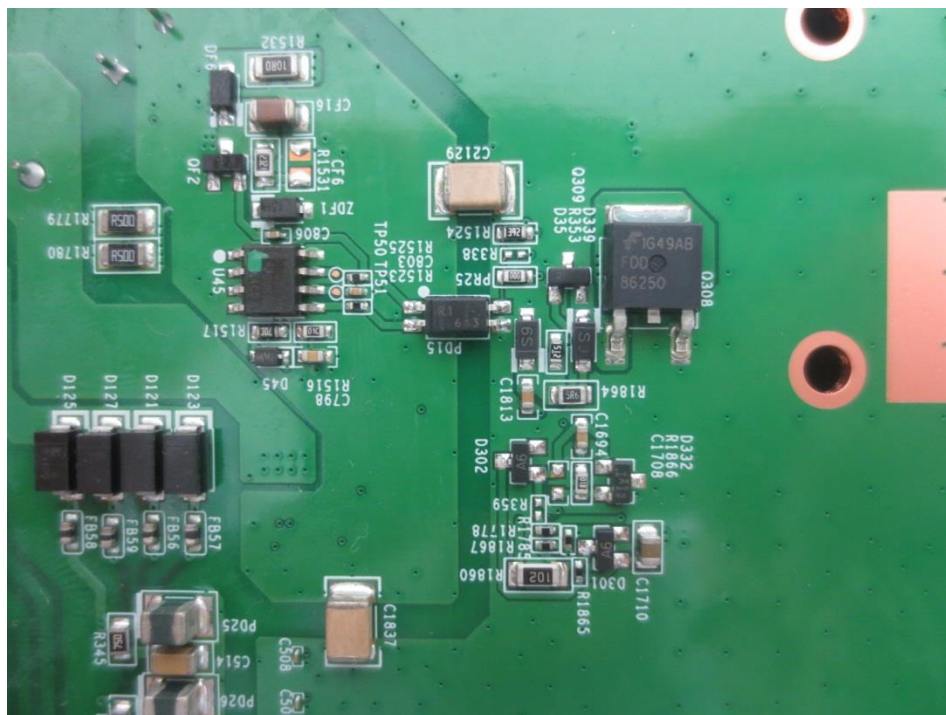
4.8 EUT – Main PCB Board Top View Chipset 1



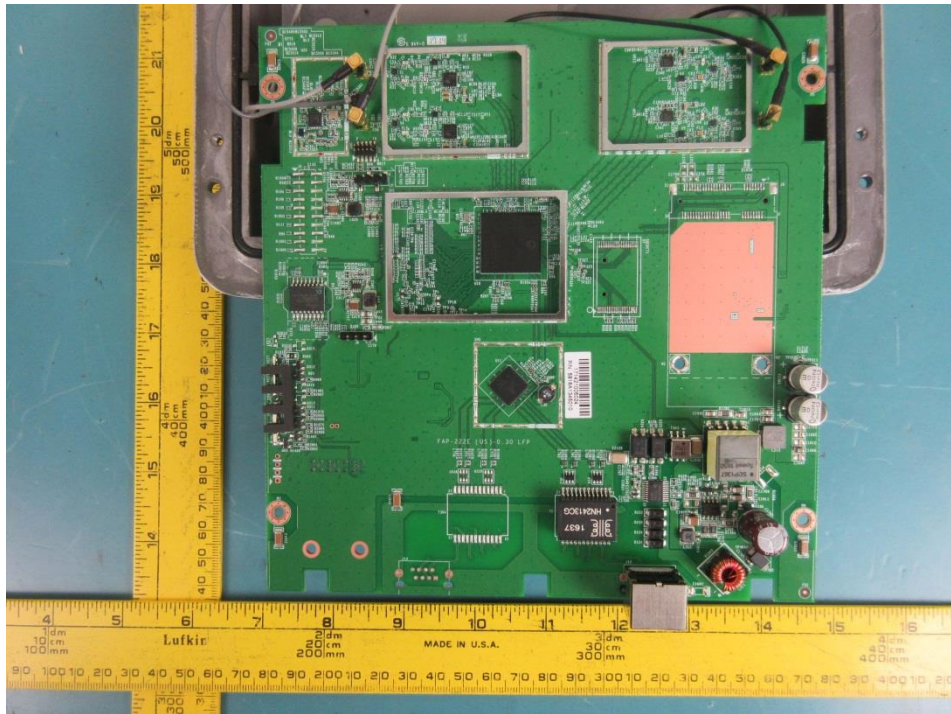
4.9 EUT – Main PCB Board Top View Chipset 2



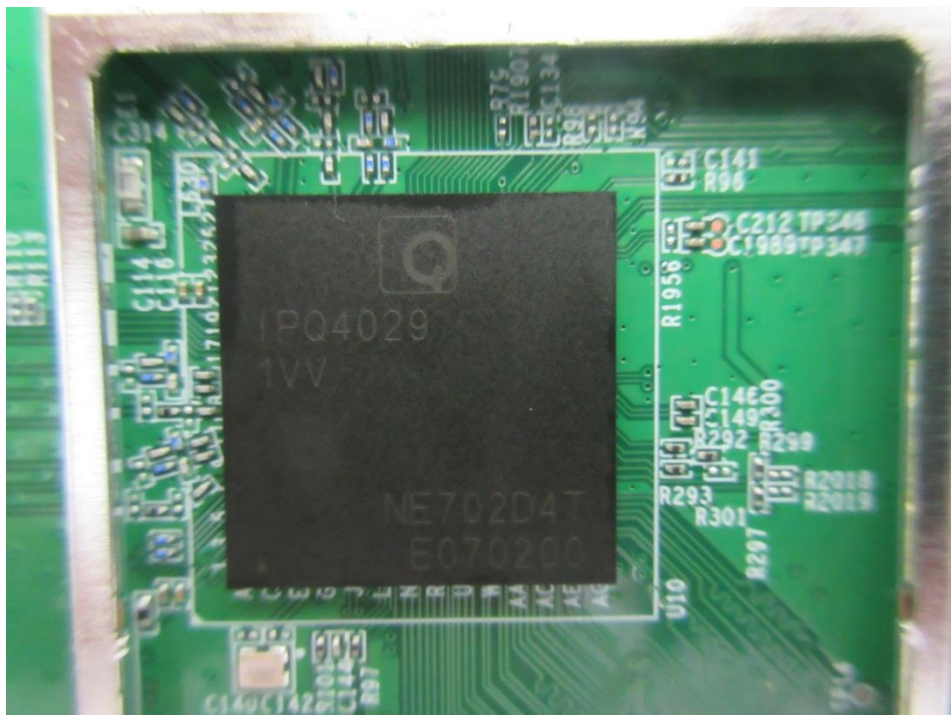
4.10 EUT – Main PCB Board Top View Chipset 3



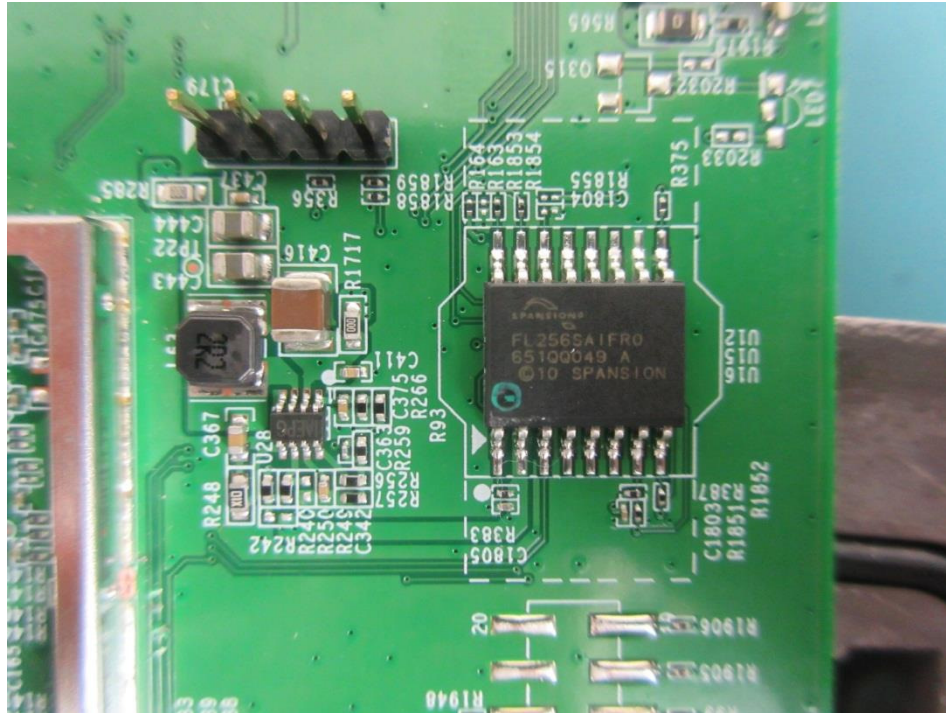
4.11 EUT – Main PCB Board Bottom View



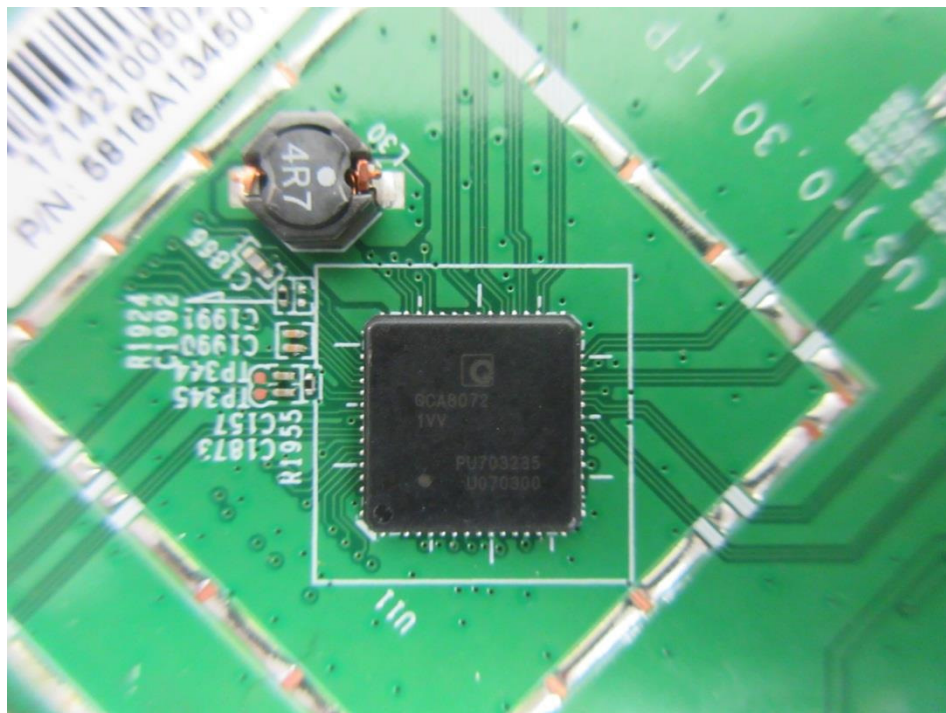
4.12 EUT – Main PCB Board Bottom View Chipset 1



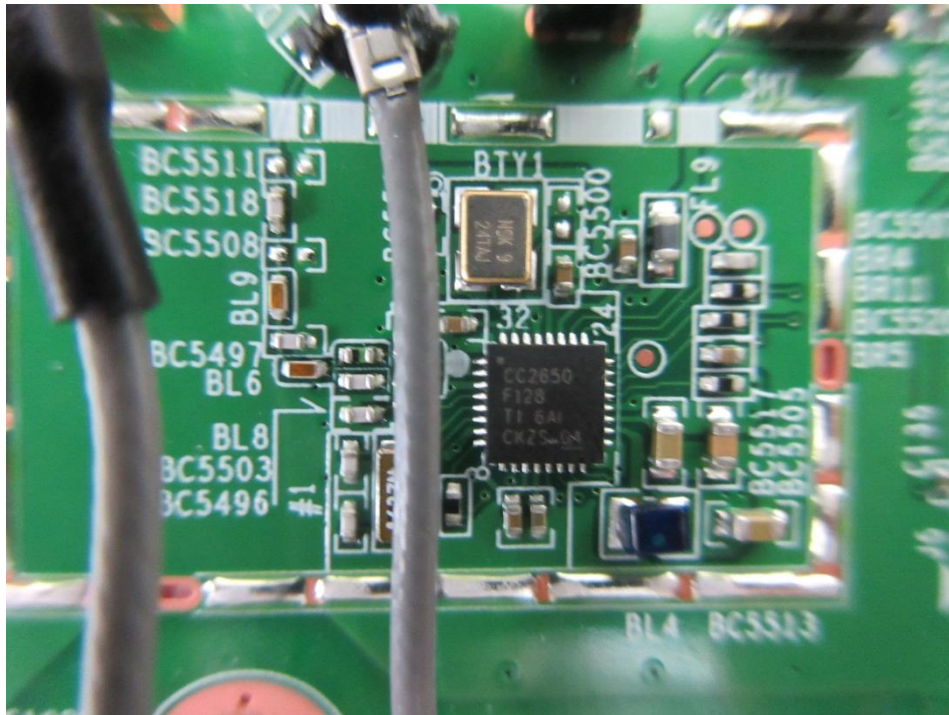
4.13 EUT- Main PCB Board Bottom View Chipset 2



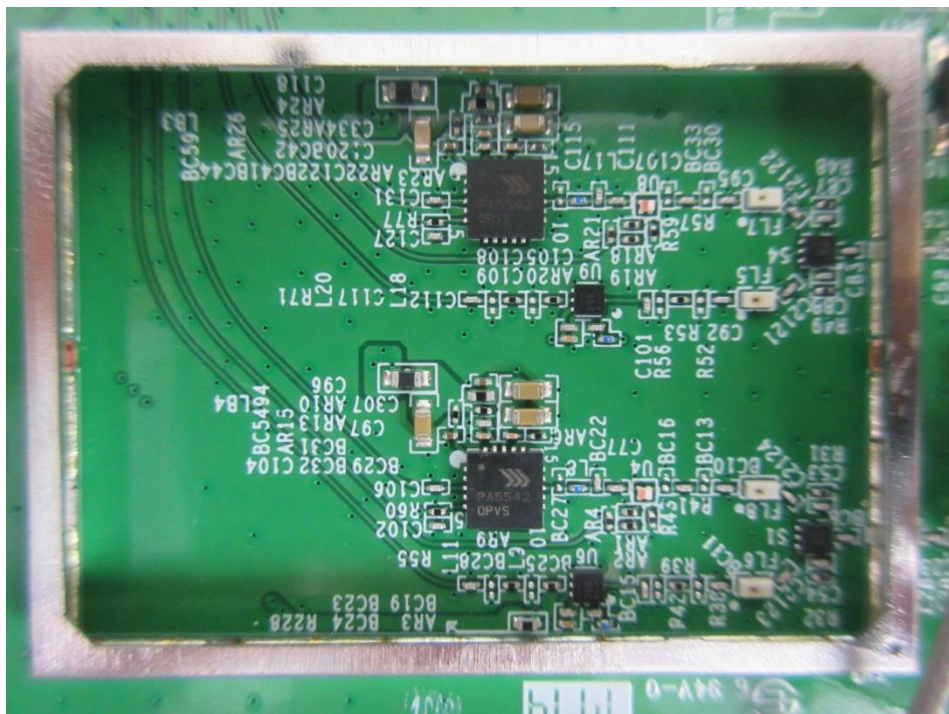
4.14 EUT- Main PCB Board Bottom View Chipset 3



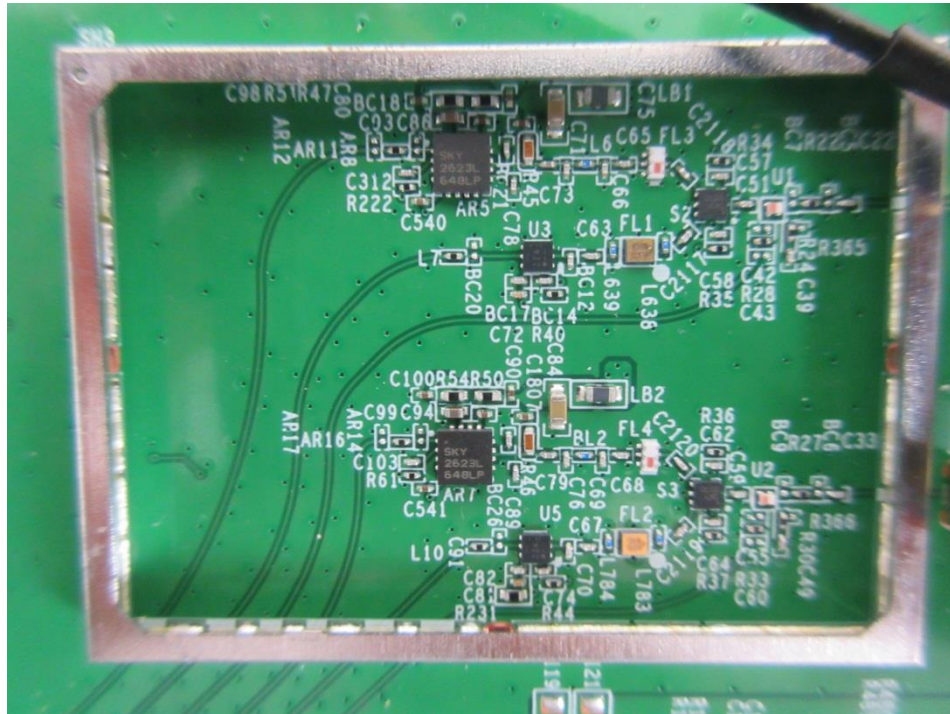
4.15 EUT-Main Board Bottom View Chipset 4



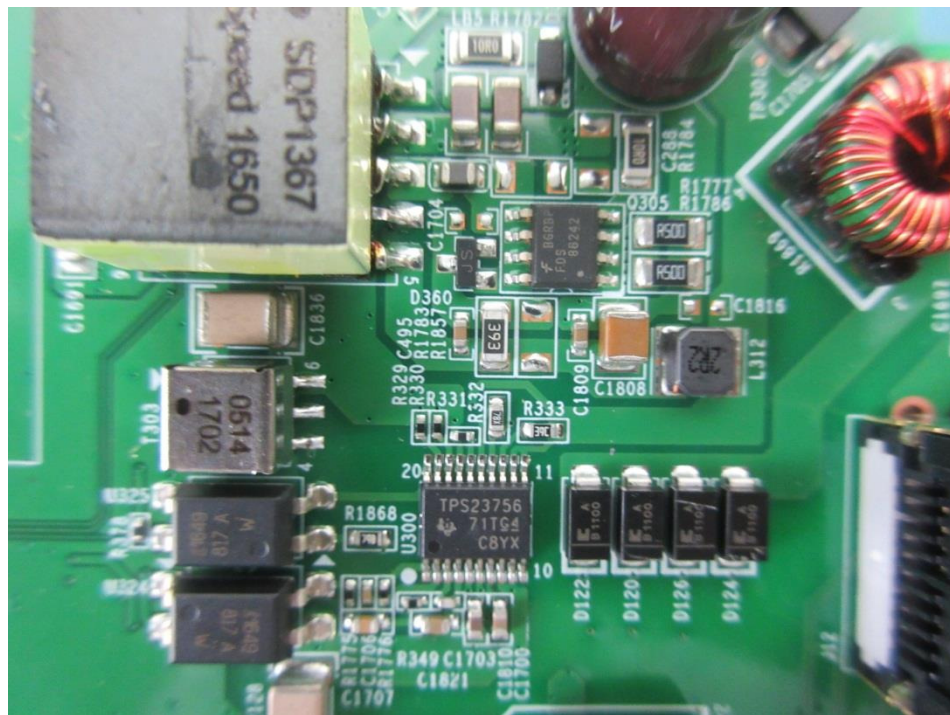
4.16 EUT-Main Board Bottom Chipset 5



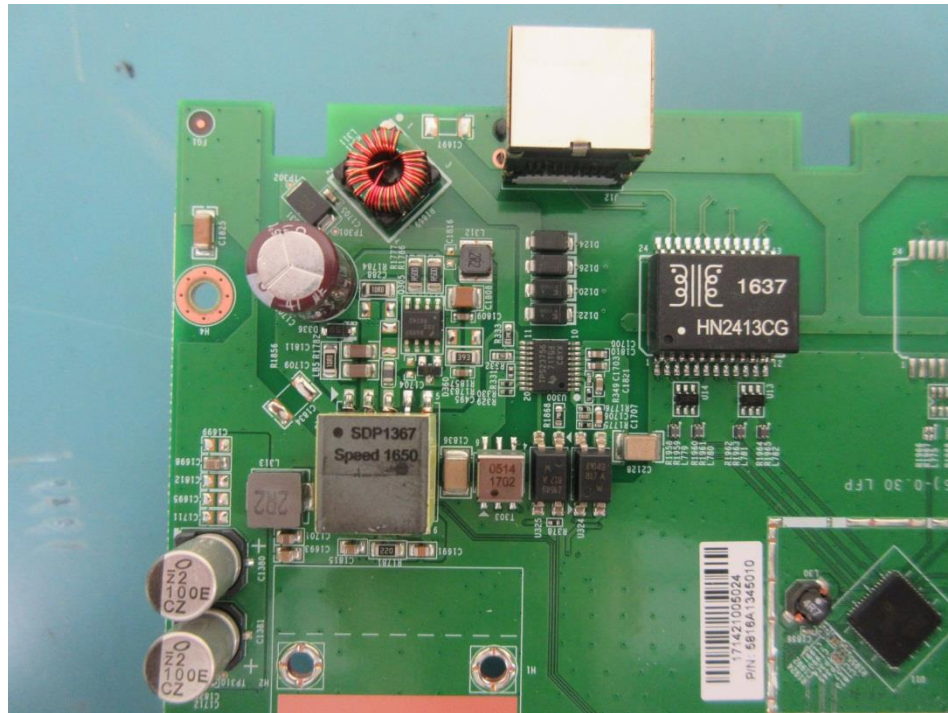
4.17 EUT-Main Board Bottom View Chipset 6



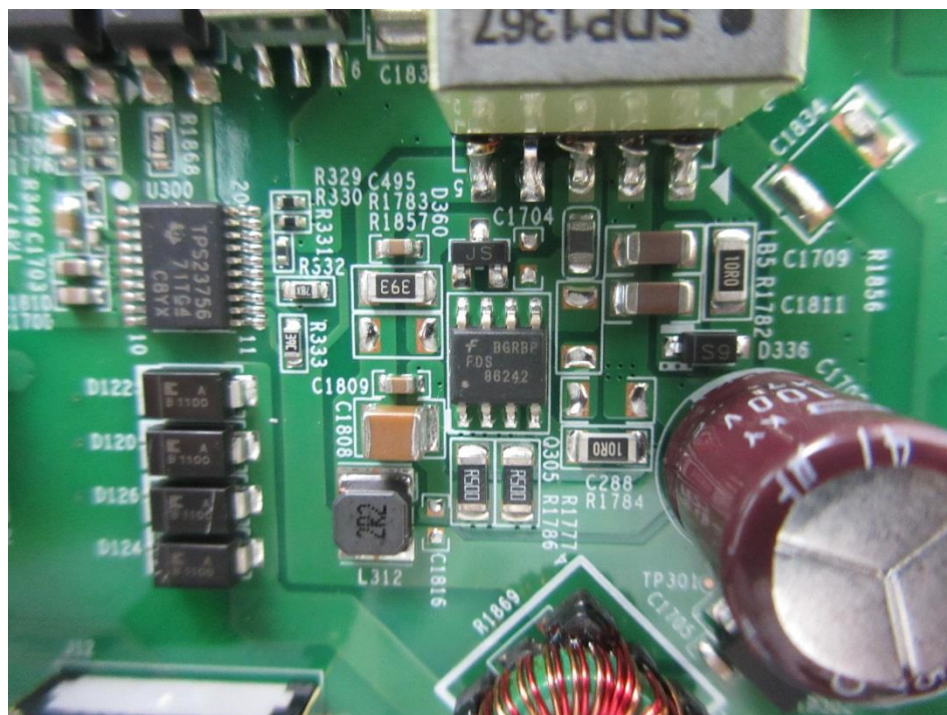
4.18 EUT-Main Board Bottom Chipset 7



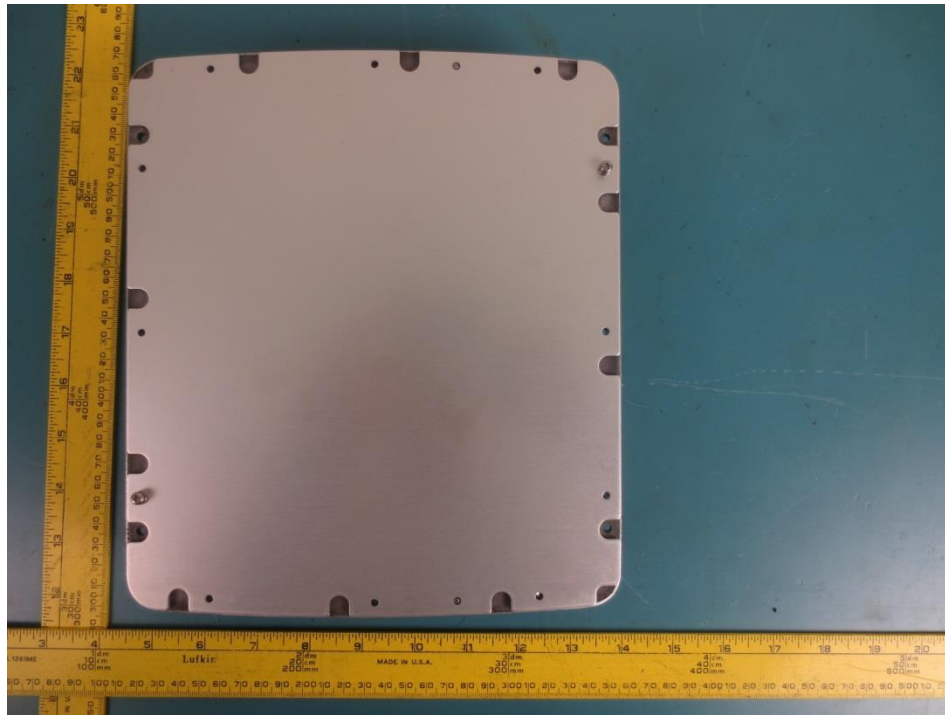
4.19 EUT-Main Board Bottom View Chipset 8



4.20 EUT-Main Board Bottom Chipset 9



4.21 EUT- Metal Plate Shielding Layer



4.22 EUT- 2.4 GHz and 5 GHz Antennas

