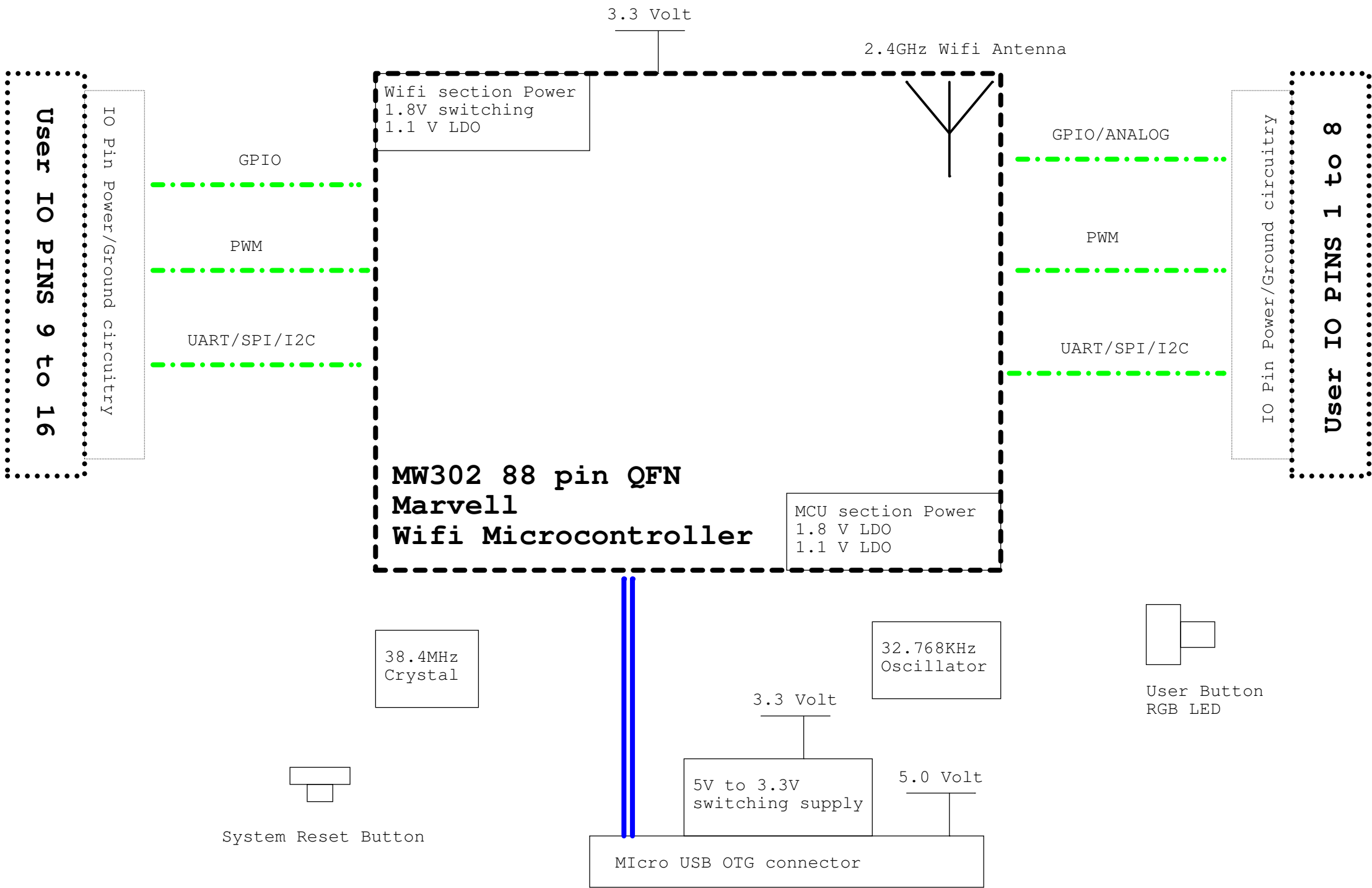


Element DVT Schematics



Optional Features

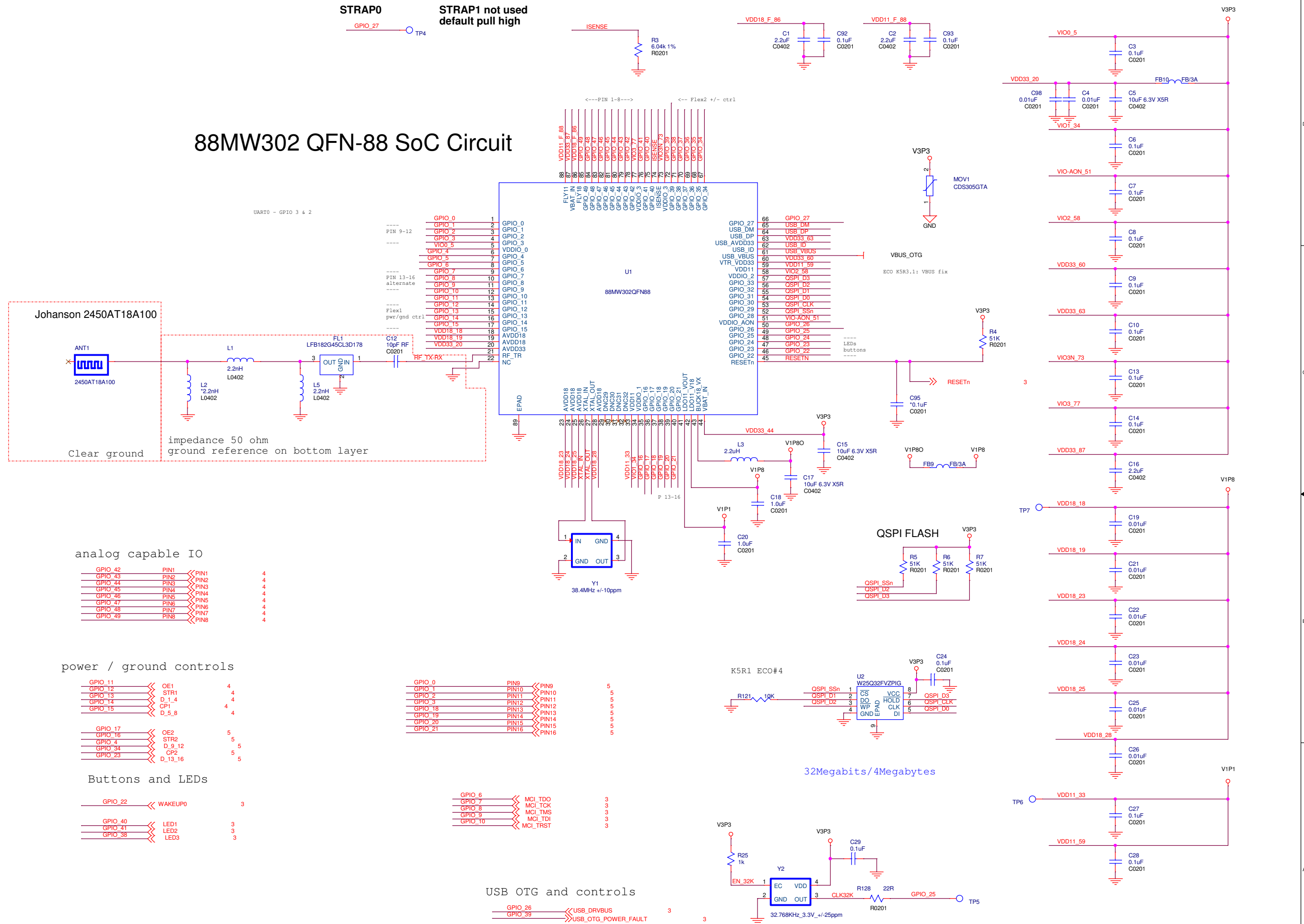
JTAG access is provided for Development use

hookup points for battery power and USB power switch

Table of Contents

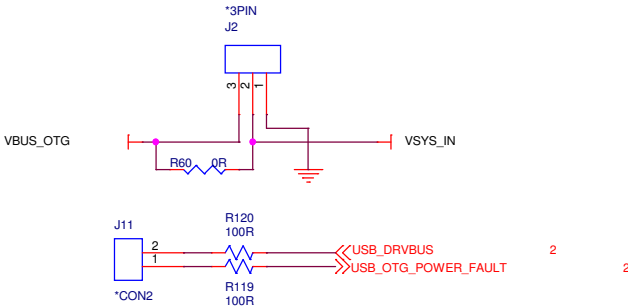
- P1 - Block Diagram
- P2 - MW300 SOC, antenna, Flash, sleep clock
- P3 - system power, USB OTG, buttons, LED, connector to dongle
- P5 - Flex connector #1 analog capable port
- P6 - Flex connector #2 digital only
- P99 - Change History

88MW302 QFN-88 SoC Circuit

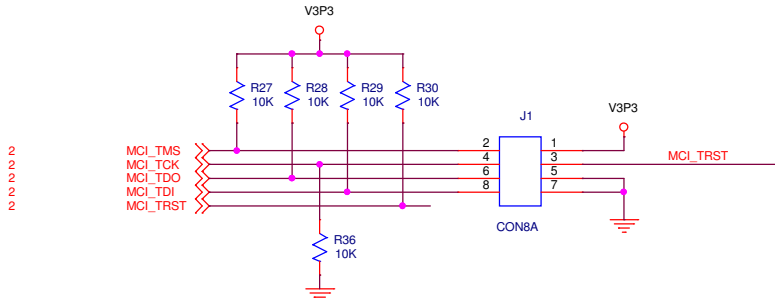


FTDI, USB OTG JTAG on dongle for development

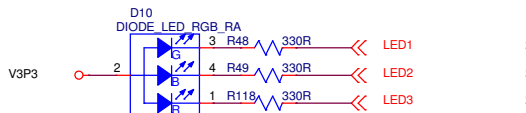
Battery Hookup



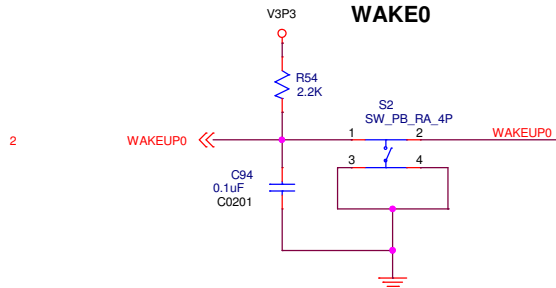
ECO K5R3.4: depopulate two pin connectors



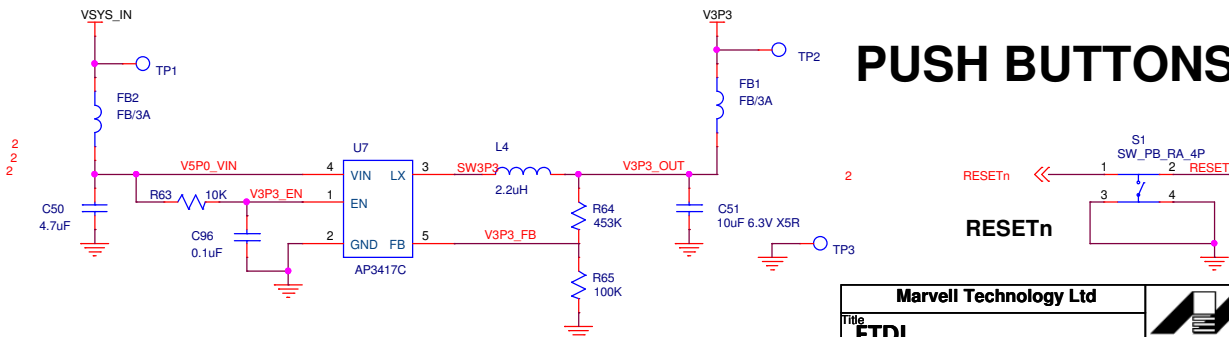
APPLICATION RGB LED



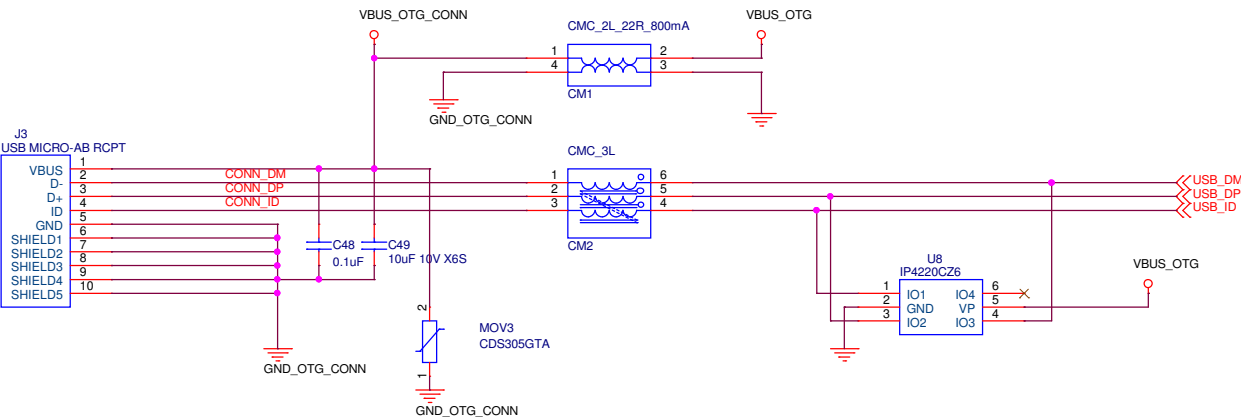
USB power switch moved to battery board

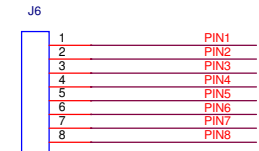
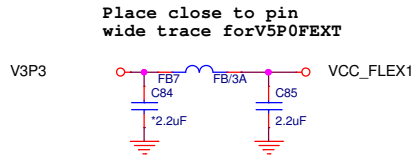
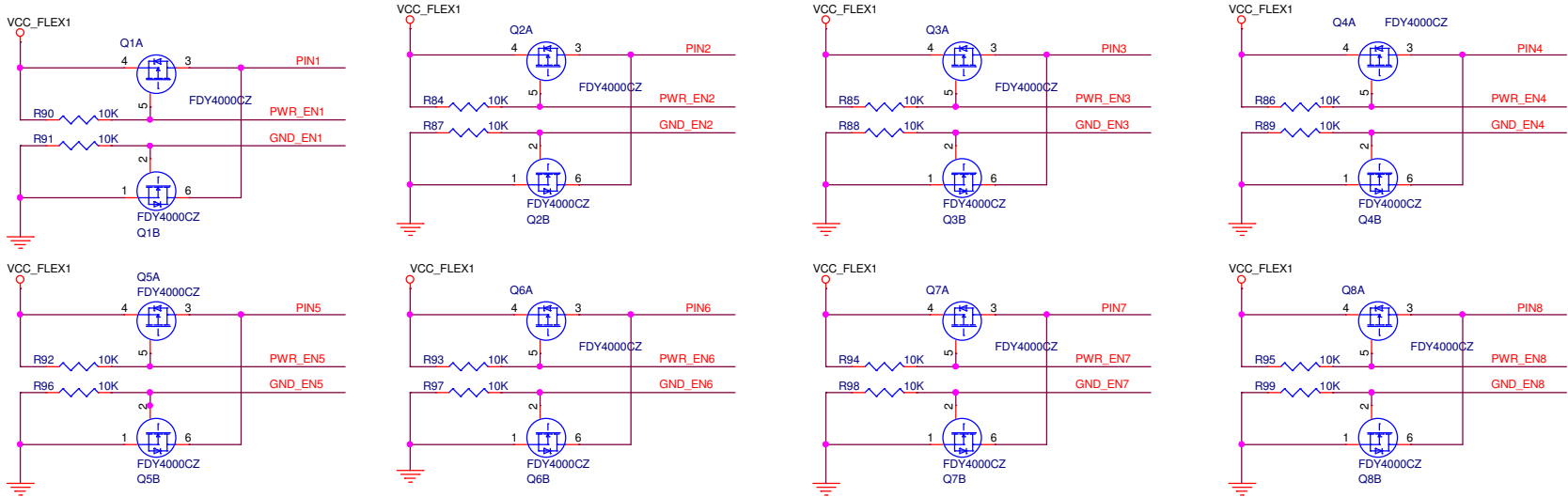


PUSH BUTTONS

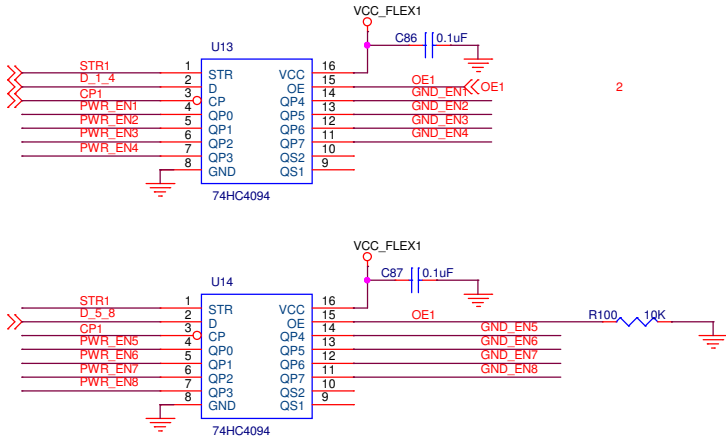
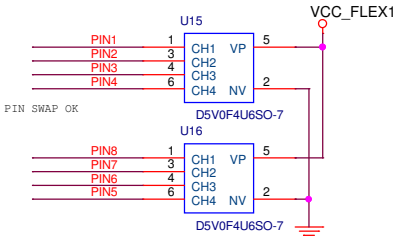


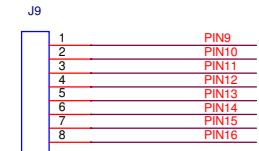
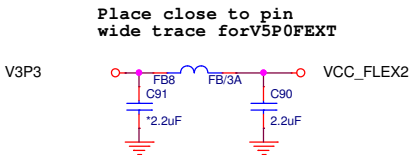
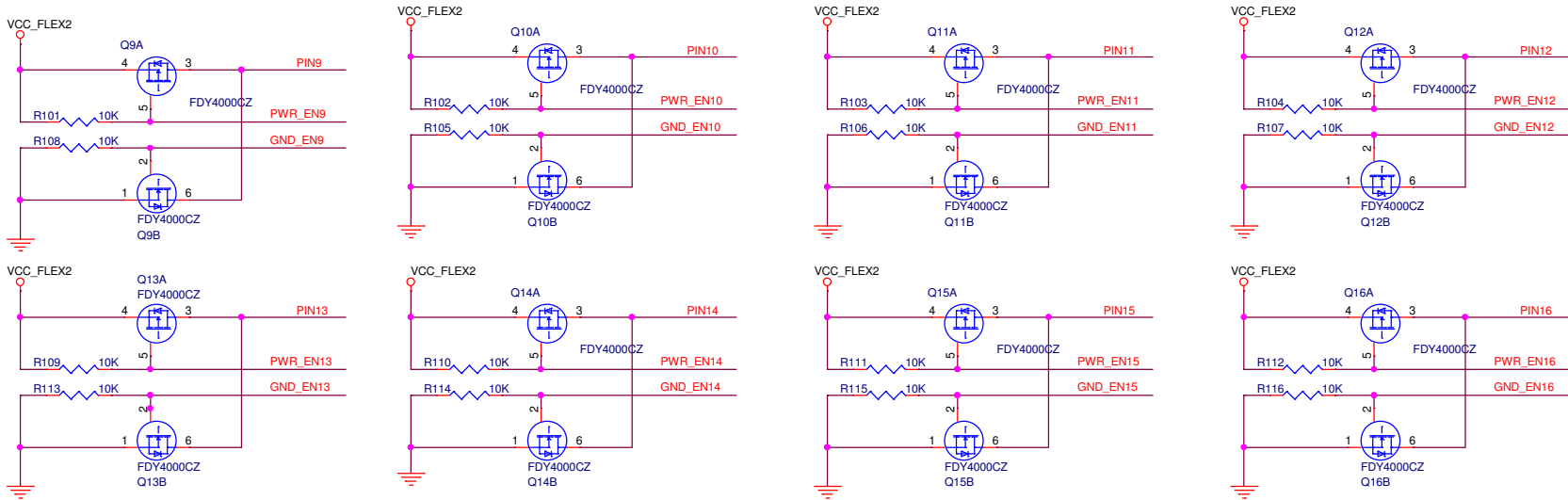
MOD: need for reset f





Note: Need location from ID

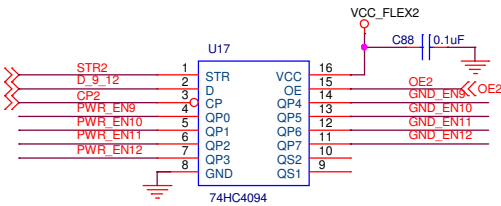




HDR1x8

2
2
2

STR2
D_9_12
CP2



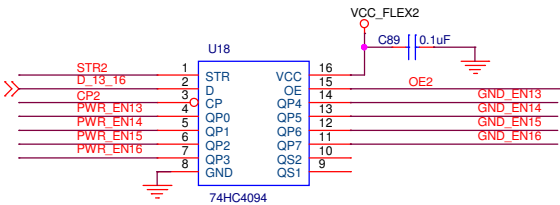
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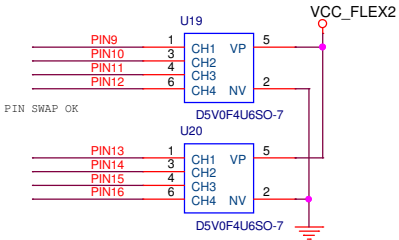
2
2
2
2
2
2
2
2

2

D_13_16



ECO K5R3.2: R117 stuffing



PIN SWAP OK

Revision History

K5R0 First Prototype
- four layer with FPGA for crossbar

K5R1 EVT1
- 2 layer design 2in x 2in for EMI prescan

K5R3 EVT2
- 2 layer shrink to 1.6in x 1.6 in
- new push button
- new MOV
- replace strap0 resistor with TP
- remove strap1 resistor
- added ground TP
- use surface mount connectors
- removed USB power switch

K5R4 EVT3 bug fix from K5R3
- USB_VBUS fix
- new mosfets
- stuff R117
- murata band pass filter
- beefed up vias near tx pin and GND
- improve decoupling of V3Pv and GND near tx pin
- improve high current loop near switcher

Element DVT1
- remove adaptor board