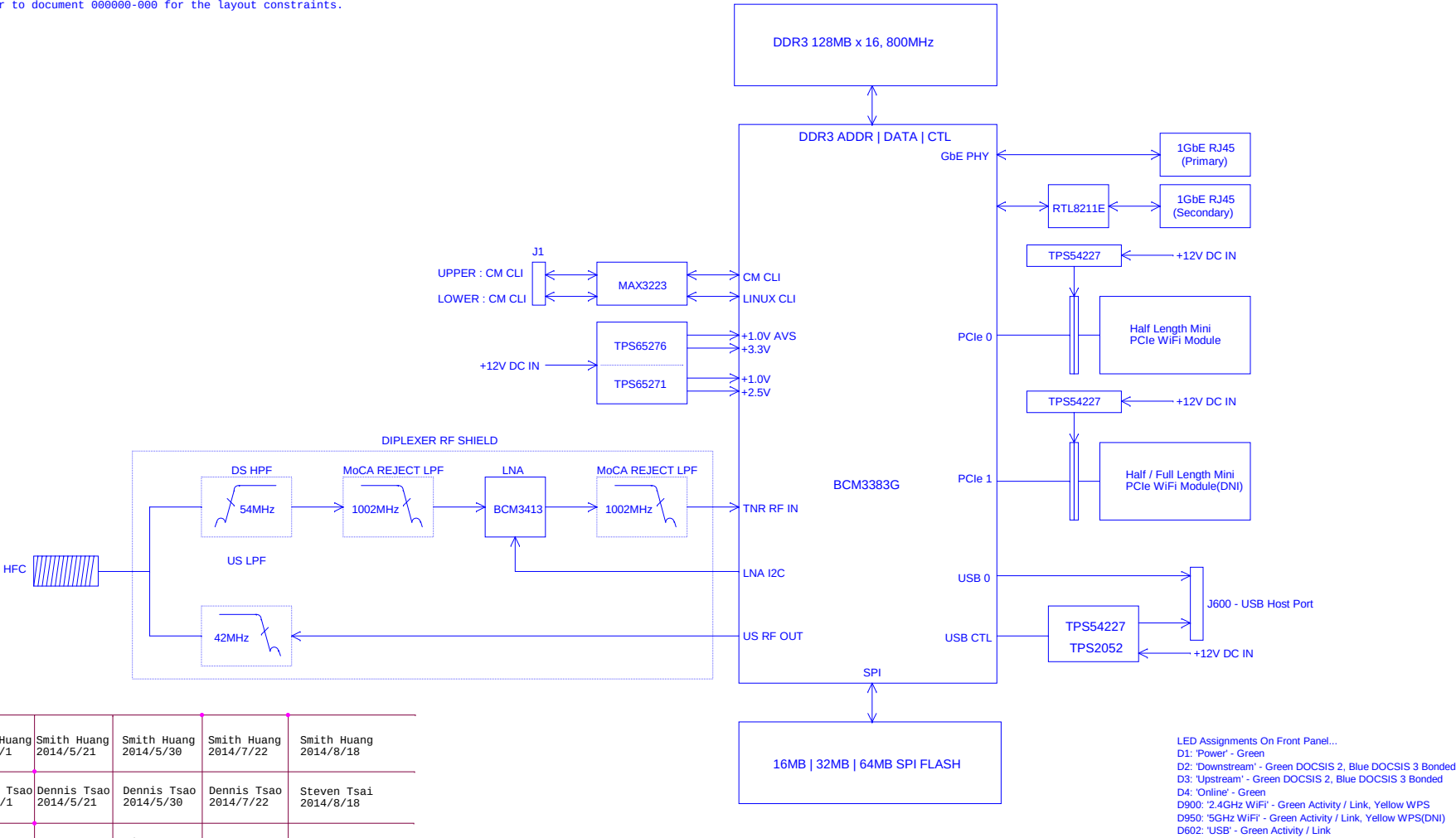
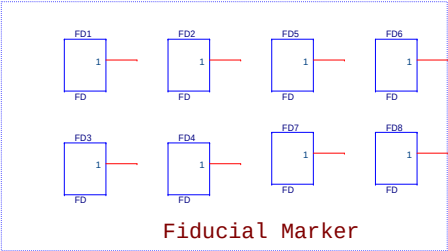


SBG6400 DOCSIS 3.0 8x4 Full Capture Bandwidth Cable Modem Gateway Schematic

- Notes:
- 1. All resistors and capacitors are 0402 unless noted otherwise.
 - 2. The DOCSIS 3.0 US/DS Diplexer, and the LNA section shall be placed inside a one piece RF shield.
 - 3. The PWB shall be no more than 4 layers.
 - 4. Exact component placement and routing for the BCM3383G and BCM3413 per the Broadcom design guide and reference design must be followed.
 - 5. Refer to document 000000-000 for the layout constraints.



Revision		Description		ECN Number	REV
APPD	Smith Huang 2014/4/1	Smith Huang 2014/5/21	Smith Huang 2014/5/30	Smith Huang 2014/7/22	Smith Huang 2014/8/18
INCORP	Dennis Tsao 2014/4/1	Dennis Tsao 2014/5/21	Dennis Tsao 2014/5/30	Dennis Tsao 2014/7/22	Steven Tsai 2014/8/18
The Rev 1 for SBG6400 (based on SBG6700-AC)		DNI R65/R74/R77/R78 update U2 part number	To correct double release schematic error, no any change in the schematic.	Put R134/R135 back for PCIe_CLKREQn/PCIE_RSTn	Changed R70&R72 from 47.5 ohm (AREC05787) to 150 ohm (AREC05528) (increased brightness, reduce 52%, changed D1&D4 to Harvek (TG16910YG (TG1691001) for retail only).
A	EB-09415	B	EB-09473	C	EB-09468
D	EB-09626	E	EB-09719		



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Date: Monday, August 18, 2014	Sheet 1 of 10		