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REVISION HISTORY

REV 00

REPLACE THE PXA255 WITH THE PXA166 AND OTHER OBSOLETE PARTS

REV 01

UPDATED PCB FOR DFM FROM JABIL - PROTOTYPE 1 DOCUMENTATION

REV 02

UPDATED SCHEMATIC WITH FIXES FROM PROTOTYPE 1 - SEE M540 CHANGES DOC FOR ALL CHANGES

REV 03

UPDATED SCHEMATIC WITH FIXES FROM PROTOTYPE 2 - SEE M540 CHANGES DOC FOR ALL CHANGES

REV 04

UPDATED SCHEMATIC WITH FIXES FROM PROTOTYPE 3 - SEE M540 CHANGES DOC FOR ALL CHANGES

REV 05

UPDATED SCHEMATIC WITH FIXES FROM PROTOTYPE 4 - SEE M540 CHANGES DOC FOR ALL CHANGES

REV 06

UPDATED SCHEMATIC WITH FIXES FROM PROTOTYPE 5 - SEE M540 CHANGES DOC FOR ALL CHANGES

REV 07

UPDATED J43 AND J44 FOOTPRINTS, ADDED CUSTOM STANDOFFS

REV 08

UPDATED ASSEMBLY DWG AND FAB DWG - NO SCHEMATIC OR BOM CHANGES

REV 09

UPDATED PRE-PROGRAMMED FLASH FILE ONLY

REV 10

UP-REV FOR TRACEABILITY ONLY Change to Diagnostic Application for Type 1 Display Field Issue

REV 11

UP-REV FOR OPTICAL LENS PART NUMBER CHANGES

REV 12

TRACK NEW V6 FIRMWARE FOR SUB FLASH / REVERT TO REV 10 OPT. LENSES

REV 13

REMOVE CONNECTION AT Y4 ENABLE PIN TO "MRL" NET. Y4.1 IS N/C

REV 14

UP-REV FOR OPTICAL LENS PART NUMBER CHANGES

REV 15

Changed J14 from 7490324 to 1801275

REV 16

Changed J14 from1801275 back to 7490324

scale

general tolerances

material

safety class

field reserved

revision text

Changed J14 from 1801275 back to 7490324

(.)

filing dept.

responsible dept.

author

See Aras

date

See Aras

name

.

language

EN

replacement for

MS30379-15

creating system

DXDESIGNER

contract

number

MS30379

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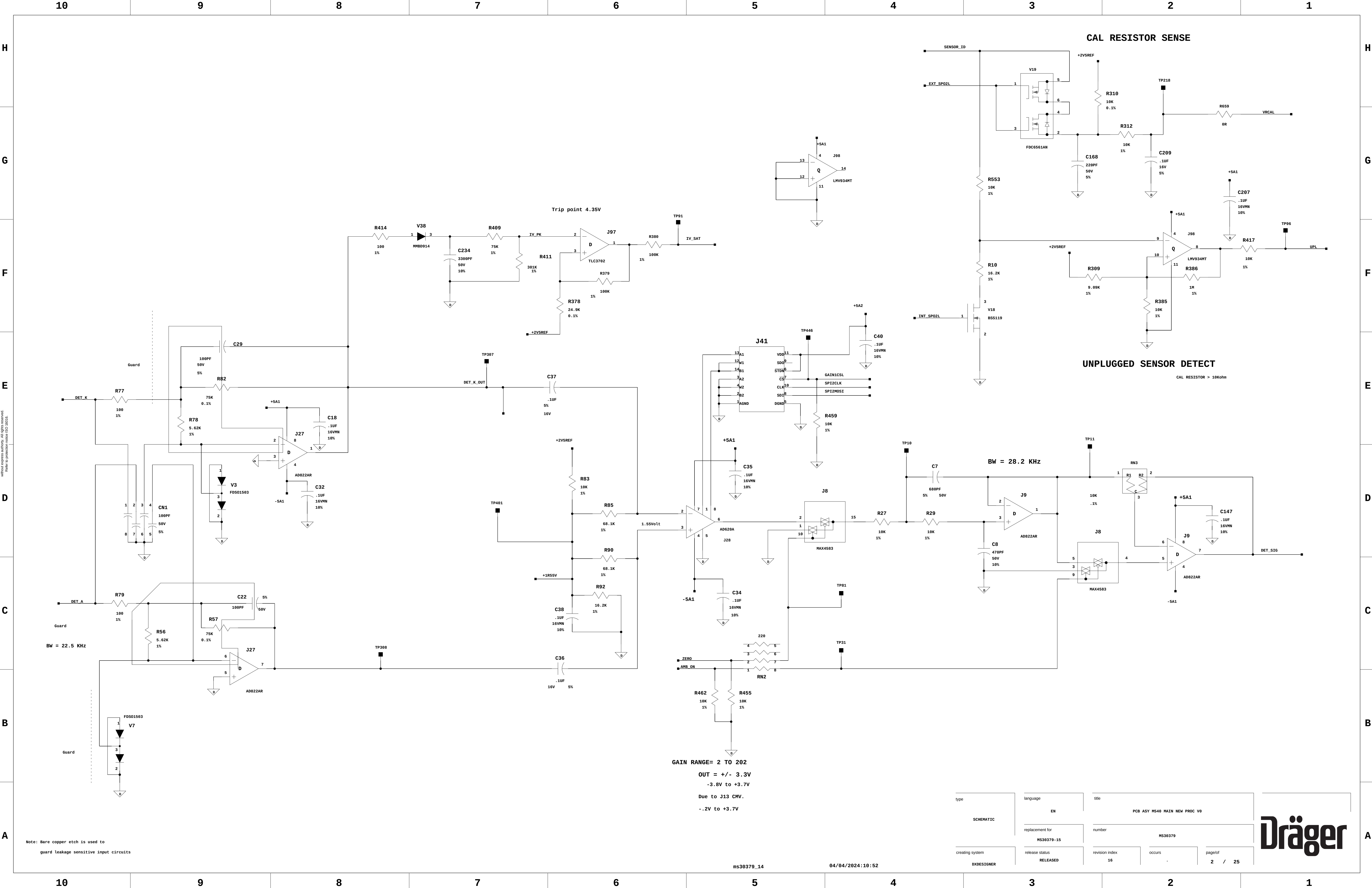
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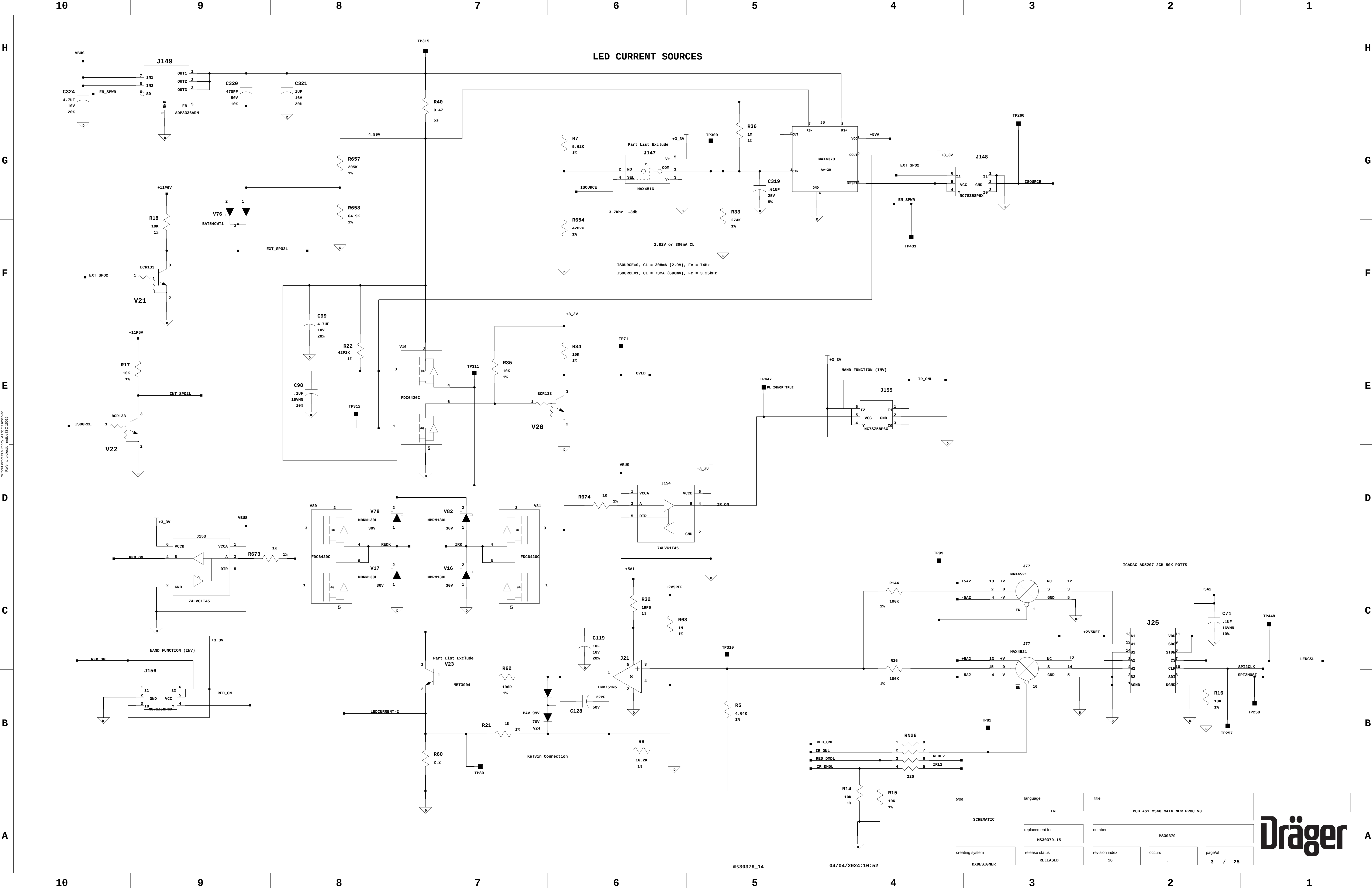
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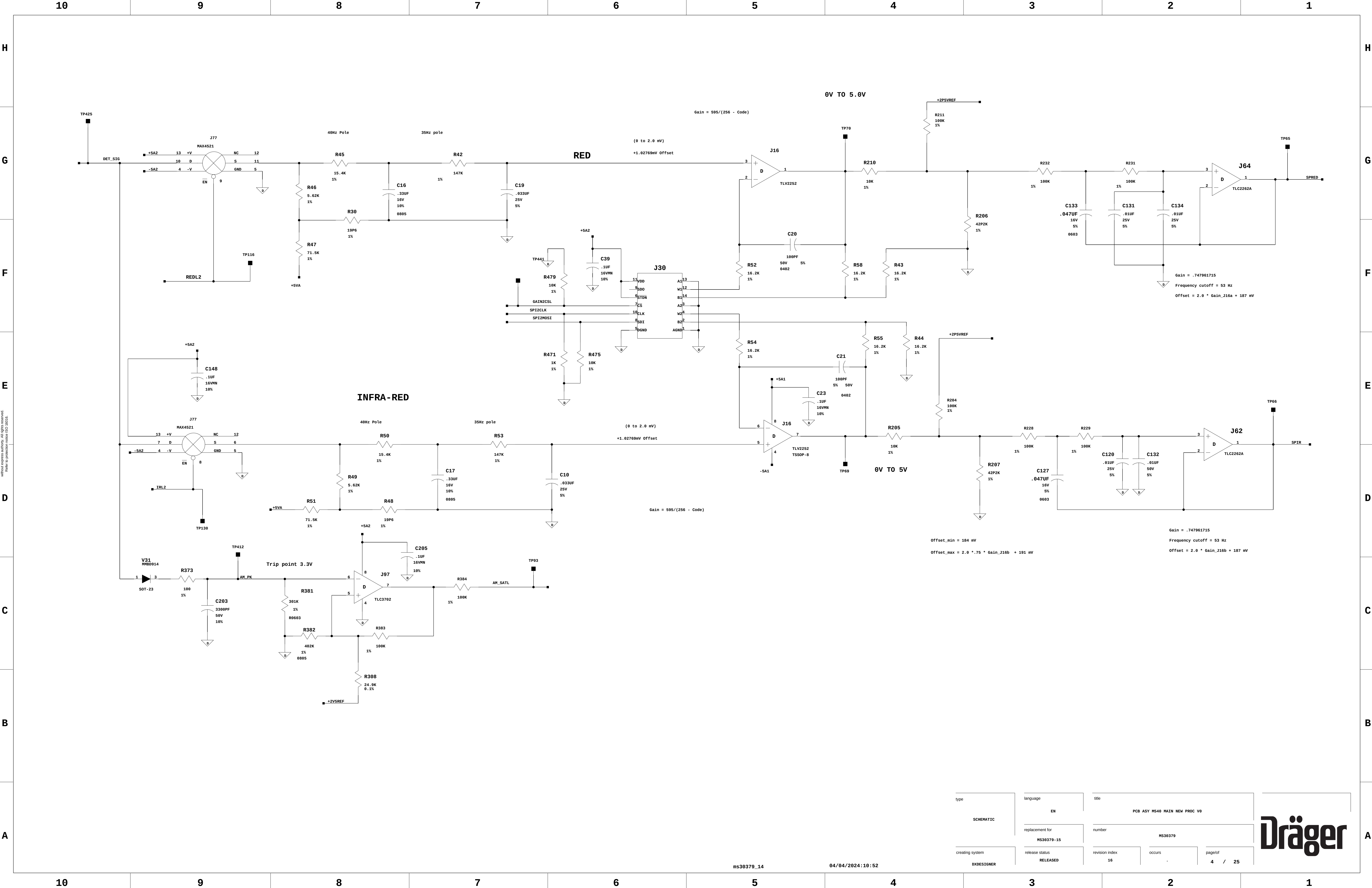
Note: Bare copper etch is used to guard leakage sensitive input circuits

GAIN RANGE= 2 TO 202
OUT = +/- 3.3V
-3.8V to +3.7V
Due to J13 CMV.
-.2V to +3.7V

type	language	title
SCHEMATIC	EN	PCB ASY MS40 MAIN NEW PROC V0
creating system	replacement for	number
DXDESIGNER	MS30379-15	MS30379
release status	revision index	occurs
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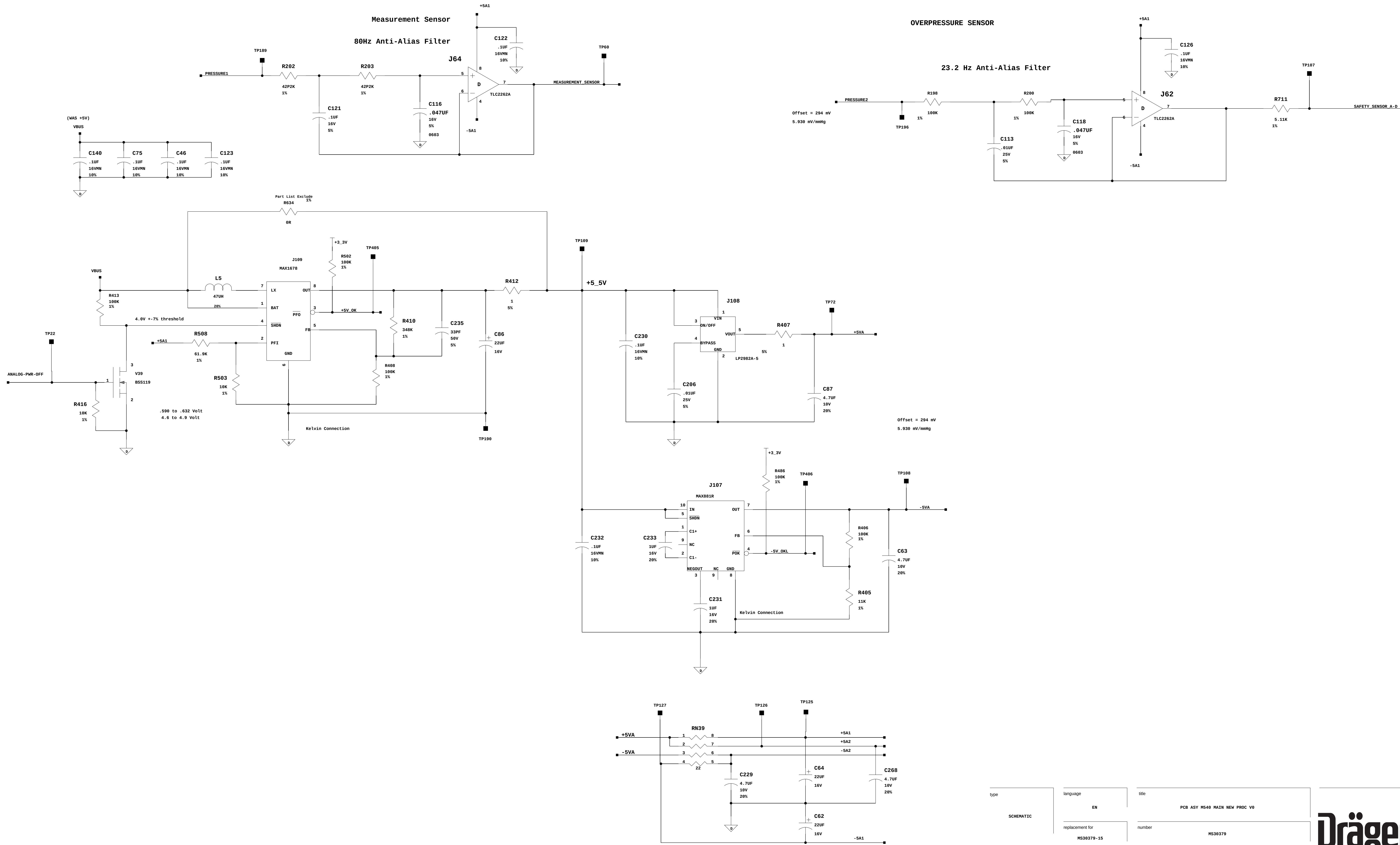


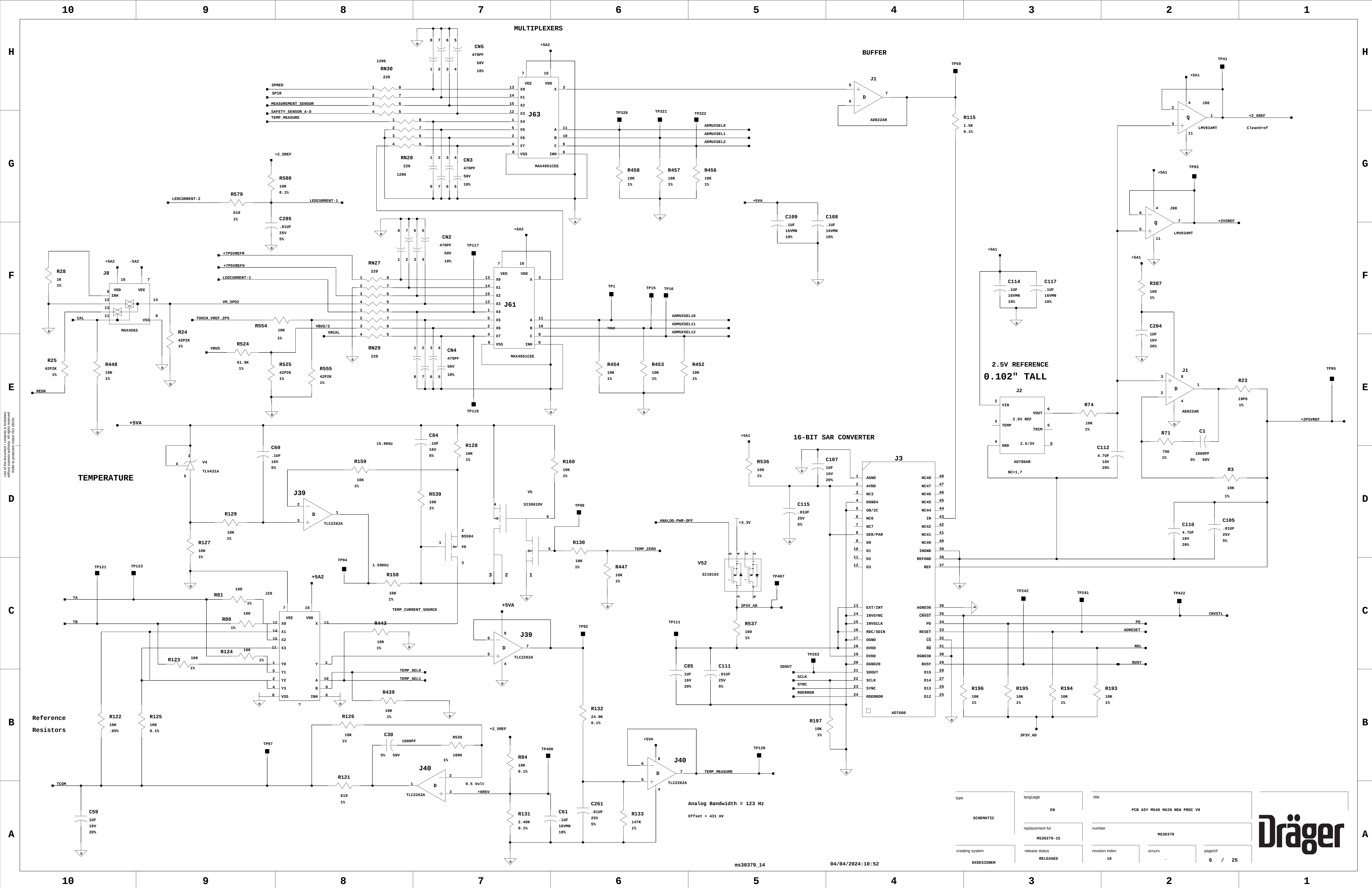


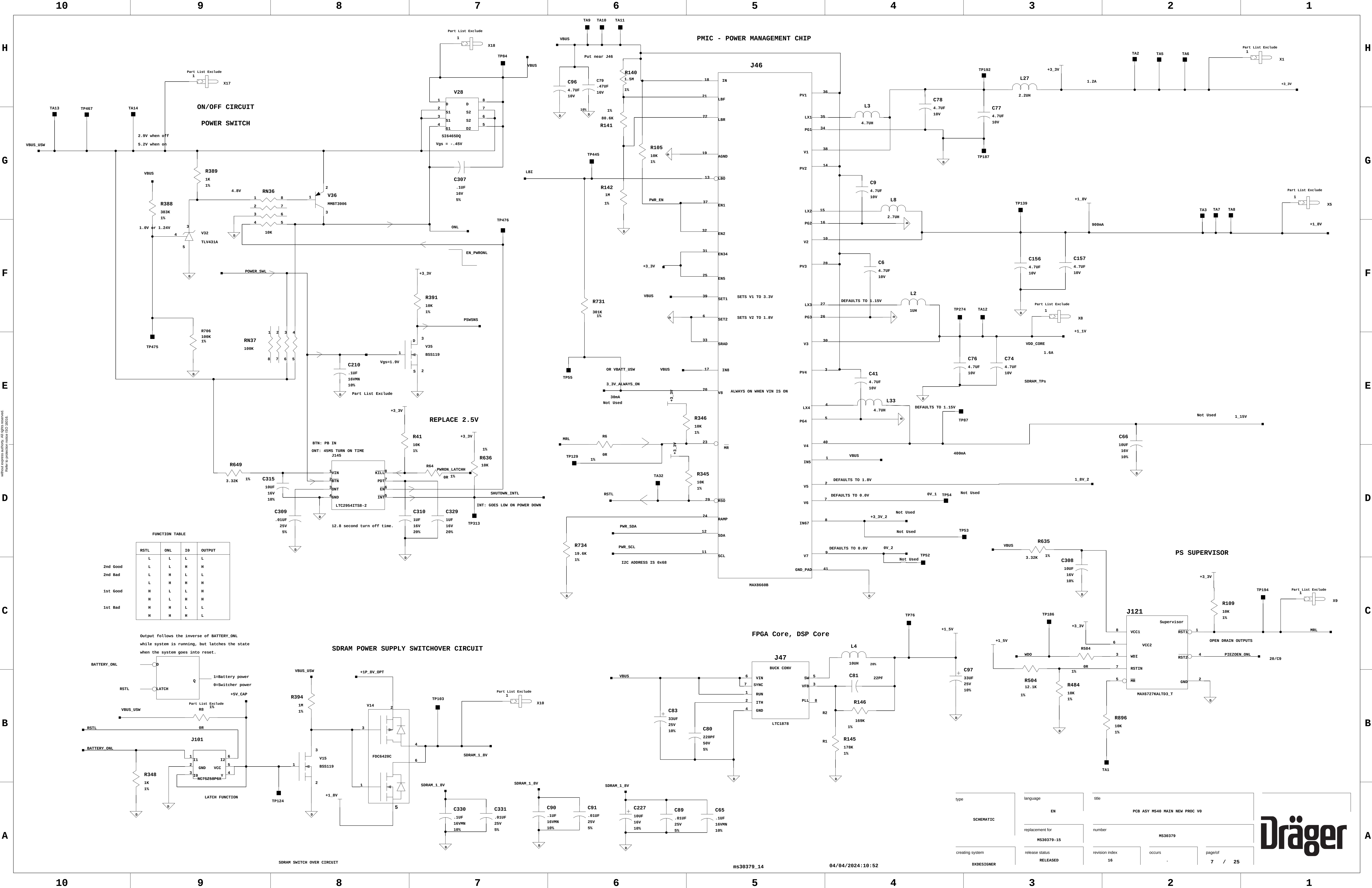
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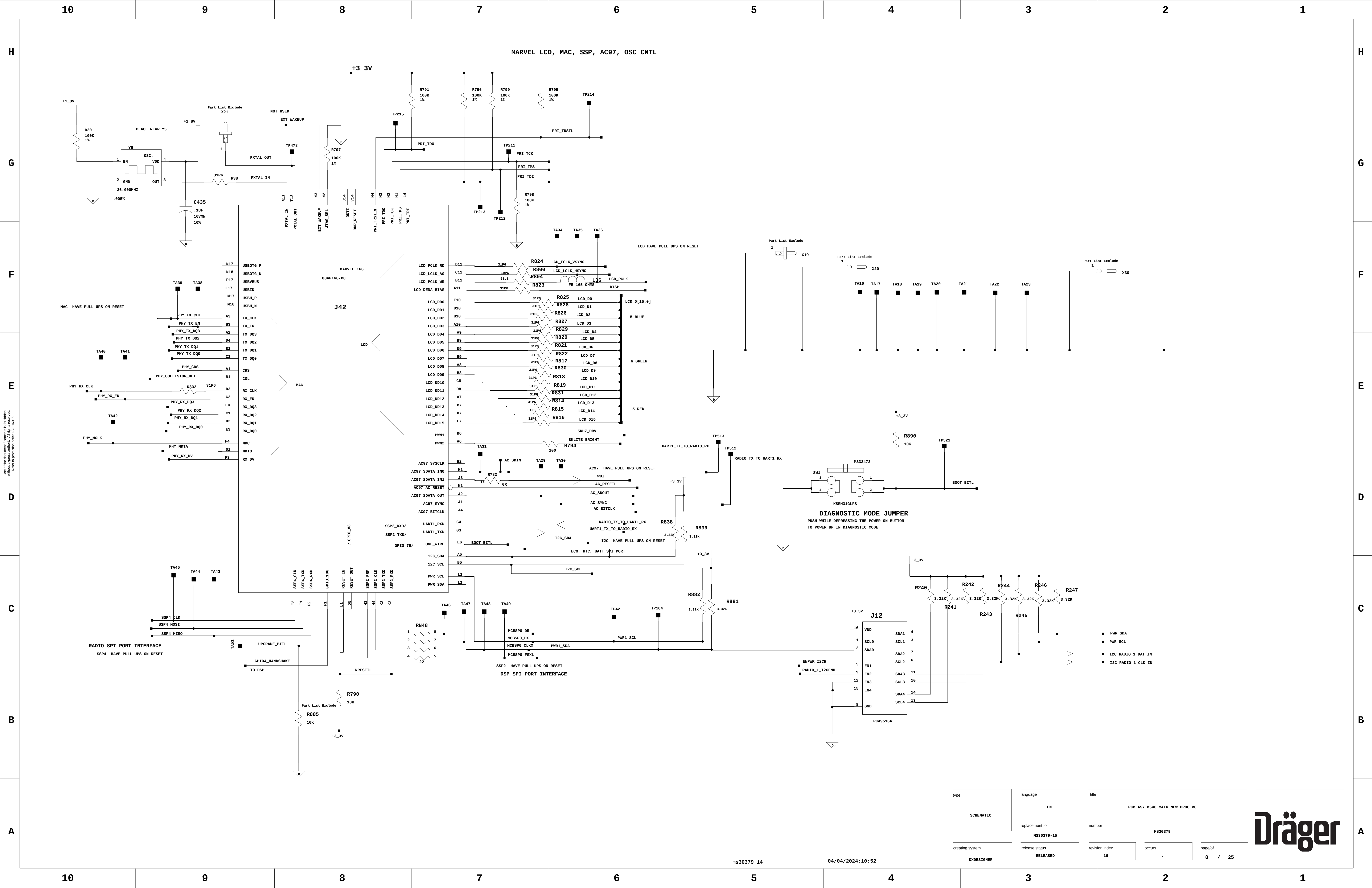


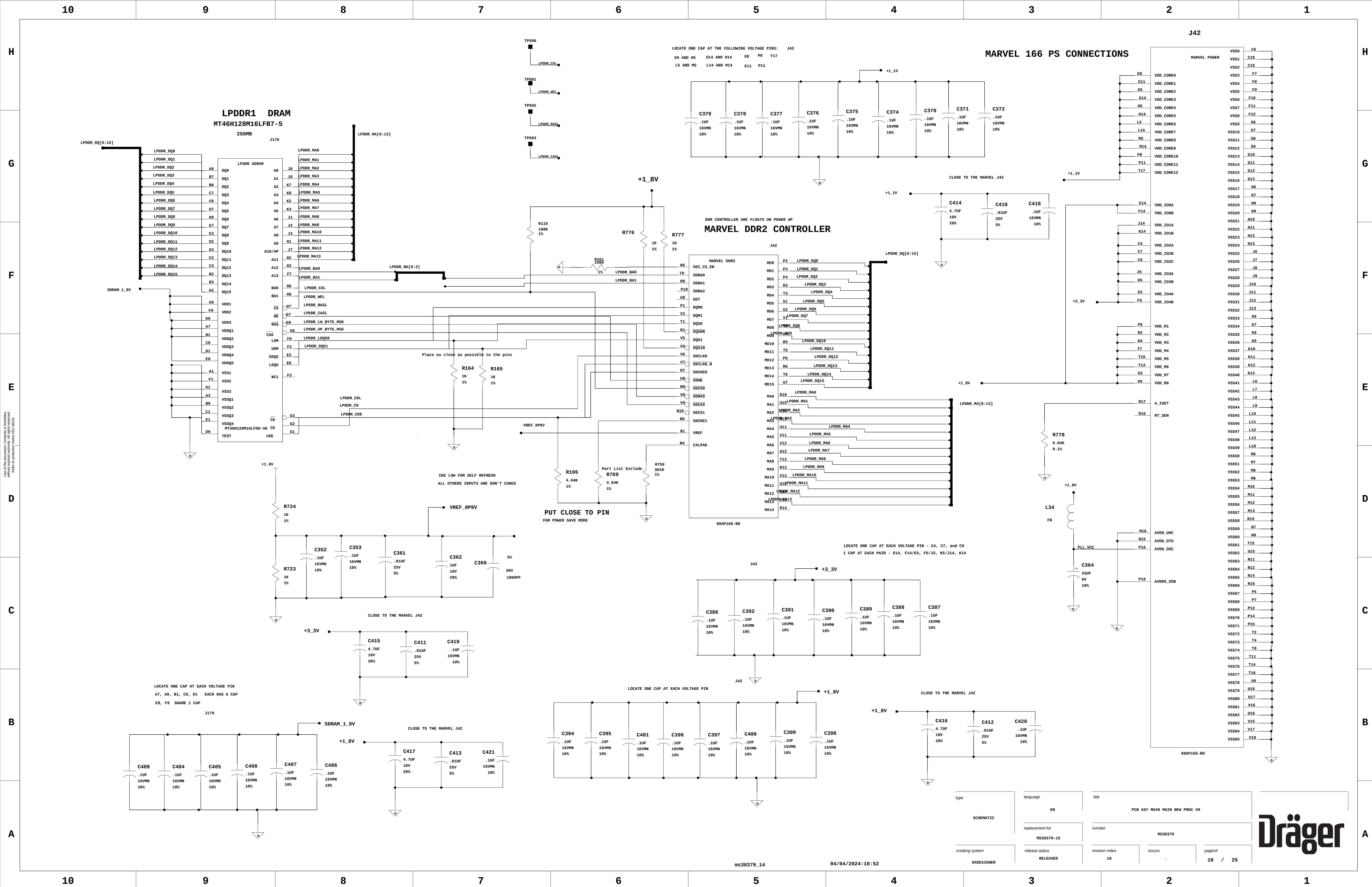


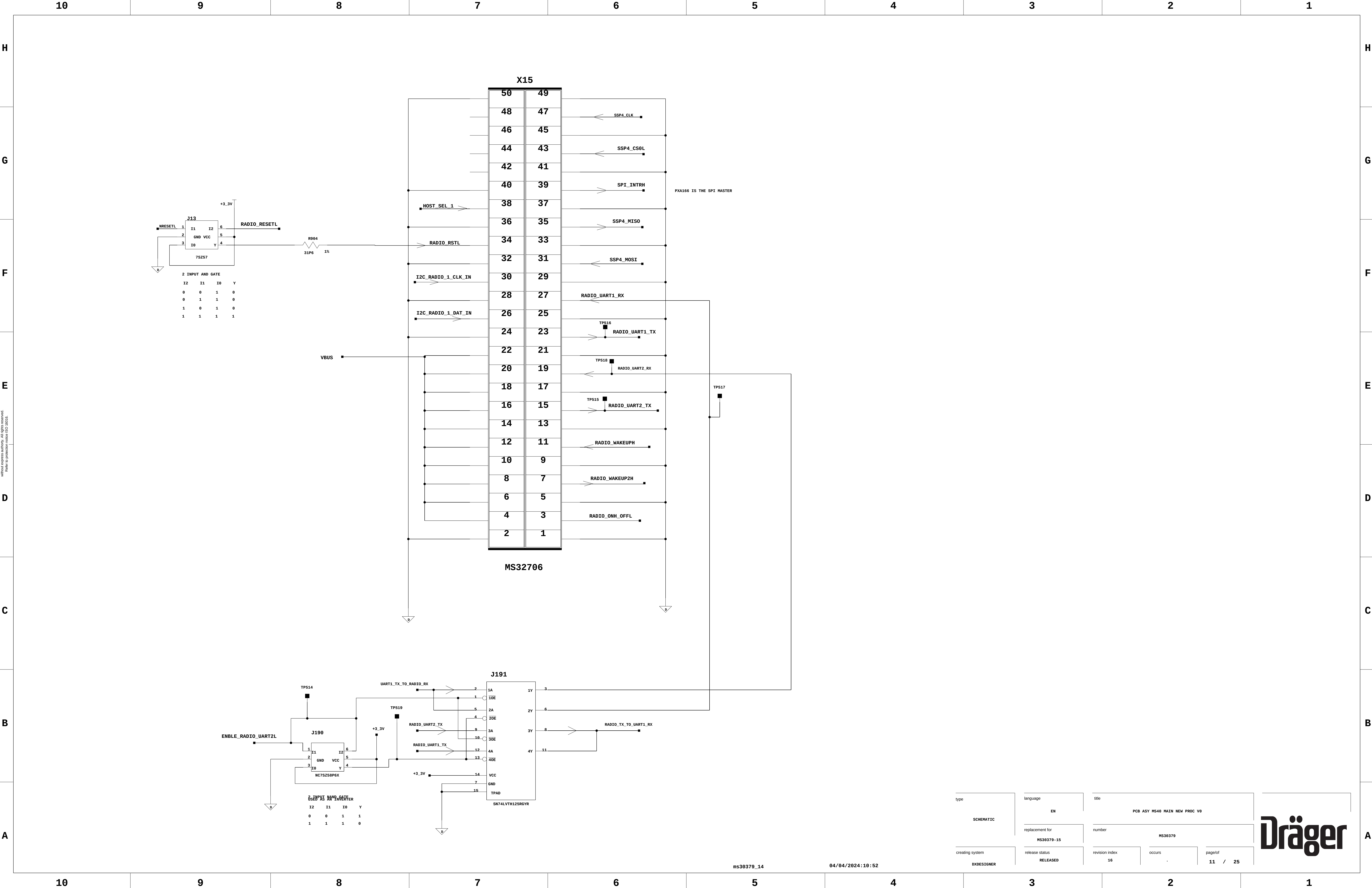




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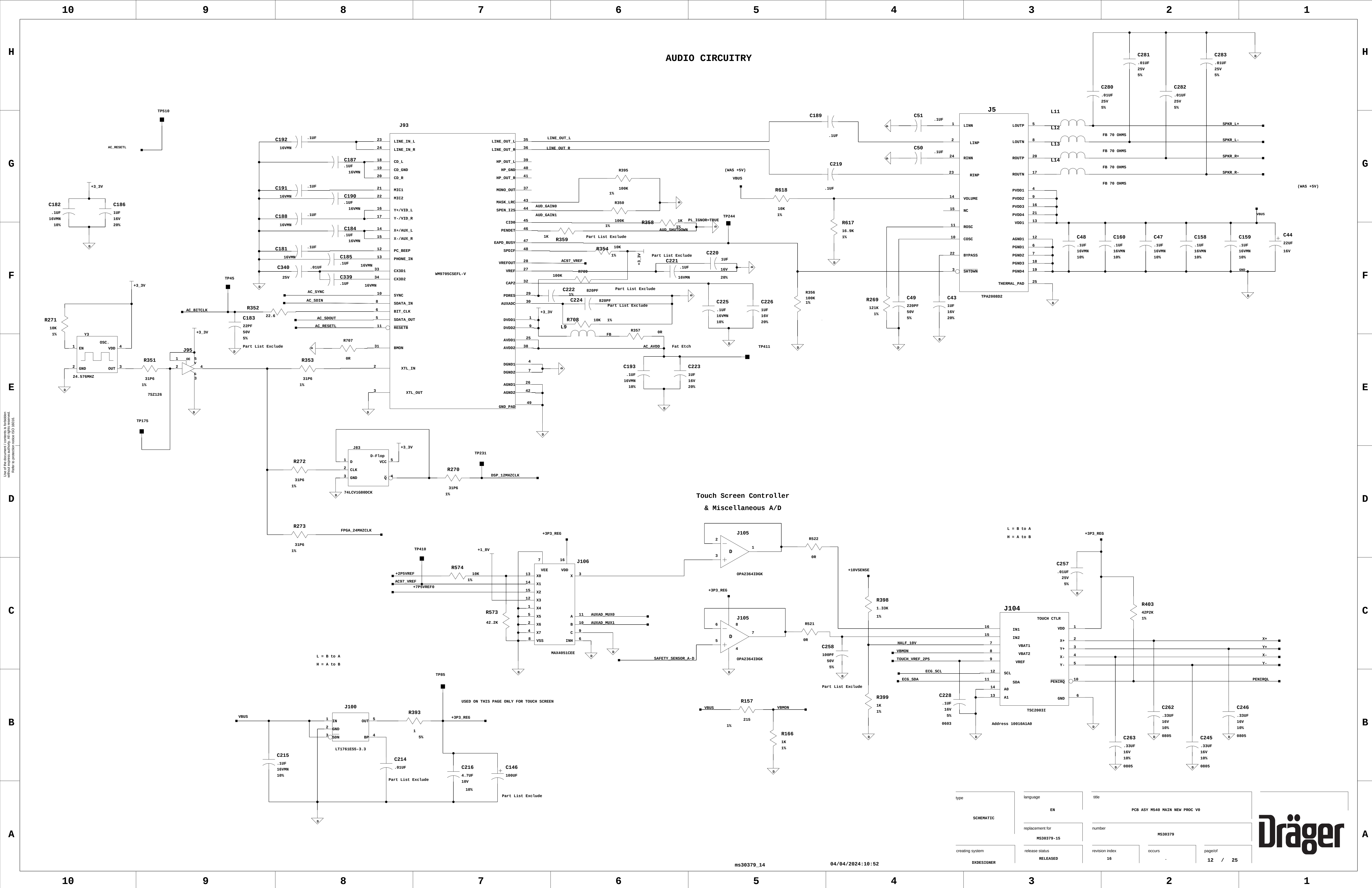


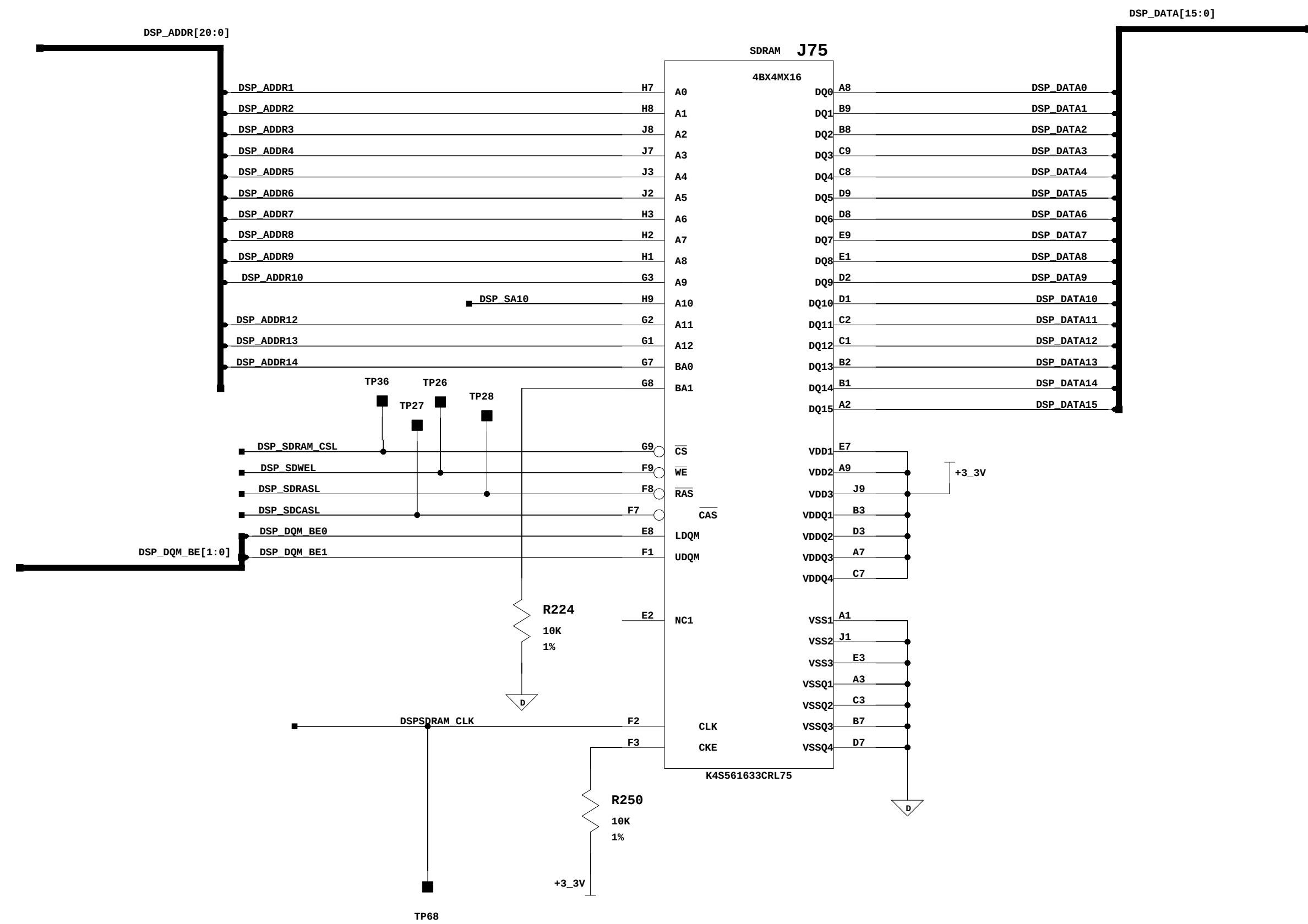
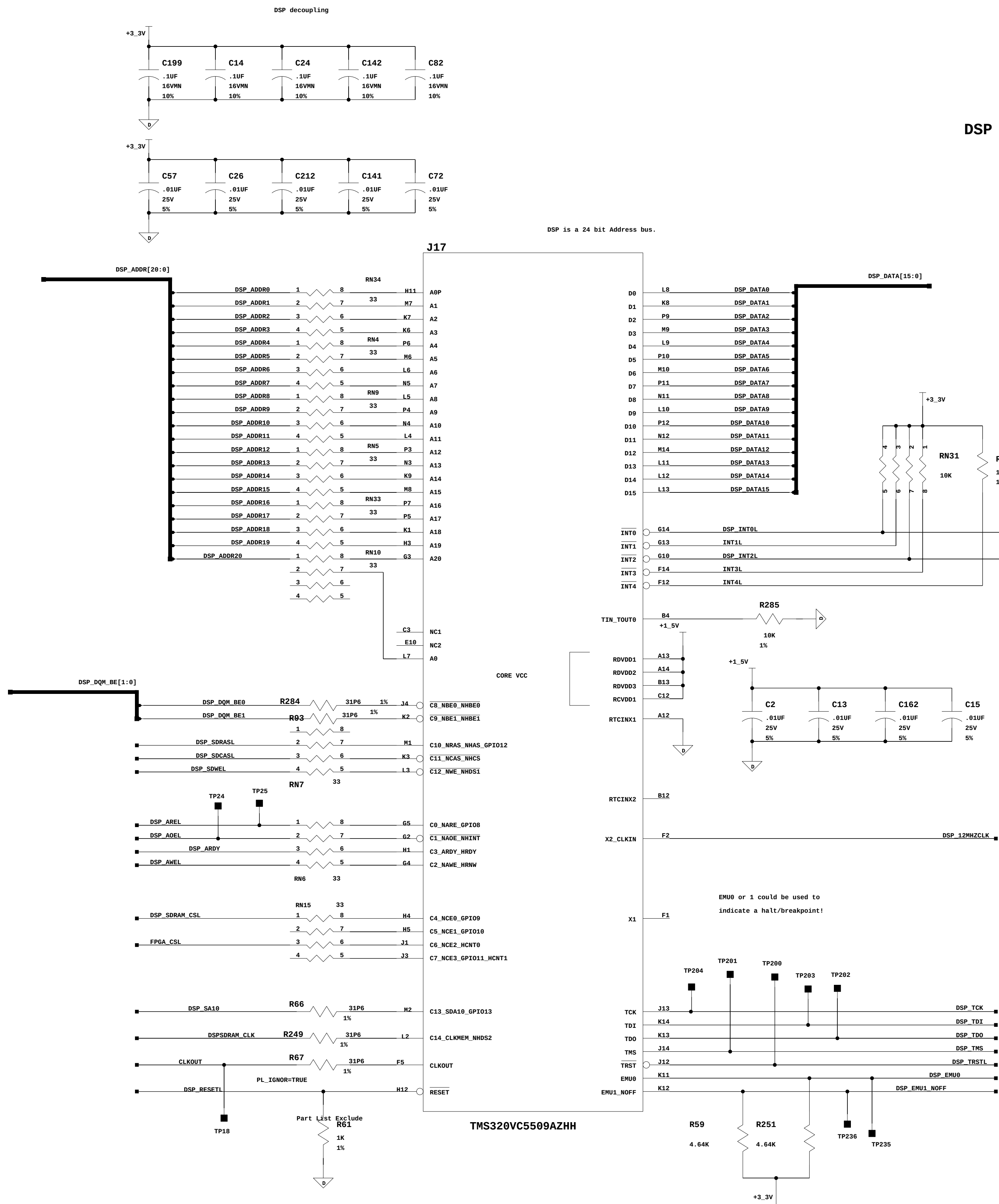




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Refer to Table 3-3 for boot configurations
page 27 of the Hardware Manual.

G

BOOTM3	BOOTM0
0000	SPI Boot 24 bit Address, MCBSP0
0010	USB Boot
0101	EHPI Boot Multiplexed mode
0110	EHPI Boot non-multiplexed mode
1000	Boot from 16 bit Ext. Async Memory
1001	SPI Boot 16 bit Address, MCBSP0
1010	Parallel EMIF Boot, 8 bit Memory
1011	Parallel EMIF Boot, 16 bit Memory
1100	Boot from 8 bit Ext. Async Memory
1110	Serial Boot 16 bit data, MCBSP0
1111	Serial Boot 8 bit data, MCBSP0

All other combinations are RESERVED.

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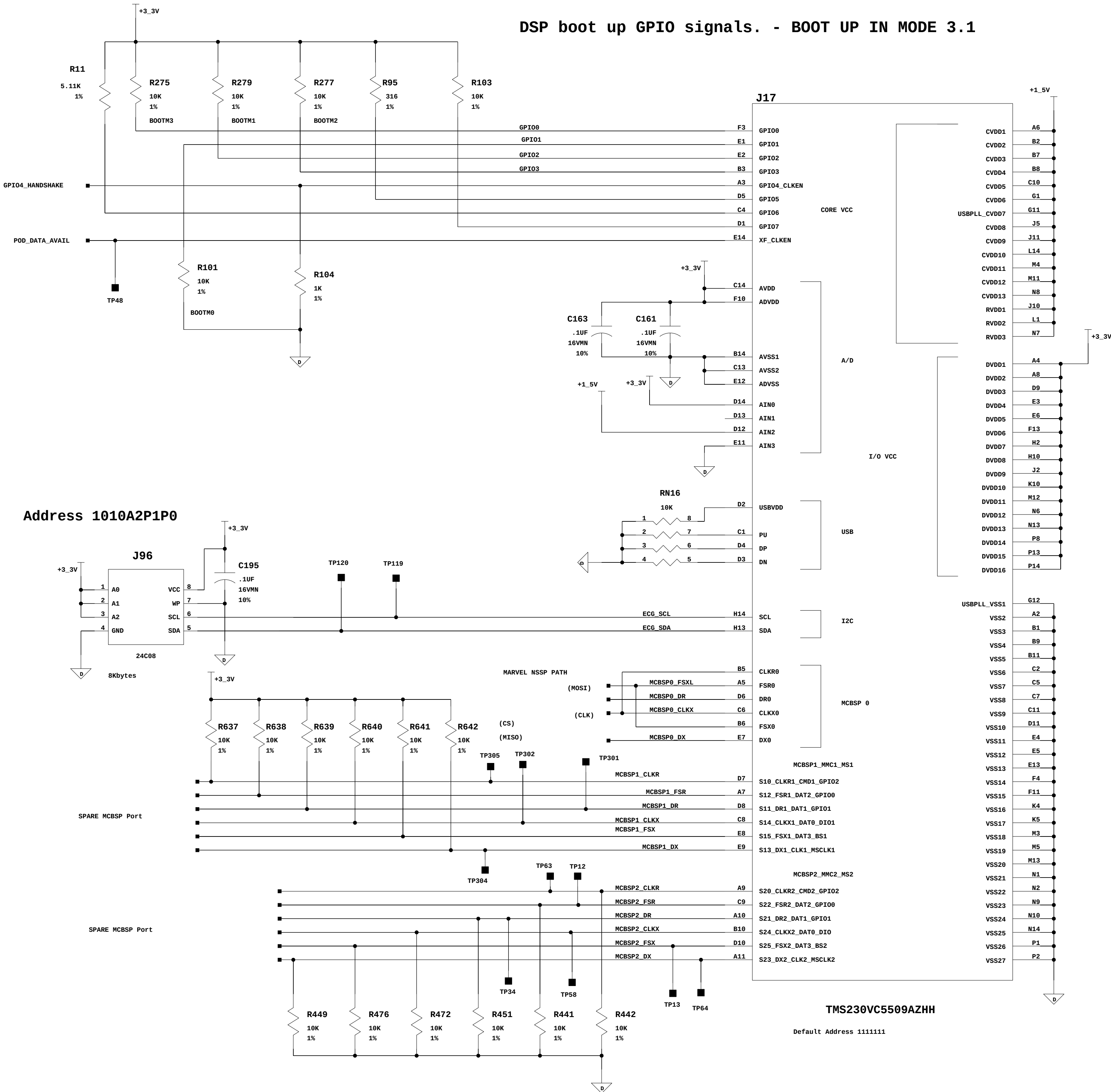
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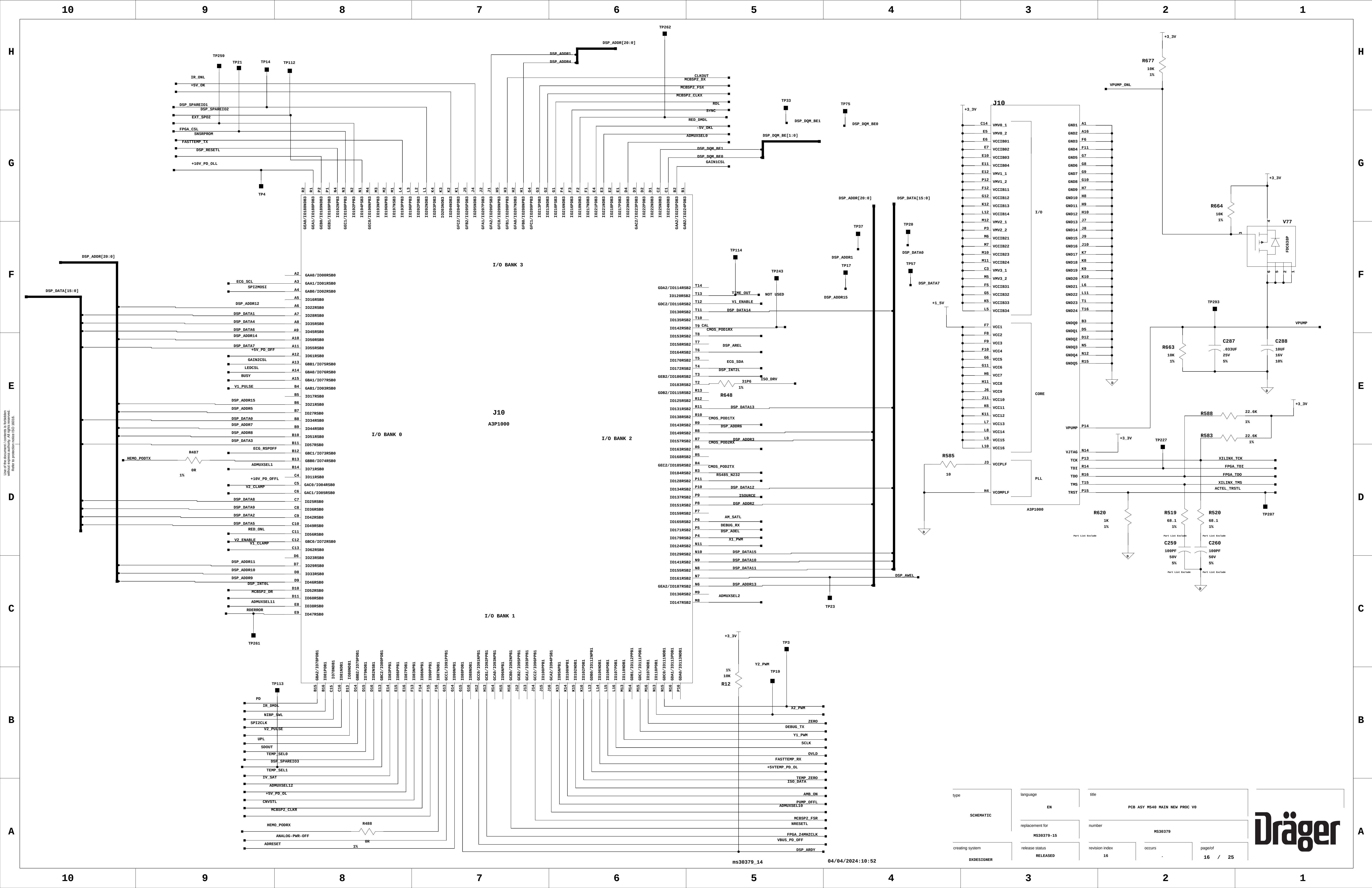
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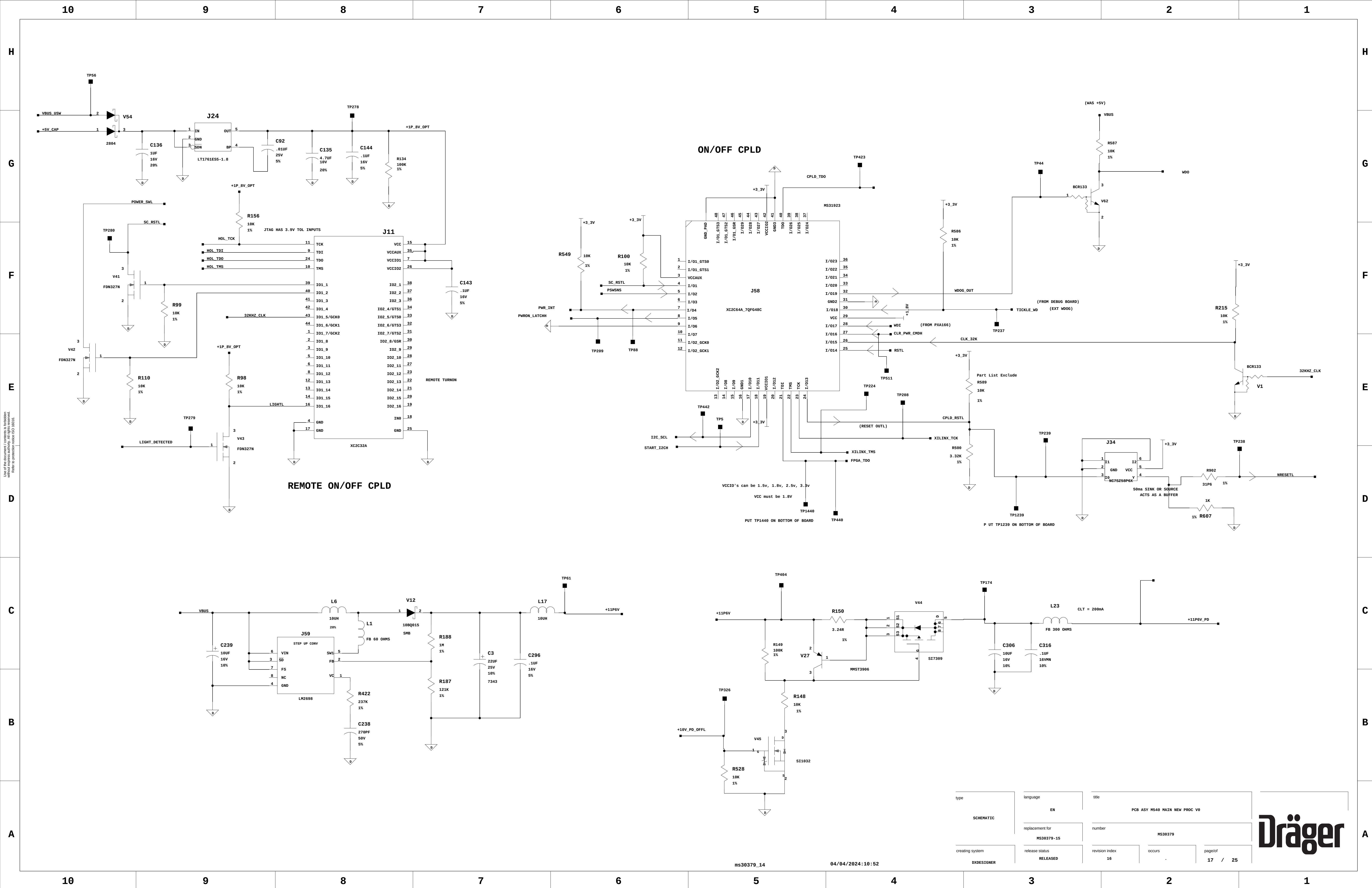


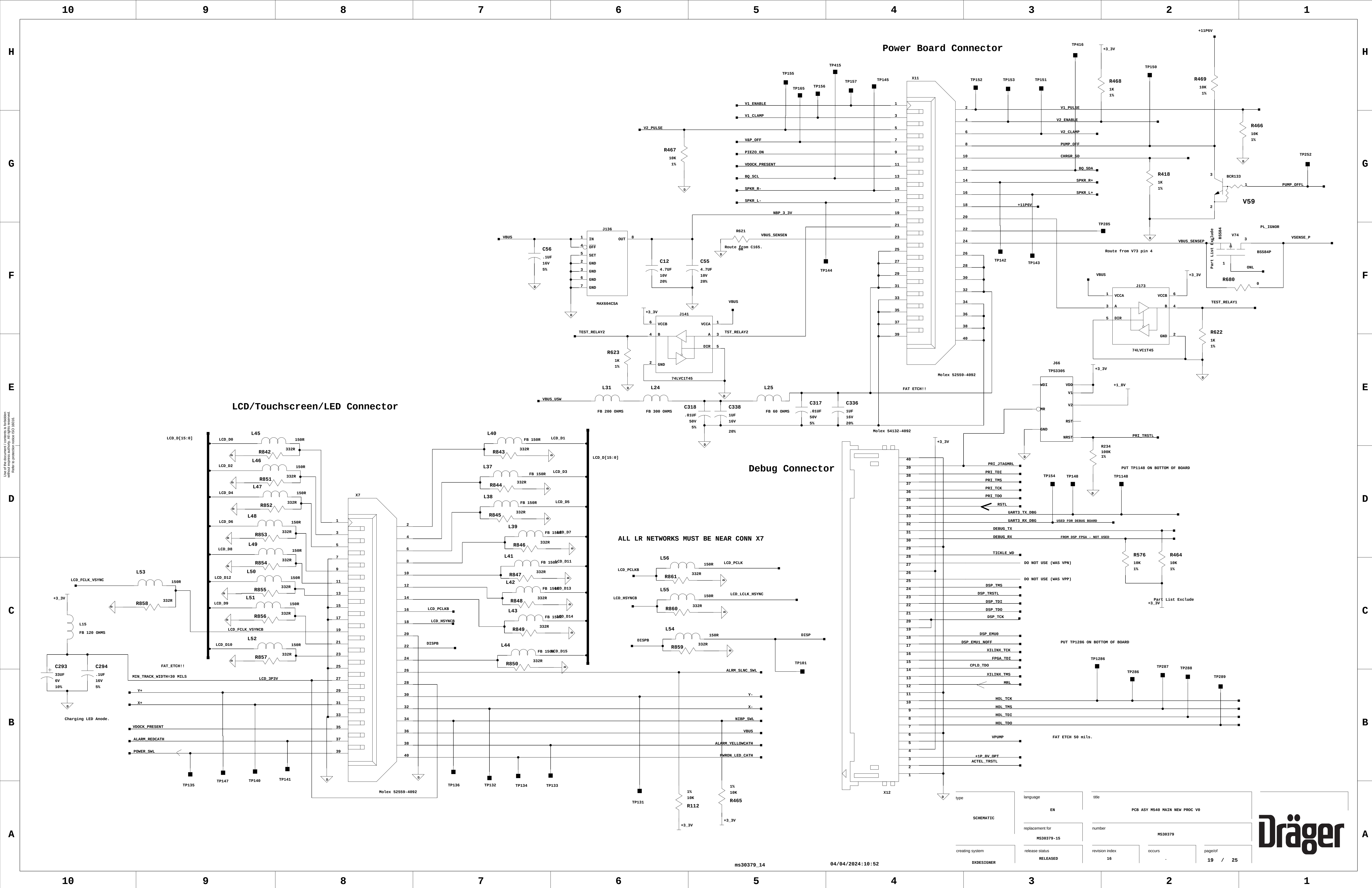
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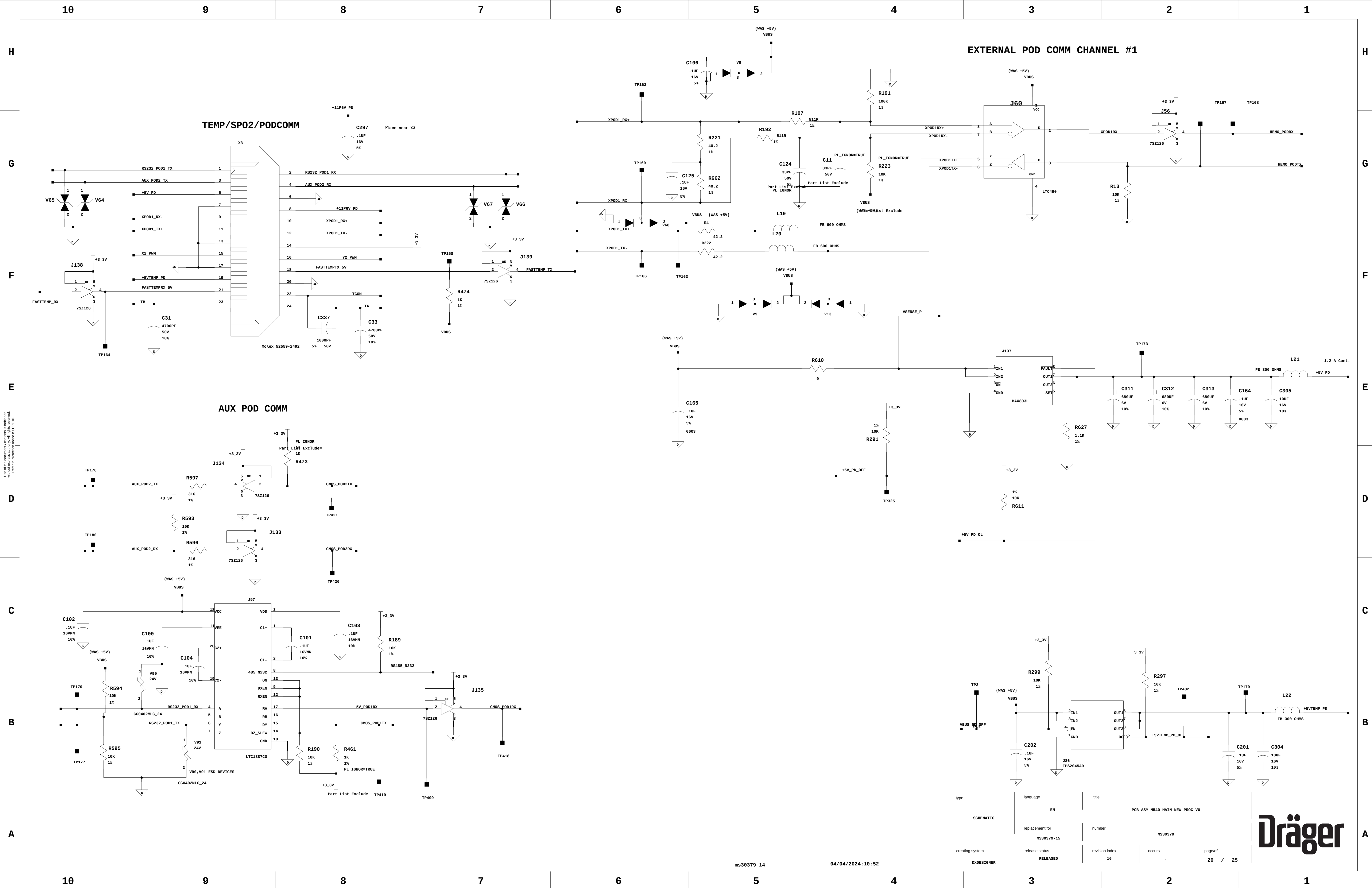


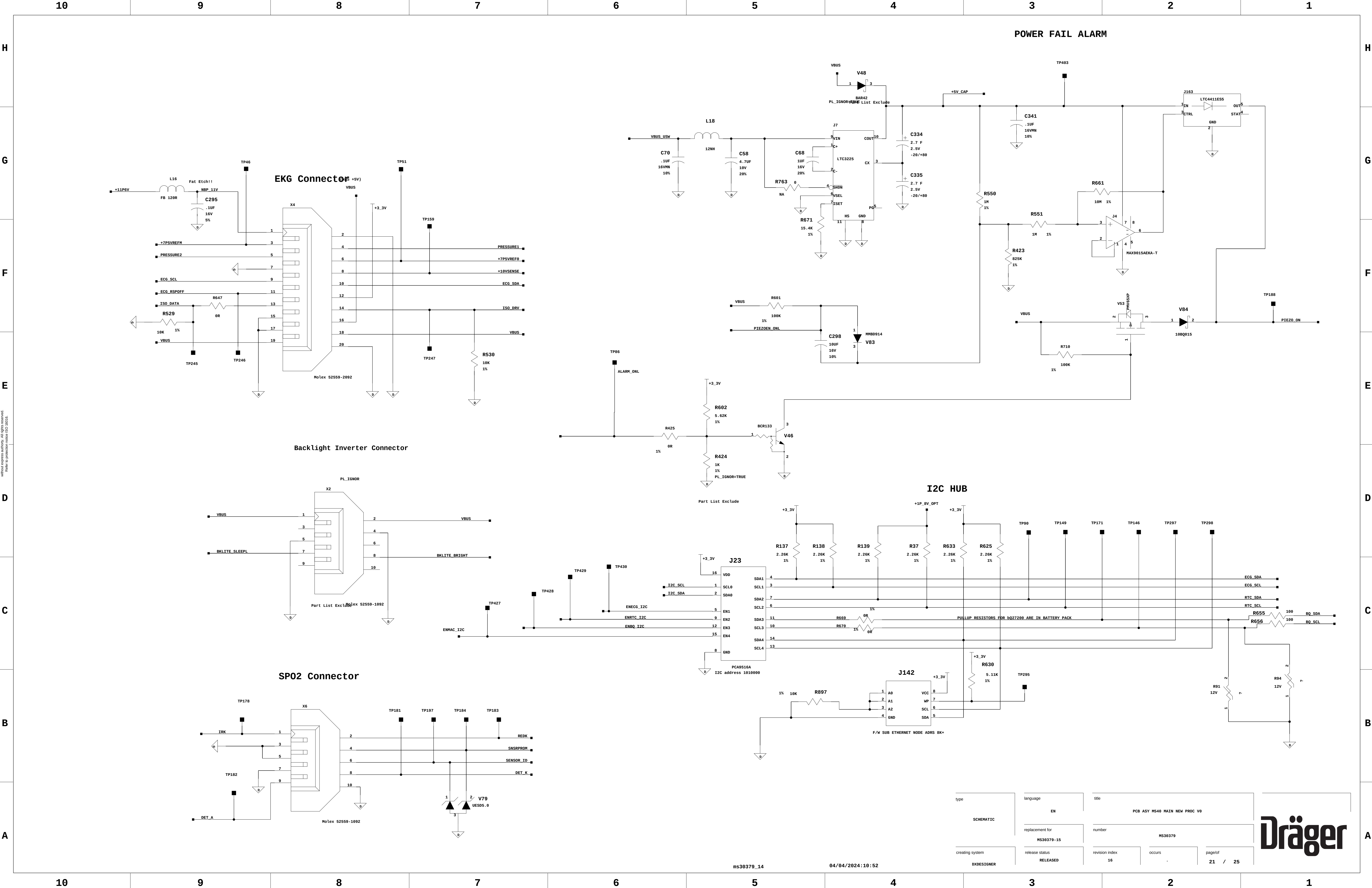
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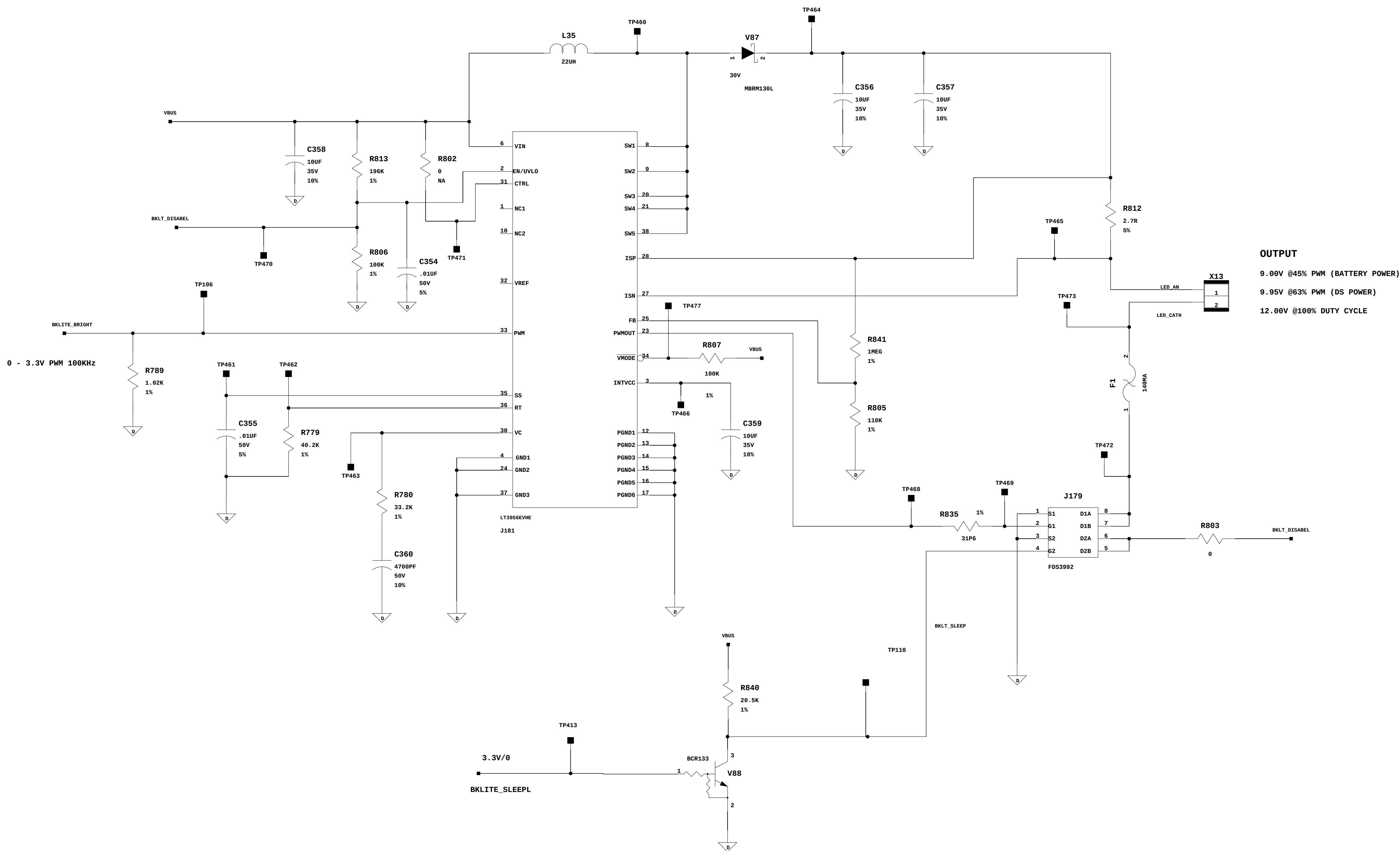








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OUTPUT

9.00V @45% PWM (BATTERY POWER)

9.95V @63% PWM (DS POWER)

12.00V @100% DUTY CYCLE

