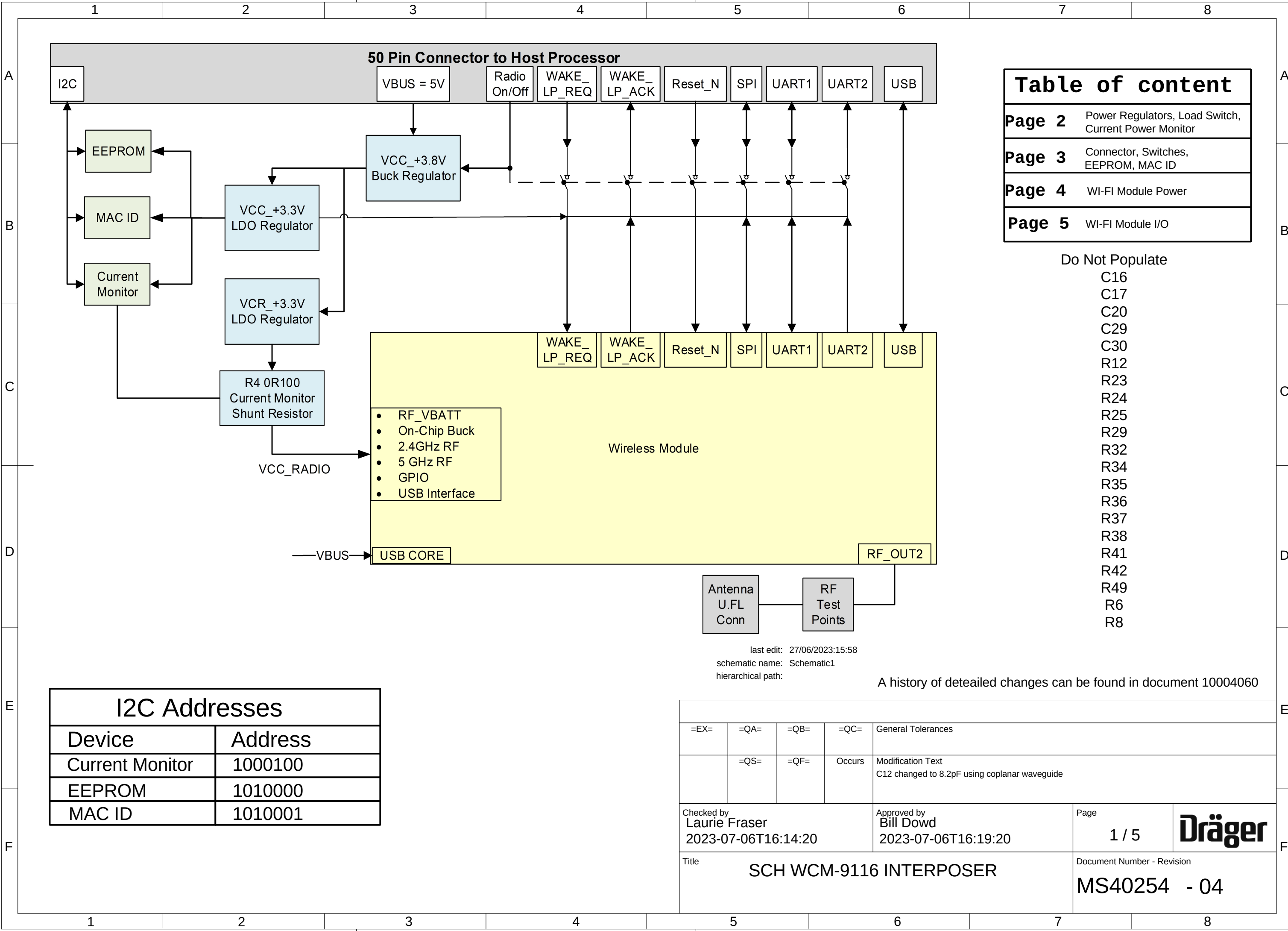


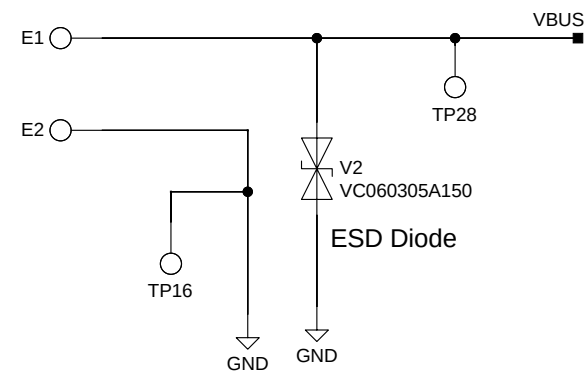
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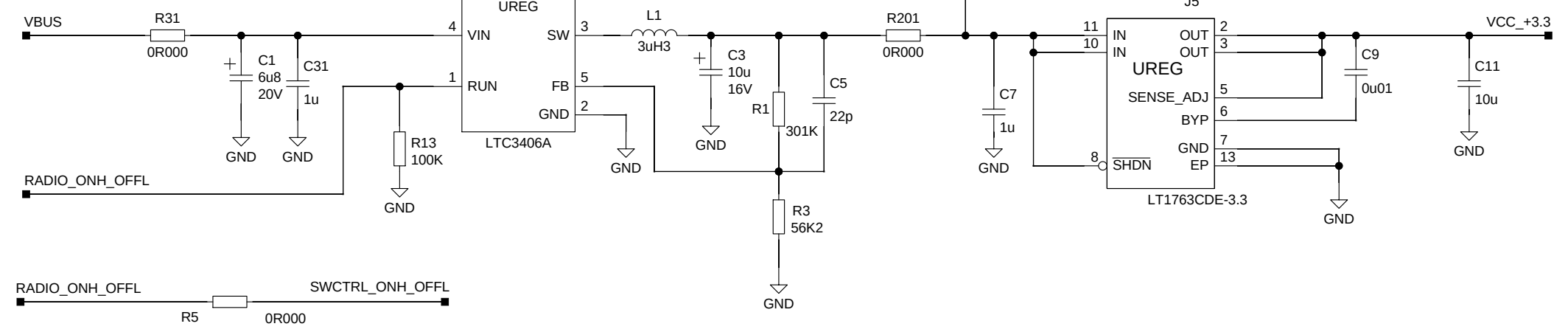
Plated Thru Holes for Ext. Power

1/10" spacing for standard header
Add silk screen for E1 - VBUS E2 - GND

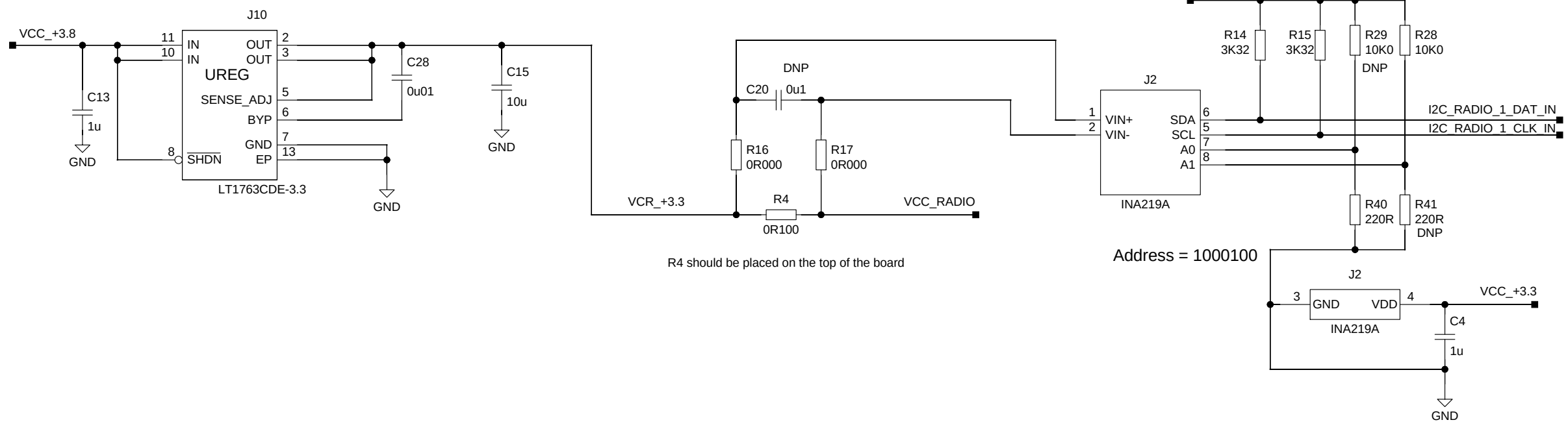


POWER REGULATORS

VBUS = +5V



CURRENT / POWER MONITOR

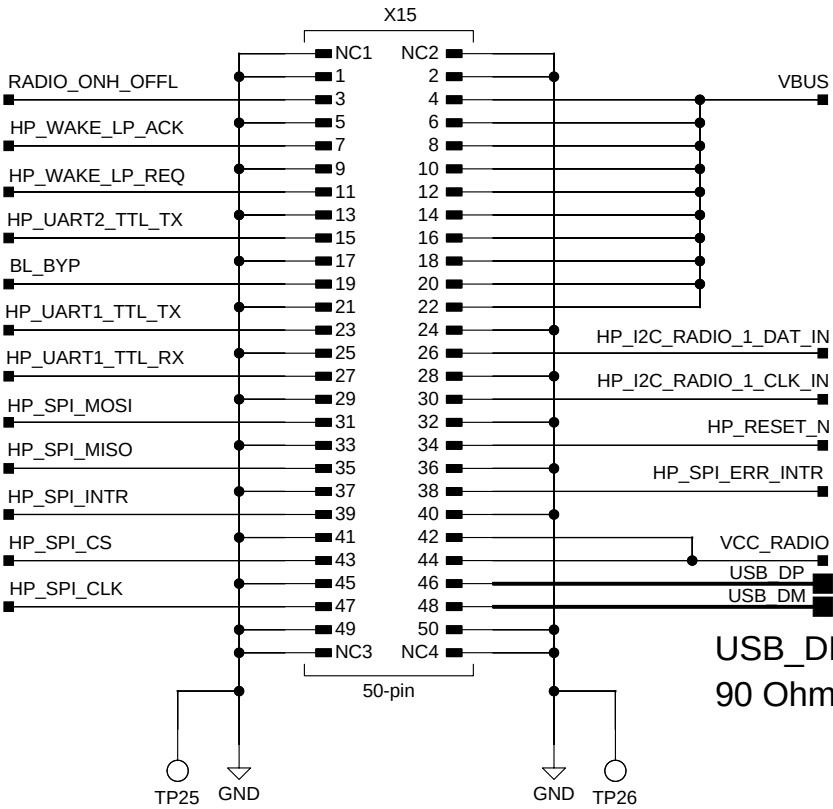


last edit: 27/06/2023:15:58
schematic name: Schematic1
hierarchical path:

* IMPORTANT: All components marked with DNP are Do Not Populate

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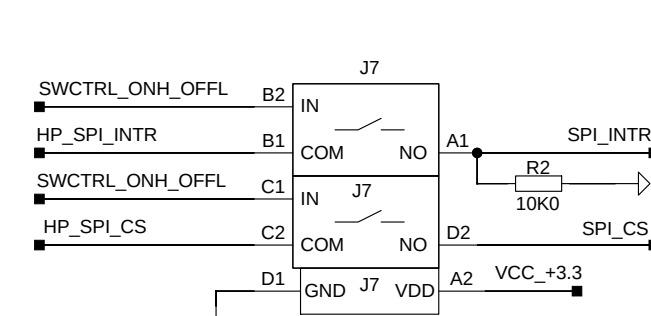
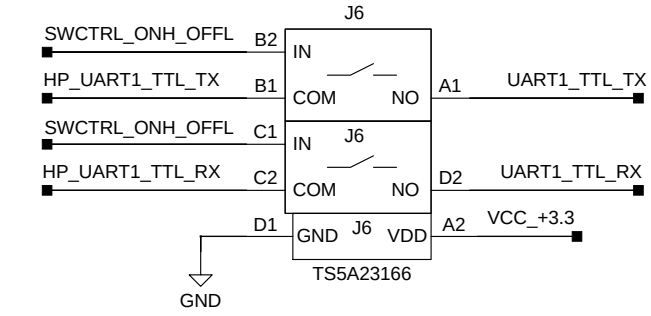
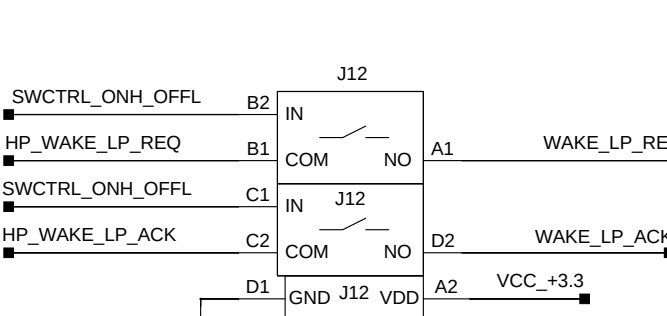
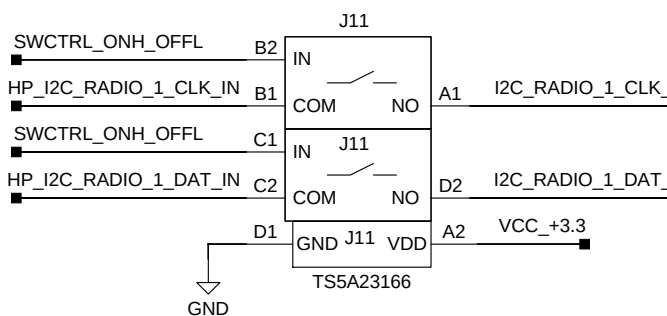
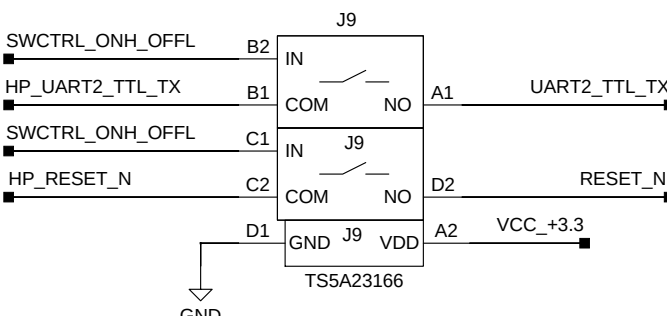
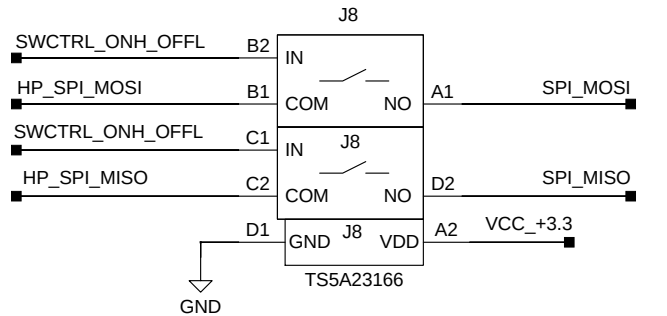
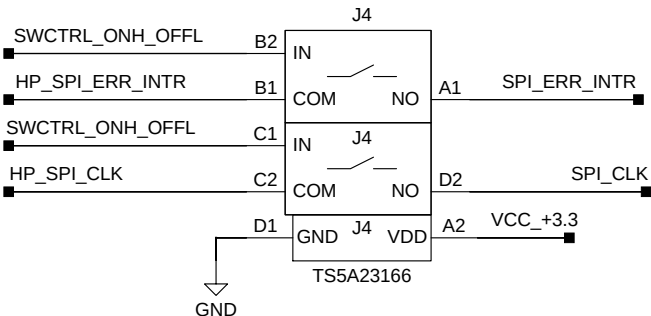
CONNECTOR



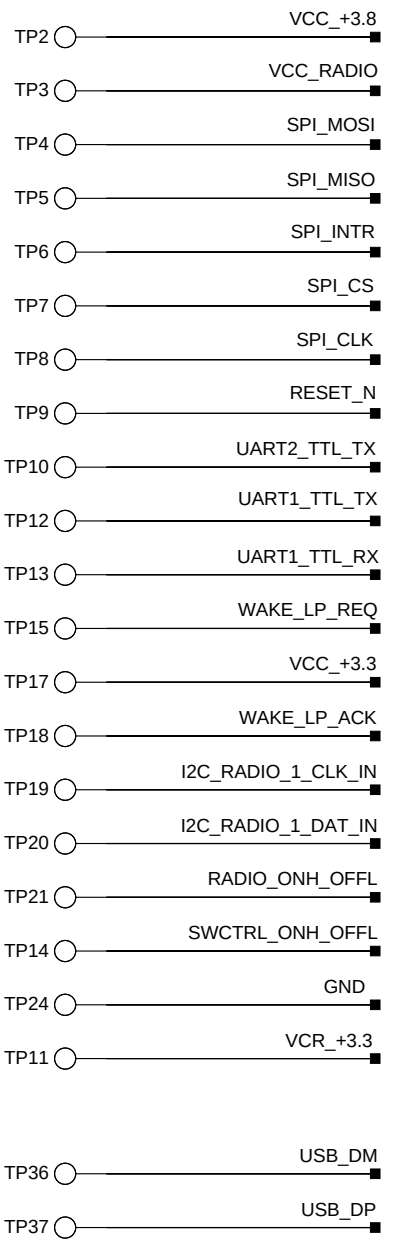
VCC_RADIO is used for troubleshooting Only
Host application should not connect

USB_DP and USB_DM
90 Ohm Differential Lines

SWITCHES

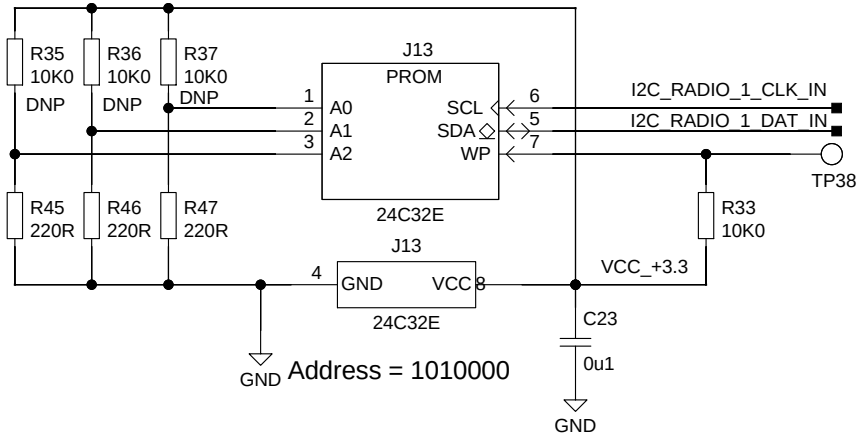


TEST POINTS



USB test point TP36 and TP37
Need to be Inline test point
<without stubs>

EEPROM

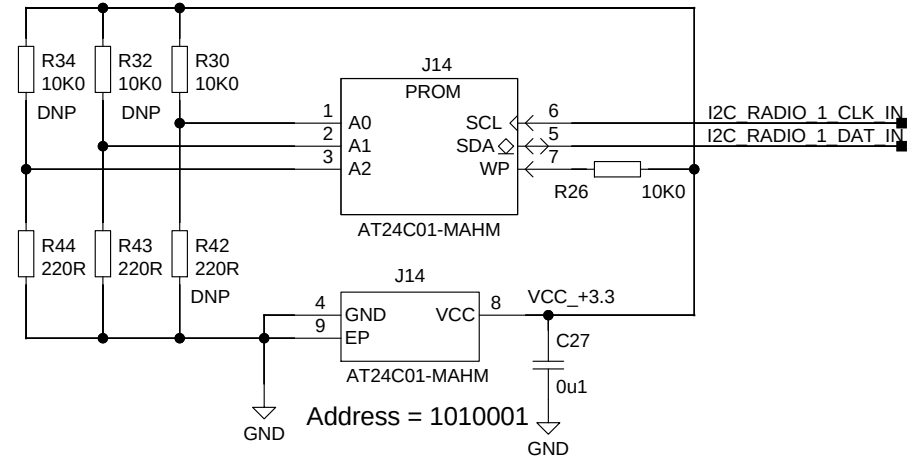


The eighth bit (LSB) of the device address is Read/Write operation select bit

A Read operation is initiated if this bit is high

A Write Operation is initiated if this bit is low

MAC ID

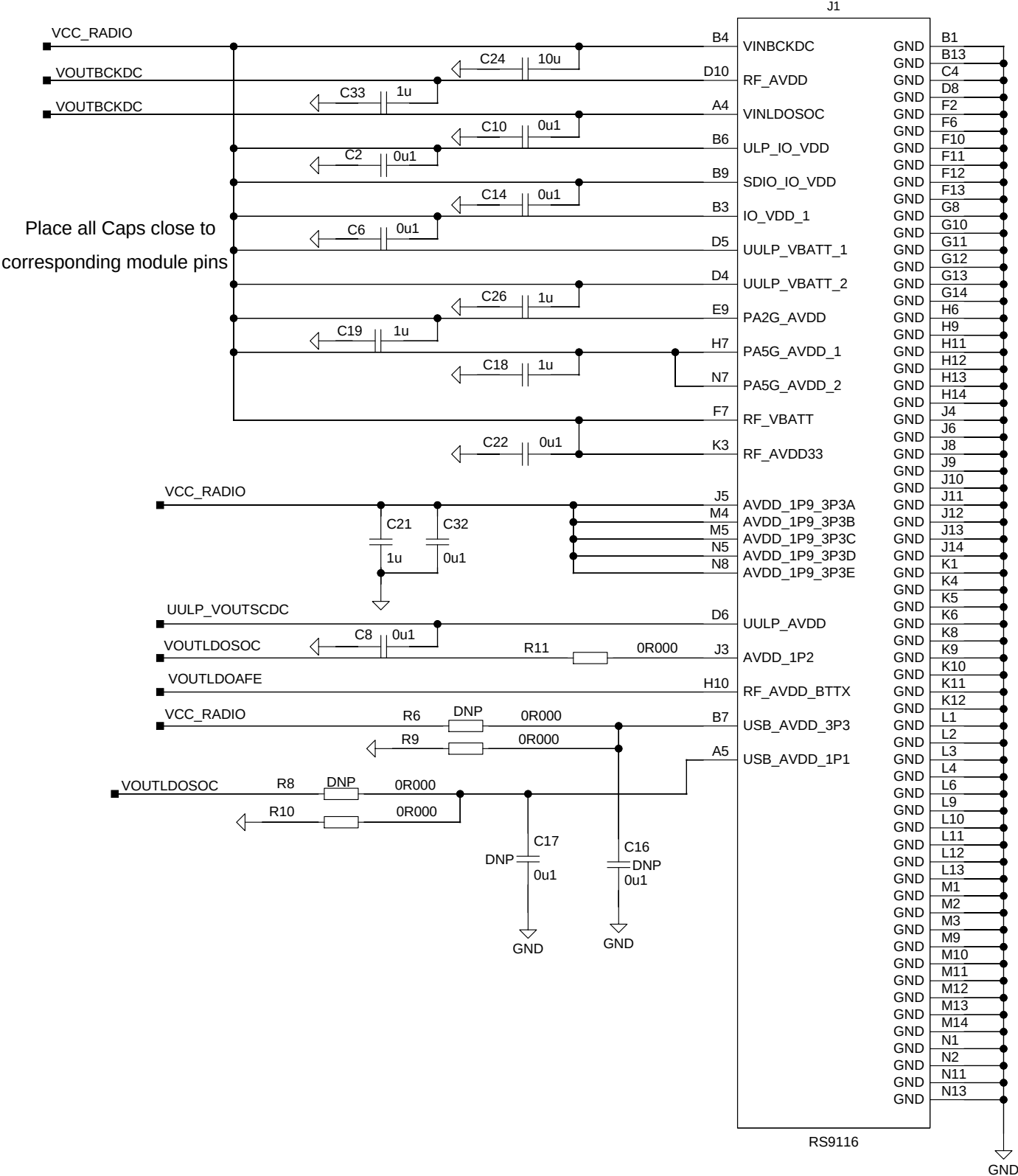


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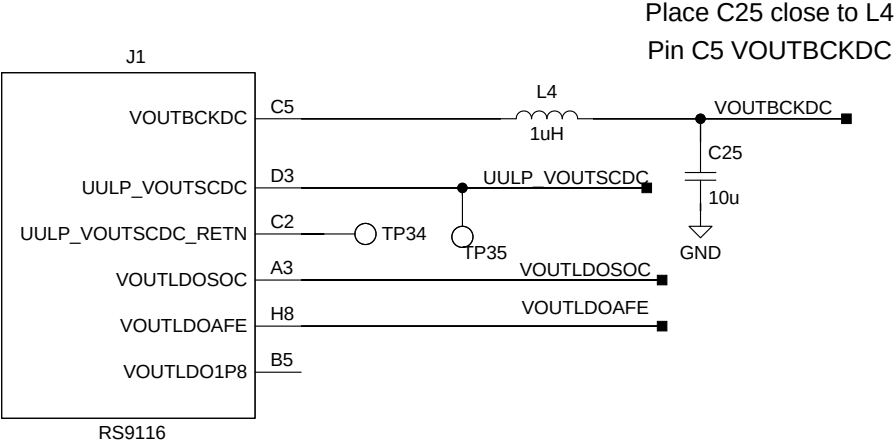
last edit: 27/06/2023:15:58
schematic name: Schematic1
hierarchical path:

RS9116 Power

VCC_RADIO Should be Star Routed



The path from VOUTBCKDC to VINLDOSOC is a high current path.
The Trace should be as short & wide as possible



Place C25 close to L4
Pin C5 VOUTBCKDC

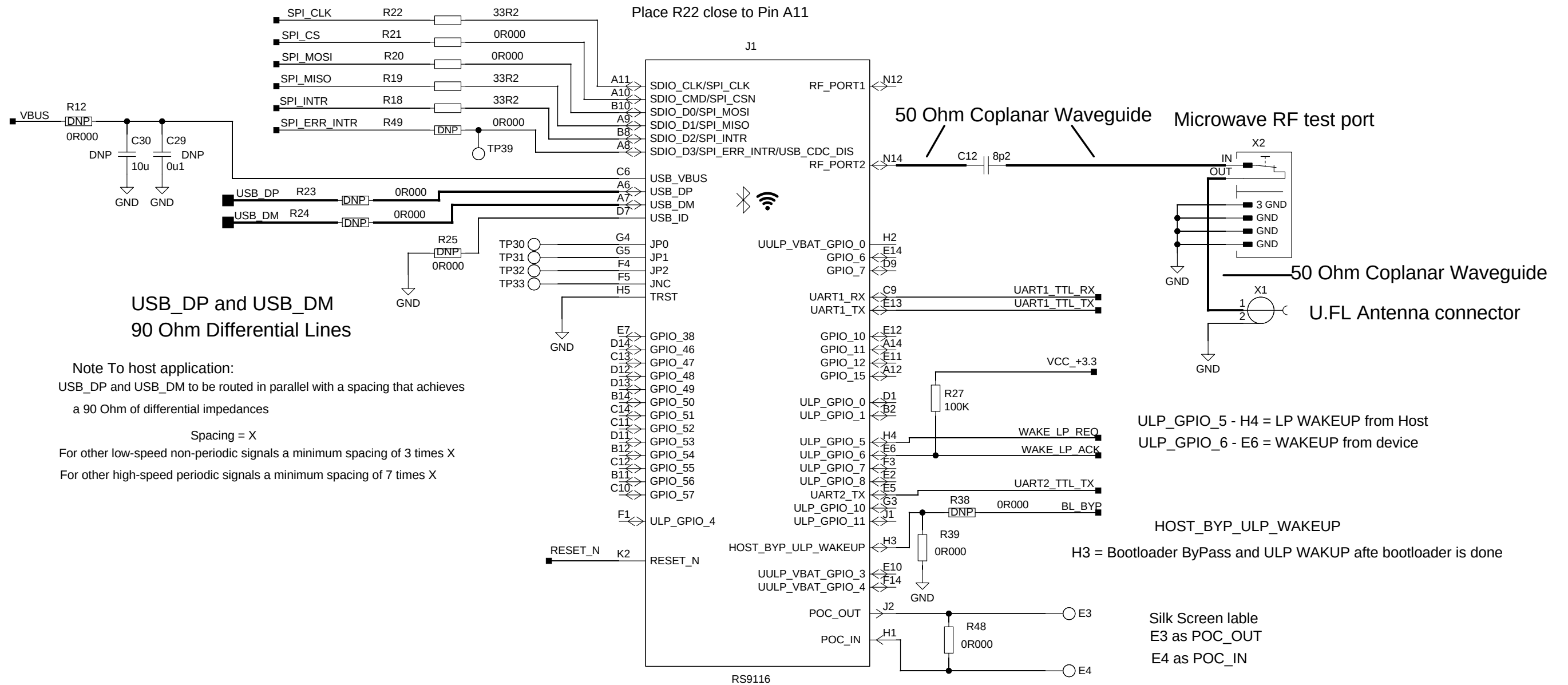
J1	
NC	A1
NC	A2
NC	A13
NC	C1
NC	D2
NC	E1
NC	E3
NC	E4
NC	G1
NC	G2
NC	G6
NC	K13
NC	K14
NC	L8
NC	L14
NC	M7
NC	M8
NC	N3
NC	N4
NC	N6
NC	N9
NC	N10
NC	C3
NC	G7
NC	C8
NC	C7

RS9116

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schematic name: Schematic1
hierarchical path:

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RS9116 I/O



last edit: 27/06/2023:15:58
schematic name: Schematic1
hierarchical path: