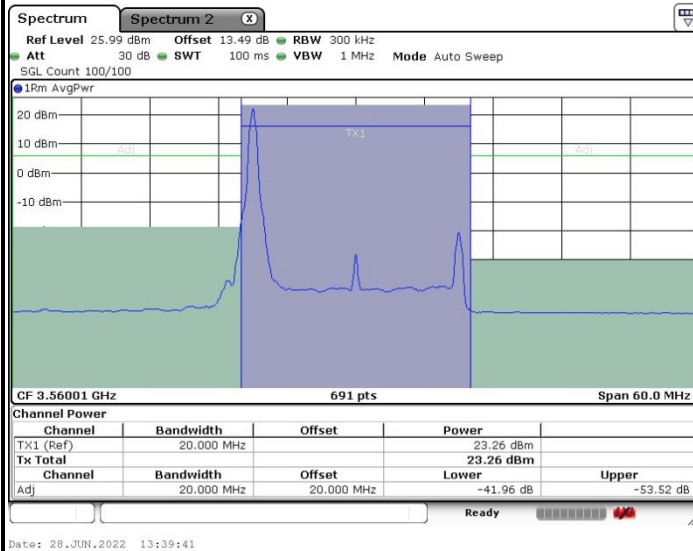




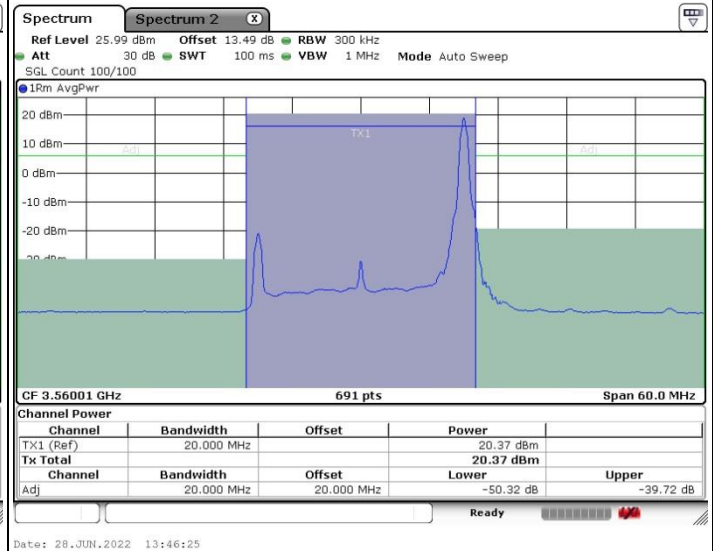
FR1 Part 96 n48 / 20MHz

16QAM

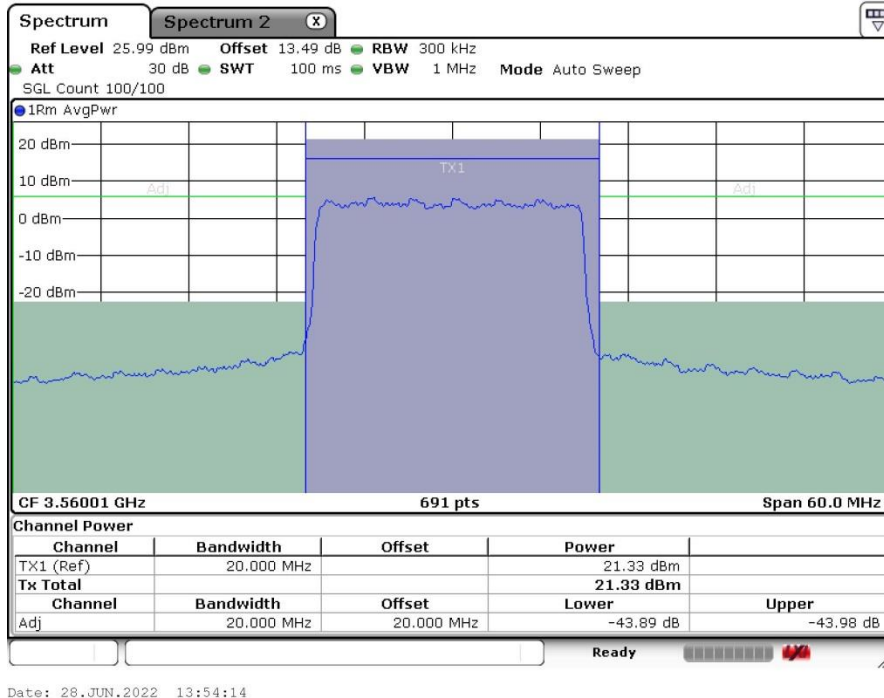
Lowest Channel / 1RB0



Lowest Channel / 1RBmax



Lowest Channel / FullRB

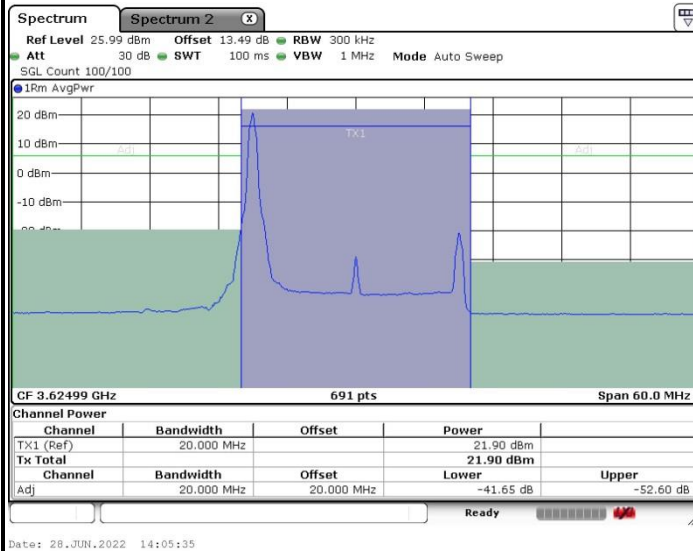




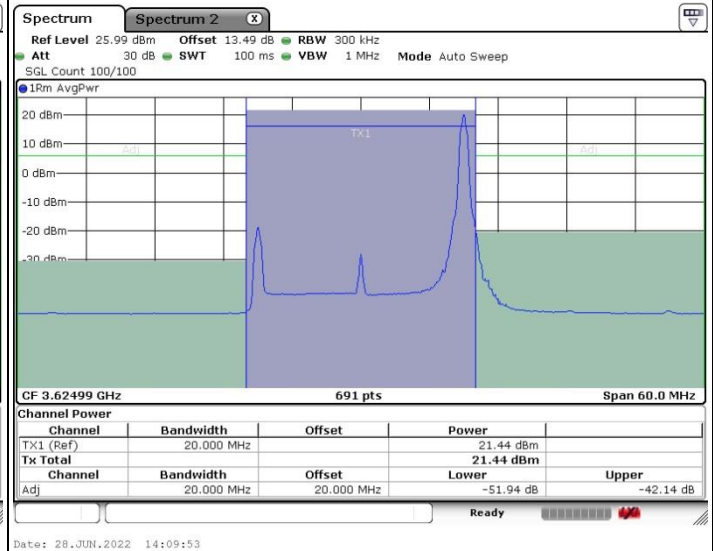
FR1 Part 96 n48 / 20MHz

16QAM

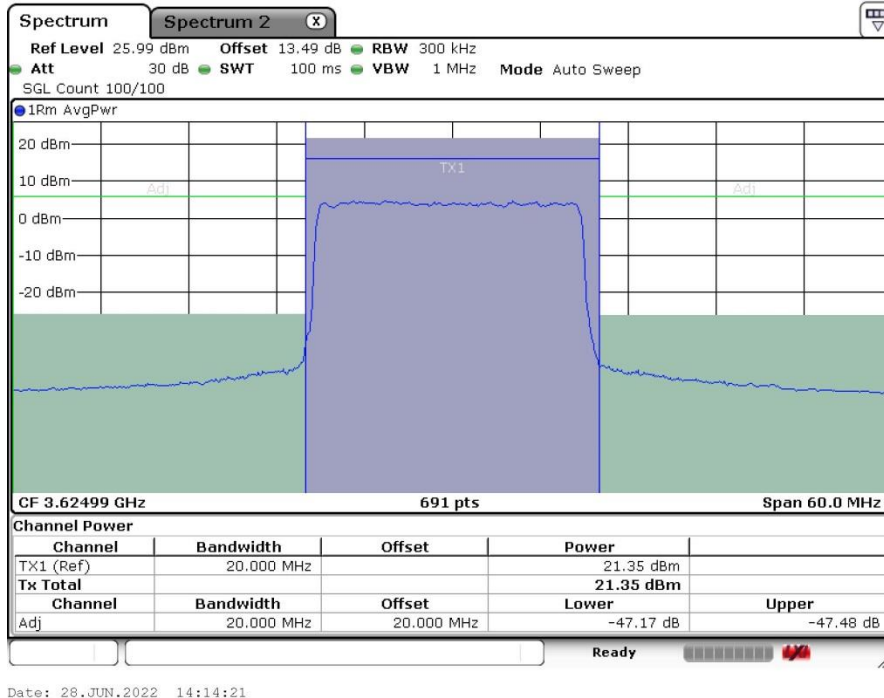
Middle Channel / 1RB0



Middle Channel / 1RBmax



Middle Channel / FullRB

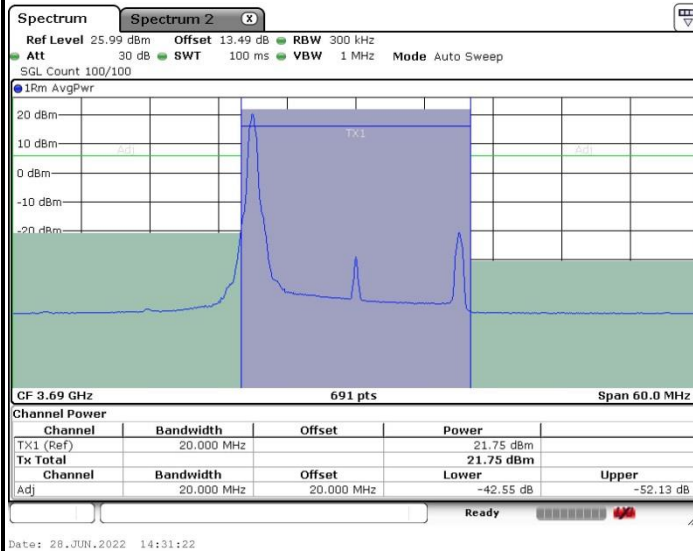




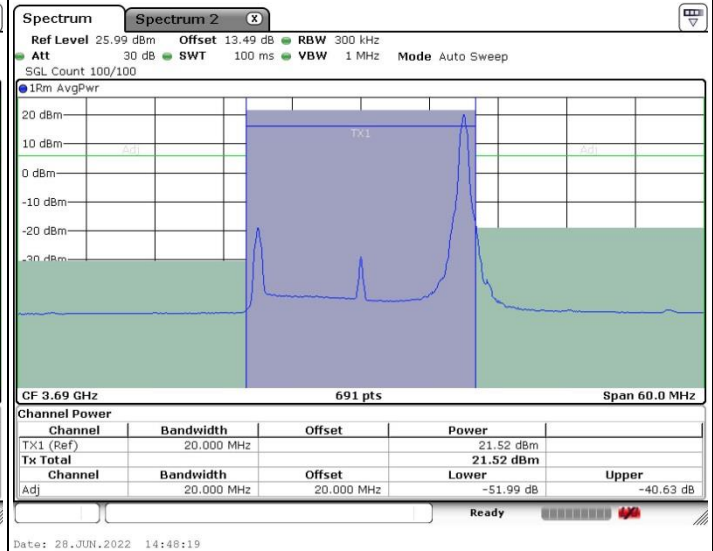
FR1 Part 96 n48 / 20MHz

16QAM

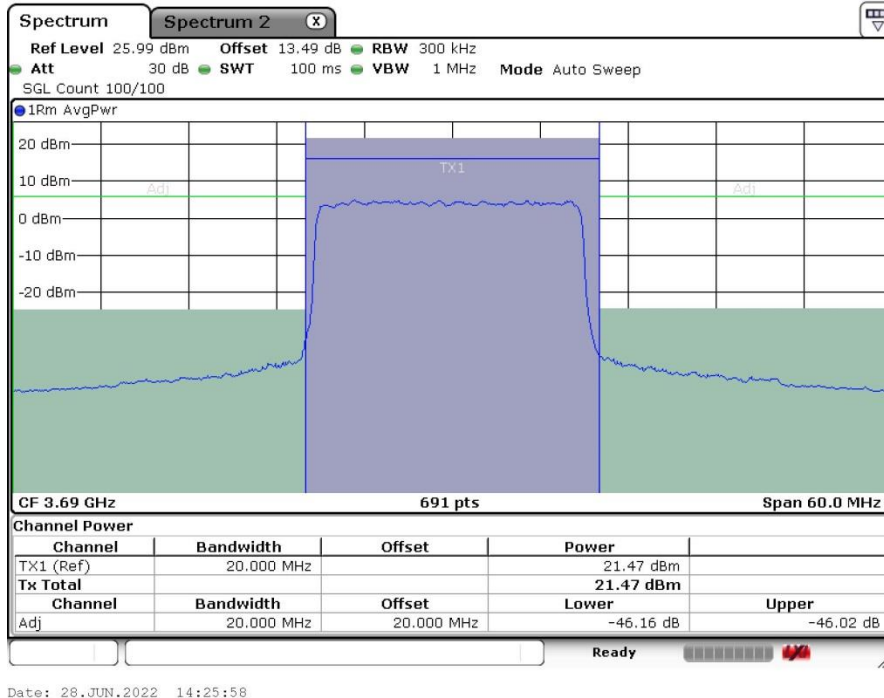
Highest Channel / 1RB0



Highest Channel / 1RBmax



Highest Channel / FullRB

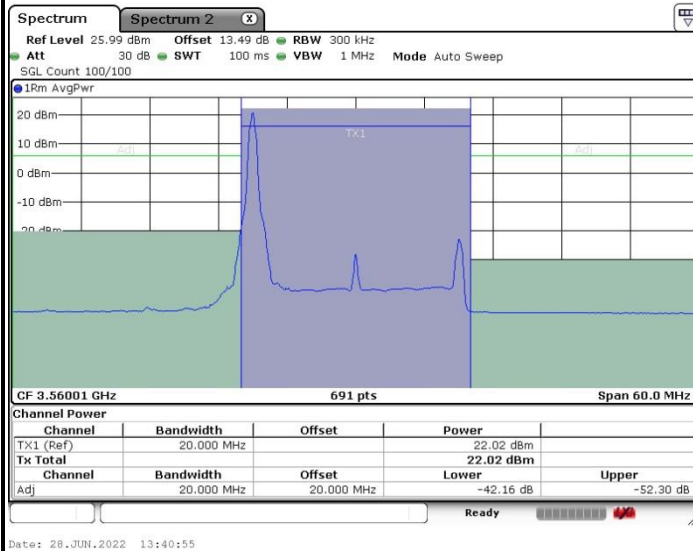




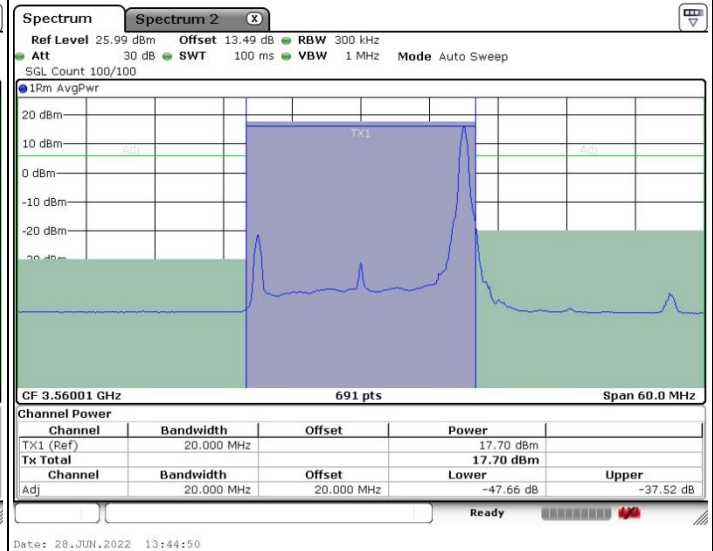
FR1 Part 96 n48 / 20MHz

64QAM

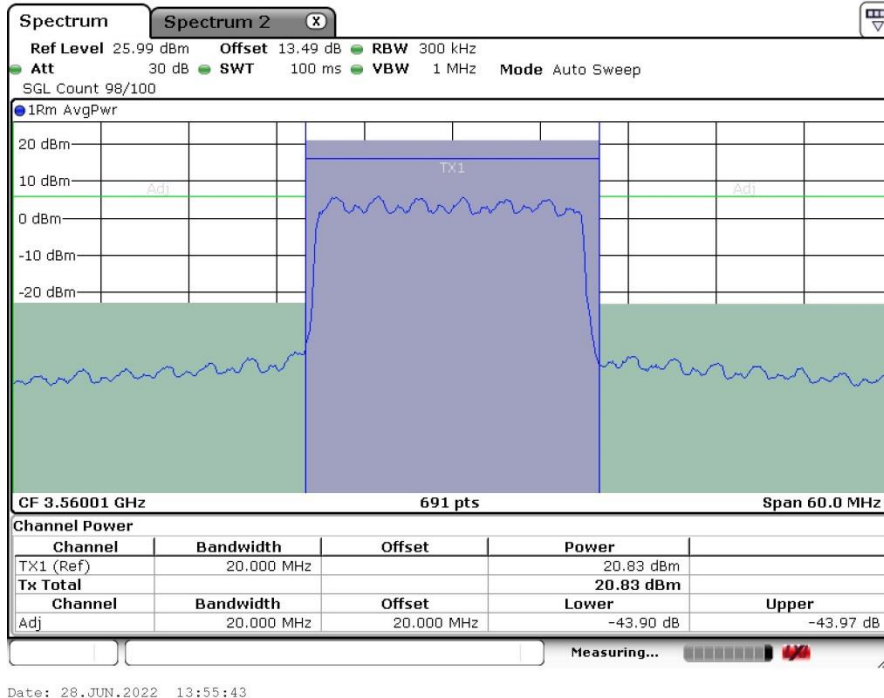
Lowest Channel / 1RB0



Lowest Channel / 1RBmax



Lowest Channel / FullIRB

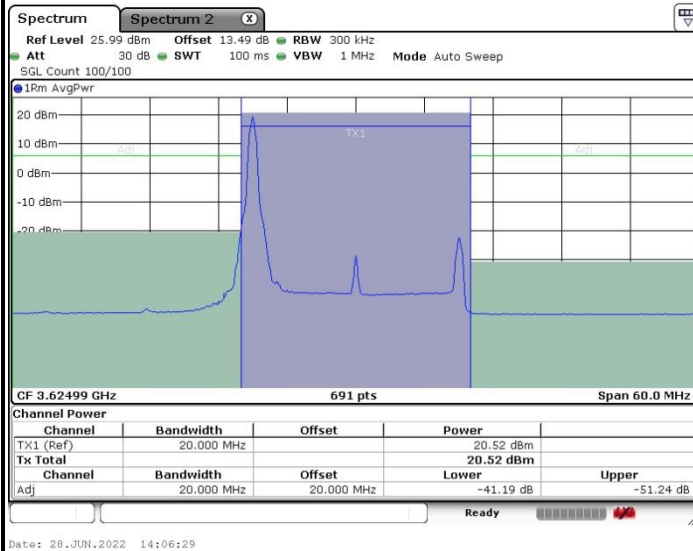




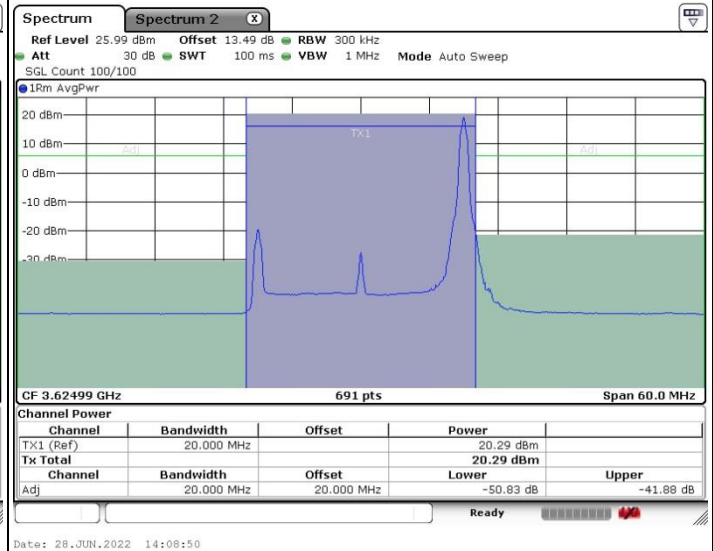
FR1 Part 96 n48 / 20MHz

64QAM

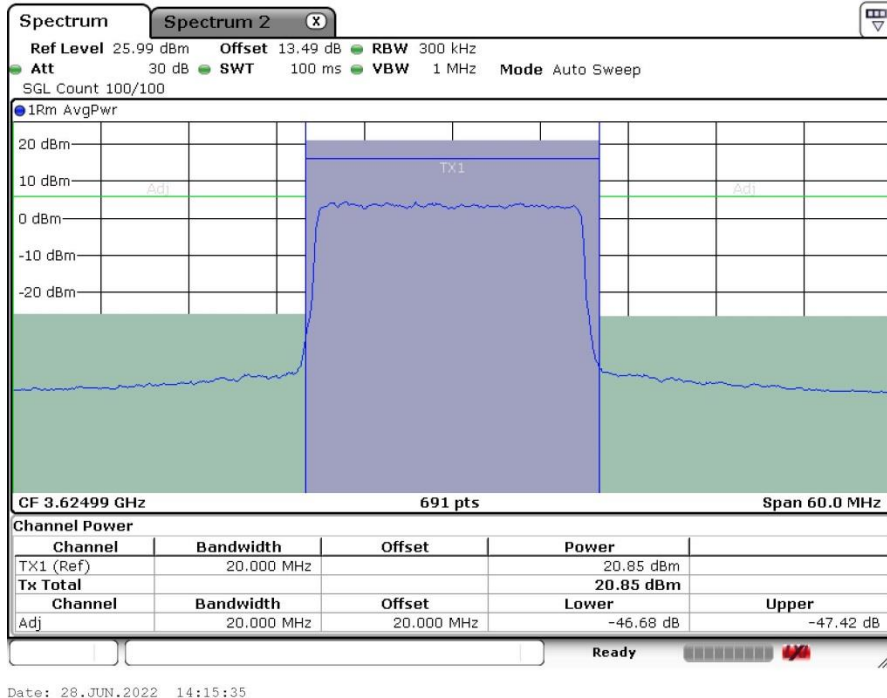
Middle Channel / 1RB0



Middle Channel / 1RBmax



Middle Channel / FullRB

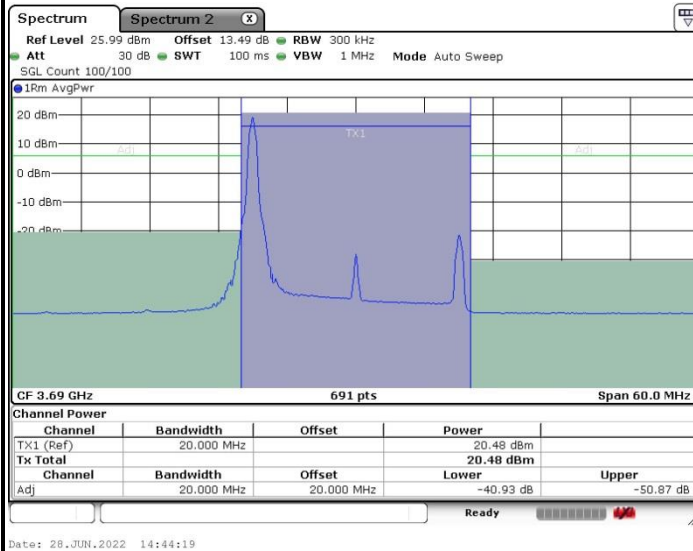




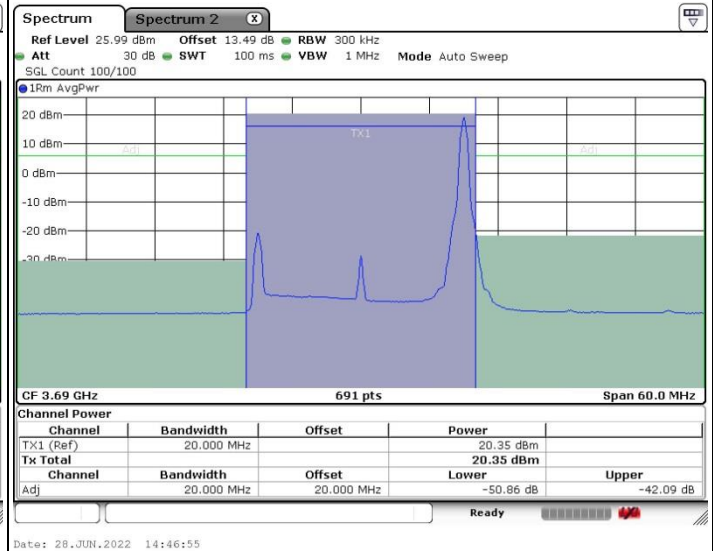
FR1 Part 96 n48 / 20MHz

64QAM

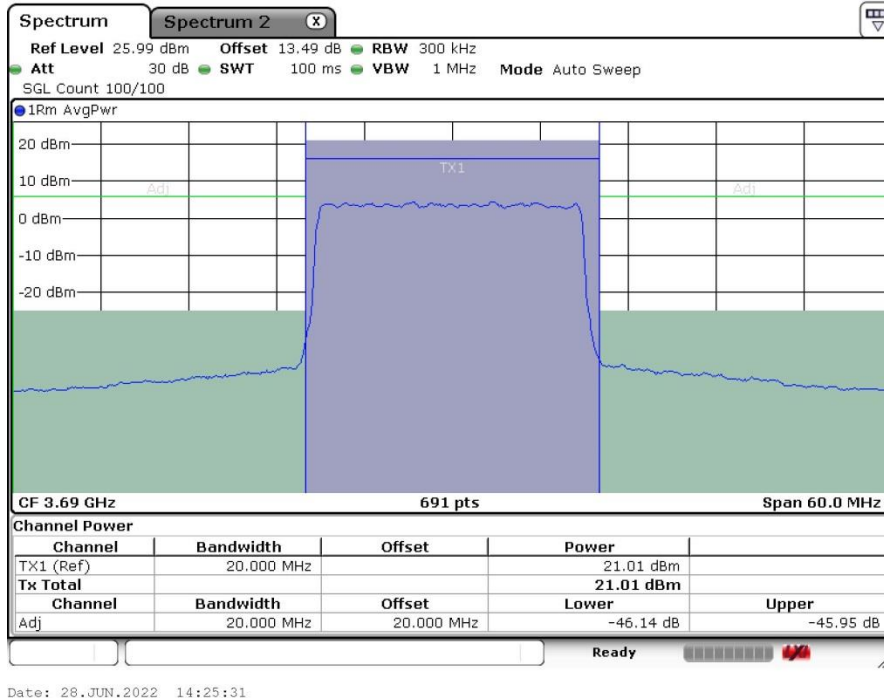
Highest Channel / 1RB0



Highest Channel / 1RBmax



Highest Channel / FullRB

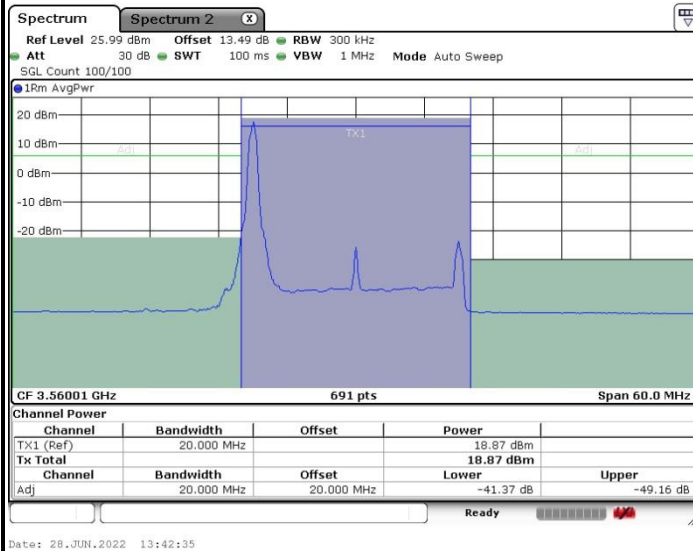




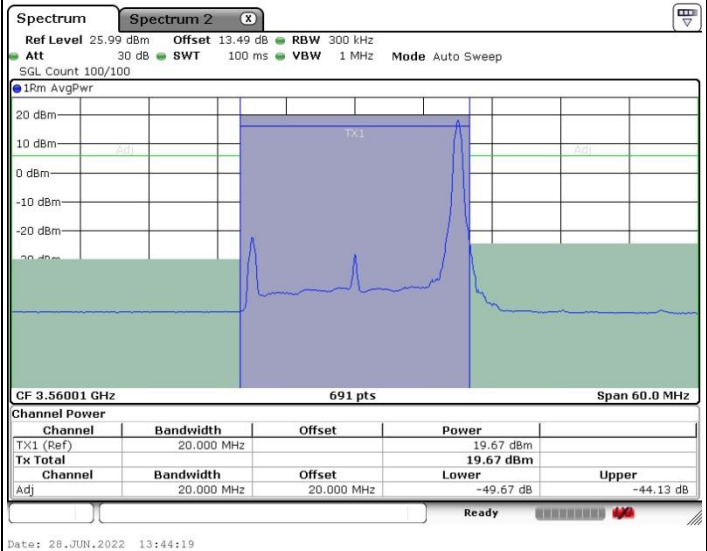
FR1 Part 96 n48 / 20MHz

256QAM

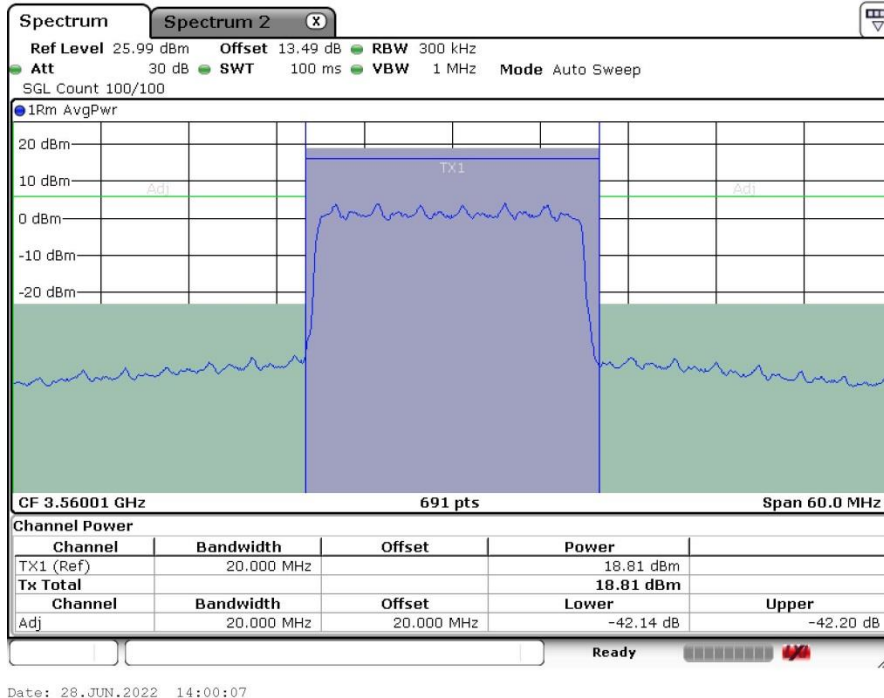
Lowest Channel / 1RB0



Lowest Channel / 1RBmax



Lowest Channel / FullIRB

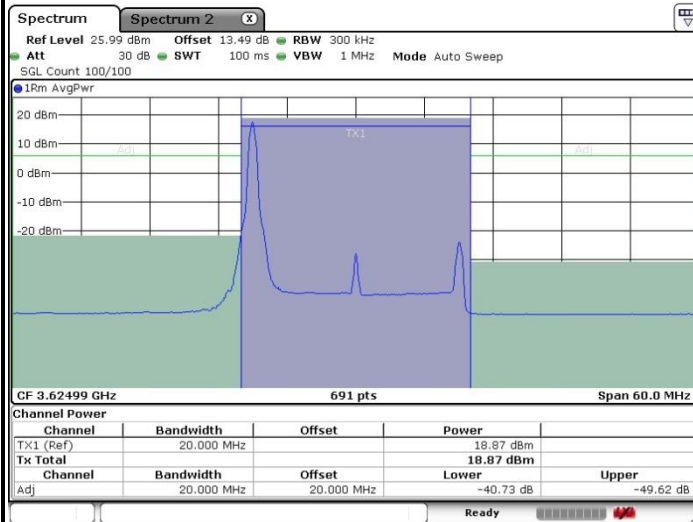




FR1 Part 96 n48 / 20MHz

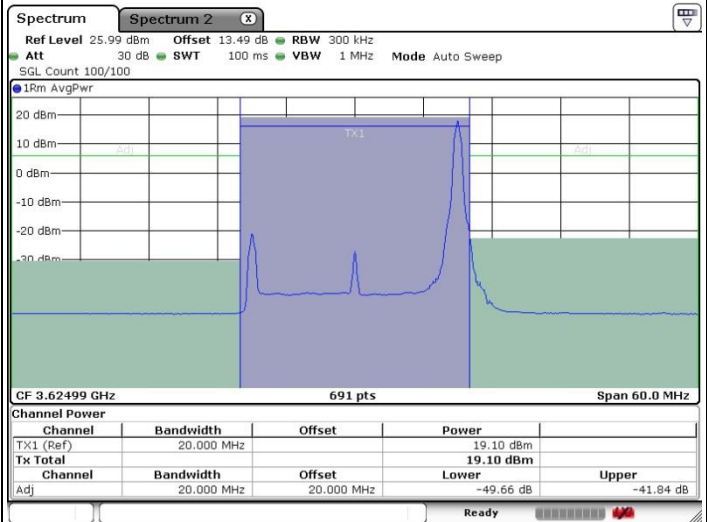
256QAM

Middle Channel / 1RB0



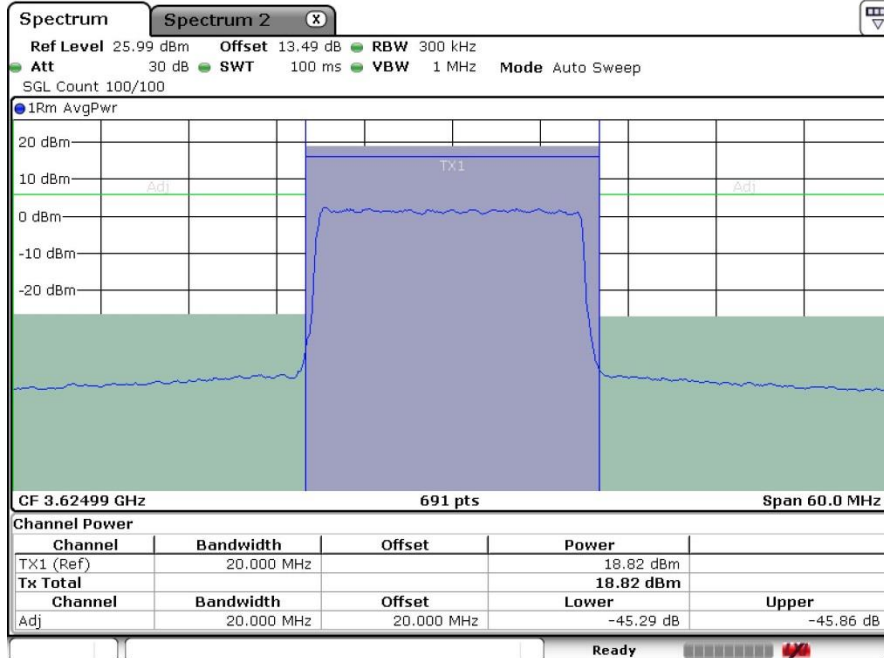
Date: 28.JUN.2022 14:06:56

Middle Channel / 1RBmax



Date: 28.JUN.2022 14:08:17

Middle Channel / FullRB



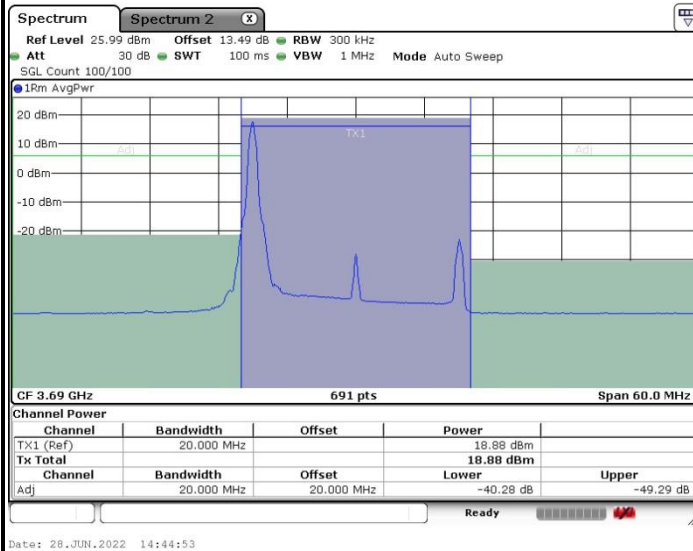
Date: 28.JUN.2022 14:16:02



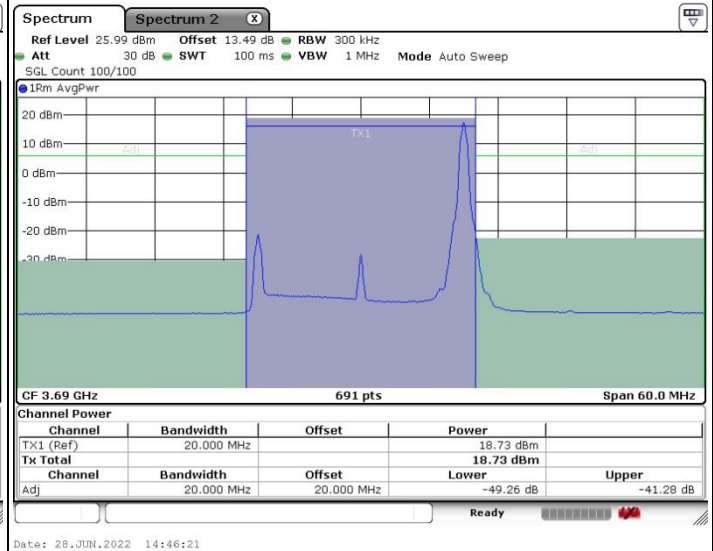
FR1 Part 96 n48 / 20MHz

256QAM

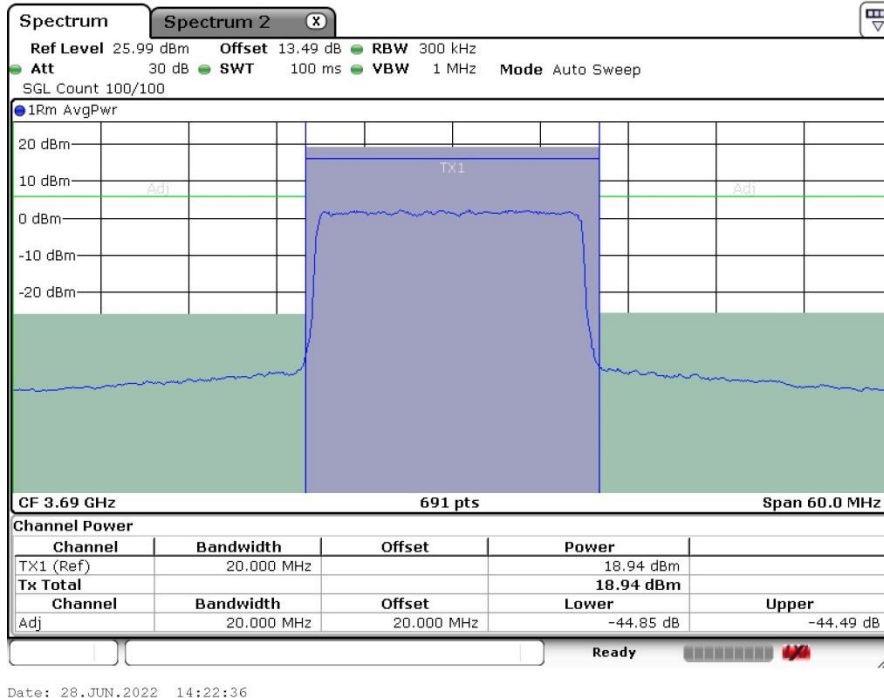
Highest Channel / 1RB0



Highest Channel / 1RBmax



Highest Channel / FullRB

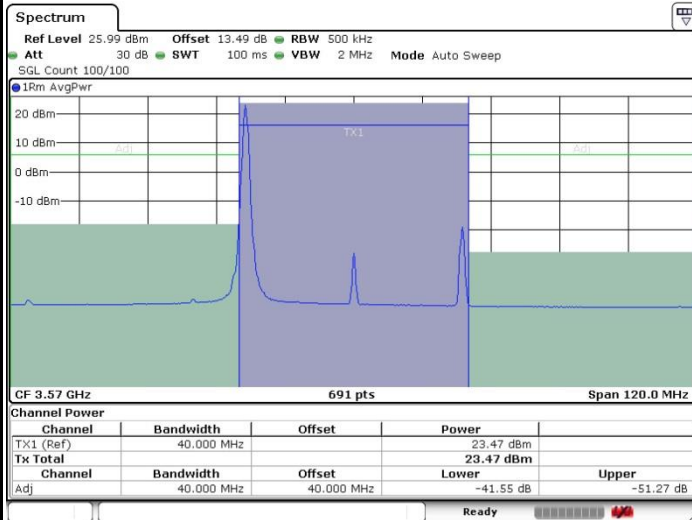




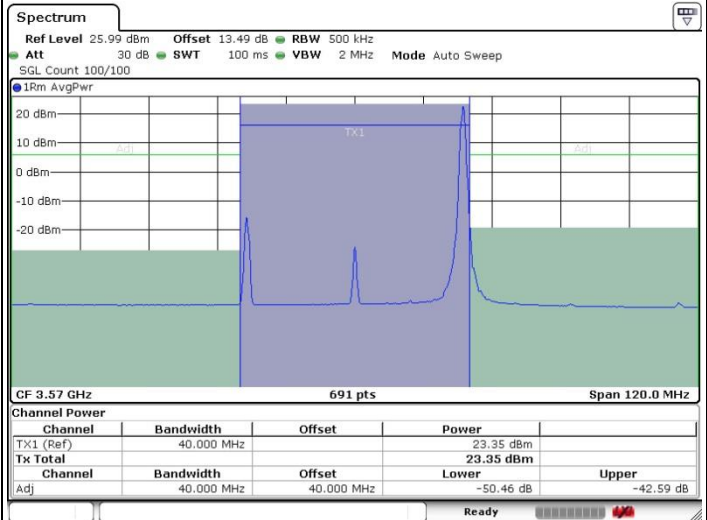
FR1 Part 96 n48 / 40MHz

BPSK

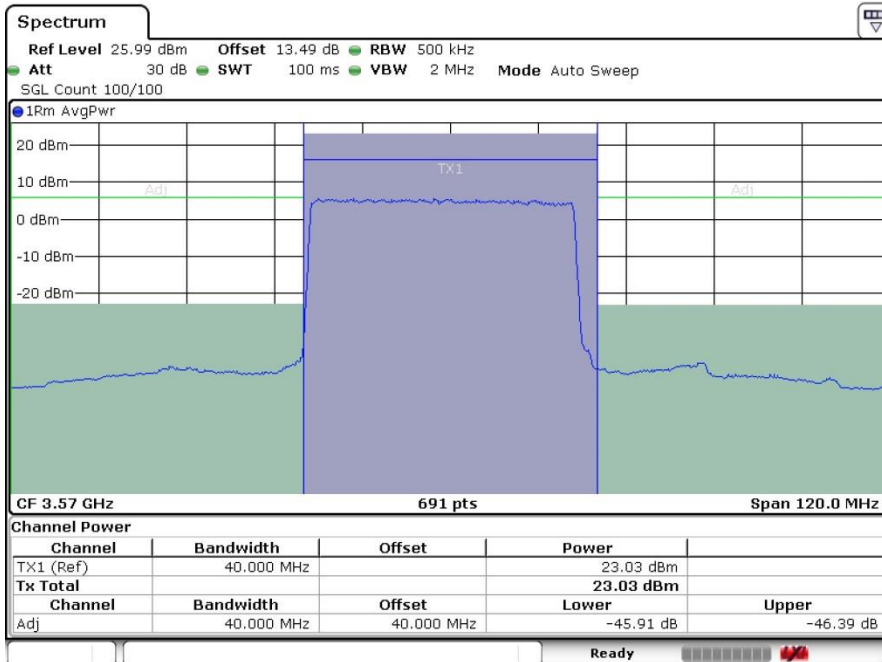
Lowest Channel / 1RB0



Lowest Channel / 1RBmax



Lowest Channel / FullIRB

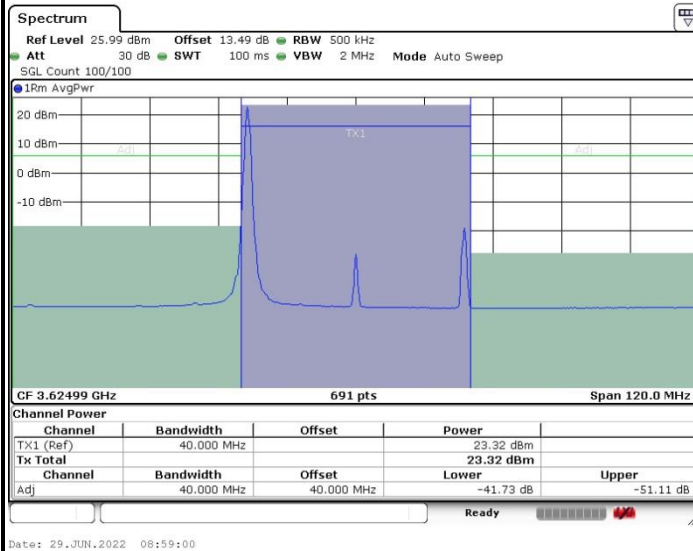




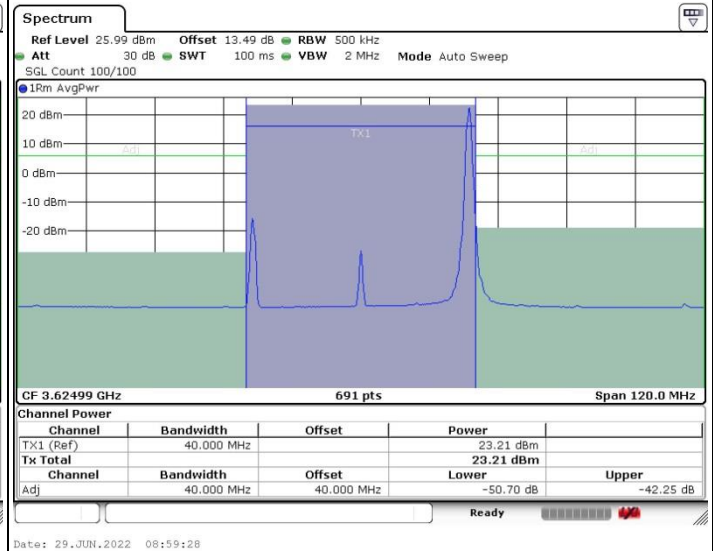
FR1 Part 96 n48 / 40MHz

BPSK

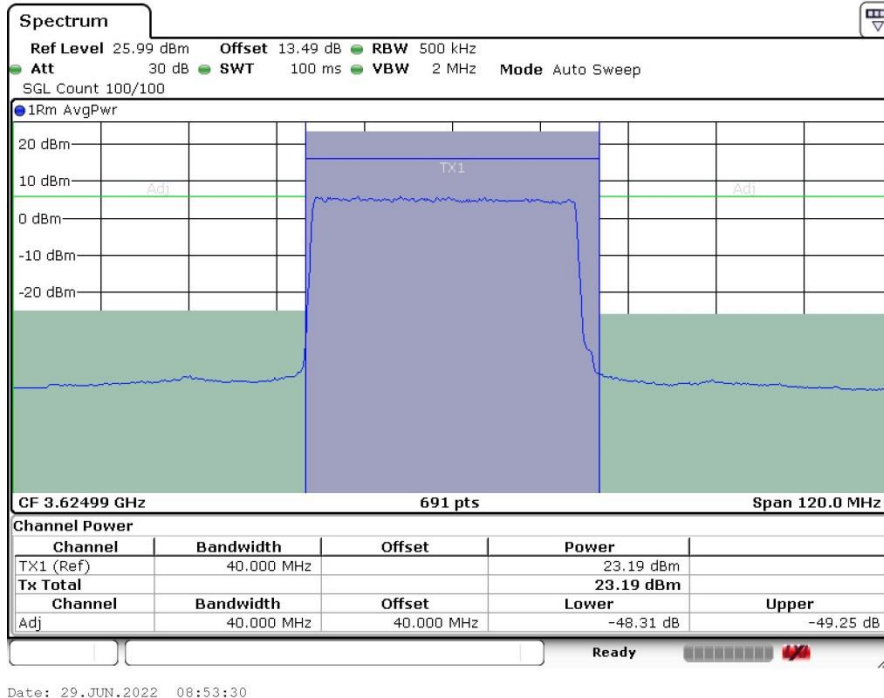
Middle Channel / 1RB0



Middle Channel / 1RBmax



Middle Channel / FullRB

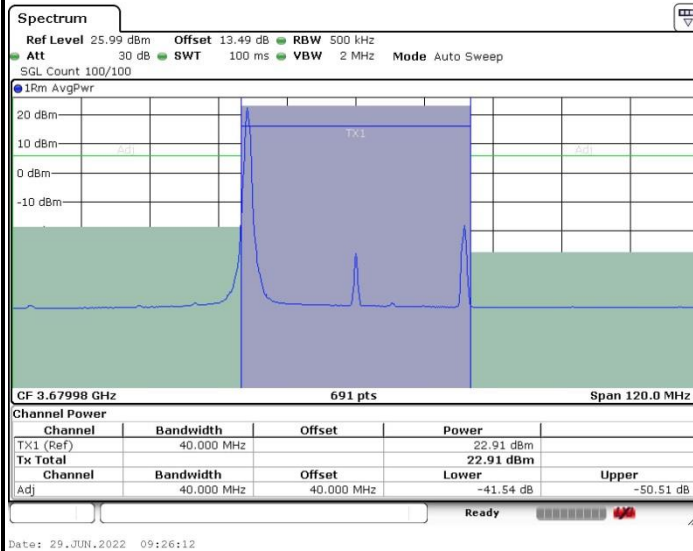




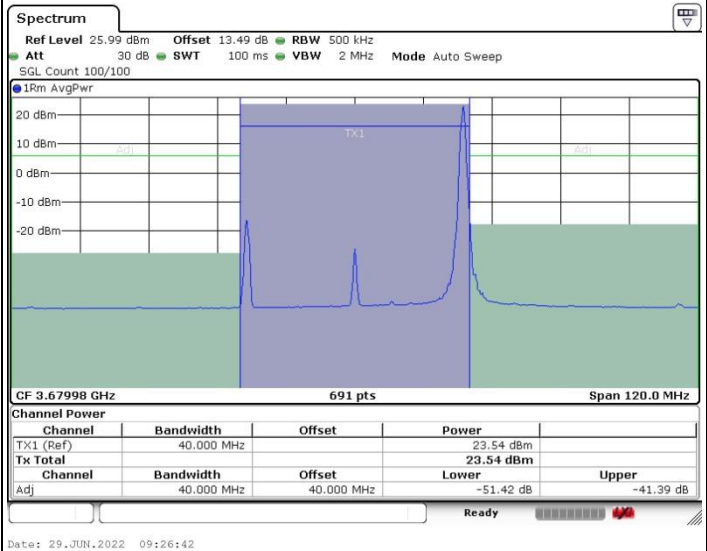
FR1 Part 96 n48 / 40MHz

BPSK

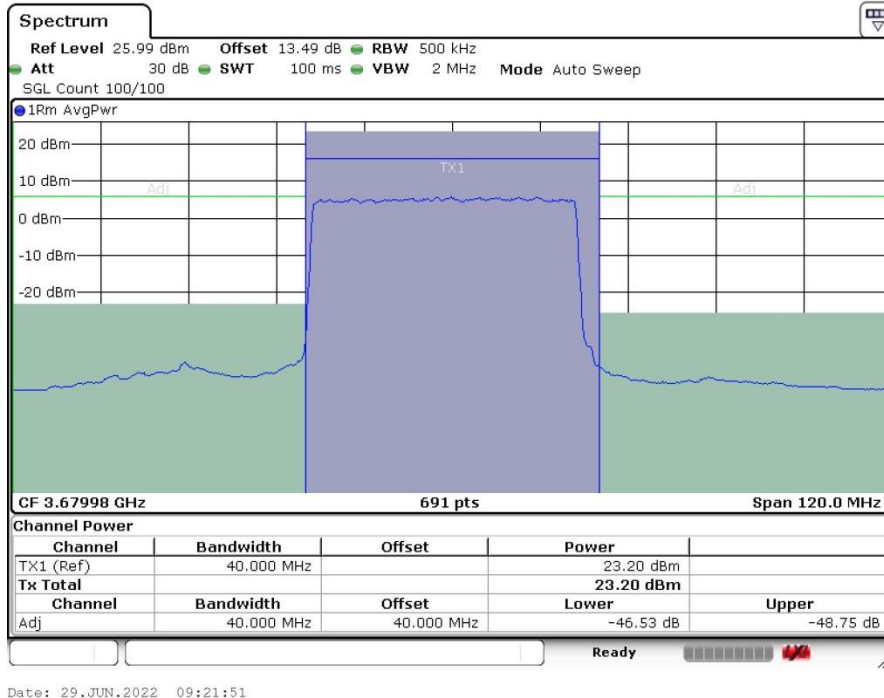
Highest Channel / 1RB0



Highest Channel / 1RBmax



Highest Channel / FullRB

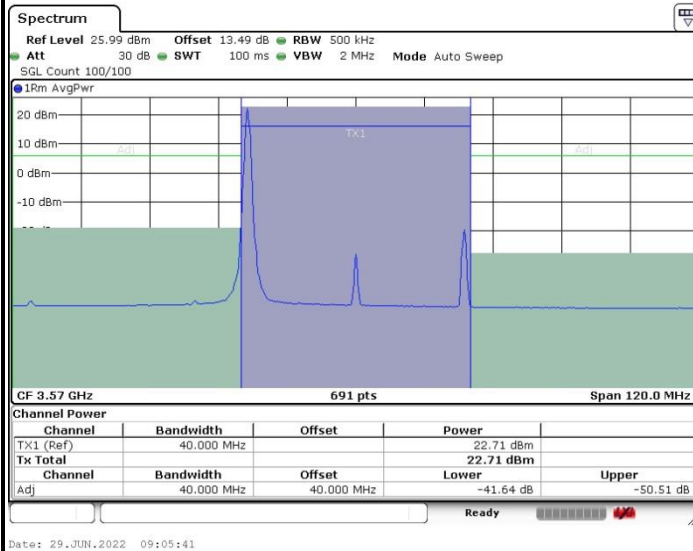




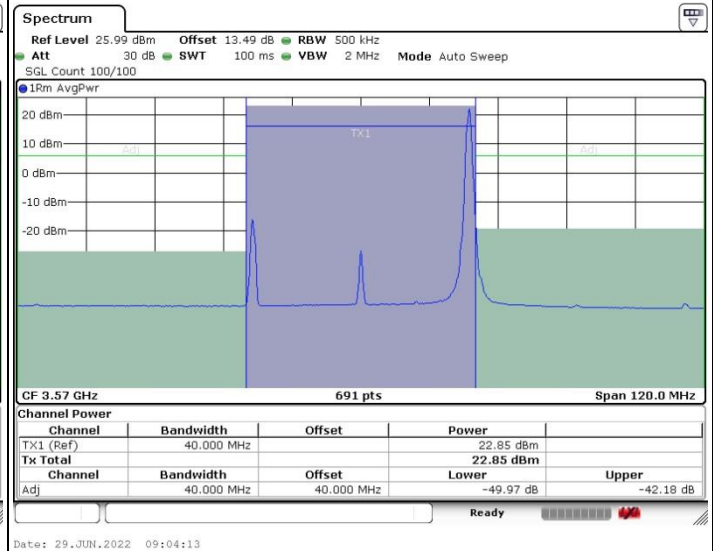
FR1 Part 96 n48 / 40MHz

QPSK

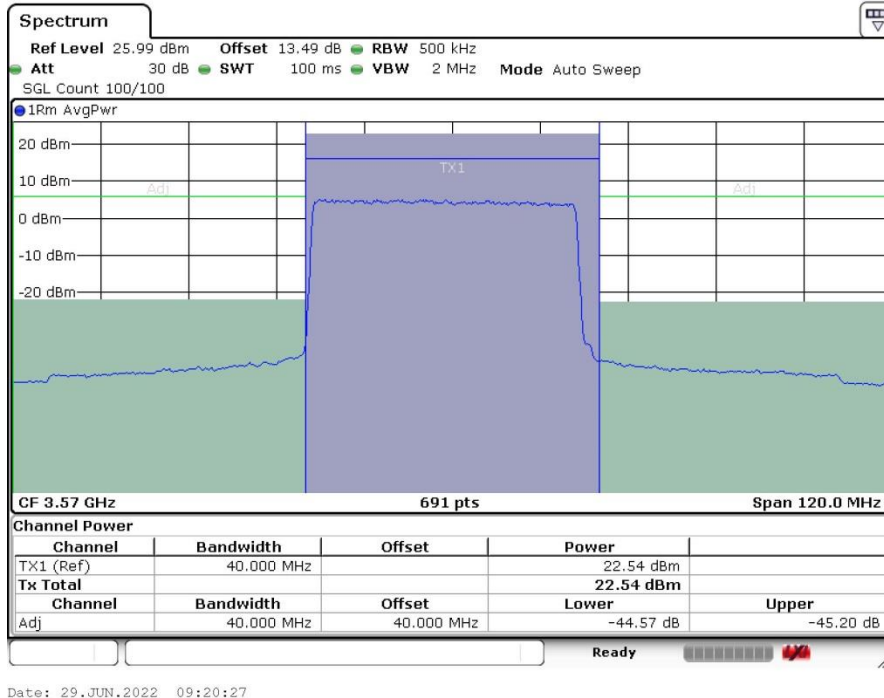
Lowest Channel / 1RB0



Lowest Channel / 1RBmax



Lowest Channel / FullIRB

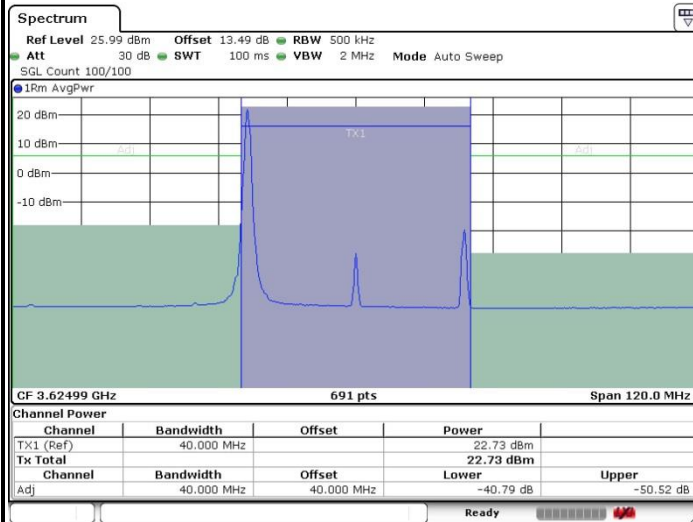




FR1 Part 96 n48 / 40MHz

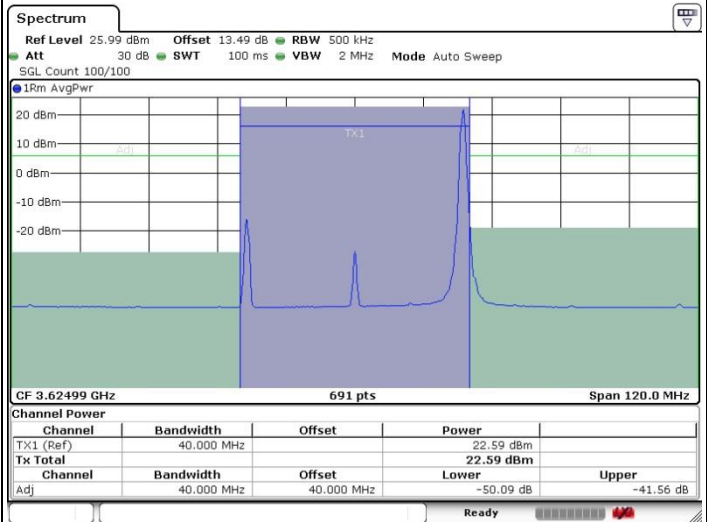
QPSK

Middle Channel / 1RB0



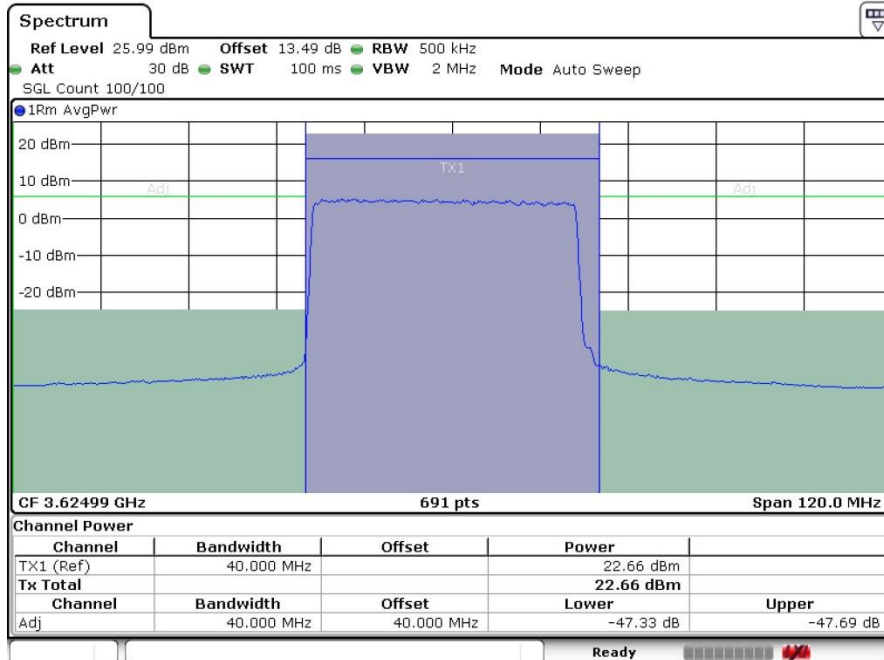
Date: 29.JUN.2022 08:58:29

Middle Channel / 1RBmax



Date: 29.JUN.2022 09:00:14

Middle Channel / FullRB



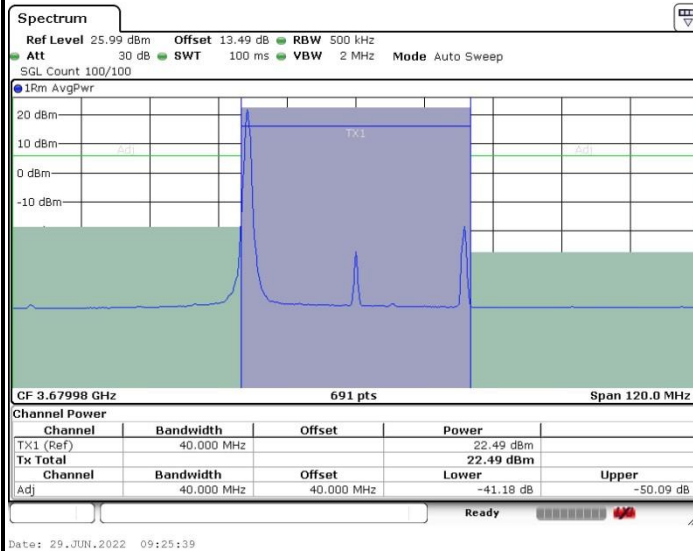
Date: 29.JUN.2022 08:54:17



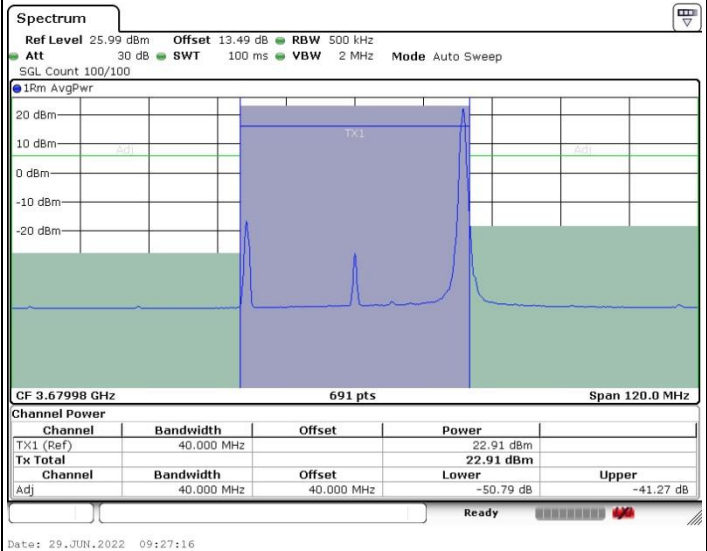
FR1 Part 96 n48 / 40MHz

QPSK

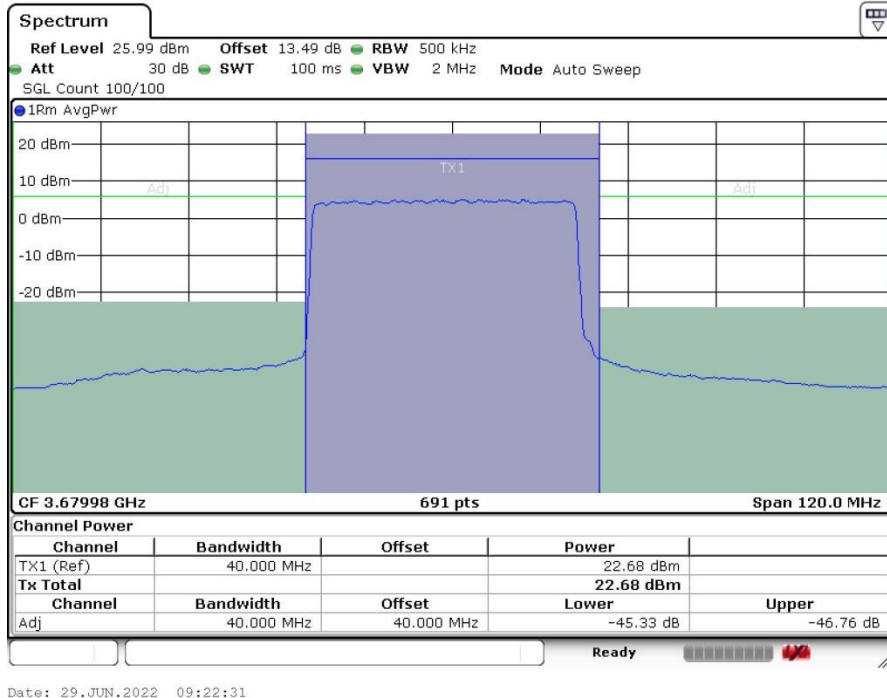
Highest Channel / 1RB0



Highest Channel / 1RBmax



Highest Channel / FullRB

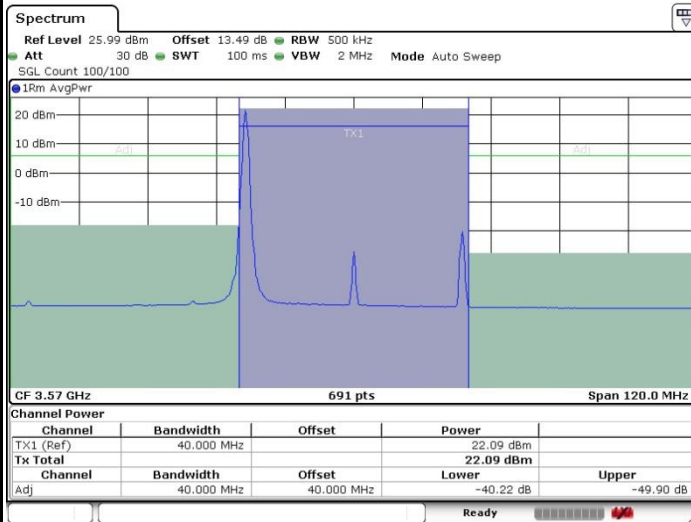




FR1 Part 96 n48 / 40MHz

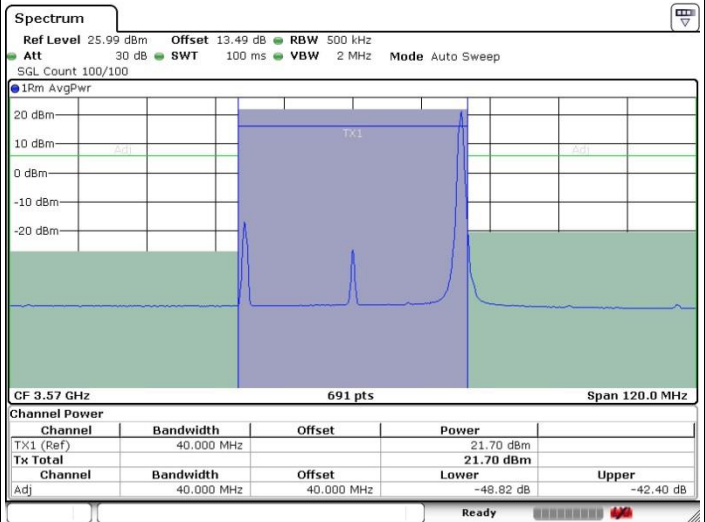
16QAM

Lowest Channel / 1RB0



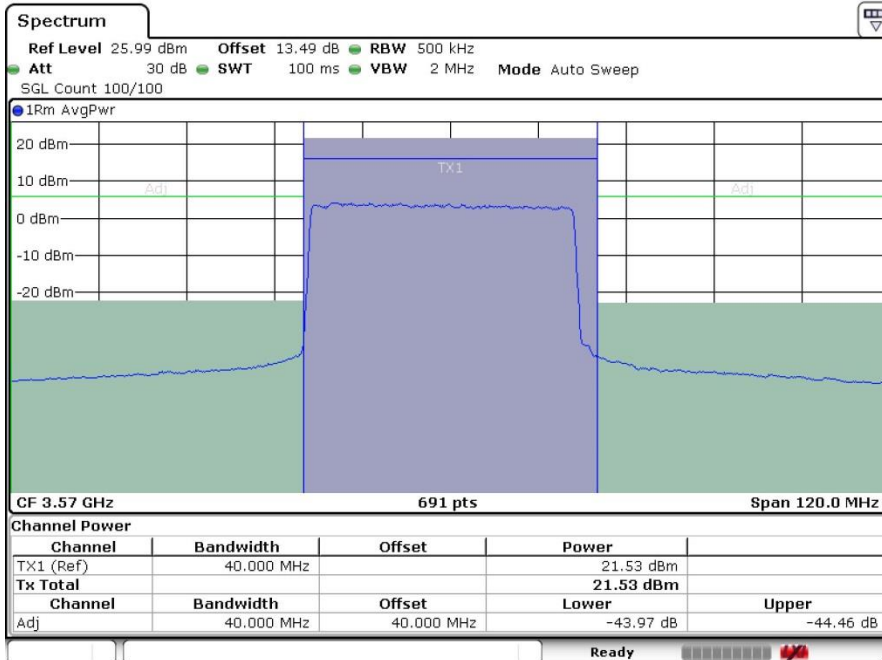
Date: 29.JUN.2022 09:06:07

Lowest Channel / 1RBmax



Date: 29.JUN.2022 09:03:48

Lowest Channel / FullIRB



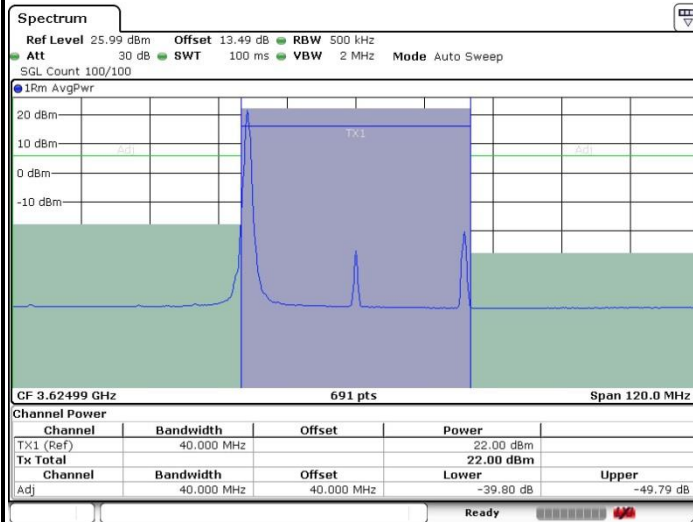
Date: 29.JUN.2022 09:10:44



FR1 Part 96 n48 / 40MHz

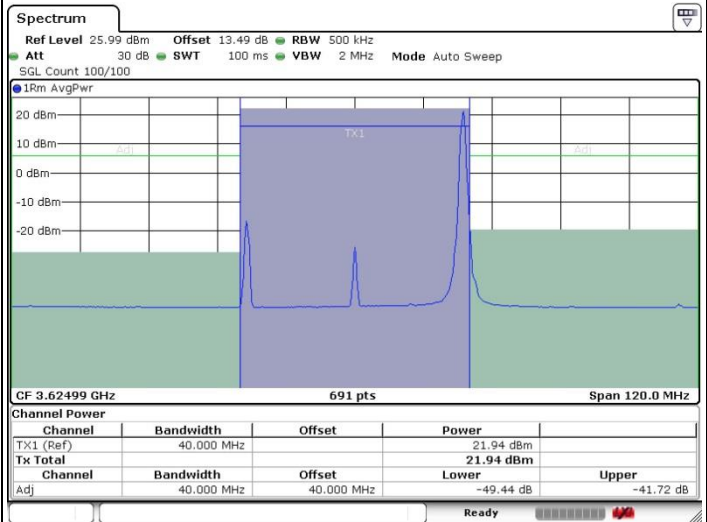
16QAM

Middle Channel / 1RB0



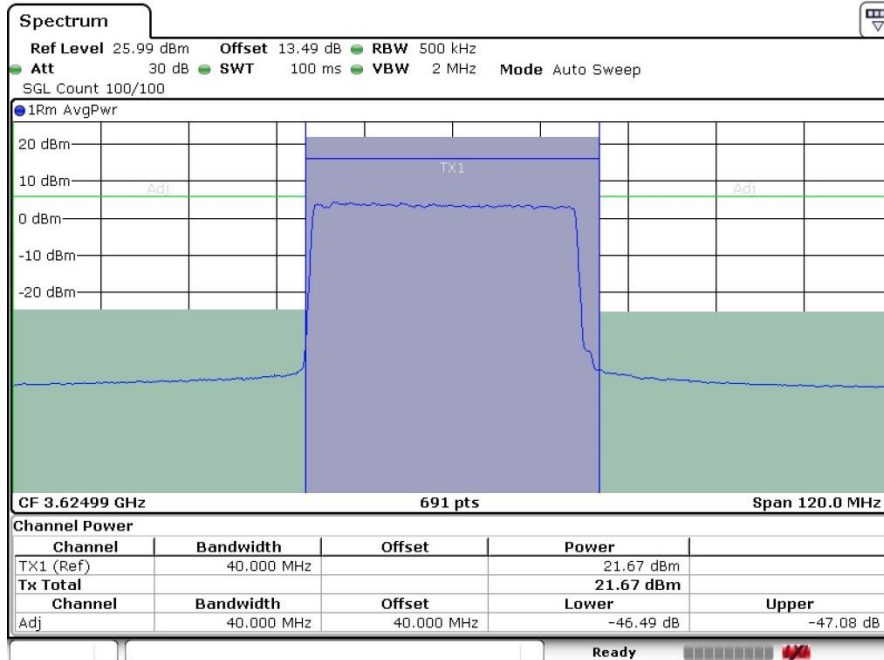
Date: 29.JUN.2022 08:56:57

Middle Channel / 1RBmax



Date: 29.JUN.2022 09:00:40

Middle Channel / FullRB



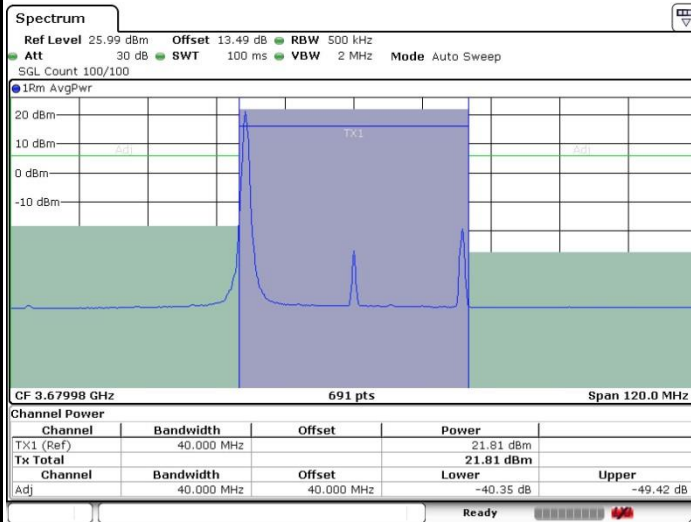
Date: 29.JUN.2022 08:54:43



FR1 Part 96 n48 / 40MHz

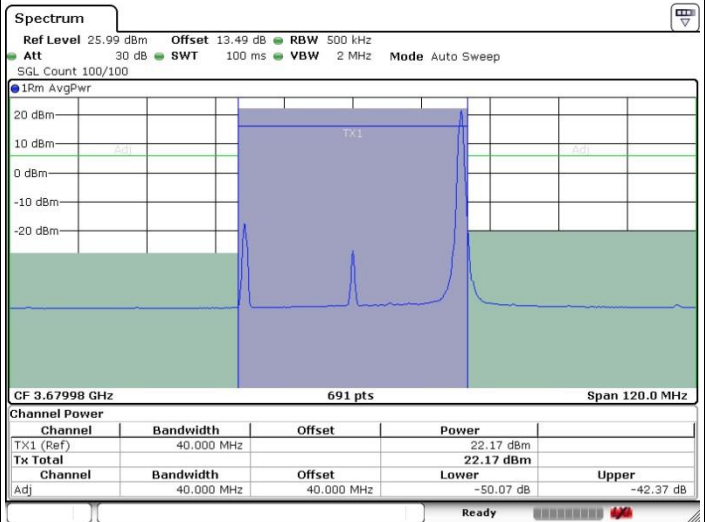
16QAM

Highest Channel / 1RB0



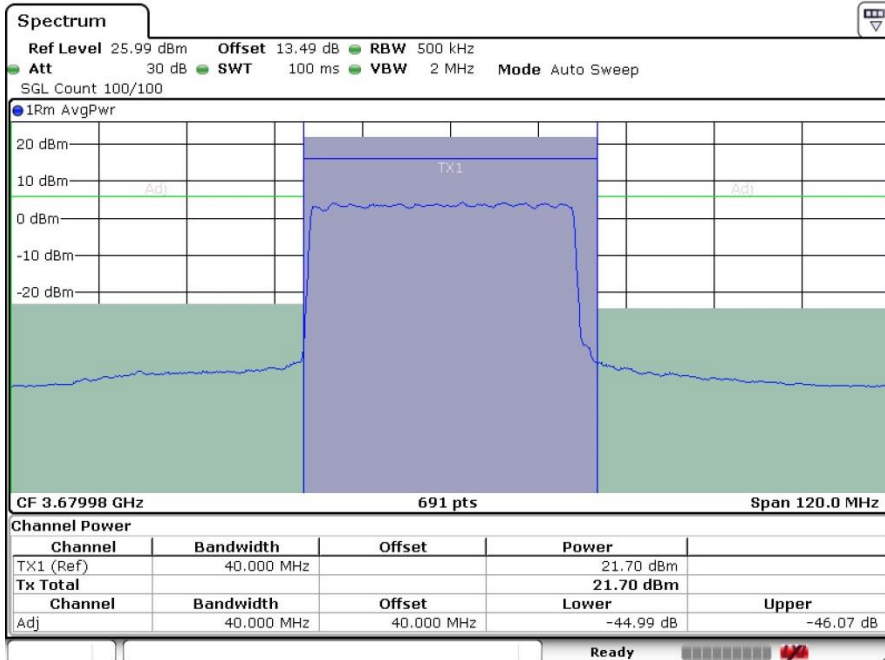
Date: 29.JUN.2022 09:25:10

Highest Channel / 1RBmax



Date: 29.JUN.2022 09:27:42

Highest Channel / FullRB



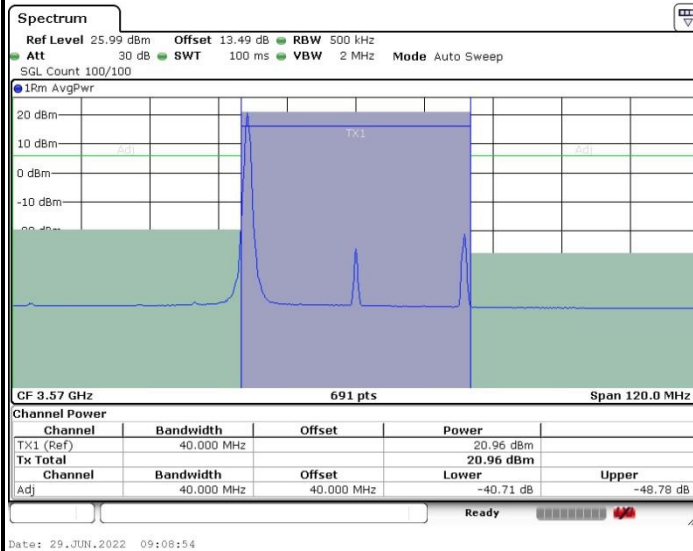
Date: 29.JUN.2022 09:22:57



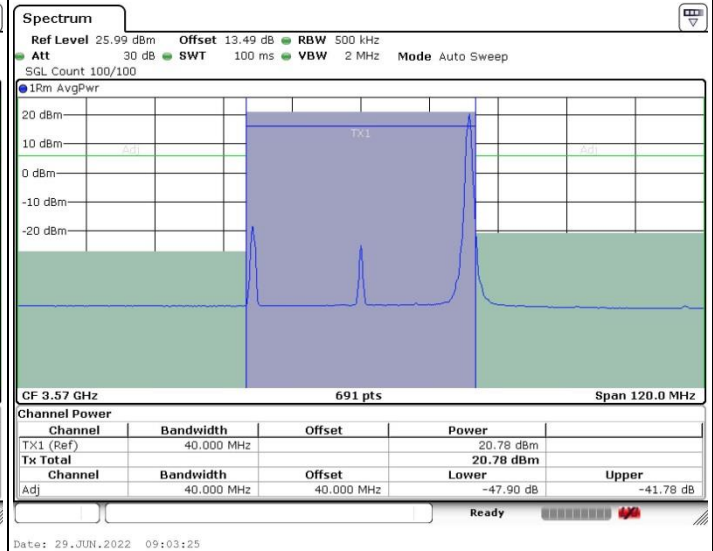
FR1 Part 96 n48 / 40MHz

64QAM

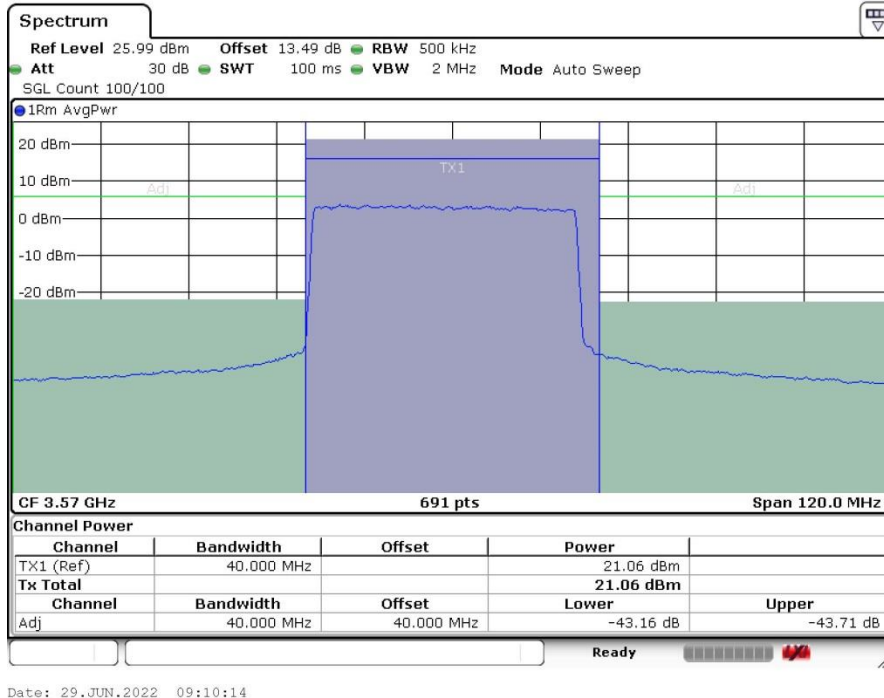
Lowest Channel / 1RB0



Lowest Channel / 1RBmax



Lowest Channel / FullIRB

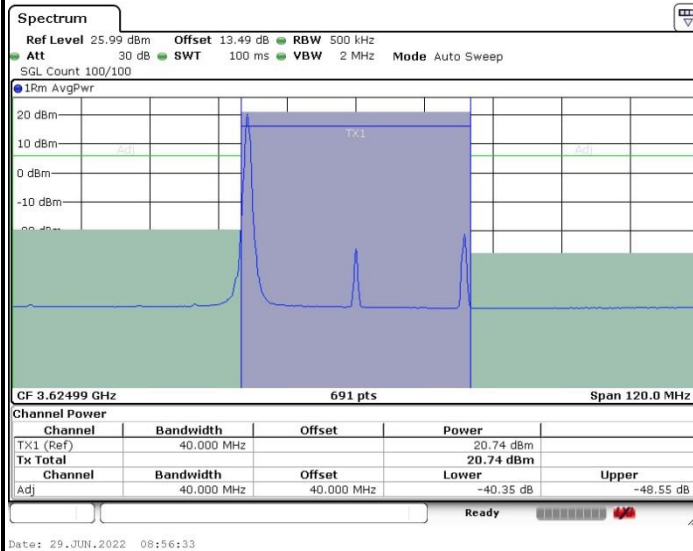




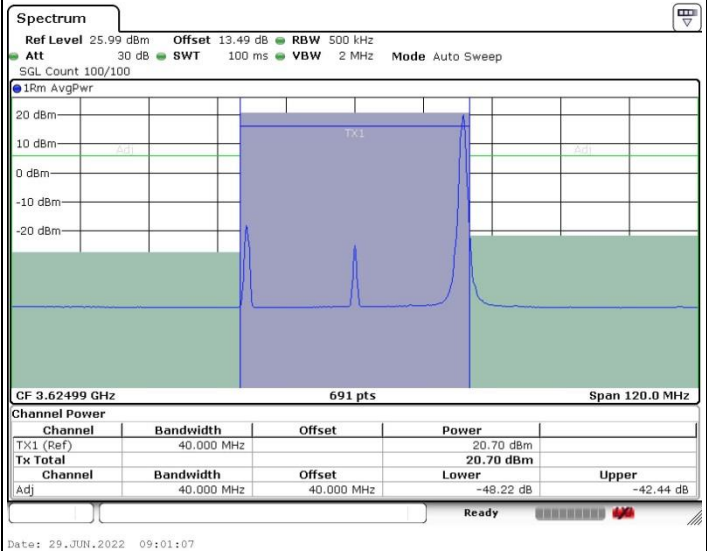
FR1 Part 96 n48 / 40MHz

64QAM

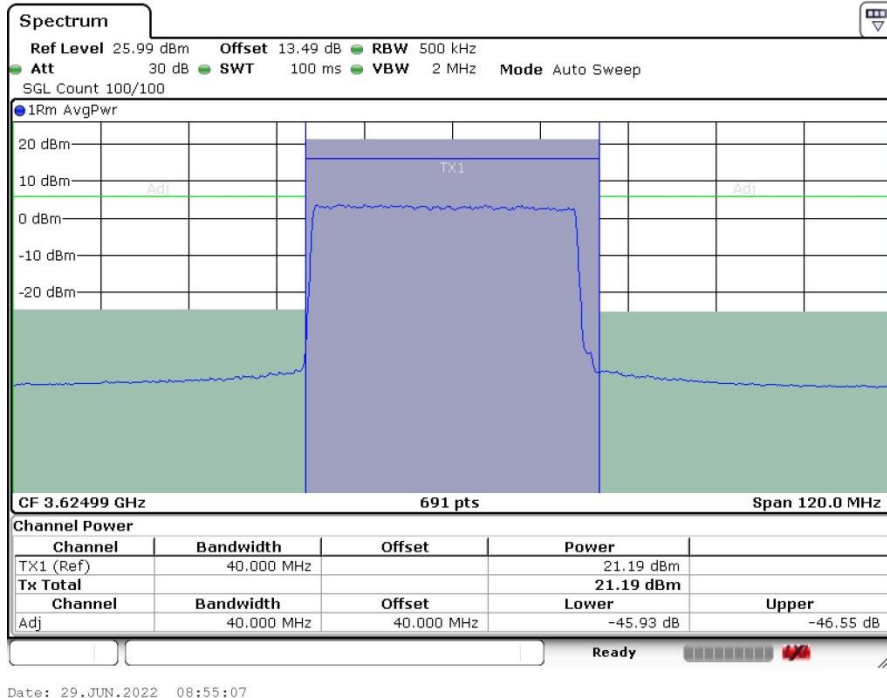
Middle Channel / 1RB0



Middle Channel / 1RBmax



Middle Channel / FullRB

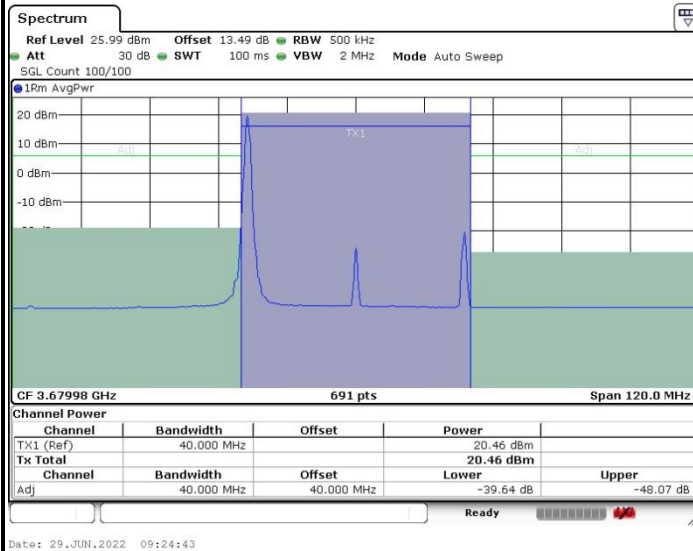




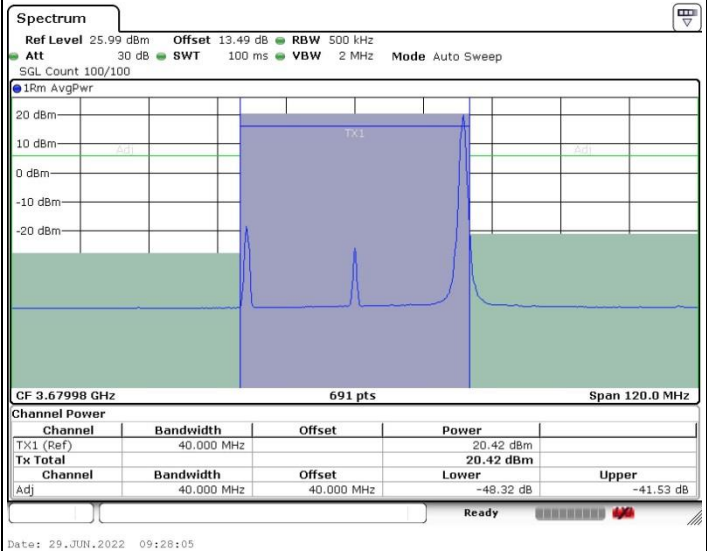
FR1 Part 96 n48 / 40MHz

64QAM

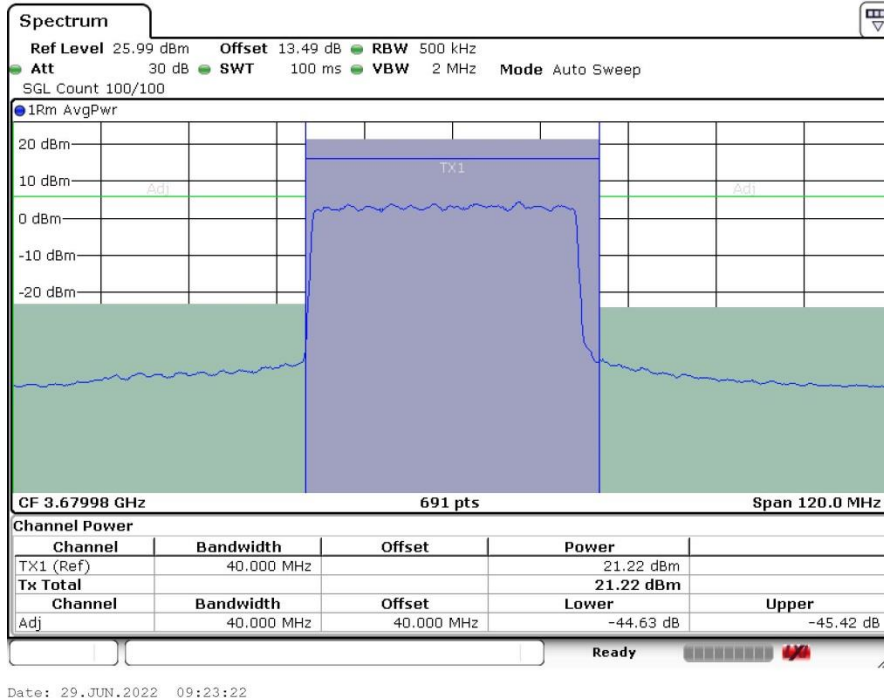
Highest Channel / 1RB0



Highest Channel / 1RBmax



Highest Channel / FullRB





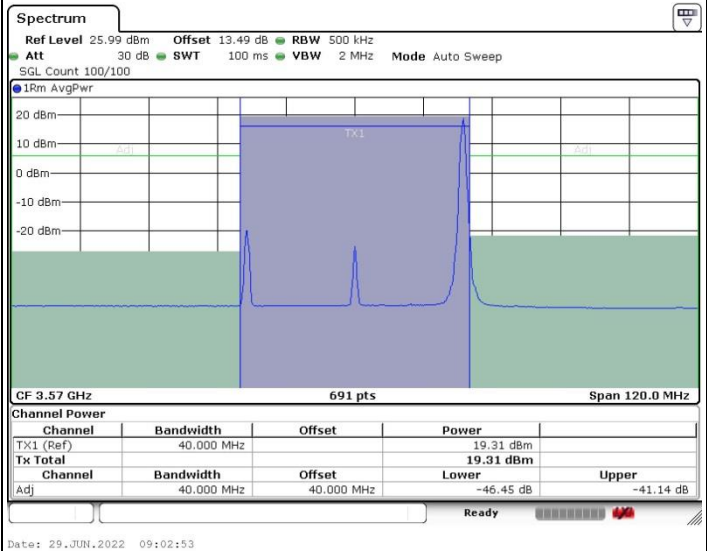
FR1 Part 96 n48 / 40MHz

256QAM

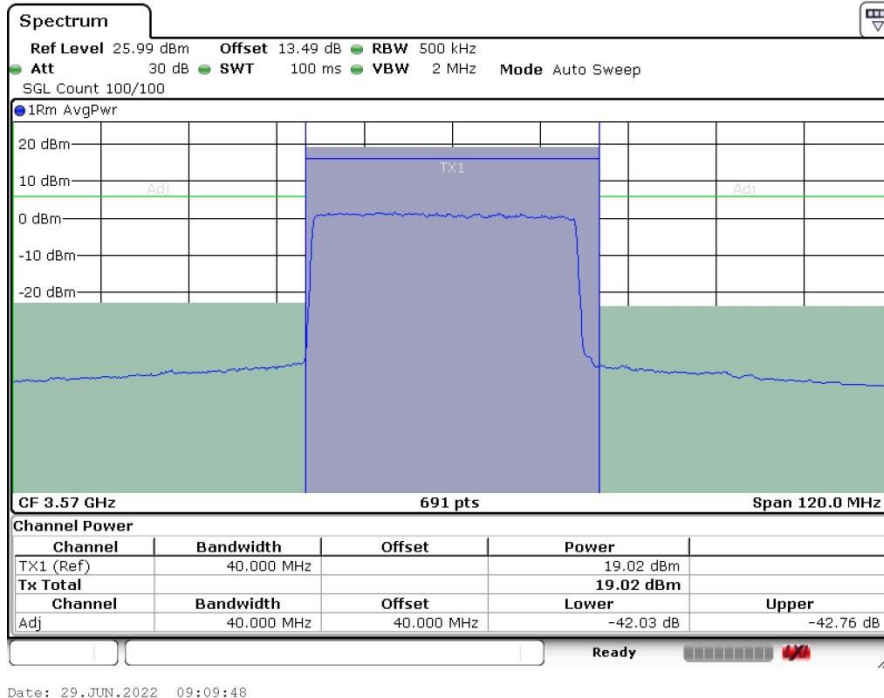
Lowest Channel / 1RB0



Lowest Channel / 1RBmax



Lowest Channel / FullIRB





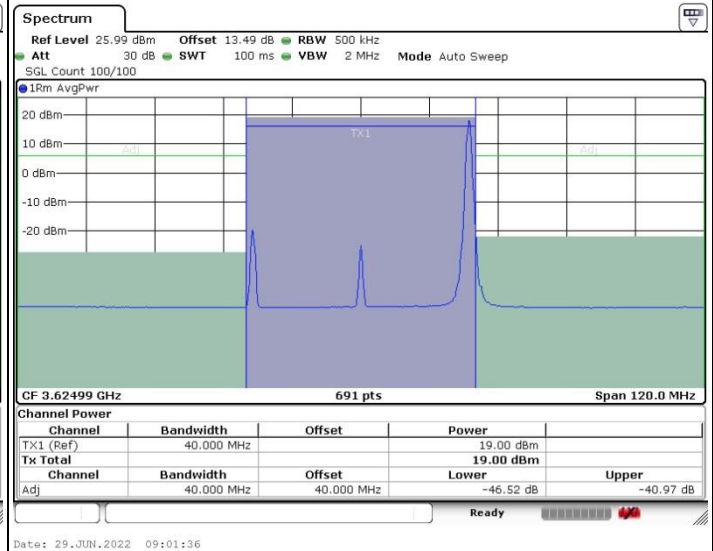
FR1 Part 96 n48 / 40MHz

256QAM

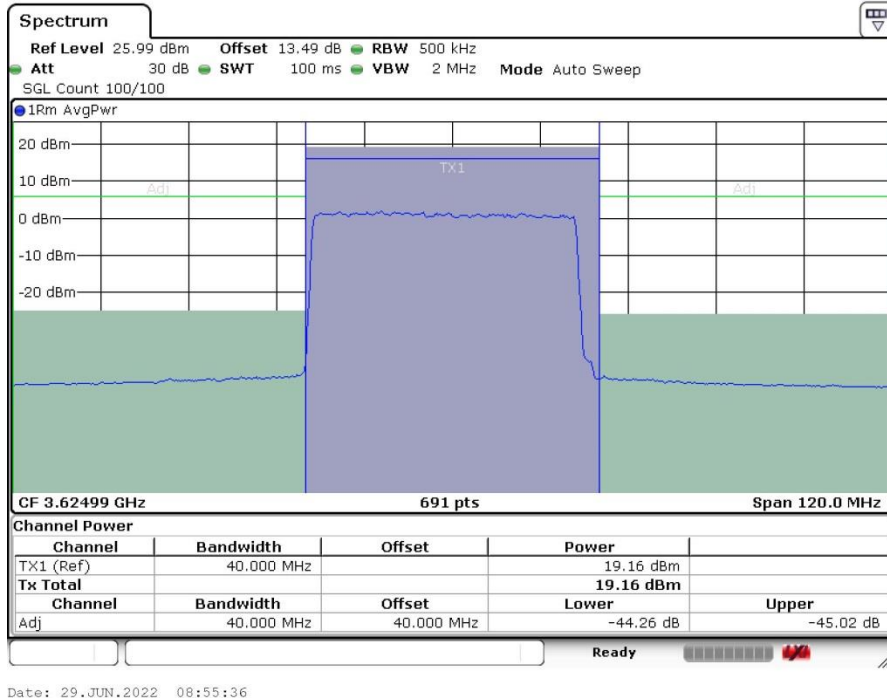
Middle Channel / 1RB0



Middle Channel / 1RBmax



Middle Channel / FullRB





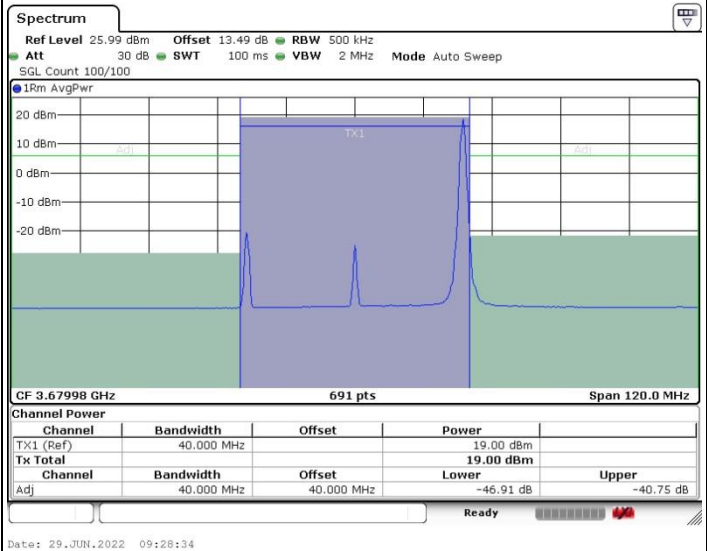
FR1 Part 96 n48 / 40MHz

256QAM

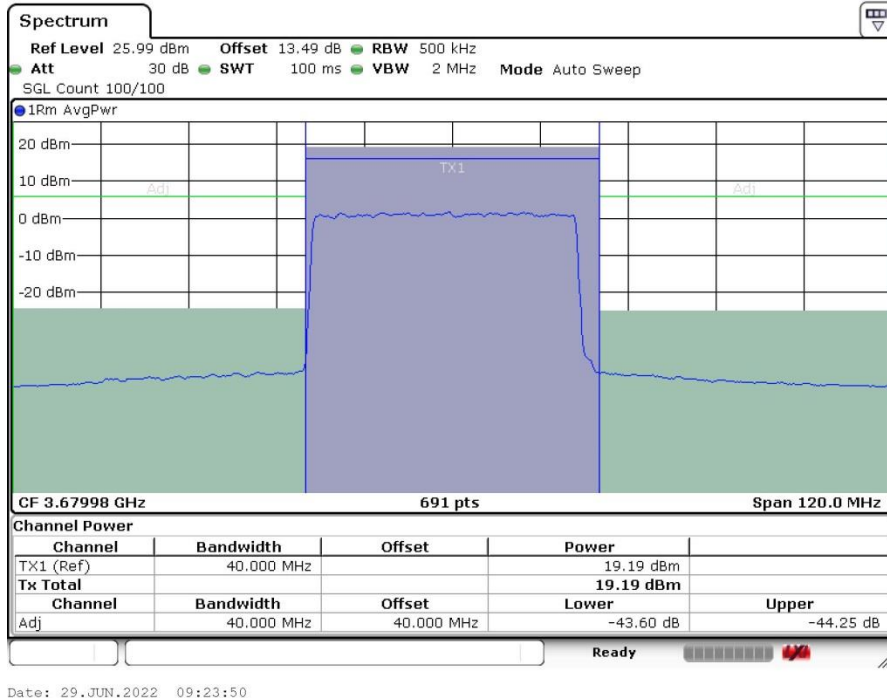
Highest Channel / 1RB0



Highest Channel / 1RBmax



Highest Channel / FullRB





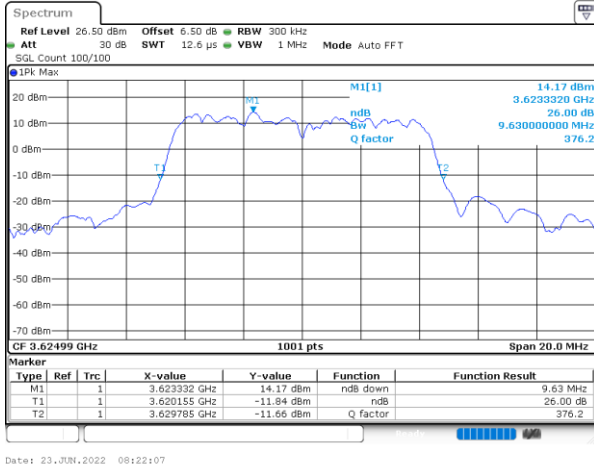
26dB Bandwidth

Mode	FR1 Part 96 N48 : 26dB BW(MHz) / CP			
BW	10M			
Mod.	QPSK	16QAM	64QAM	256QAM
Middle CH	9.63	9.29	9.33	9.37
BW	20M			
Mod.	QPSK	16QAM	64QAM	256QAM
Middle CH	20.54	20.34	20.42	20.42
BW	40M			
Mod.	QPSK	16QAM	64QAM	256QAM
Middle CH	40.28	40.28	40.44	40.44

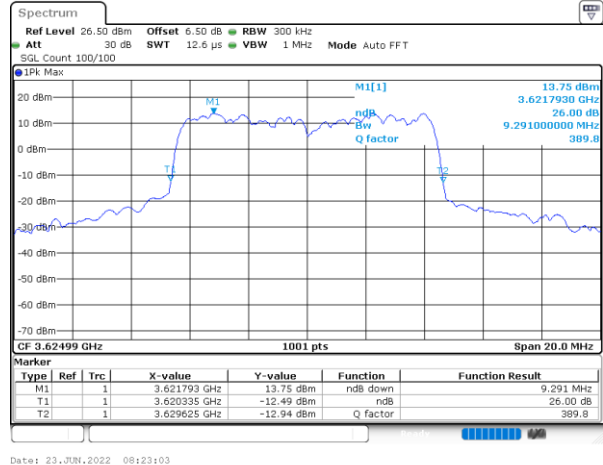


10M

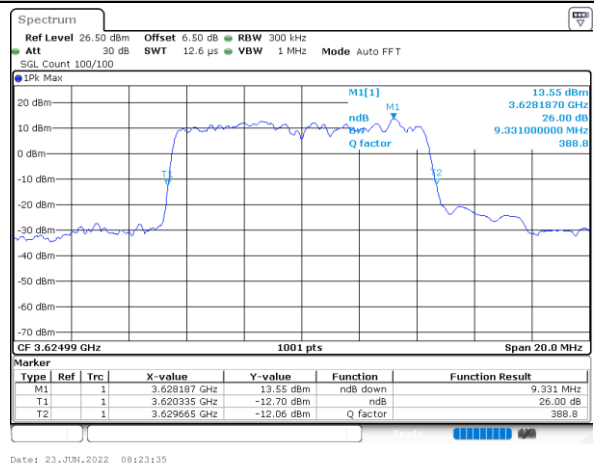
QPSK



16QAM



64QAM



256QAM

