

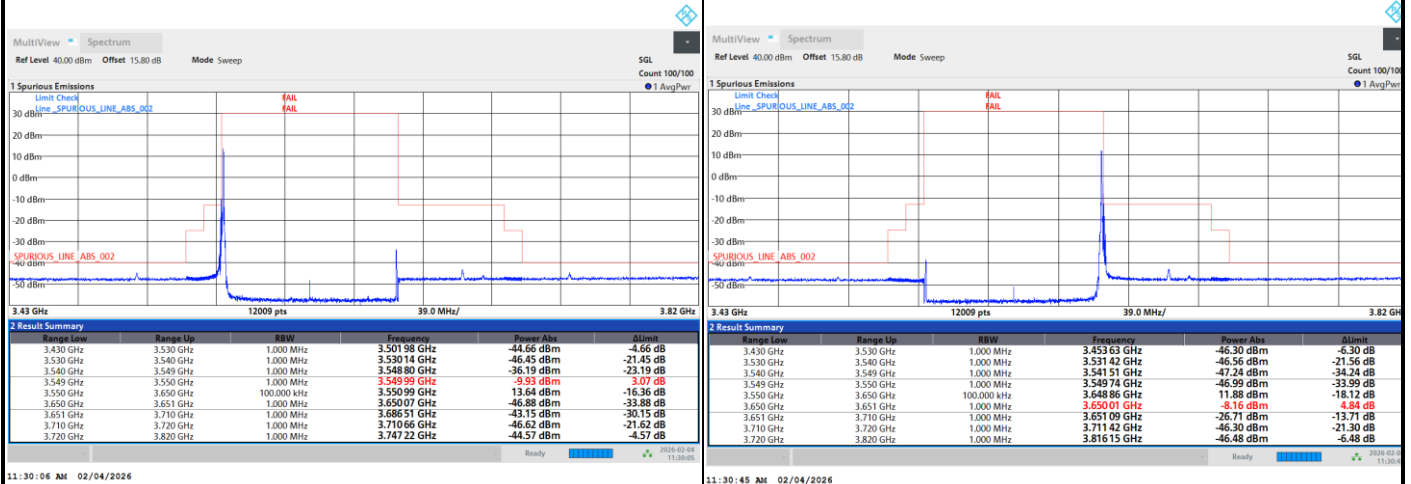


## FR1 n77 / 100MHz / DFT-S OFDM / PI/2 BPSK

## Lowest Channel

## 1RB0

## 1RBmax

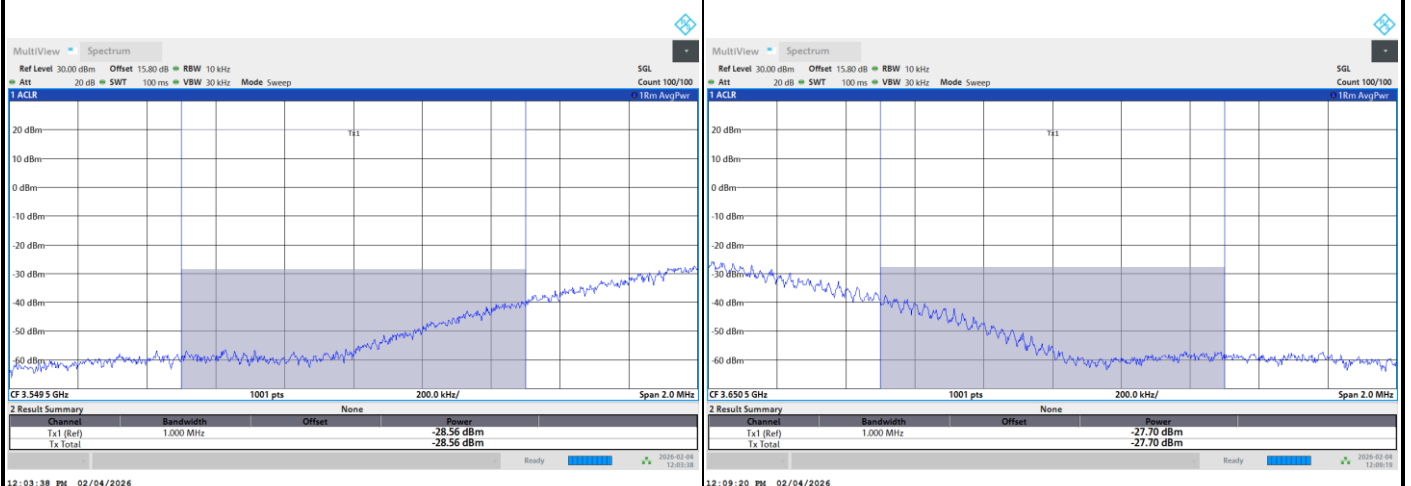


## Lowest Channel 1RB0

Channel power -13dBm &gt; -28.56dBm (PASS)

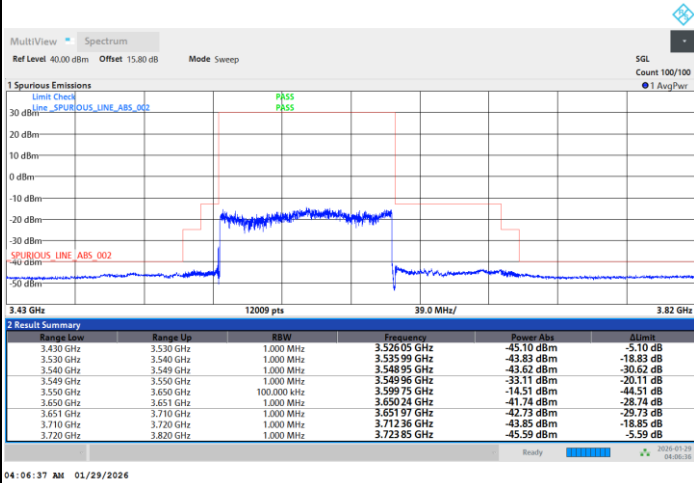
## Lowest Channel 1RBmax

Channel power -13dBm &gt; -27.70dBm (PASS)





Full RB

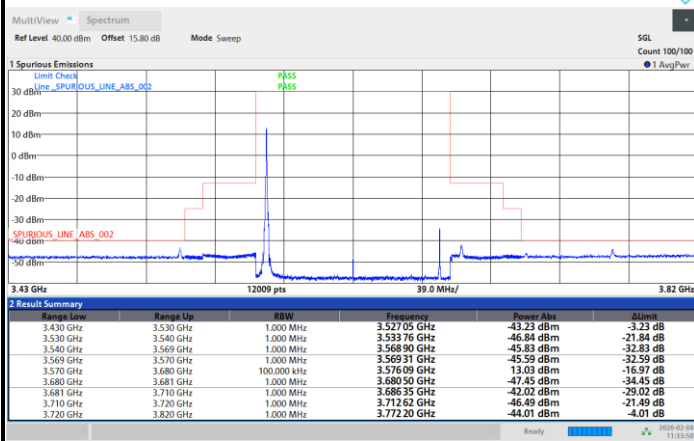




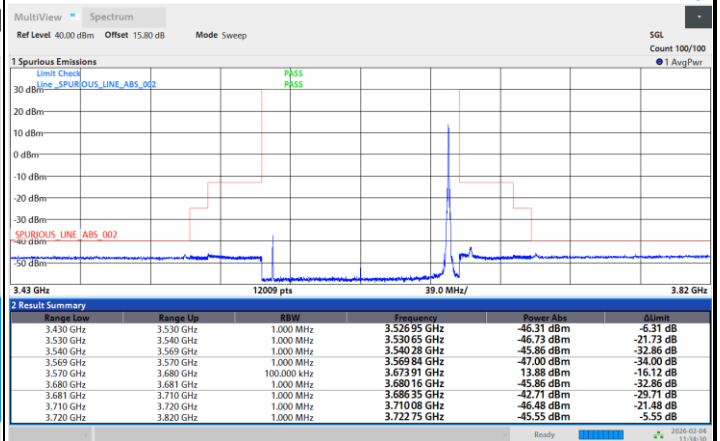
## FR1 n77 / 100MHz / DFT-S OFDM / PI/2 BPSK

## Middle Channel

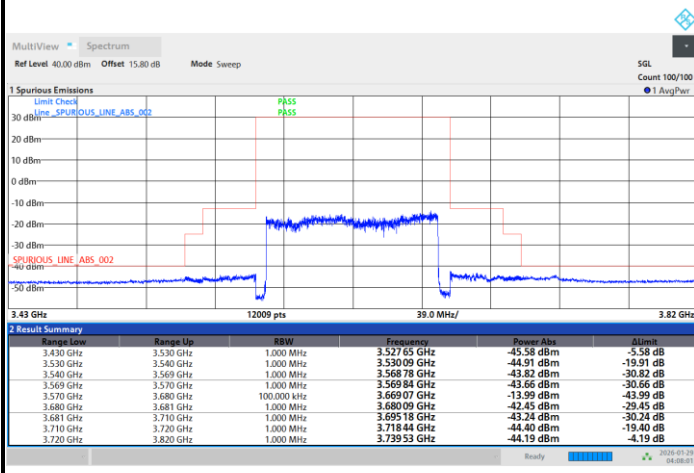
## 1RB0



## 1RBmax



## Full RB



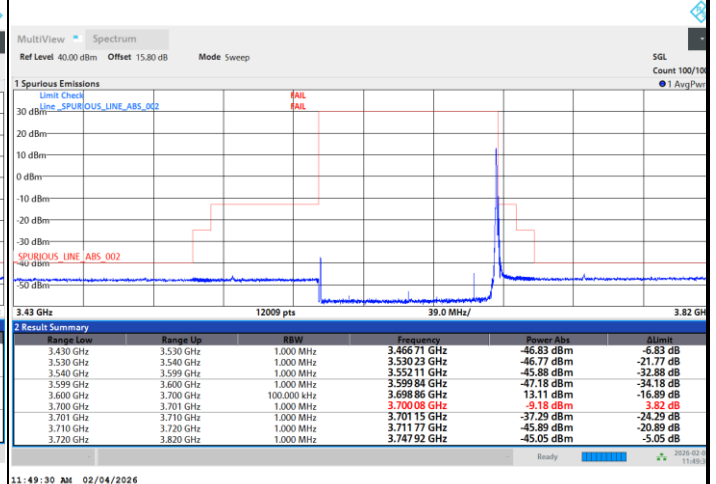
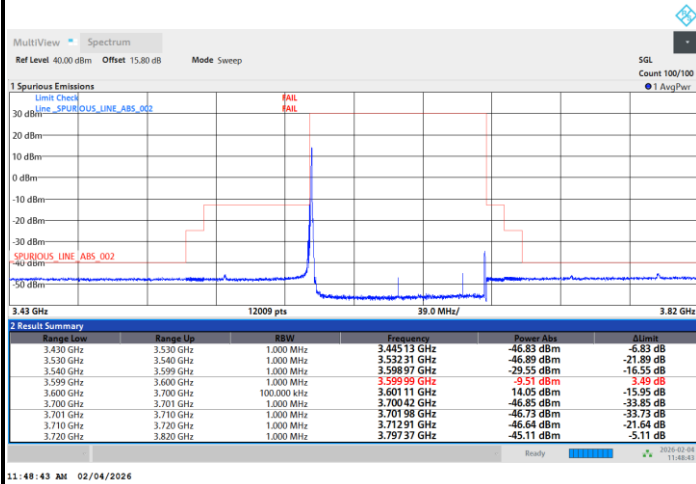


## FR1 n77 / 100MHz / DFT-S OFDM / PI/2 BPSK

## Highest Channel

## 1RB0

## 1RBmax

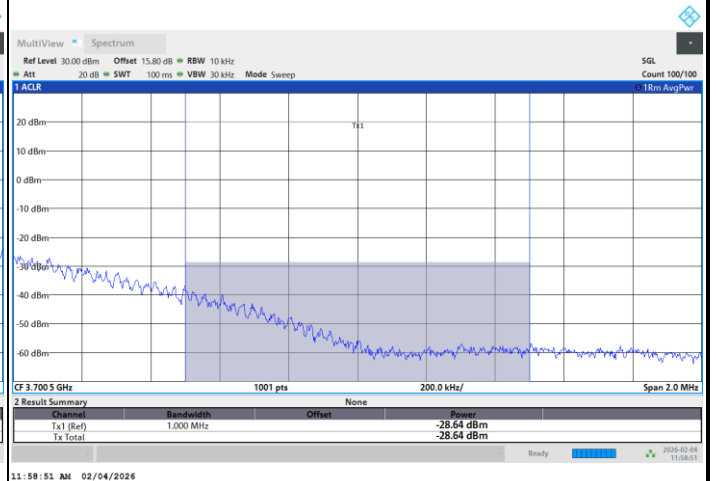
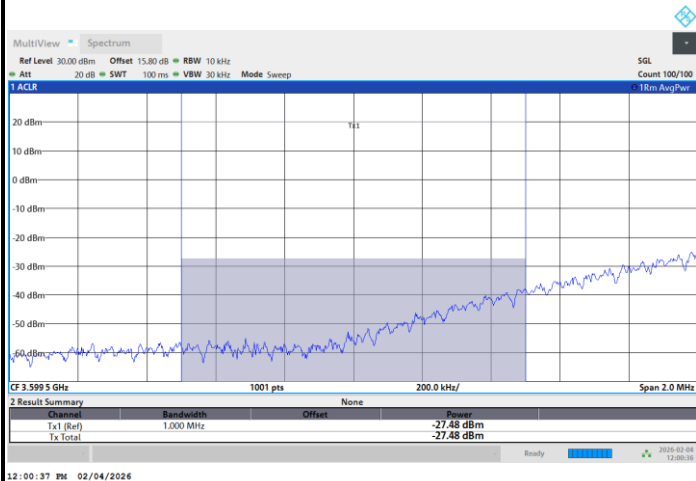


## Highest Channel 1RB0

Channel power -13dBm &gt; -27.48dBm (PASS)

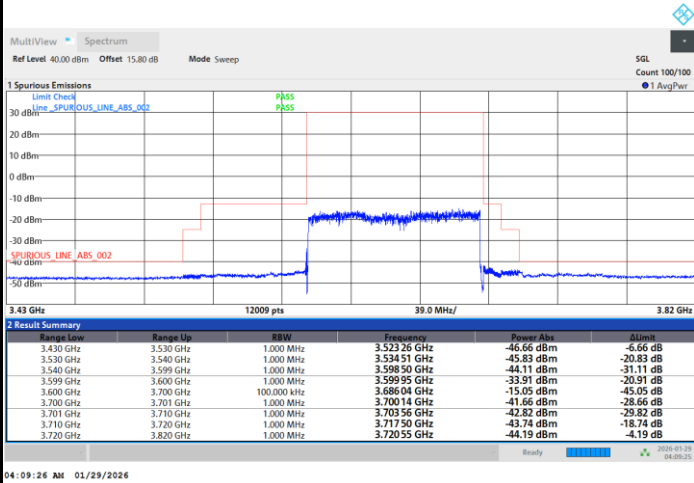
## Highest Channel 1RBmax

Channel power -13dBm &gt; -28.64dBm (PASS)





Full RB

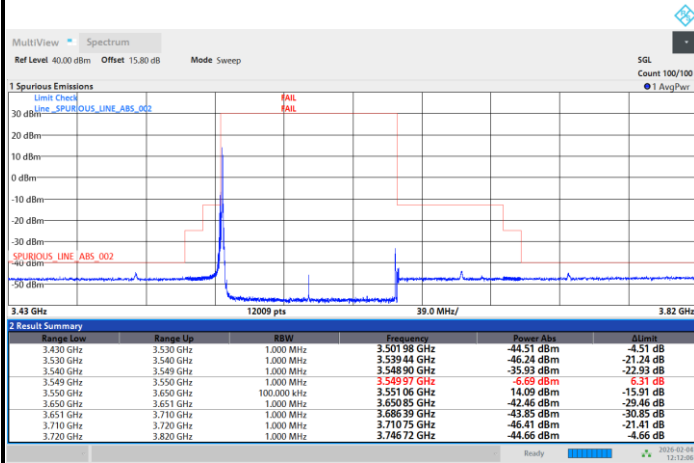




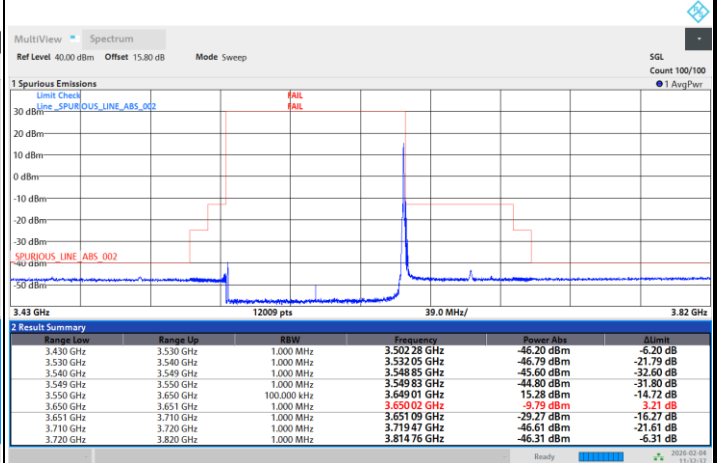
## FR1 n77 / 100MHz / DFT-S OFDM / QPSK

## Lowest Channel

## 1RB0

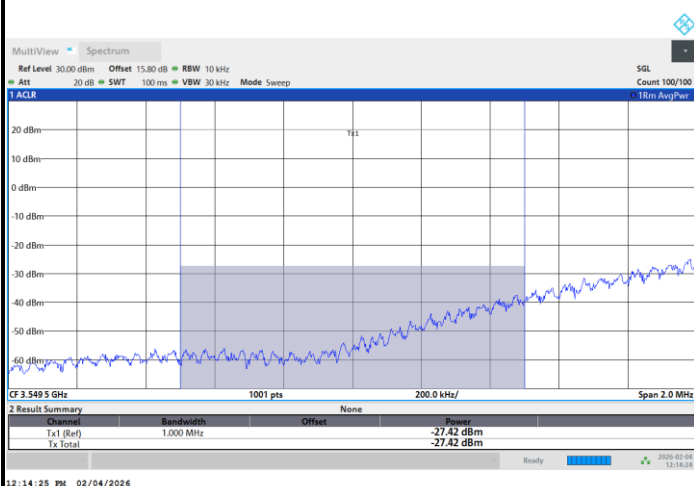


## 1RBmax



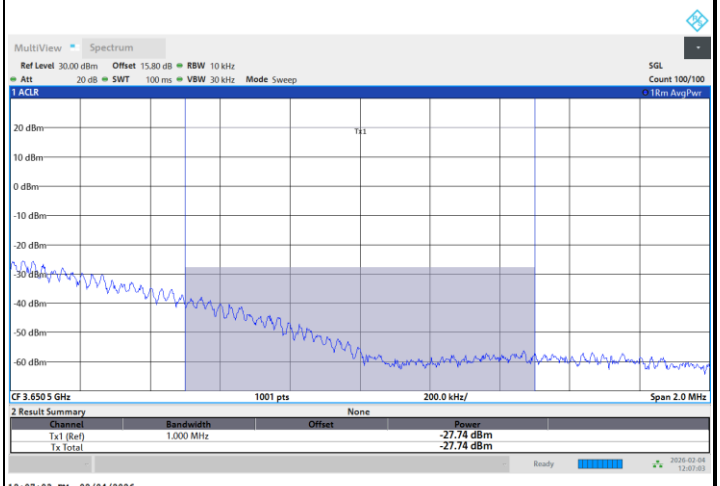
## Lowest Channel 1RB0

Channel power -13dBm &gt; -27.42dBm (PASS)



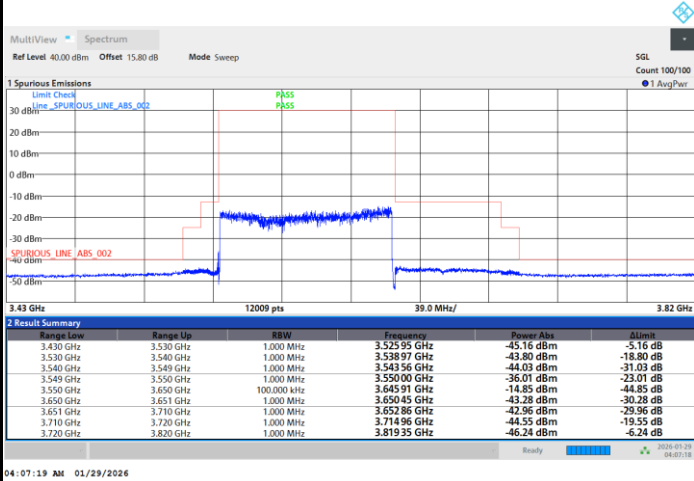
## Lowest Channel 1RBmax

Channel power -13dBm &gt; -27.74dBm (PASS)





Full RB

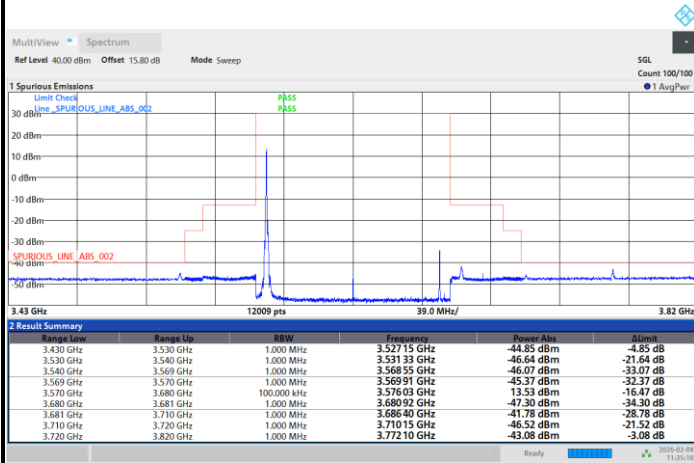




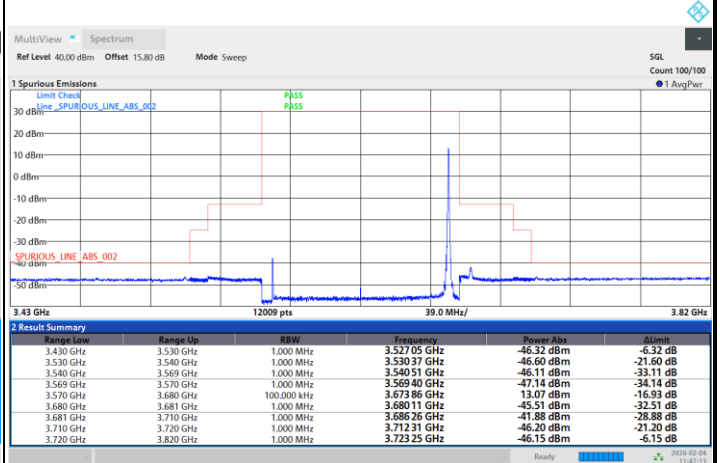
## FR1 n77 / 100MHz / DFT-S OFDM / QPSK

## Middle Channel

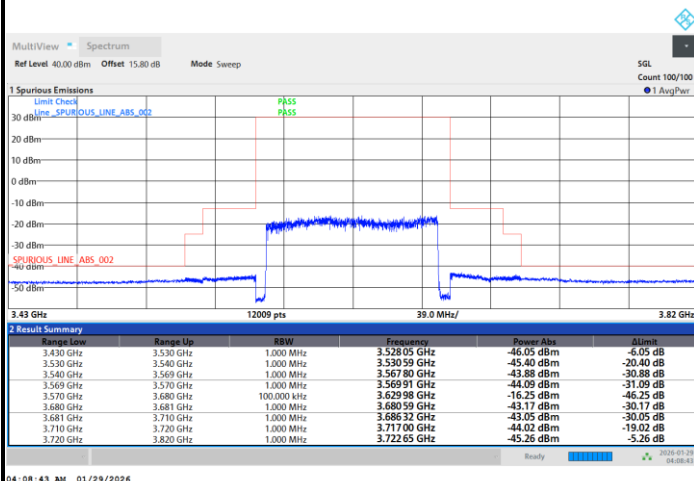
## 1RB0



## 1RBmax



## Full RB

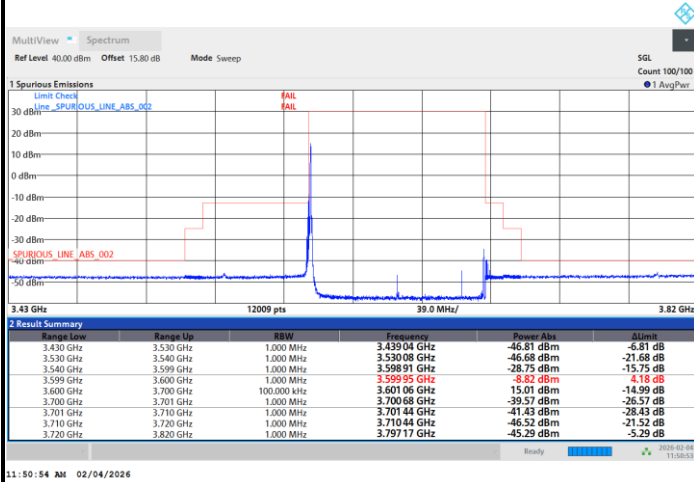




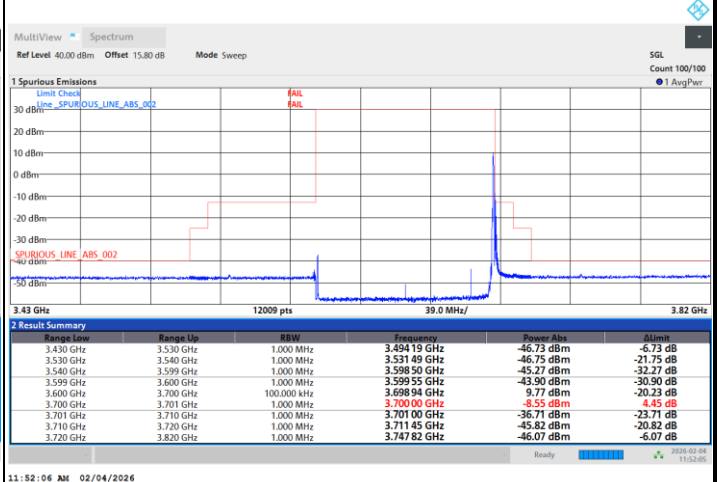
## FR1 n77 / 100MHz / DFT-S OFDM / QPSK

## Highest Channel

## 1RB0

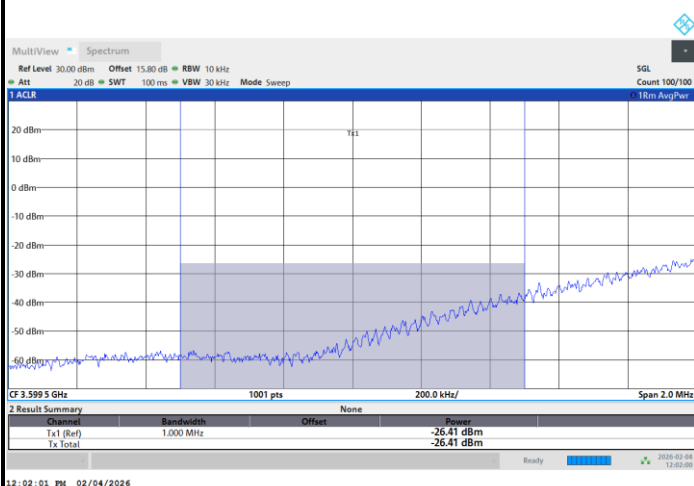


## 1RBmax



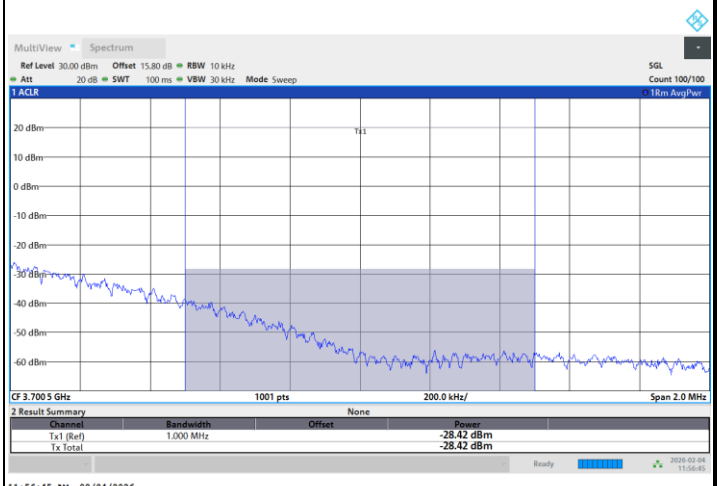
## Highest Channel 1RB0

Channel power -13dBm &gt; -26.41dBm (PASS)



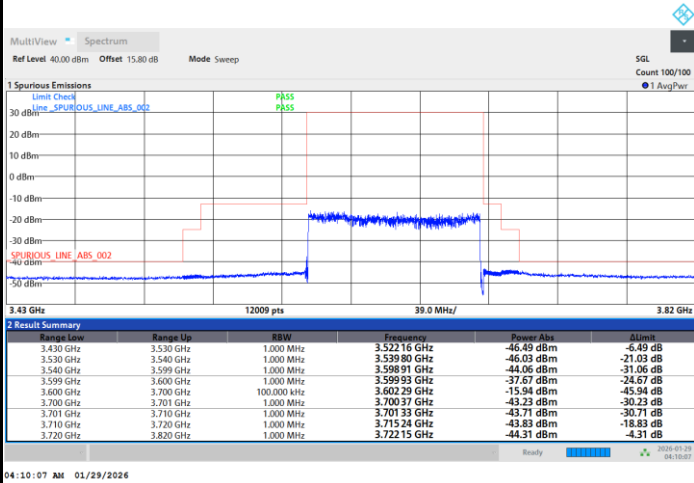
## Highest Channel 1RBmax

Channel power -13dBm &gt; -28.42dBm (PASS)



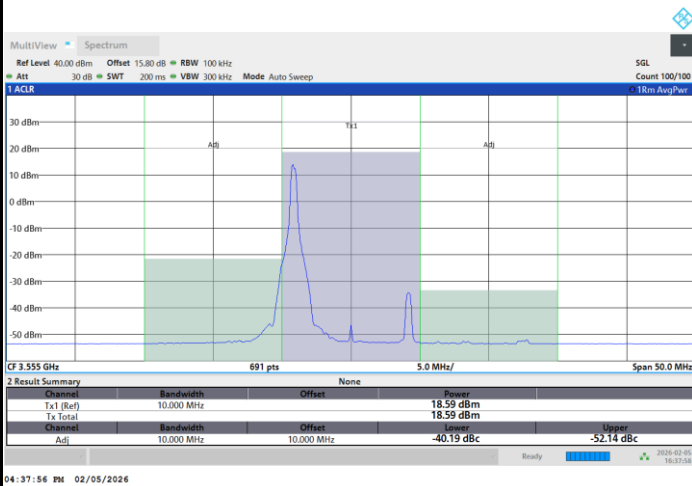
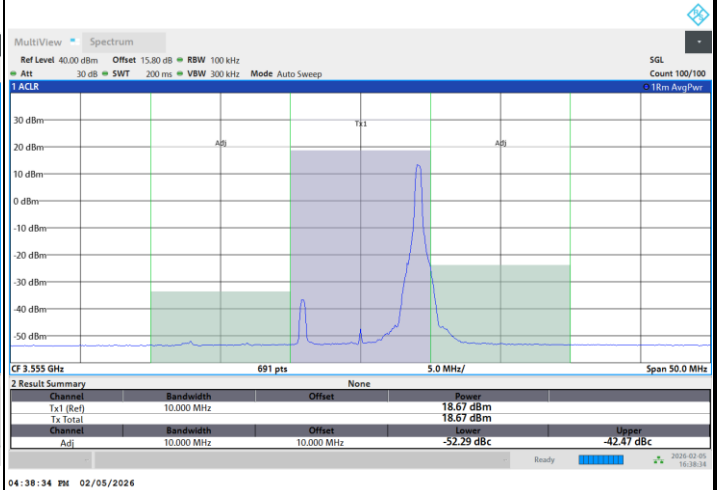
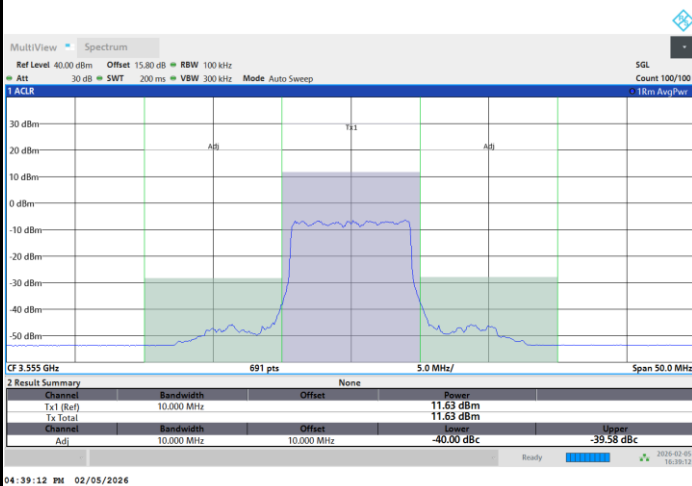


Full RB



**Adjacent Channel Leakage Ratio (ACLR)**

FR1 n77 Part96 / 10MHz / DFT-S OFDM / PI/2 BPSK

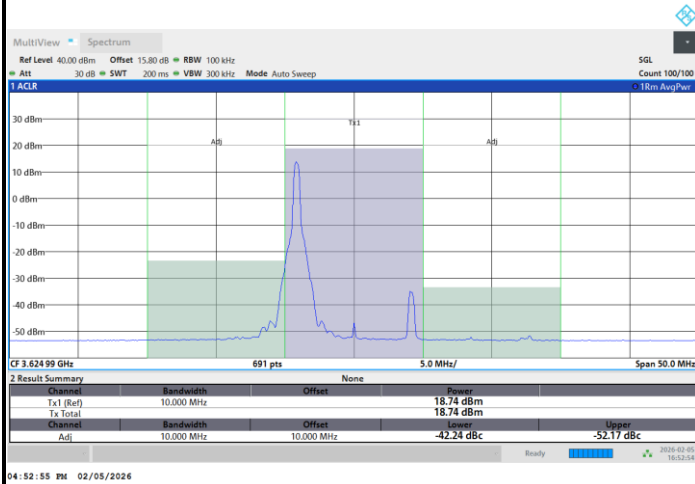
**Lowest Channel****1RB0****1RBmax****Full RB**



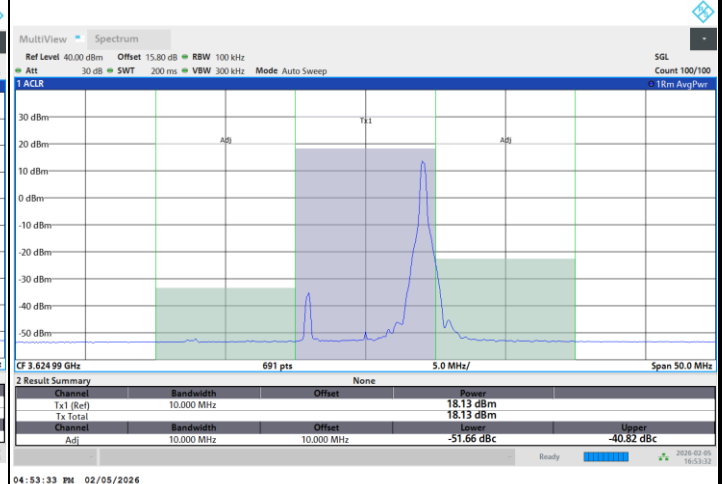
## FR1 n77 Part96 / 10MHz / DFT-S OFDM / PI/2 BPSK

## Middle Channel

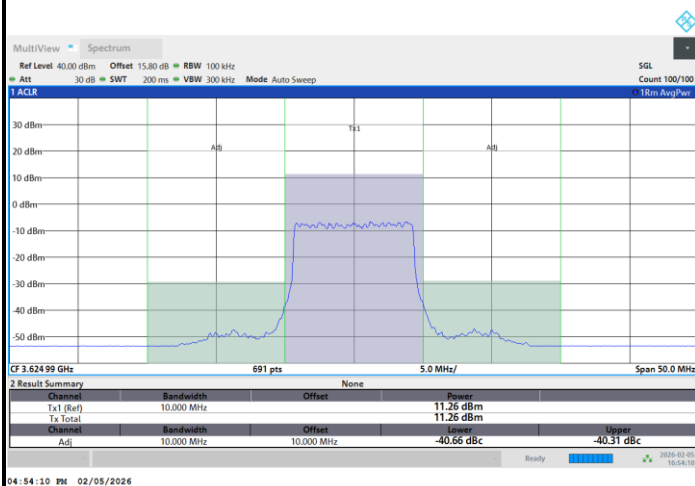
## 1RB0



## 1RBmax



## Full RB

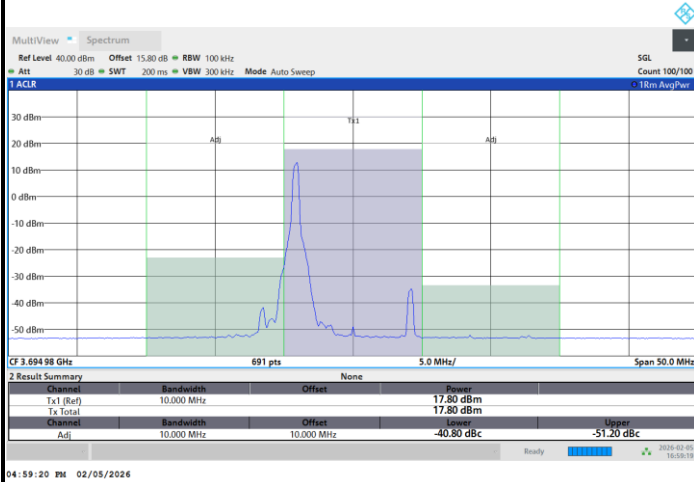




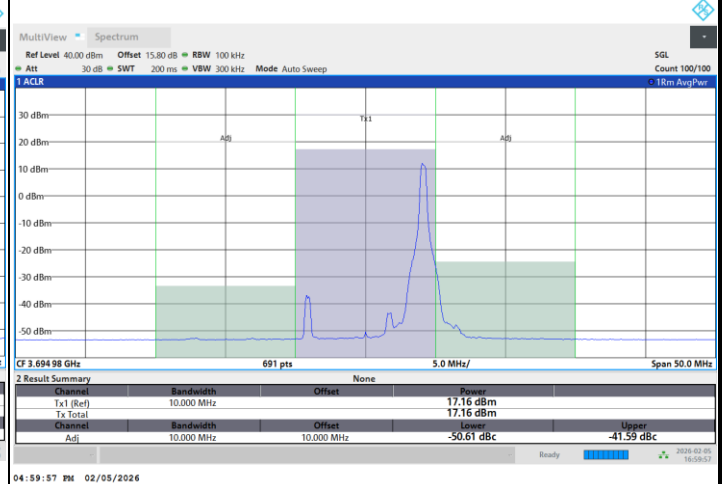
## FR1 n77 Part96 / 10MHz / DFT-S OFDM / PI/2 BPSK

## Highest Channel

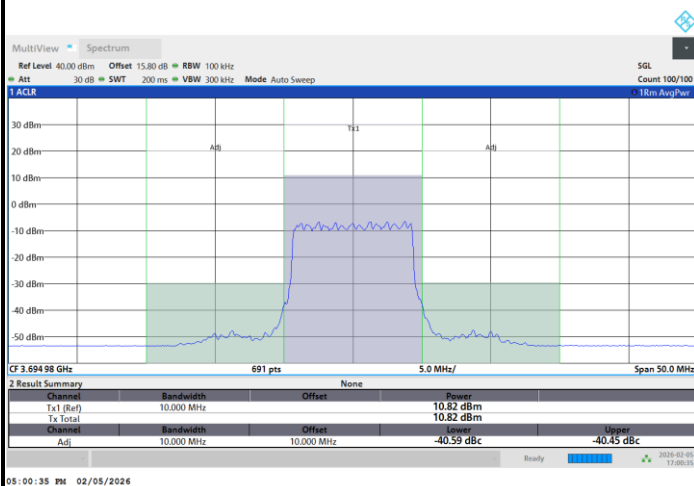
## 1RB0



## 1RBmax



## Full RB

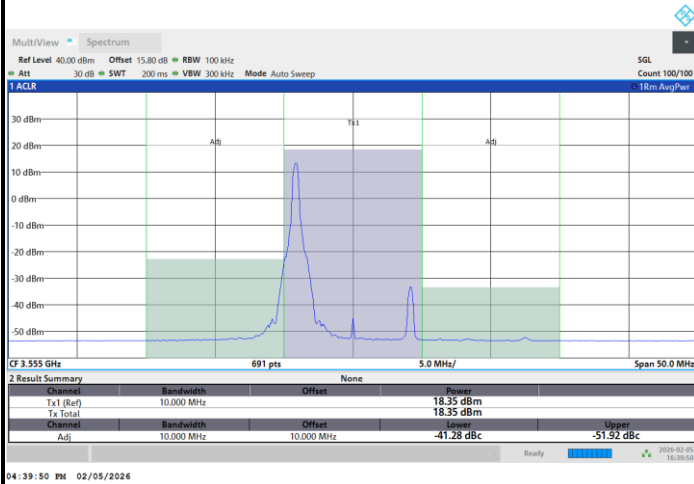




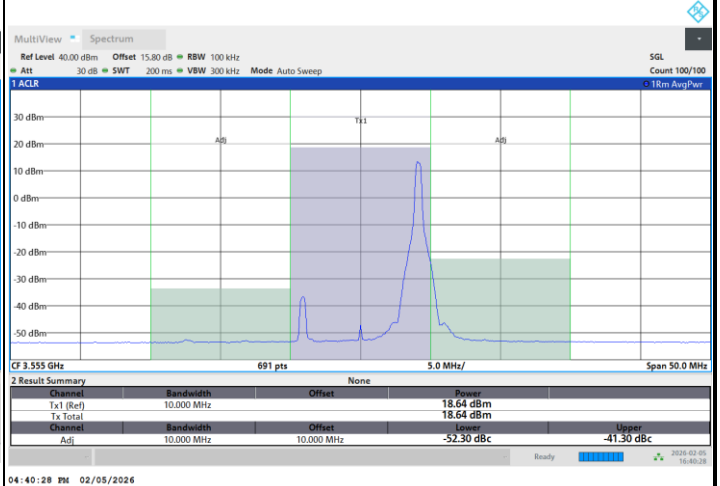
## FR1 n77 Part96 / 10MHz / DFT-S OFDM / QPSK

## Lowest Channel

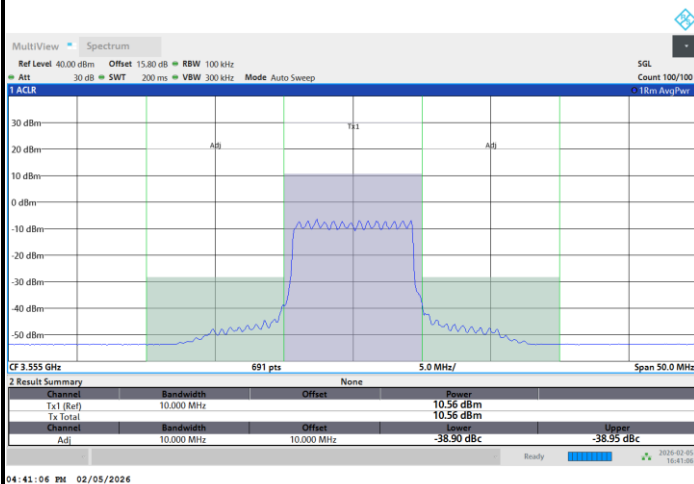
## 1RB0



## 1RBmax



## Full RB

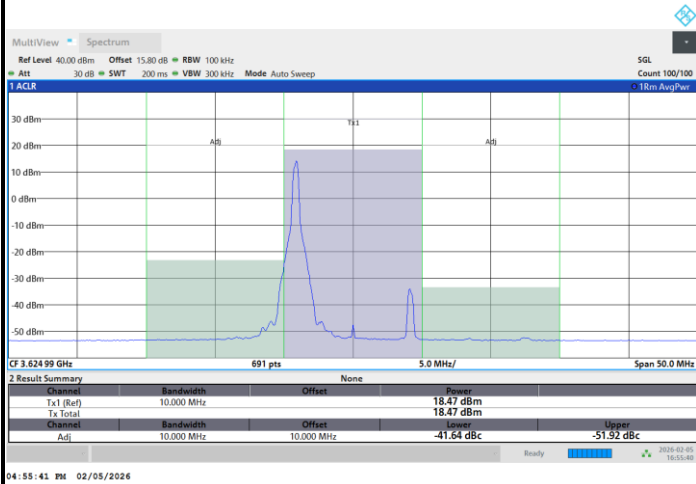




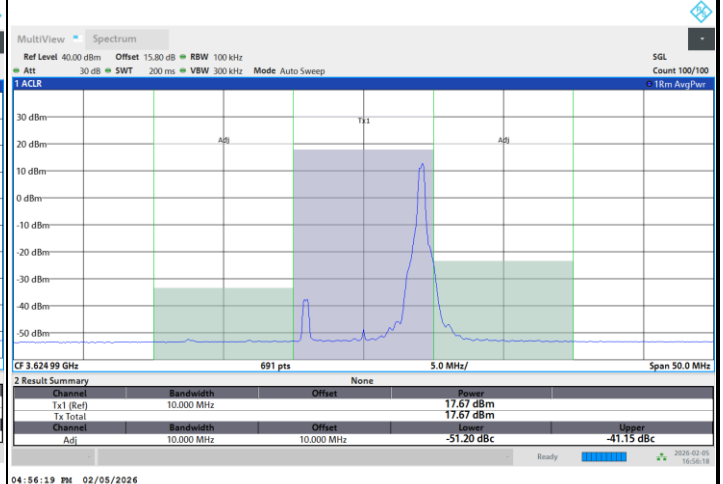
## FR1 n77 Part96 / 10MHz / DFT-S OFDM / QPSK

## Middle Channel

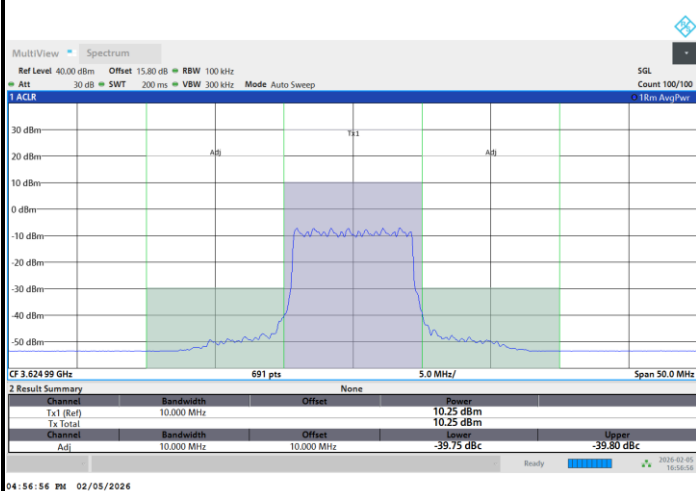
## 1RB0



## 1RBmax



## Full RB

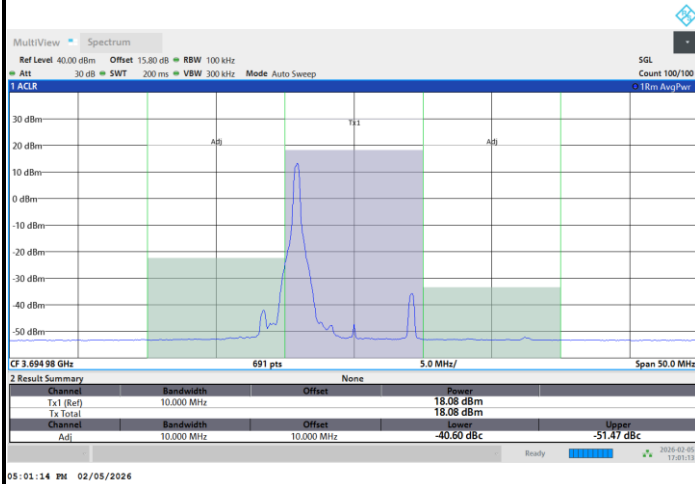




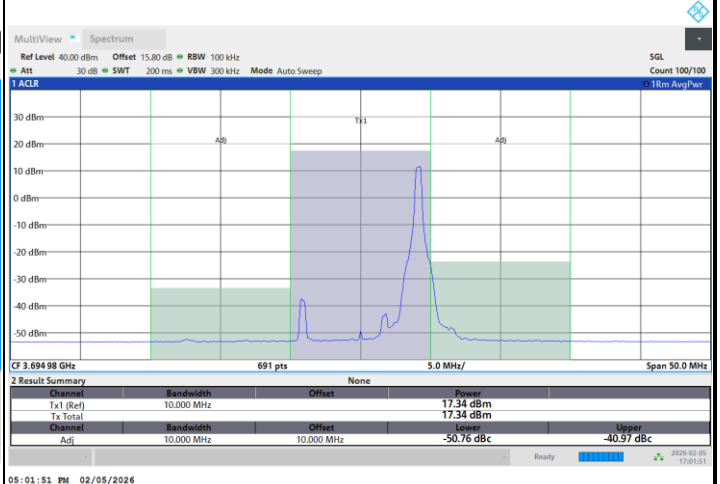
## FR1 n77 Part96 / 10MHz / DFT-S OFDM / QPSK

## Highest Channel

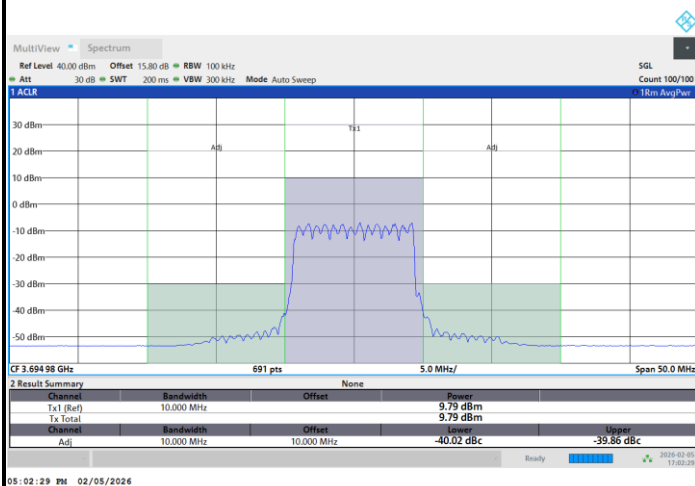
## 1RB0



## 1RBmax



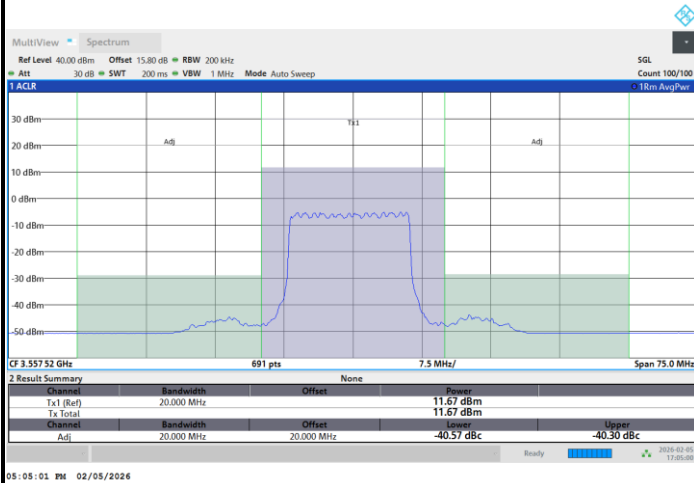
## Full RB



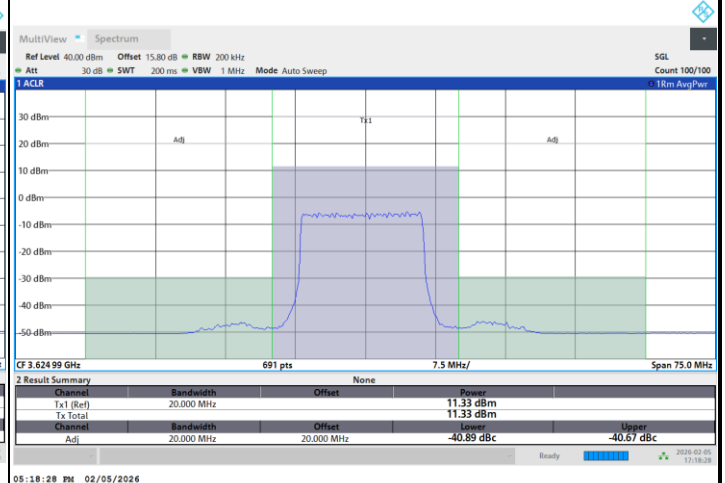


## FR1 n77 Part96 / 15MHz / DFT-S OFDM / PI/2 BPSK

## Lowest Full RB



## Middle Full RB



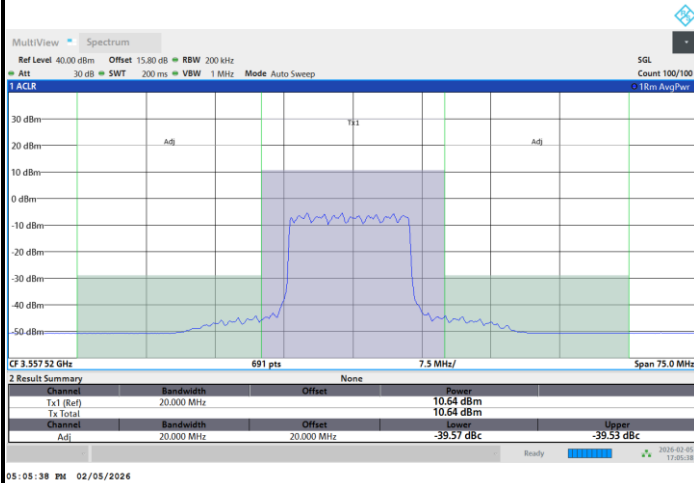
## Highest Full RB



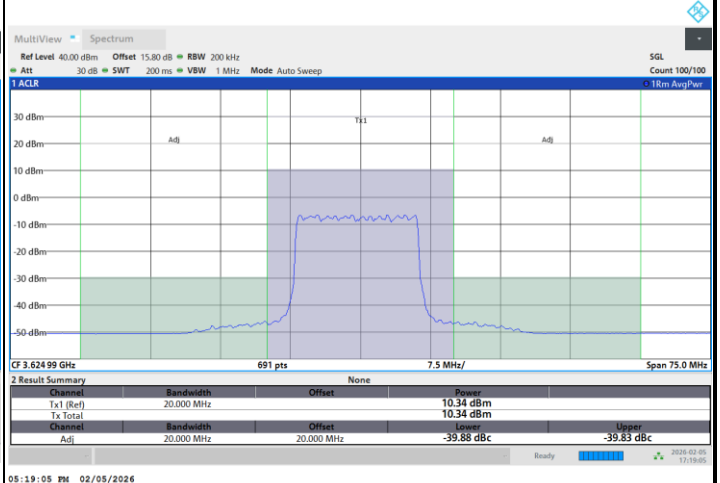


## FR1 n77 Part96 / 15MHz / DFT-S OFDM / QPSK

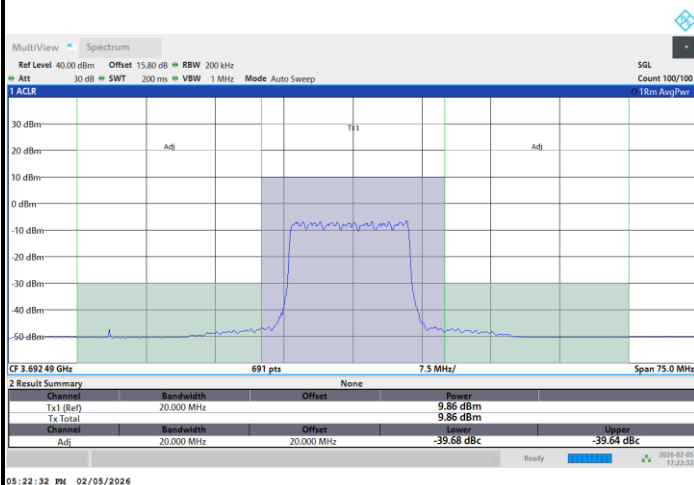
## Lowest Full RB



## Middle Full RB



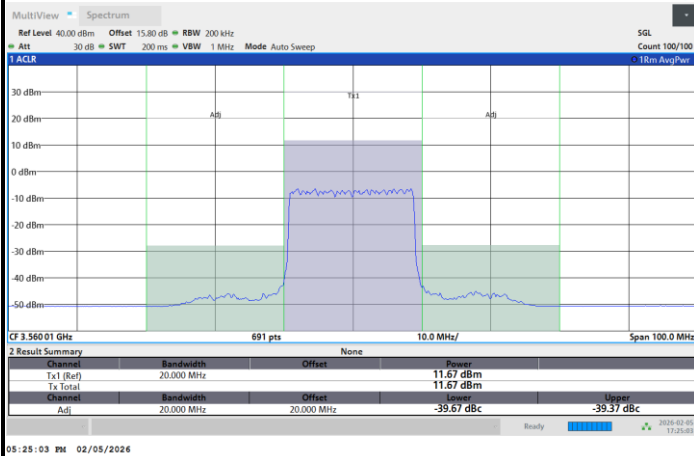
## Highest Full RB



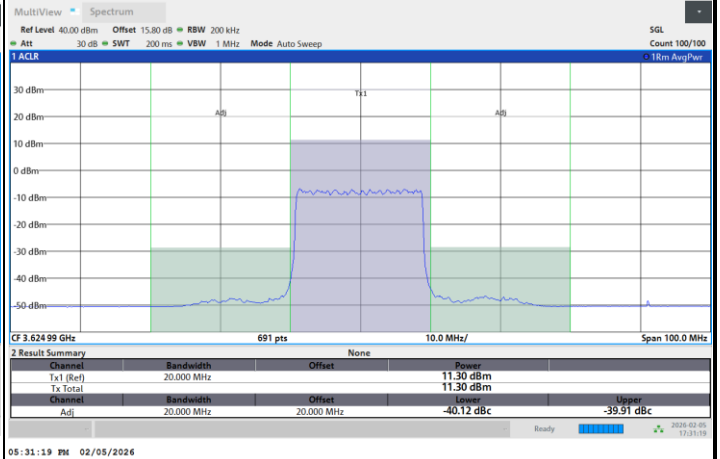


## FR1 n77 Part96 / 20MHz / DFT-S OFDM / PI/2 BPSK

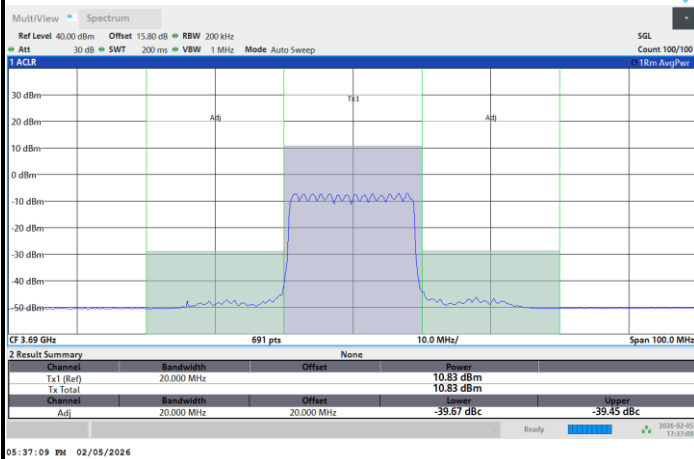
## Lowest Full RB



## Middle Full RB



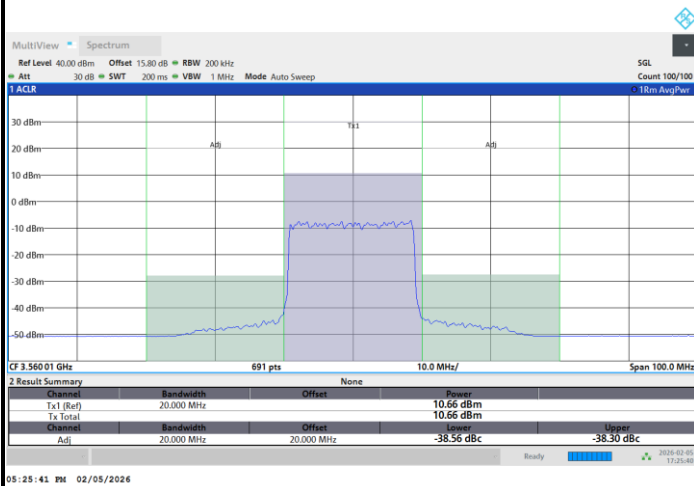
## Highest Full RB





## FR1 n77 Part96 / 20MHz / DFT-S OFDM / QPSK

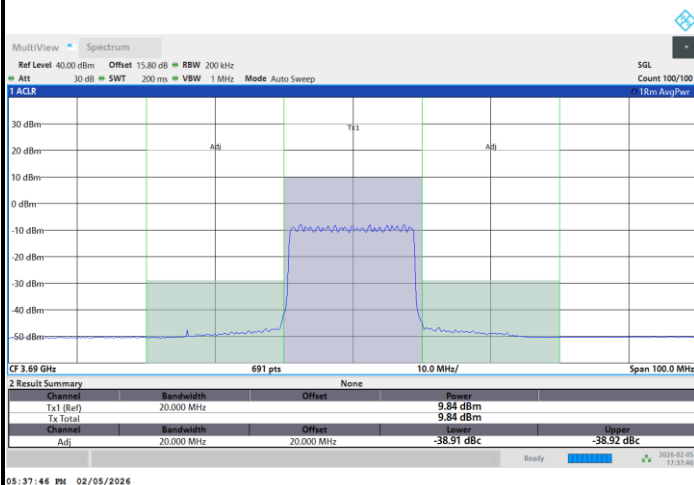
## Lowest Full RB



## Middle Full RB



## Highest Full RB



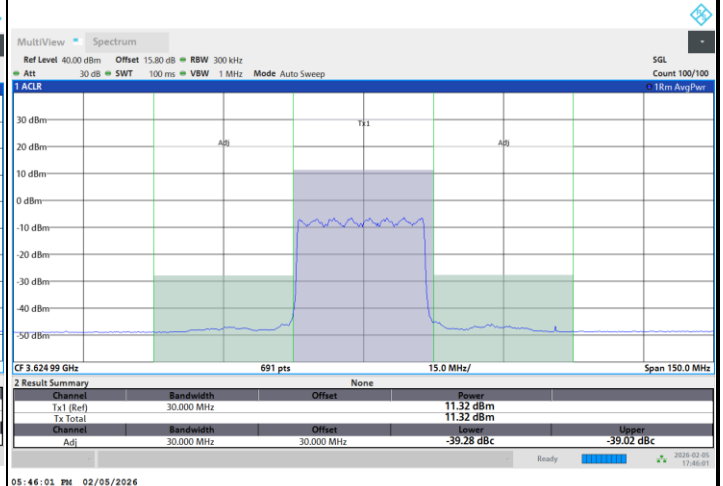


FR1 n77 Part96 / 30MHz / DFT-S OFDM / PI/2 BPSK

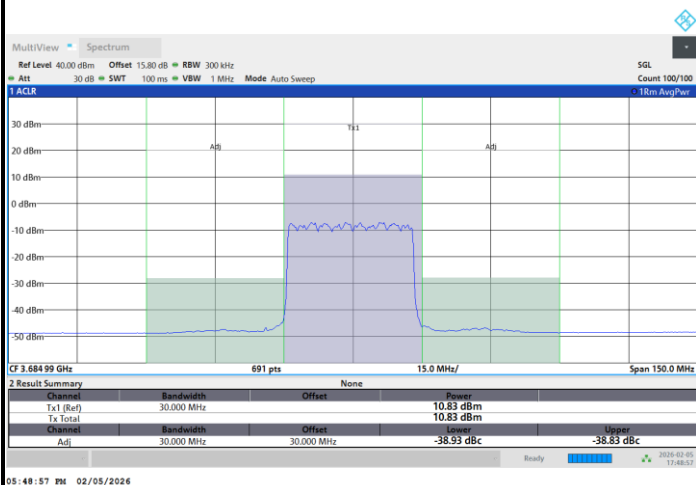
Lowest Full RB



Middle Full RB



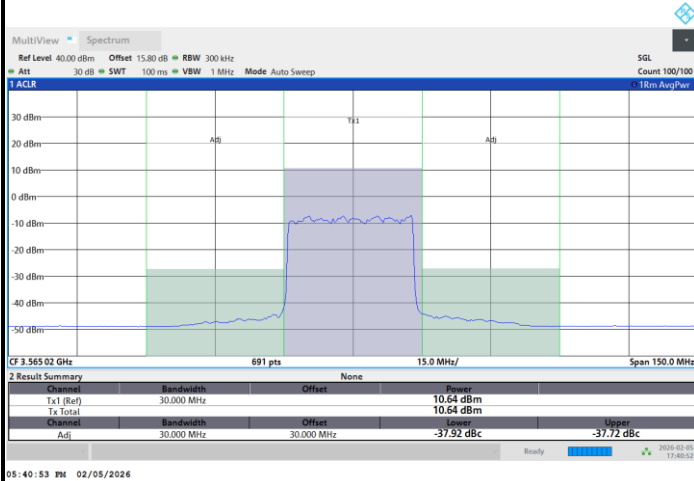
Highest Full RB





## FR1 n77 Part96 / 30MHz / DFT-S OFDM / QPSK

## Lowest Full RB



## Middle Full RB



## Highest Full RB



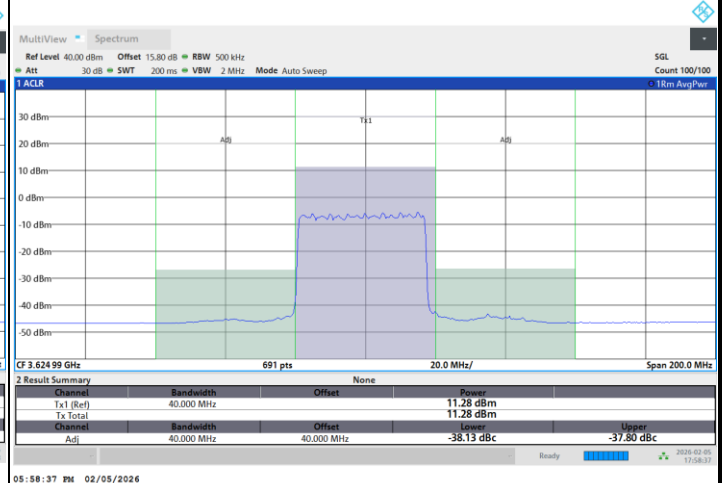


## FR1 n77 Part96 / 40MHz / DFT-S OFDM / PI/2 BPSK

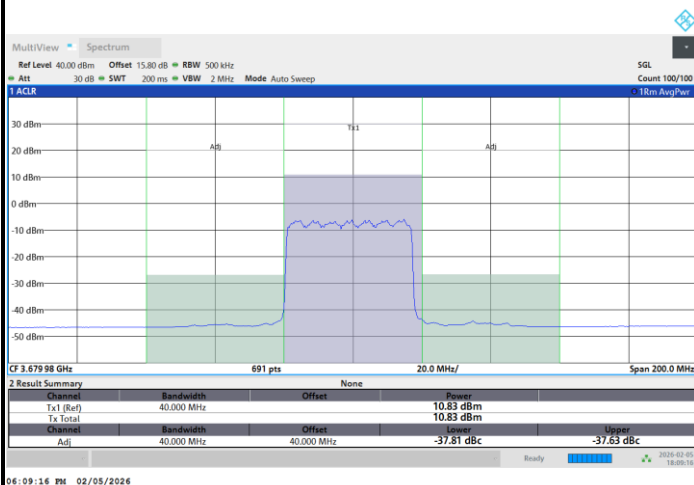
## Lowest Full RB



## Middle Full RB



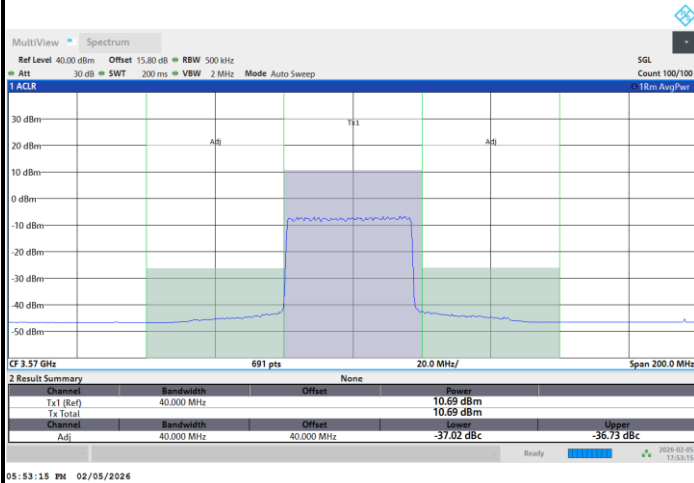
## Highest Full RB



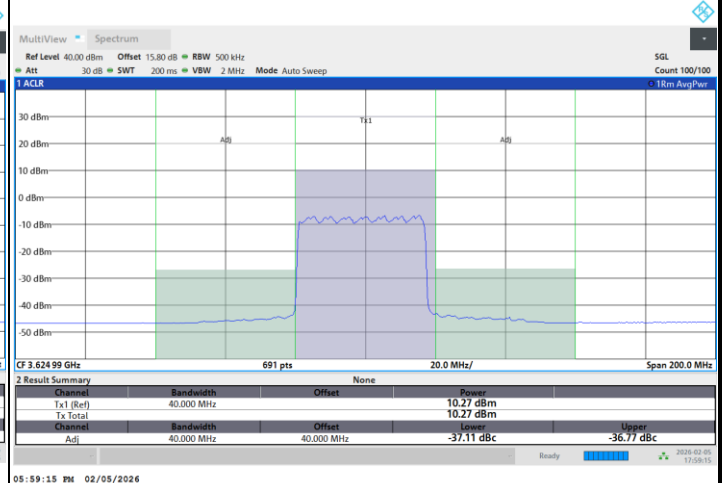


## FR1 n77 Part96 / 40MHz / DFT-S OFDM / QPSK

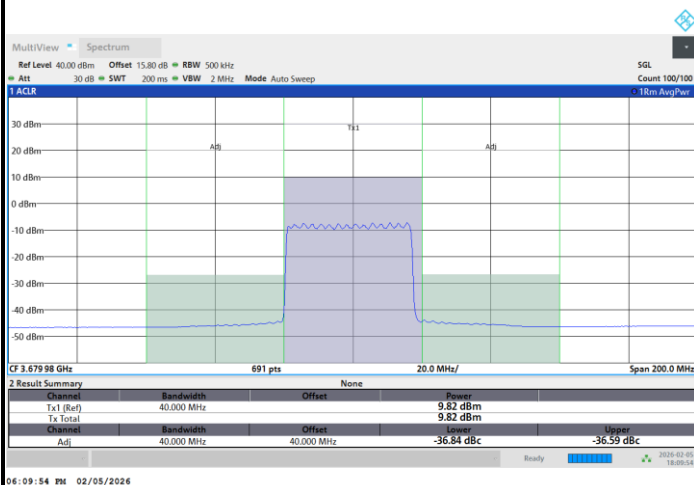
## Lowest Full RB



## Middle Full RB



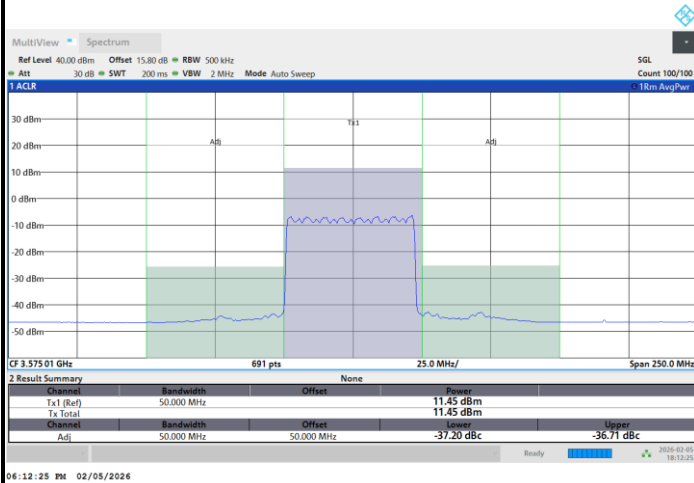
## Highest Full RB



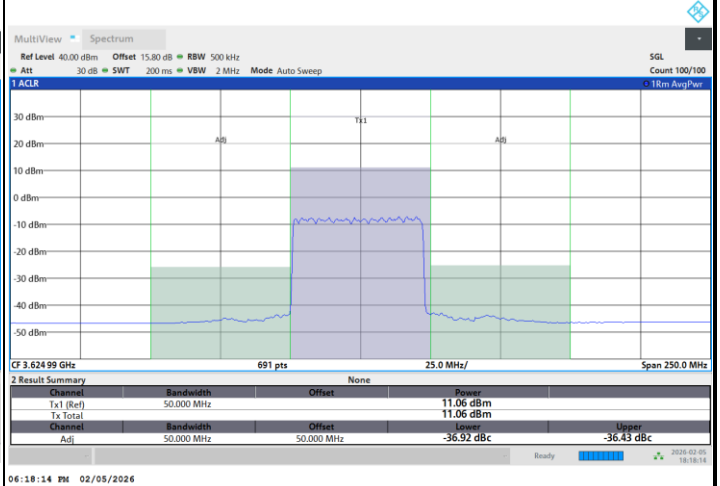


## FR1 n77 Part96 / 50MHz / DFT-S OFDM / PI/2 BPSK

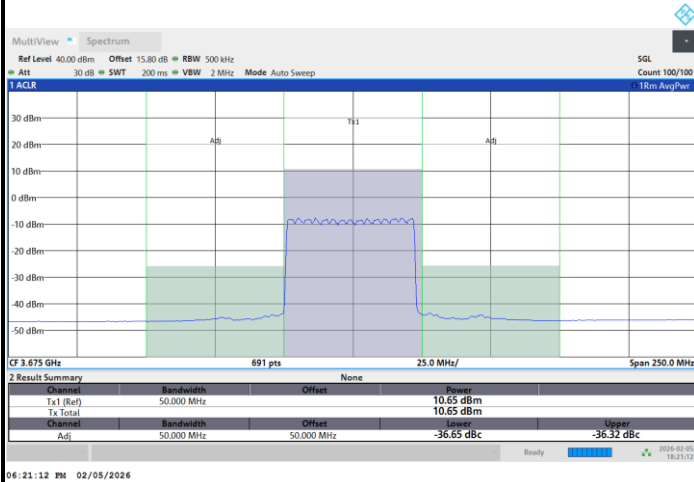
## Lowest Full RB



## Middle Full RB



## Highest Full RB



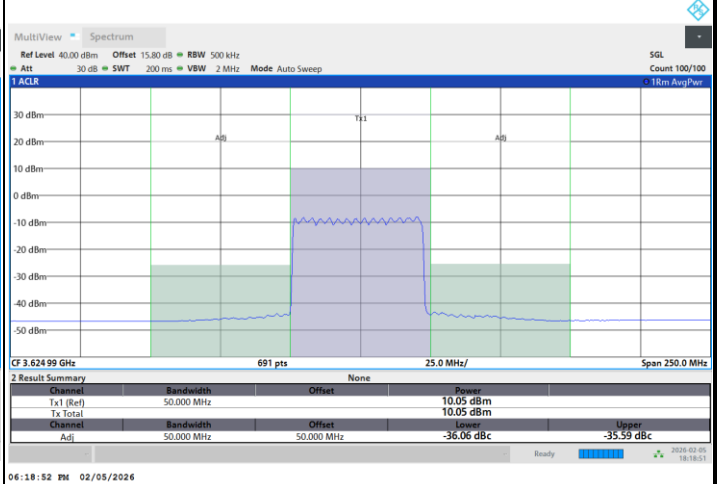


## FR1 n77 Part96 / 50MHz / DFT-S OFDM / QPSK

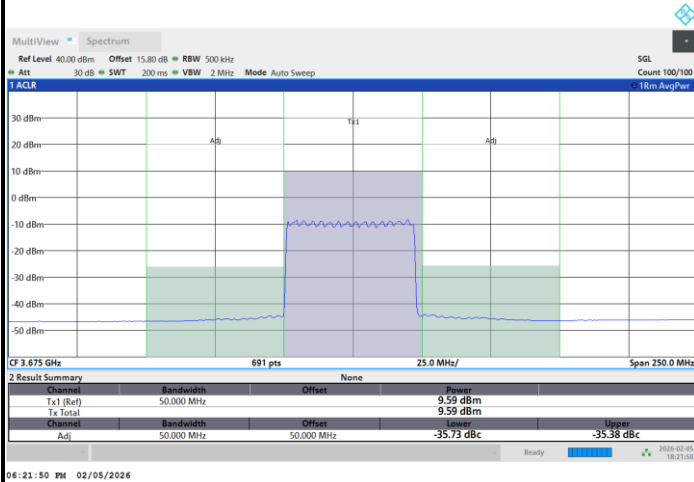
## Lowest Full RB



## Middle Full RB



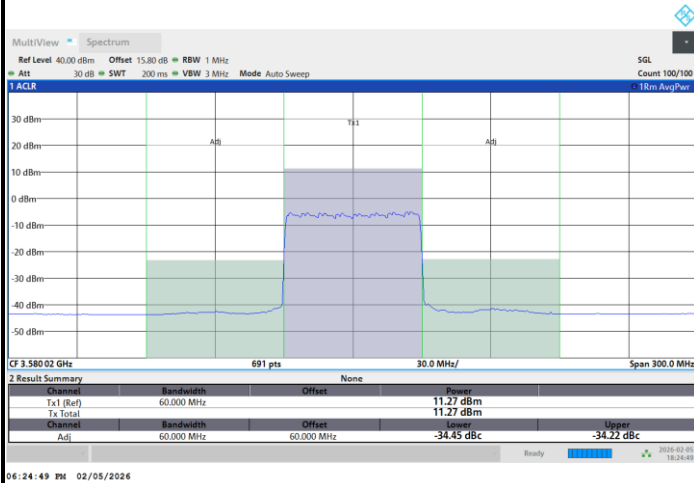
## Highest Full RB



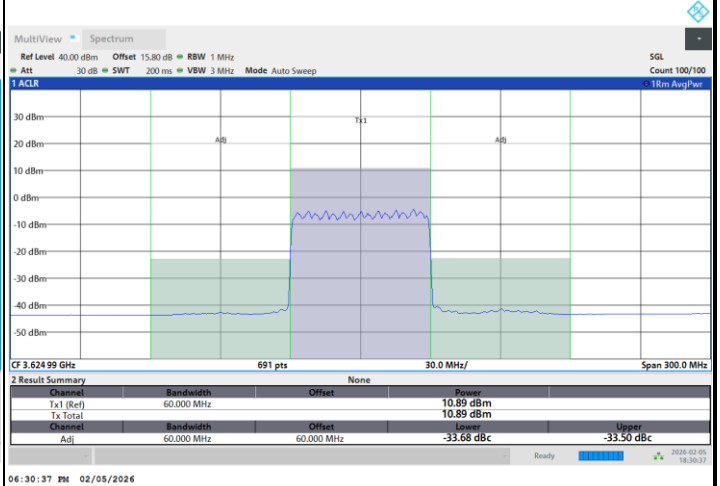


## FR1 n77 Part96 / 60MHz / DFT-S OFDM / PI/2 BPSK

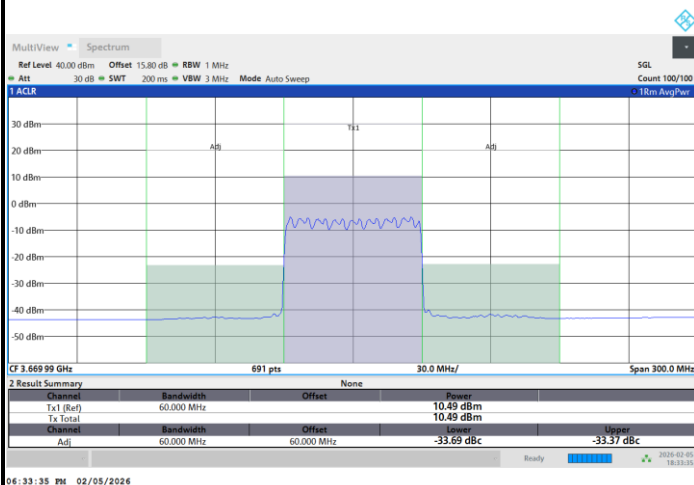
## Lowest Full RB



## Middle Full RB



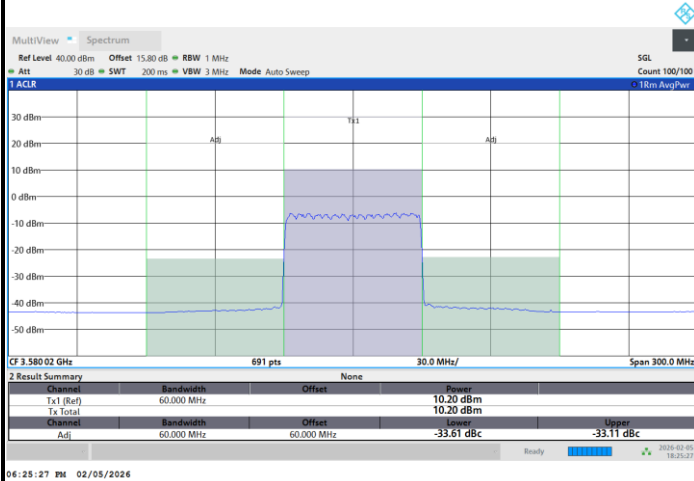
## Highest Full RB



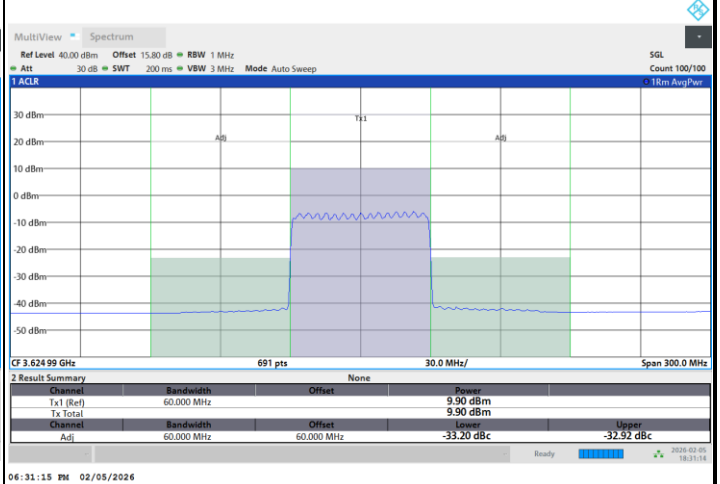


## FR1 n77 Part96 / 60MHz / DFT-S OFDM / QPSK

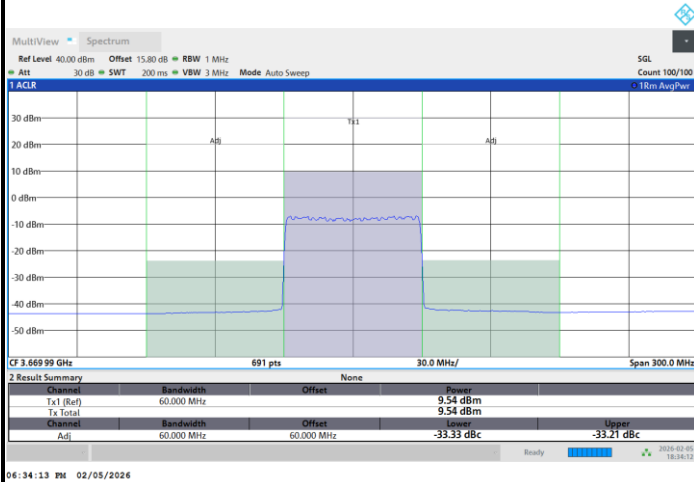
## Lowest Full RB



## Middle Full RB



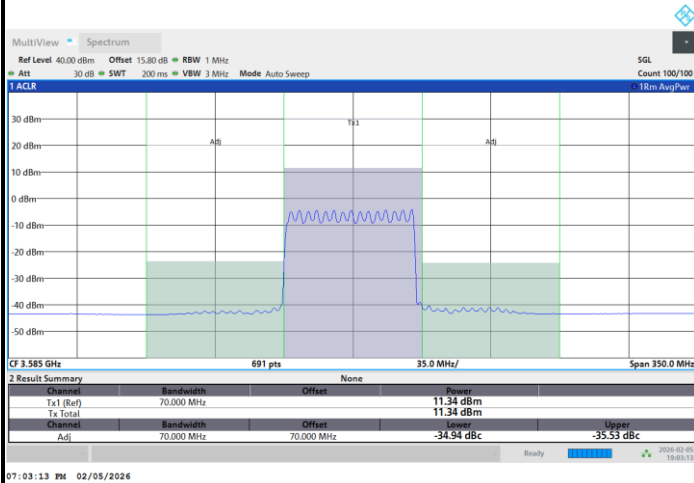
## Highest Full RB



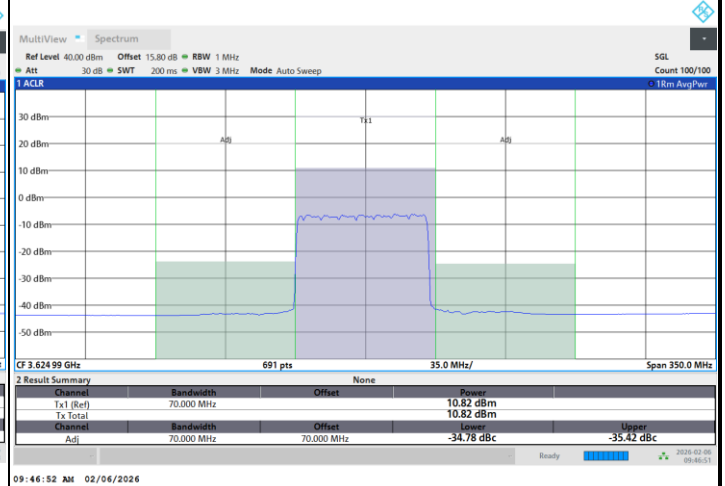


## FR1 n77 Part96 / 70MHz / DFT-S OFDM / PI/2 BPSK

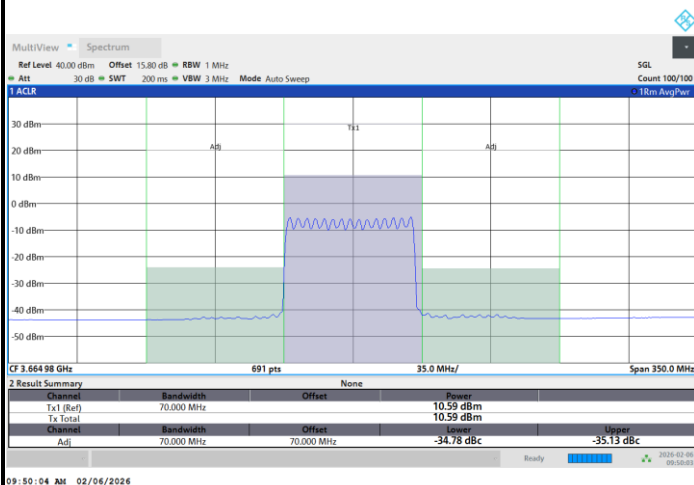
## Lowest Full RB



## Middle Full RB



## Highest Full RB



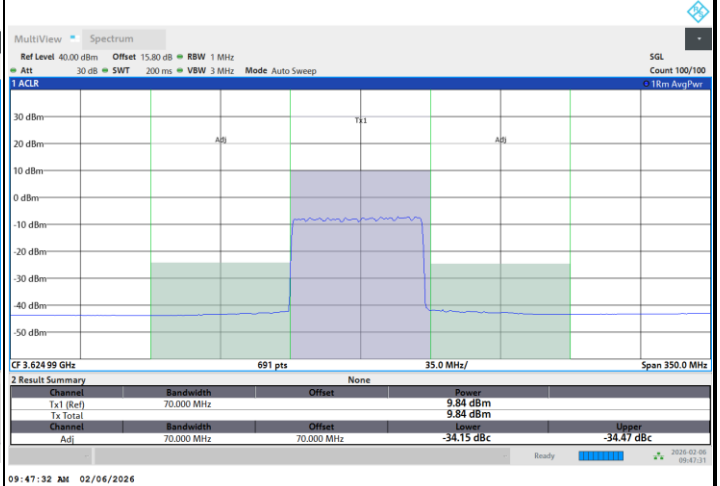


## FR1 n77 Part96 / 70MHz / DFT-S OFDM / QPSK

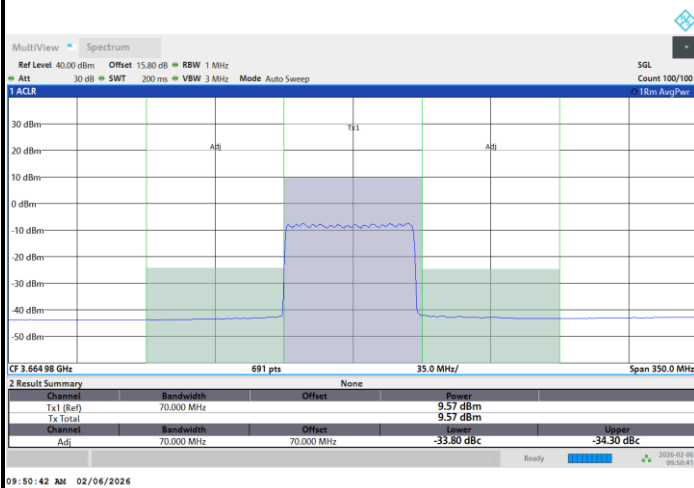
## Lowest Full RB



## Middle Full RB



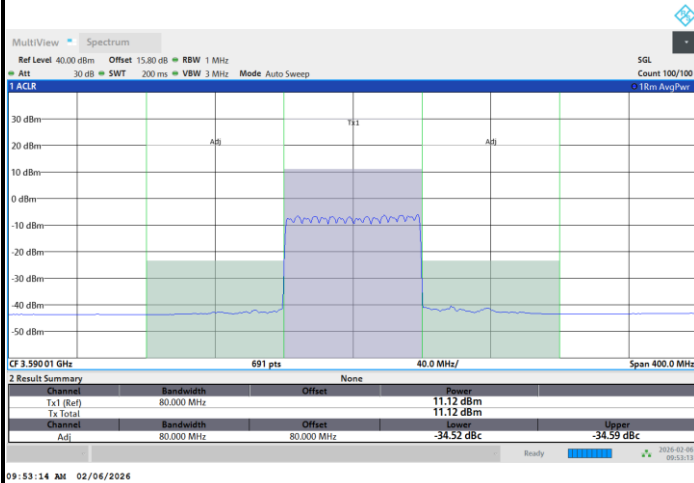
## Highest Full RB



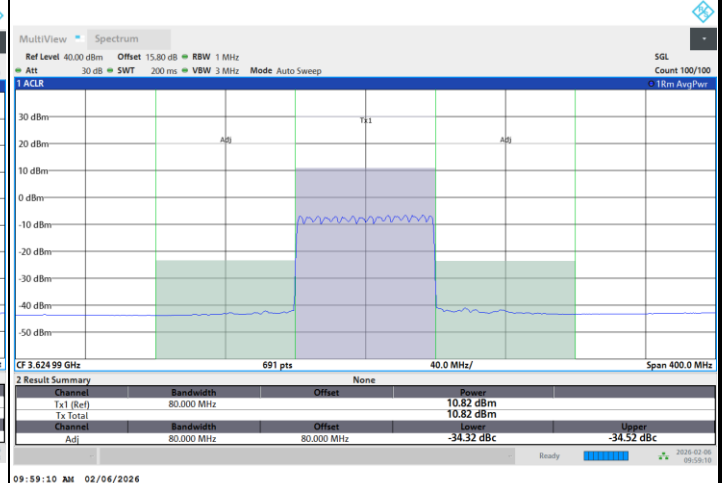


## FR1 n77 Part96 / 80MHz / DFT-S OFDM / PI/2 BPSK

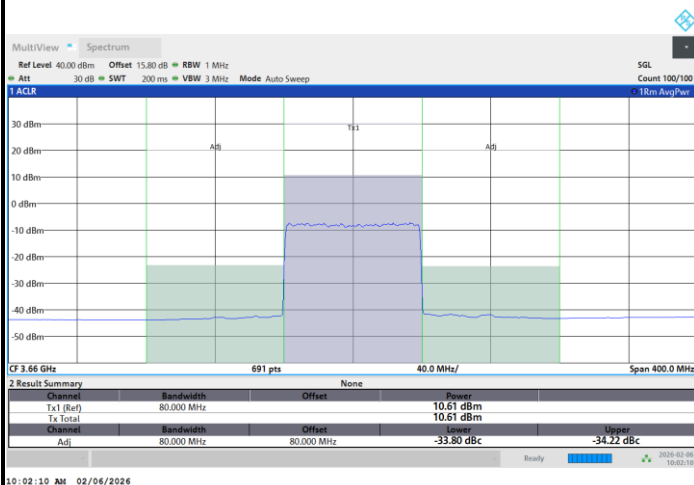
## Lowest Full RB



## Middle Full RB



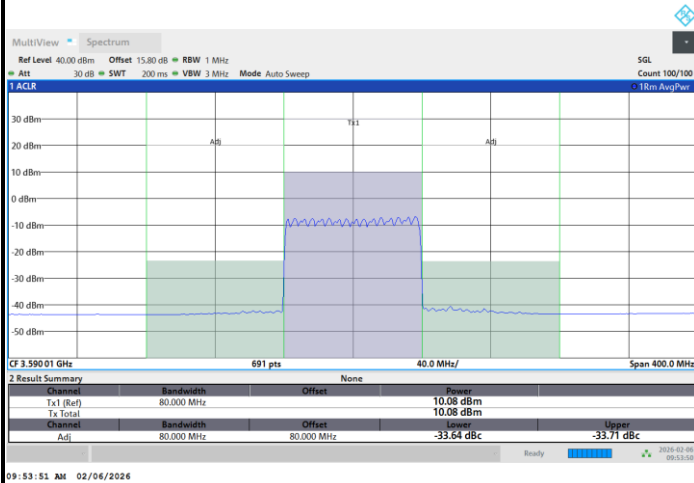
## Highest Full RB



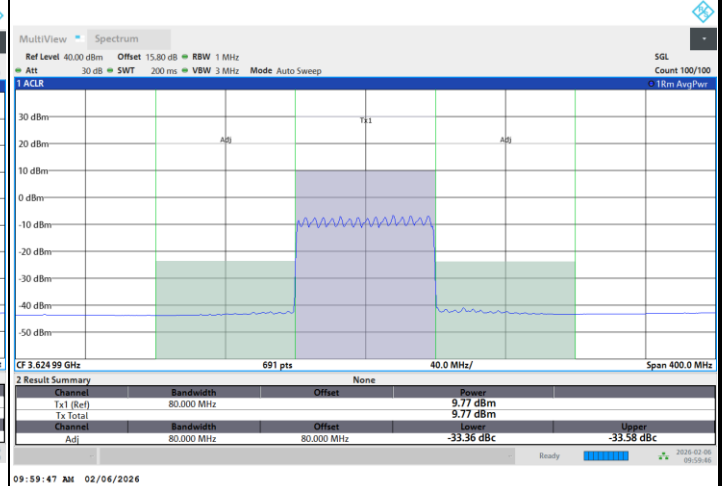


## FR1 n77 Part96 / 80MHz / DFT-S OFDM / QPSK

## Lowest Full RB



## Middle Full RB



## Highest Full RB

