FCC PART 15.247

TEST REPORT

For

Avantronics Limited

The 4th Floor Yuepeng Building, NO.1019 Jiabin Rd, Luohu District, Shenzhen, China

FCC ID: WJ5-AS6P

Report Type:	Product Type:
Original Report	Bluetooth Stereo Headset

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Report Number:	R2DG140430001-00A
Report Date:	2014-05-14
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GENERAL INFORMATION

Product Description for Equipment under Test (EUT)

The *Avantronics Limited*'s product, model number: *BTHS-AS6P (FCC ID: WJ5-AS6P)* (the "EUT") in this report was a *Bluetooth Stereo Headset,* which was measured approximately: 8.5 cm (L) x 7.2 cm (W) x 2.55 cm (H), rated input voltage: DC 3.7V rechargeable Li-ion battery.

All measurement and test data in this report was gathered from production sample serial number: 140430001 (Assigned by BACL, Dongguan). The EUT was received on 2014-04-30.

Objective

This report is prepared on behalf of *Avantronics Limited* in accordance with Part 2, Subpart J, Part 15, Subparts A, B and C of the Federal Communications Commission's rules

The tests were performed in order to determine the Bluetooth BDR and EDR mode of EUT compliance with FCC Part 15, Subpart C, and section 15.203, 15.205, 15.207, 15.209 and 15.247 rules.

Related Submittal(s)/Grant(s)

FCC Part 15C DTS submissions with FCC ID: *WJ5-AS6P* for Bluetooth LE mode.

Test Methodology

All measurements contained in this report were conducted with ANSI C63.4-2003, American National Standard for Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electronic Equipment in the range of 9 kHz to 40 GHz.

All emissions measurement was performed and Bay Area Compliance Laboratories Corp. (Dongguan).

Test Facility

The Test site used by Bay Area Compliance Laboratories Corp. (Dongguan) to collect test data is located on the No.69 Pulongcun, Puxinhu Industrial Zone, Tangxia, Dongguan, Guangdong, China

Test site at Bay Area Compliance Laboratories Corp. (Dongguan) has been fully described in reports submitted to the Federal Communications Commission (FCC). The details of these reports have been found to be in compliance with the requirements of Section 2.948 of the FCC Rules on February 02, 2012. The facility also complies with the radiated and AC line conducted test site criteria set forth in ANSI C63.4-2003.

The Federal Communications Commission has the reports on file and is listed under FCC Registration No.: 273710. The test site has been approved by the FCC for public use and is listed in the FCC Public Access Link (PAL) database.

SYSTEM TEST CONFIGURATION

Description of Test Configuration

The system was configured for testing in an engineering mode, which was provided by manufacturer. The engineering mode was configured the system transmitting with maximum power.

EUT Exercise Software

The software "Bluetooth 2.5.0" was used for testing, which was provided by manufacturer. The worst condition (maximum power) was setting by the software as following table:

Test Software Version		Bluetooth 2.5.0		
Test Frequency		2402MHz	2441MHz	2480MHz
Power Level Setting	GFSK	63	63	63
	π/4 DQPSK	100	100	100
	8DPSK	100	100	100

Equipment Modifications

No modification was made to the EUT.

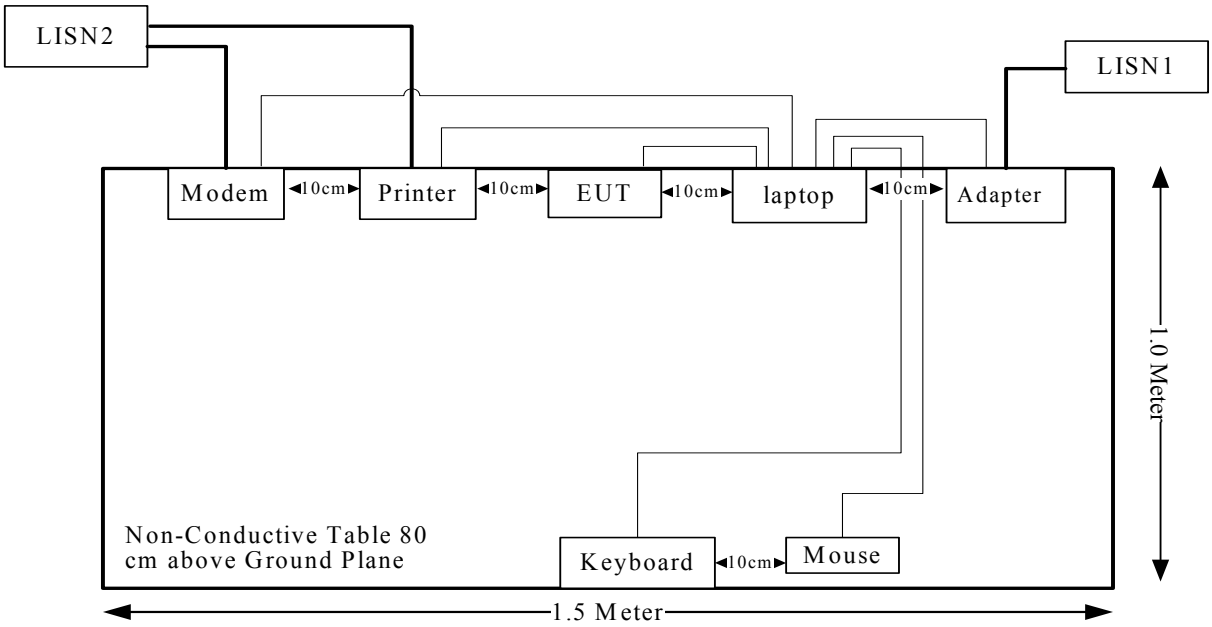
Support Equipment List and Details

Manufacturer	Description	Model	Serial Number
DELL	Laptop	PP11L	QDS-BRCM1017
HP	Printer	C3941A	JPTVOB2337
DELL	Keyboard	L100	CNORH656658907BL05DC
HP	Mouse	UV-69a	32617-001
SAST	Modem	AEM-2100	0293

External Cable

Cable Description	Shielding Type	Ferrite Core	Length (m)	From Port	To
Serial Cable	Yes	No	1.5	Serial Port of Laptop	Modem
Parallel Cable	Yes	No	1.5	Parallel Port of Laptop	Printer
USB Cable	No	No	0.6	USB port of Laptop	EUT
USB Cable	Yes	No	1.6	USB port of Laptop	Mouse
USB Cable	Yes	No	1.8	USB port of Laptop	Keyboard

Block Diagram of Test Setup



SUMMARY OF TEST RESULTS

FCC Rules	Description of Test	Result
FCC §15.247 (i) & §1.1310 & §2.1093	RF Exposure	Compliace
§15.203	Antenna Requirement	Compliance
§15.207 (a)	Conducted Emissions	Compliance
§15.205, §15.209, §15.247(d)	Radiated Emissions	Compliance
§15.247 (a)(1)	20 dB Bandwidth	Compliance
§15.247(a)(1)	Channel Separation Test	Compliance
§15.247(a)(1)(iii)	Time of Occupancy (Dwell Time)	Compliance
§15.247(a)(1)(iii)	Quantity of hopping channel Test	Compliance
§15.247(b)(1)	Peak Output Power Measurement	Compliance
§15.247(d)	Band Edges	Compliance

FCC §15.247 (i) & §1.1310 & §2.1093- RF EXPOSURE

Applicable Standard

According to§15.247(i) and §1.1310, systems operating under the provisions of this section shall be operated in a manner that ensure that the public is not exposed to radio frequency energy level in excess of the Commission's guideline.

According to KDB447498 D01 General RF Exposure Guidance v05r02:

The 1-g and 10-g SAR test exclusion thresholds for 100 MHz to 6 GHz at test separation distances ≤ 50 mm are determined by:
[(max. power of channel, including tune-up tolerance, mW)/(min. test separation distance, mm)] · [√f(GHz)] ≤ 3.0 for 1-g SAR and ≤ 7.5 for 10-g extremity SAR, where

- f(GHz) is the RF channel transmit frequency in GHz
- Power and distance are rounded to the nearest mW and mm before calculation
- The result is rounded to one decimal place for comparison
- 3.0 and 7.5 are referred to as the numeric thresholds in the step 2 below

The test exclusions are applicable only when the minimum test separation distance is ≤ 50 mm and for transmission frequencies between 100 MHz and 6 GHz. When the minimum test separation distance is < 5 mm, a distance of 5 mm according to 5) in section 4.1 is applied to determine SAR test exclusion.

Measurement Result

The maximum conducted output power= 0.67 dBm (1.17mW) at 2441MHz
[(max. power of channel, mW)/(min. test separation distance, mm)] · [√f(GHz)]
= 1.17/5*(√2.441) = 0.366 < 3.0

So the stand-alone SAR evaluation is not necessary.

FCC §15.203 - ANTENNA REQUIREMENT

Applicable Standard

According to FCC § 15.203, an intentional radiator shall be designed to ensure that no antenna other than that furnished by the responsible party shall be used with the device. The use of a permanently attached antenna or of an antenna that uses a unique coupling to the intentional radiator shall be considered sufficient to comply with the provisions of this Section. The manufacturer may design the unit so that a broken antenna can be replaced by the user, but the use of a standard antenna jack or electrical connector is prohibited.

Antenna Connector Construction

The EUT has one integral antenna arrangement, which was permanently attached and the antenna gain is 2.0 dBi, fulfill the requirement of this section. Please refer to the EUT photos.

Result: Compliance.

FCC §15.207 – AC LINE CONDUCTED EMISSIONS

Measurement Uncertainty

Compliance or non- compliance with a disturbance limit shall be determined in the following manner:

If U_{lab} is less than or equal to U_{cispr} of Table 1, then:
- compliance is deemed to occur if no measured disturbance level exceeds the disturbance limit;
- non - compliance is deemed to occur if any measured disturbance level exceeds the disturbance limit.

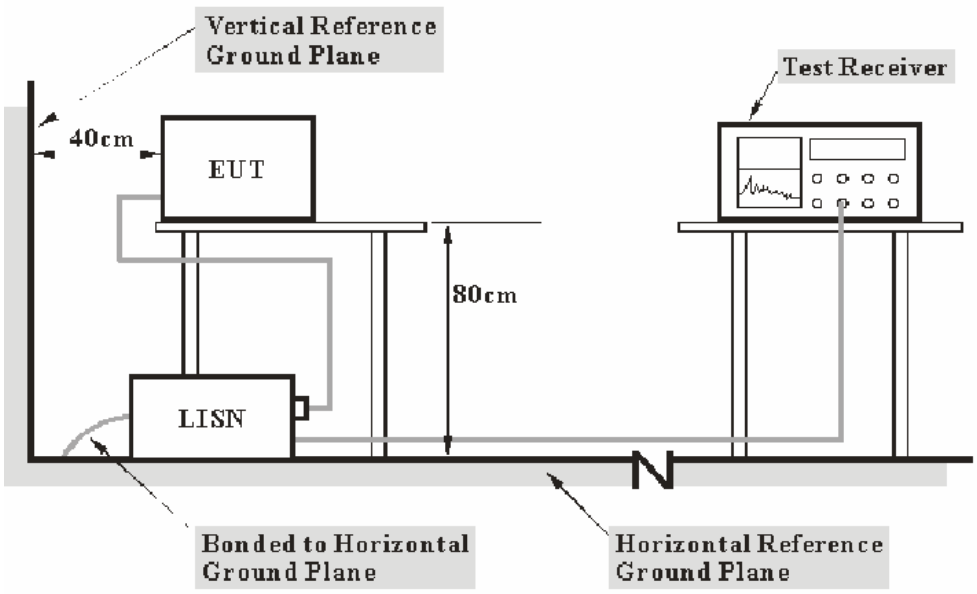
If U_{lab} is greater than U_{cispr} of Table 1, then:
- compliance is deemed to occur if no measured disturbance level, increased by ($U_{lab} - U_{cispr}$), exceeds the disturbance limit;
- non - compliance is deemed to occur if any measured disturbance level, increased by ($U_{lab} - U_{cispr}$), exceeds the disturbance limit.

Based on CISPR 16-4-2: 2011, measurement uncertainty of conducted disturbance at mains port using AMN at Bay Area Compliance Laboratories Corp. (Dongguan) is 3.46 dB (150 kHz to 30 MHz).

Table 1 – Values of U_{cispr}

Measurement	U_{cispr}
Conducted disturbance at mains port using AMN (150 kHz to 30 MHz)	3.4 dB

EUT Setup



Note: 1. Support units were connected to second LISN.
2. Both of LISNs (AMN) 80 cm from EUT and at the least 80 cm from other units and other metal planes support units.

The setup of EUT is according with per ANSI C63.4-2003 measurement procedure. The specification used was with the FCC Part 15.207 Class B limits.

The spacing between the peripherals was 10 cm.

The adapter of laptop was connected to a 120 VAC/60 Hz power source

EMI Test Receiver Setup

The EMI test receiver was set to investigate the spectrum from 150 kHz to 30 MHz.

During the conducted emission test, the EMI test receiver was set with the following configurations:

Frequency Range	IF B/W
150 kHz – 30 MHz	9 kHz

Test Procedure

During the conducted emission test, the adapter of laptop was connected to the outlet of the first LISN and the other support equipments were connected to the outlet of the second LISN.

Maximizing procedure was performed on the six (6) highest emissions of the EUT.

All data was recorded in the Quasi-peak and average detection mode.

Corrected Amplitude & Margin Calculation

The basic equation is as follows:

$V_C = V_R + A_C + VDF$
$C_f = A_C + VDF$

Herein,
V_C (cord. Reading): corrected voltage amplitude
V_R: reading voltage amplitude
A_c: attenuation caused by cable loss
VDF: voltage division factor of AMN
C_f: Correction Factor

The "**Margin**" column of the following data tables indicates the degree of compliance within the applicable limit. For example, a margin of 7dB means the emission is 7dB below the maximum limit. The equation for margin calculation is as follows:

Margin = Limit – Corrected Amplitude

Test Equipment List and Details

Manufacturer	Description	Model	Serial Number	Calibration Date	Calibration Due Date
R&S	EMI Test Receiver	ESCS 30	830245/006	2013-11-20	2014-11-19
R&S	Two-line V-network	ENV216	3560.6550.12	2014-01-22	2015-01-21
R&S	L.I.S.N	ESH3-Z5	100113	N/A	N/A
R&S	Test Software	EMC32	Version8.53.0	N/A	N/A

*** Statement of Traceability:** Bay Area Compliance Laboratories Corp. (Dongguan) attests that all calibrations have been performed, traceable to National Primary Standards and International System of Units (SI).

Test Results Summary

According to the recorded data in following table, the EUT complied with the FCC Part 15.207, with the worst margin reading of:

6.50 dB at **0.317235 MHz** in the **Neutral** conducted mode

Test Data

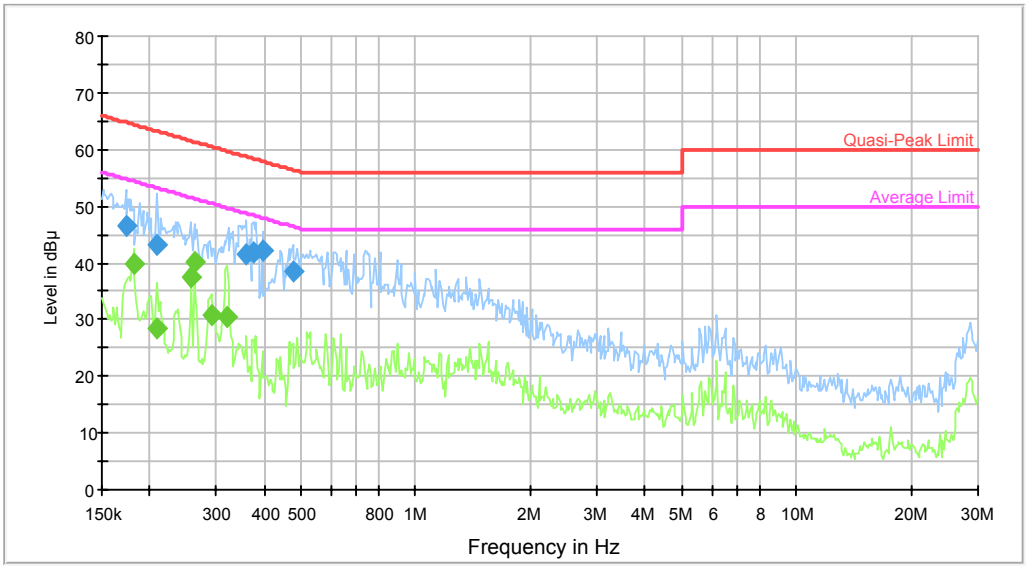
Environmental Conditions

Temperature:	23.4 °C
Relative Humidity:	66 %
ATM Pressure:	100.8 kPa

The testing was performed by Allen Qiao on 2014-05-08.

Test mode: Charging&Transmitting

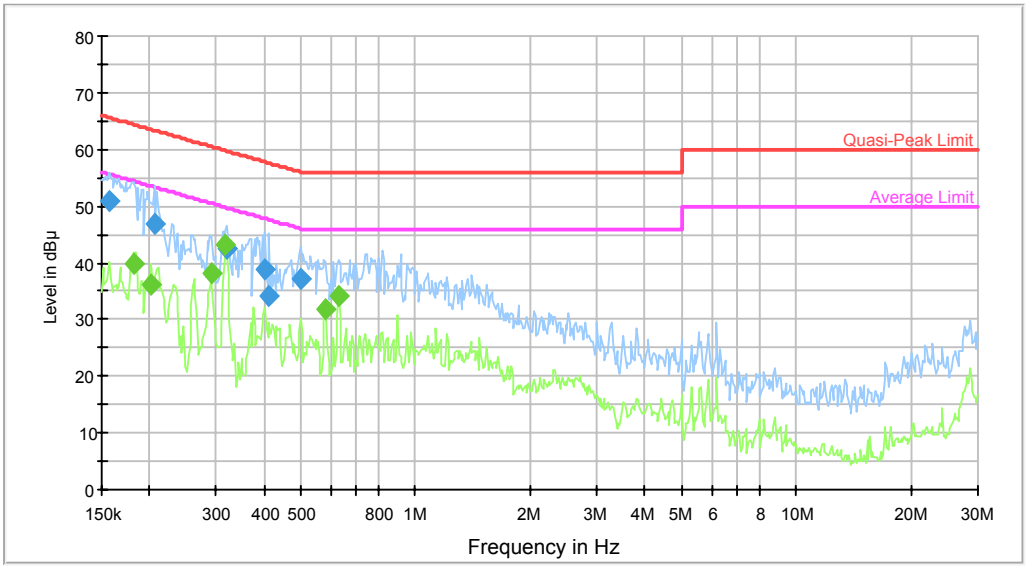
AC120 V, 60 Hz, Line:



Frequency (MHz)	QuasiPeak (dBµV)	Bandwidth (kHz)	Line	Corr. (dB)	Margin (dB)	Limit (dBµV)	Comment
0.173134	46.5	9.000	L1	10.3	18.3	64.8	Compliance
0.209621	43.1	9.000	L1	10.8	20.1	63.2	Compliance
0.360371	41.6	9.000	L1	10.7	17.1	58.7	Compliance
0.375019	41.8	9.000	L1	10.7	16.6	58.4	Compliance
0.396530	42.0	9.000	L1	10.6	15.9	57.9	Compliance
0.476287	38.6	9.000	L1	10.4	17.8	56.4	Compliance

Frequency (MHz)	Average (dBµV)	Bandwidth (kHz)	Line	Corr. (dB)	Margin (dB)	Limit (dBµV)	Comment
0.181612	39.8	9.000	L1	10.5	14.6	54.4	Compliance
0.209621	28.5	9.000	L1	10.8	24.7	53.2	Compliance
0.257874	37.5	9.000	L1	10.7	14.0	51.5	Compliance
0.262017	40.0	9.000	L1	10.7	11.3	51.4	Compliance
0.290613	30.7	9.000	L1	10.7	19.8	50.5	Compliance
0.319773	30.3	9.000	L1	10.7	19.4	49.7	Compliance

AC120 V, 60 Hz, Neutral:



Frequency (MHz)	QuasiPeak (dBµV)	Bandwidth (kHz)	Line	Corr. (dB)	Margin (dB)	Limit (dBµV)	Comment
0.157346	51.0	9.000	N	10.4	14.6	65.6	Compliance
0.206306	46.8	9.000	N	11.3	16.5	63.4	Compliance
0.319773	42.4	9.000	N	11.1	17.3	59.7	Compliance
0.402900	38.7	9.000	N	10.8	19.1	57.8	Compliance
0.412647	34.1	9.000	N	10.8	23.5	57.6	Compliance
0.499611	37.0	9.000	N	10.4	19.0	56.0	Compliance

Frequency (MHz)	Average (dBµV)	Bandwidth (kHz)	Line	Corr. (dB)	Margin (dB)	Limit (dBµV)	Comment
0.181612	39.8	9.000	N	10.9	14.6	54.4	Compliance
0.203045	36.0	9.000	N	11.4	17.5	53.5	Compliance
0.290613	38.3	9.000	N	11.2	12.2	50.5	Compliance
0.317235	43.3	9.000	N	11.1	6.5	49.8	Compliance
0.581275	31.7	9.000	N	10.4	14.3	46.0	Compliance
0.629488	34.2	9.000	N	10.5	11.8	46.0	Compliance

FCC §15.209, §15.205 & §15.247(d) - SPURIOUS EMISSIONS

Applicable Standard

FCC §15.247 (d); §15.209; §15.205;

Measurement Uncertainty

Compliance or non- compliance with a disturbance limit shall be determined in the following manner：

If U_{lab} is less than or equal to U_{cispr} of Table 1, then:
- compliance is deemed to occur if no measured disturbance level exceeds the disturbance limit;
- non - compliance is deemed to occur if any measured disturbance level exceeds the disturbance limit.

If U_{lab} is greater than U_{cispr} of Table 1, then:
- compliance is deemed to occur if no measured disturbance level, increased by ($U_{lab} - U_{cispr}$), exceeds the disturbance limit;
- non - compliance is deemed to occur if any measured disturbance level, increased by ($U_{lab} - U_{cispr}$), exceeds the disturbance limit.

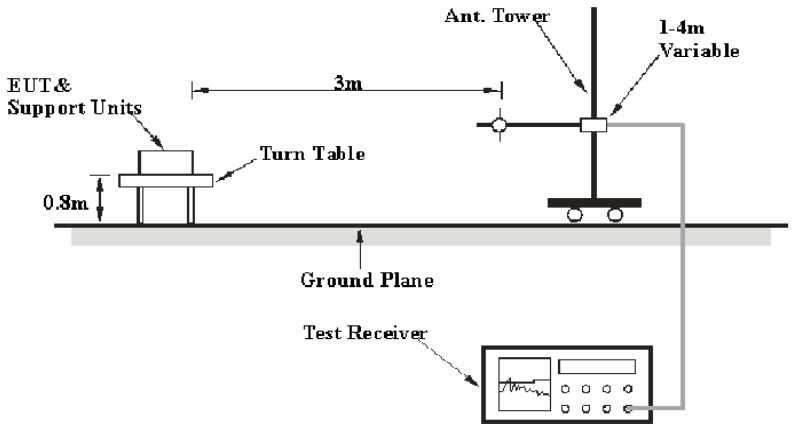
Based on CISPR 16-4-2: 2011, measurement uncertainty of radiated emission at a distance of 3m at Bay Area Compliance Laboratories Corp. (Dongguan) is:
30M~200MHz: 5.0 dB
200M~1GHz: 6.2 dB
1G~6GHz: 4.45 dB
6G~18GHz: 5.23 dB

Table 1 – Values of U_{cispr}

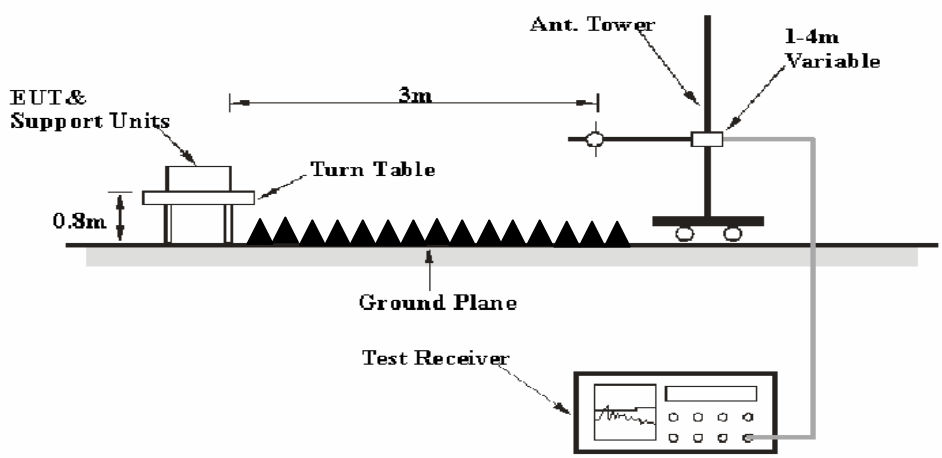
Measurement	U_{cispr}
Radiated disturbance (electric field strength at an OATS or in a SAC) (30 MHz to 1000 MHz)	6.3 dB
Radiated disturbance (electric field strength in a FAR) (1 GHz to 6 GHz)	5.2 dB
Radiated disturbance (electric field strength in a FAR) (6 GHz to 18 GHz)	5.5 dB

EUT Setup

Below 1GHz:



Above 1GHz:



The radiated emission tests were performed in the 3 meters test site, using the setup accordance with the ANSI C63.4-2003. The specification used was the FCC 15.209, and FCC 15.247 limits.

The external I/O cables were draped along the test table and formed a bundle 30 to 40 cm long in the middle.

The spacing between the peripherals was 10 cm.

EMI Test Receiver & Spectrum Analyzer Setup

The system was investigated from 30 MHz to 25 GHz.

During the radiated emission test, the EMI test receiver & Spectrum Analyzer Setup were set with the following configurations:

Frequency Range	RBW	Video B/W	IF B/W	Detector
30 MHz – 1000 MHz	120 kHz	300 kHz	120 kHz	QP
Above 1 GHz	1MHz	3 MHz	/	PK
	1MHz	10 Hz	/	Ave.

Test Procedure

Maximizing procedure was performed on the highest emissions to ensure that the EUT complied with all installation combinations.

Data was recorded in Quasi-peak detection mode for frequency range of 30 MHz - 1 GHz, peak and average detection modes for frequencies above 1 GHz.

Test Equipment List and Details

Manufacturer	Description	Model	Serial Number	Calibration Date	Calibration Due Date
R&S	EMI Test Receiver	ESCI	100224	2014-05-06	2015-05-05
Sunol Sciences	Antenna	JB3	A060611-1	2011-09-06	2014-09-05
HP	Amplifier	8447E	2434A02181	2013-09-06	2014-09-05
R&S	Spectrum Analyzer	FSEM	DE31388	2014-05-07	2015-05-06
ETS LINDGREN	Horn Antenna	3115	000 527 35	2012-09-06	2015-09-05
Mini-Circuit	Amplifier	ZVA-213-S+	054201245	2014-02-19	2015-02-18
R&S	Spectrum Analyzer	FSP 38	100478	2013-06-16	2014-06-15
Ducommun Technolagies	Horn Antenna	ARH-4223-02	1007726-01 1304	2013-06-16	2014-06-15
Quinstar	Amplifier	QLW-18405536-JO	15964001001	2013-09-06	2014-09-05

*** Statement of Traceability:** Bay Area Compliance Laboratories Corp. (Dongguan) attests that all calibrations have been performed, traceable to National Primary Standards and International System of Units (SI).

Corrected Amplitude & Margin Calculation

The Corrected Amplitude is calculated by adding the Antenna Factor and Cable Loss, and subtracting the Amplifier Gain from the Meter Reading. The basic equation is as follows:

Corrected Amplitude = Meter Reading + Antenna Factor + Cable Loss - Amplifier Gain

The "**Margin**" column of the following data tables indicates the degree of compliance with the applicable limit. For example, a margin of 7dB means the emission is 7dB below the limit. The equation for margin calculation is as follows:

Margin = Limit – Corrected Amplitude

Test Results Summary

According to the recorded data in following table, the EUT complied with the FCC Title 47, Part 15, Subpart C, and section 15.205, 15.209 and 15.247, with the worst margin reading of:

14.29 dB at **6960 MHz** in the **Horizontal** polarization of BDR Mode (GFSK)

Test Data

Environmental Conditions

Temperature:	24.3 °C
Relative Humidity:	60 %
ATM Pressure:	100.9 kPa

The testing was performed by Allen Qiao on 2014-05-08.

Mode: Transmitting

BDR Mode (GFSK):

Frequency	Receiver		Rx Antenna		Cable loss (dB)	Amplifier Gain (dB)	Corrected Amplitude (dBμV/m)	FCC 15.247	
(MHz)	Reading (dBμV)	Detector (PK/QP/AV)	Polar (H/V)	Factor (dB)				Limit (dBμV/m)	Margin (dB)
Low Channel: 2402 MHz									
2402	98.33	PK	H	25.65	4.42	27.32	101.08	N/A	N/A
2402	59.80	AV	H	25.65	4.42	27.32	62.55	N/A	N/A
2402	96.40	PK	V	25.65	4.42	27.32	99.15	N/A	N/A
2402	58.35	AV	V	25.65	4.42	27.32	61.10	N/A	N/A
2390	44.89	PK	H	25.61	4.39	27.32	47.57	74.00	26.43
2390	28.87	AV	H	25.61	4.39	27.32	31.55	54.00	22.45
4804	43.71	PK	H	30.59	5.98	27.41	52.87	74.00	21.13
4804	24.58	AV	H	30.59	5.98	27.41	33.74	54.00	20.26
7206	33.79	PK	H	34.09	7.45	25.91	49.42	74.00	24.58
7206	19.77	AV	H	34.09	7.45	25.91	35.40	54.00	18.60
2580	44.04	PK	H	26.11	4.63	27.41	47.37	74.00	26.63
2580	25.64	AV	H	26.11	4.63	27.41	28.97	54.00	25.03
1199	42.14	PK	H	22.82	2.79	26.81	40.94	74.00	33.06
1199	23.59	AV	H	22.82	2.79	26.81	22.39	54.00	31.61
Middle Channel: 2441 MHz									
2441	98.24	PK	H	25.74	4.40	27.34	101.04	N/A	N/A
2441	59.96	AV	H	25.74	4.40	27.34	62.76	N/A	N/A
2441	96.12	PK	V	25.74	4.40	27.34	98.92	N/A	N/A
2441	58.63	AV	V	25.74	4.40	27.34	61.43	N/A	N/A
4880	44.11	PK	H	30.79	6.08	27.42	53.56	74.00	20.44
4880	25.34	AV	H	30.79	6.08	27.42	34.79	54.00	19.21
7320	34.12	PK	H	34.37	7.51	25.88	50.12	74.00	23.88
7320	19.89	AV	H	34.37	7.51	25.88	35.89	54.00	18.11
2585	43.85	PK	H	26.12	4.63	27.41	47.19	74.00	26.81
2585	24.91	AV	H	26.12	4.63	27.41	28.25	54.00	25.75
6960	41.64	PK	H	33.5	7.3	26.33	56.11	74.00	17.89
6960	24.22	AV	H	33.5	7.3	26.33	38.69	54.00	15.31
1120	43.01	PK	H	22.61	2.72	26.68	41.66	74.00	32.34
1120	23.47	AV	H	22.61	2.72	26.68	22.12	54.00	31.88
High Channel: 2480 MHz									
2480	98.40	PK	H	25.85	4.48	27.36	101.37	N/A	N/A
2480	58.77	AV	H	25.85	4.48	27.36	61.74	N/A	N/A
2480	96.74	PK	V	25.85	4.48	27.36	99.71	N/A	N/A
2480	60.08	AV	V	25.85	4.48	27.36	63.05	N/A	N/A
2483.5	50.14	PK	H	25.86	4.49	27.36	53.13	74.00	20.87
2483.5	28.57	AV	H	25.86	4.49	27.36	31.56	54.00	22.44
4960	43.68	PK	H	31.00	5.90	27.43	53.15	74.00	20.85
4960	28.67	AV	H	31.00	5.90	27.43	38.14	54.00	15.86
7440	33.52	PK	H	34.66	7.58	25.97	49.79	74.00	24.21
7440	20.03	AV	H	34.66	7.58	25.97	36.30	54.00	17.70
2584	43.98	PK	H	26.12	4.63	27.41	47.32	74.00	26.68
2584	25.16	AV	H	26.12	4.63	27.41	28.50	54.00	25.50
6960	41.03	PK	H	33.50	7.30	26.33	55.50	74.00	18.50
6960	25.24	AV	H	33.50	7.30	26.33	39.71	54.00	14.29

EDR Mode (π/4-DQPSK):

Frequency	Receiver		Rx Antenna		Cable loss (dB)	Amplifier Gain (dB)	Corrected Amplitude (dBμV/m)	FCC 15.247	
(MHz)	Reading (dBμV)	Detector (PK/QP/AV)	Polar (H/V)	Factor (dB)				Limit (dBμV/m)	Margin (dB)
Low Channel: 2402 MHz									
2402	97.40	PK	H	25.65	4.42	27.32	100.15	N/A	N/A
2402	60.21	AV	H	25.65	4.42	27.32	62.96	N/A	N/A
2402	96.74	PK	V	25.65	4.42	27.32	99.49	N/A	N/A
2402	59.35	AV	V	25.65	4.42	27.32	62.10	N/A	N/A
2390	44.52	PK	H	25.61	4.39	27.32	47.20	74.00	26.80
2390	28.19	AV	H	25.61	4.39	27.32	30.87	54.00	23.13
4804	43.66	PK	H	30.59	5.98	27.41	52.82	74.00	21.18
4804	25.03	AV	H	30.59	5.98	27.41	34.19	54.00	19.81
7206	34.51	PK	H	34.09	7.45	25.91	50.14	74.00	23.86
7206	20.27	AV	H	34.09	7.45	25.91	35.90	54.00	18.10
2587	44.36	PK	H	26.13	4.63	27.41	47.71	74.00	26.29
2587	25.31	AV	H	26.13	4.63	27.41	28.66	54.00	25.34
1122	41.88	PK	H	22.62	2.72	26.69	40.53	74.00	33.47
1122	22.67	AV	H	22.62	2.72	26.69	21.32	54.00	32.68
Middle Channel: 2441 MHz									
2441	97.32	PK	H	25.74	4.40	27.34	100.12	N/A	N/A
2441	59.25	AV	H	25.74	4.40	27.34	62.05	N/A	N/A
2441	95.77	PK	V	25.74	4.40	27.34	98.57	N/A	N/A
2441	58.69	AV	V	25.74	4.40	27.34	61.49	N/A	N/A
4880	43.15	PK	H	30.79	6.08	27.42	52.60	74.00	21.40
4880	24.36	AV	H	30.79	6.08	27.42	33.81	54.00	20.19
7320	33.98	PK	H	34.37	7.51	25.88	49.98	74.00	24.02
7320	20.16	AV	H	34.37	7.51	25.88	36.16	54.00	17.84
2580	42.35	PK	H	26.11	4.63	27.41	45.68	74.00	28.32
2580	24.74	AV	H	26.11	4.63	27.41	28.07	54.00	25.93
1119	41.75	PK	H	22.61	2.71	26.68	40.39	74.00	33.61
1119	23.32	AV	H	22.61	2.71	26.68	21.96	54.00	32.04
6958	43.52	PK	H	33.49	7.30	26.33	57.98	74.00	16.02
6958	24.74	AV	H	33.49	7.30	26.33	39.20	54.00	14.80
High Channel: 2480 MHz									
2480	96.28	PK	H	25.85	4.48	27.36	99.25	N/A	N/A
2480	56.12	AV	H	25.85	4.48	27.36	59.09	N/A	N/A
2480	95.85	PK	V	25.85	4.48	27.36	98.82	N/A	N/A
2480	55.84	AV	V	25.85	4.48	27.36	58.81	N/A	N/A
2483.5	52.57	PK	H	25.86	4.49	27.36	55.56	74.00	18.44
2483.5	27.96	AV	H	25.86	4.49	27.36	30.95	54.00	23.05
4960	43.17	PK	H	31.00	5.90	27.43	52.64	74.00	21.36
4960	28.55	AV	H	31.00	5.90	27.43	38.02	54.00	15.98
7440	34.25	PK	H	34.66	7.58	25.97	50.52	74.00	23.48
7440	20.31	AV	H	34.66	7.58	25.97	36.58	54.00	17.42
2588	41.85	PK	H	26.13	4.63	27.41	45.20	74.00	28.80
2588	24.36	AV	H	26.13	4.63	27.41	27.71	54.00	26.29
6955	42.31	PK	H	33.48	7.30	26.34	56.75	74.00	17.25
6955	24.87	AV	H	33.48	7.30	26.34	39.31	54.00	14.69

EDR Mode (8-DPSK):

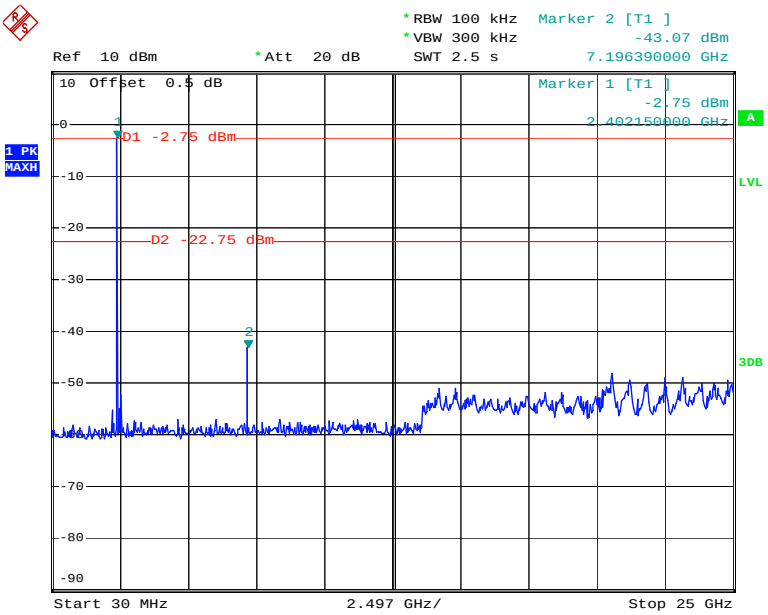
Frequency	Receiver		Rx Antenna		Cable loss (dB)	Amplifier Gain (dB)	Corrected Amplitude (dBμV/m)	FCC 15.247	
(MHz)	Reading (dBμV)	Detector (PK/QP/AV)	Polar (H/V)	Factor (dB)				Limit (dBμV/m)	Margin (dB)
Low Channel: 2402 MHz									
2402	97.23	PK	H	25.65	4.42	27.32	99.98	N/A	N/A
2402	59.88	AV	H	25.65	4.42	27.32	62.63	N/A	N/A
2402	95.66	PK	V	25.65	4.42	27.32	98.41	N/A	N/A
2402	59.14	AV	V	25.65	4.42	27.32	61.89	N/A	N/A
2390	43.64	PK	H	25.61	4.39	27.32	46.32	74.00	27.68
2390	27.96	AV	H	25.61	4.39	27.32	30.64	54.00	23.36
4804	44.01	PK	H	30.59	5.98	27.41	53.17	74.00	20.83
4804	26.11	AV	H	30.59	5.98	27.41	35.27	54.00	18.73
7206	34.12	PK	H	34.09	7.45	25.91	49.75	74.00	24.25
7206	19.85	AV	H	34.09	7.45	25.91	35.48	54.00	18.52
2586	42.63	PK	H	26.12	4.63	27.41	45.97	74.00	28.03
2586	24.21	AV	H	26.12	4.63	27.41	27.55	54.00	26.45
1120	41.69	PK	H	22.61	2.72	26.68	40.34	74.00	33.66
1120	24.22	AV	H	22.61	2.72	26.68	22.87	54.00	31.13
Middle Channel: 2441 MHz									
2441	97.89	PK	H	25.74	4.40	27.34	100.69	N/A	N/A
2441	60.23	AV	H	25.74	4.40	27.34	63.03	N/A	N/A
2441	96.03	PK	V	25.74	4.40	27.34	98.83	N/A	N/A
2441	59.22	AV	V	25.74	4.40	27.34	62.02	N/A	N/A
4880	43.87	PK	H	30.79	6.08	27.42	53.32	74.00	20.68
4880	25.91	AV	H	30.79	6.08	27.42	35.36	54.00	18.64
7320	33.68	PK	H	34.37	7.51	25.88	49.68	74.00	24.32
7320	19.57	AV	H	34.37	7.51	25.88	35.57	54.00	18.43
2584	42.74	PK	H	26.12	4.63	27.41	46.08	74.00	27.92
2584	25.01	AV	H	26.12	4.63	27.41	28.35	54.00	25.65
1122	41.33	PK	H	22.62	2.72	26.69	39.98	74.00	34.02
1122	24.72	AV	H	22.62	2.72	26.69	23.37	54.00	30.63
6966	42.11	PK	H	33.51	7.31	26.31	56.62	74.00	17.38
6966	23.92	AV	H	33.51	7.31	26.31	38.43	54.00	15.57
High Channel: 2480 MHz									
2480	96.79	PK	H	25.85	4.48	27.36	99.76	N/A	N/A
2480	58.63	AV	H	25.85	4.48	27.36	61.60	N/A	N/A
2480	95.33	PK	V	25.85	4.48	27.36	98.30	N/A	N/A
2480	57.25	AV	V	25.85	4.48	27.36	60.22	N/A	N/A
2483.5	51.74	PK	H	25.86	4.49	27.36	54.73	74.00	19.27
2483.5	28.13	AV	H	25.86	4.49	27.36	31.12	54.00	22.88
4960	44.36	PK	H	31.00	5.90	27.43	53.83	74.00	20.17
4960	28.72	AV	H	31.00	5.90	27.43	38.19	54.00	15.81
7440	34.66	PK	H	34.66	7.58	25.97	50.93	74.00	23.07
7440	20.31	AV	H	34.66	7.58	25.97	36.58	54.00	17.42
2584	42.37	PK	H	26.12	4.63	27.41	45.71	74.00	28.29
2584	24.56	AV	H	26.12	4.63	27.41	27.90	54.00	26.10
6955	41.88	PK	H	33.48	7.30	26.34	56.32	74.00	17.68
6955	24.62	AV	H	33.48	7.30	26.34	39.06	54.00	14.94

Note: For below 1GHz, all spurious emissions are 20 dB below the limit or are on the system noise floor level.

Conducted Spurious Emissions at Antenna Port

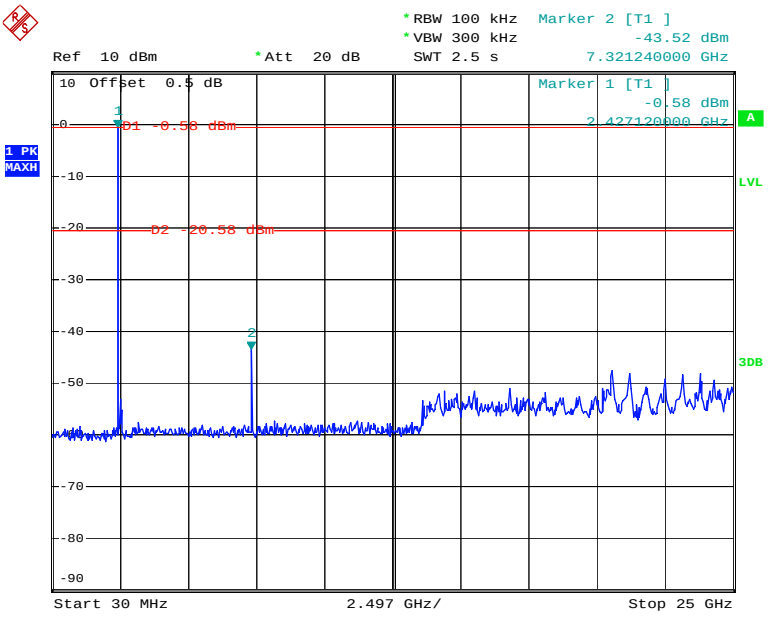
BDR Mode (GFSK):

Low Channel



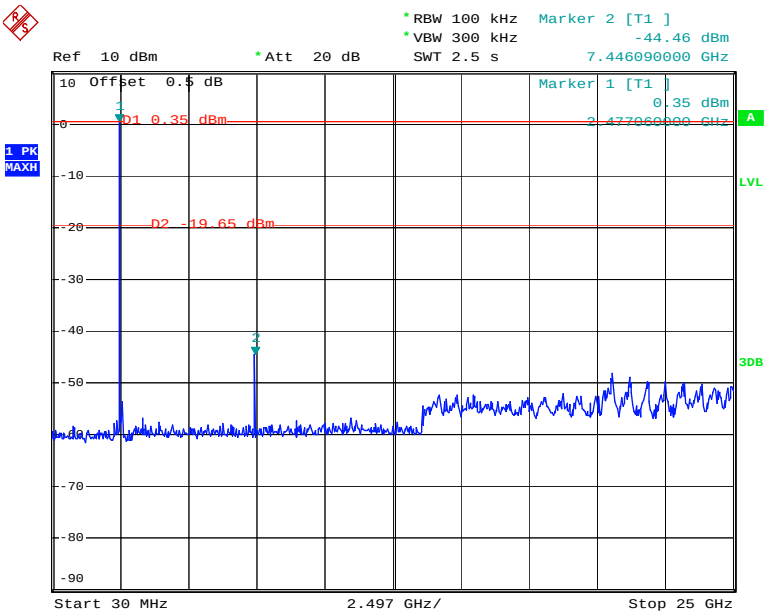
Date: 8.MAY.2014 15:20:15

Middle Channel



Date: 8.MAY.2014 15:33:58

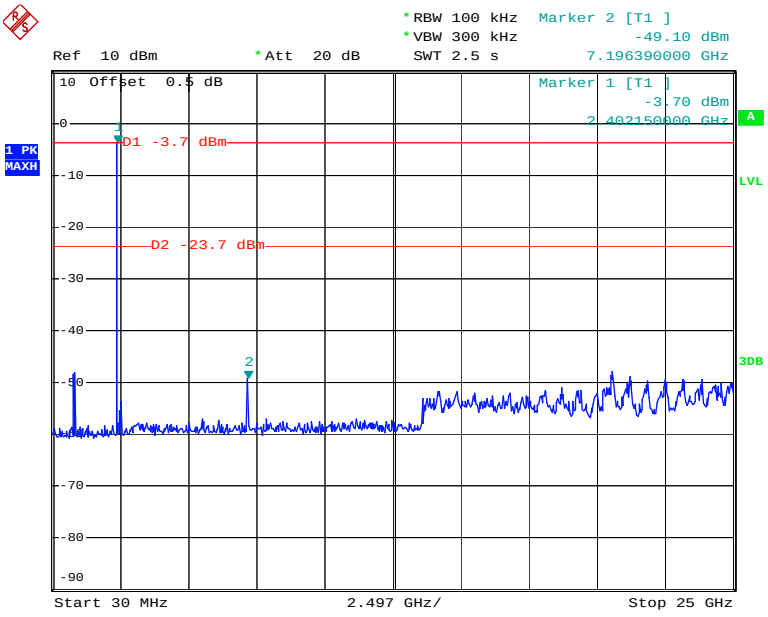
High Channel



Date: 8.MAY.2014 15:34:52

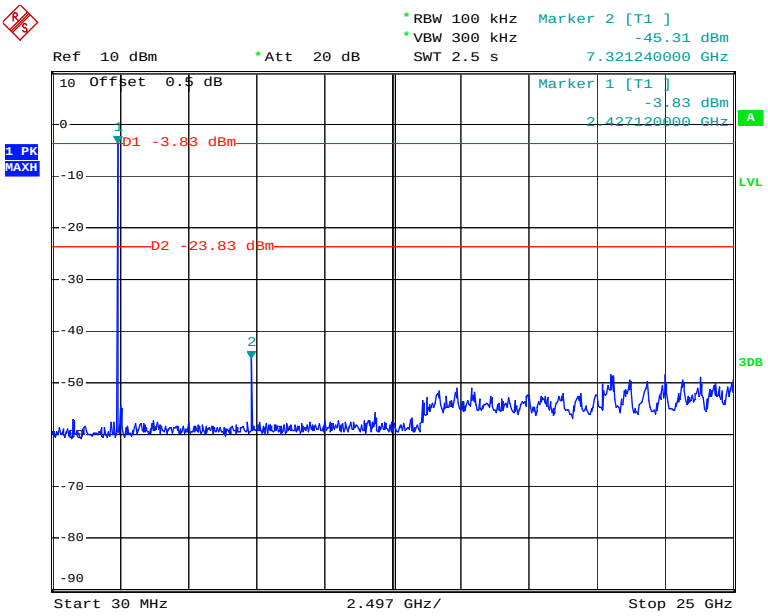
EDR Mode (π/4-DQPSK):

Low Channel



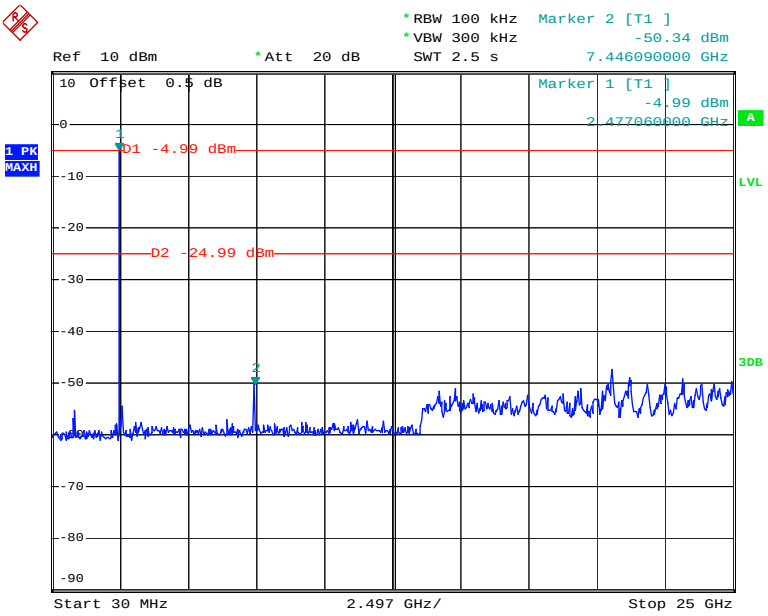
Date: 8.MAY.2014 15:22:50

Middle Channel



Date: 8.MAY.2014 15:25:00

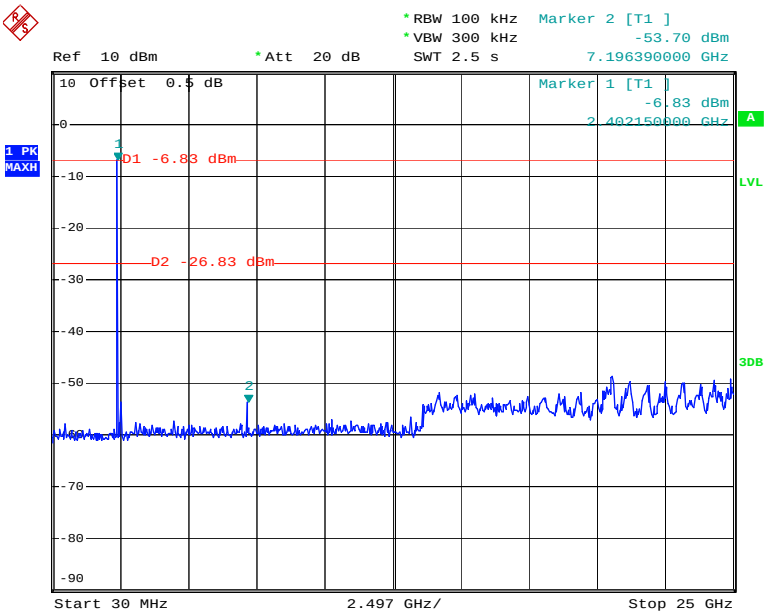
High Channel



Date: 8.MAY.2014 15:26:11

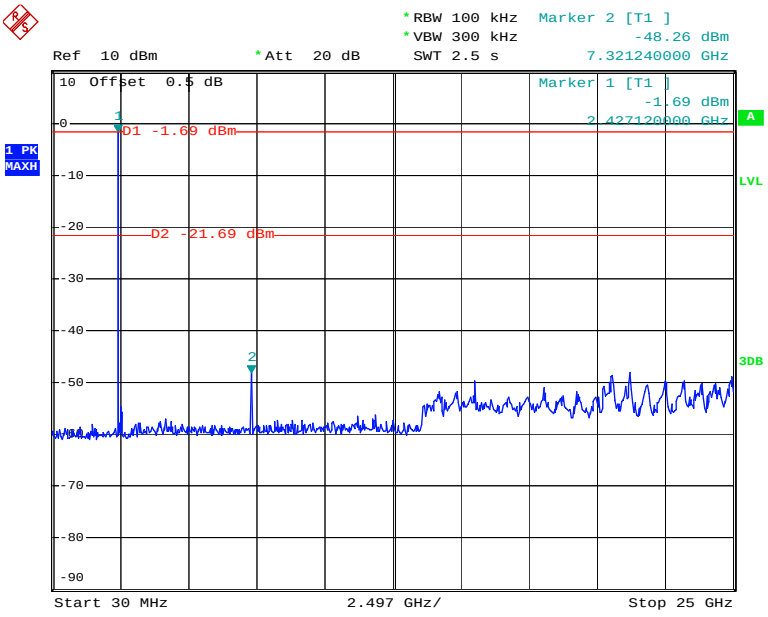
EDR Mode (8-DPSK):

Low Channel



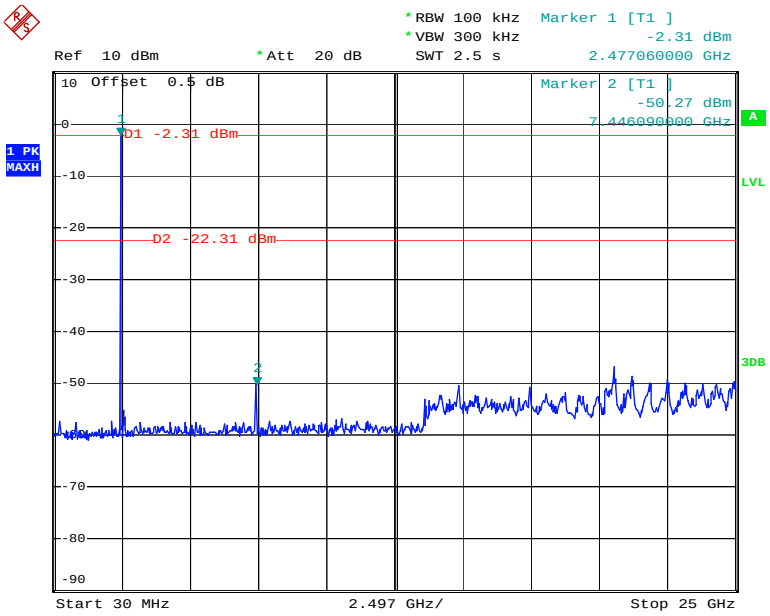
Date: 8.MAY.2014 15:32:21

Middle Channel



Date: 8.MAY.2014 15:30:34

High Channel



Date: 8.MAY.2014 15:28:21

FCC §15.247(a) (1) - CHANNEL SEPARATION TEST

Applicable Standard

Frequency hopping systems shall have hoping channel carrier frequencies separated by a minimum of 25 kHz or the 20dB bandwidth of the hopping channel, whichever is greater. Alternatively, frequency hopping systems operating in the 2400-2483.50 MHz band may have hopping channel carrier frequencies that are separated by 25 kHz or two-thirds of the 20dB bandwidth of the hopping channel, whichever is greater provided the systems operate with an output power no greater than 125 mW.

Test Equipment List and Details

Manufacturer	Description	Model	Serial Number	Calibration Date	Calibration Due Date
R&S	Spectrum Analyzer	FSEM	DE31388	2014-05-07	2015-05-06

*** Statement of Traceability:** Bay Area Compliance Laboratories Corp. (Dongguan) attests that all calibrations have been performed, traceable to National Primary Standards and International System of Units (SI).

Test Procedure

1. Set the EUT in transmitting mode, spectrum Bandwidth was set at 30 kHz, maxhold the channel.
2. Set the adjacent channel of the EUT maxhold another truce
3. Measure the channel separation.

Test Data

Environmental Conditions

Temperature:	25.1 °C
Relative Humidity:	64 %
ATM Pressure:	100.6 kPa

** The testing was performed by Allen Qiao on 2014-05-08.*

Test Result: Compliance.

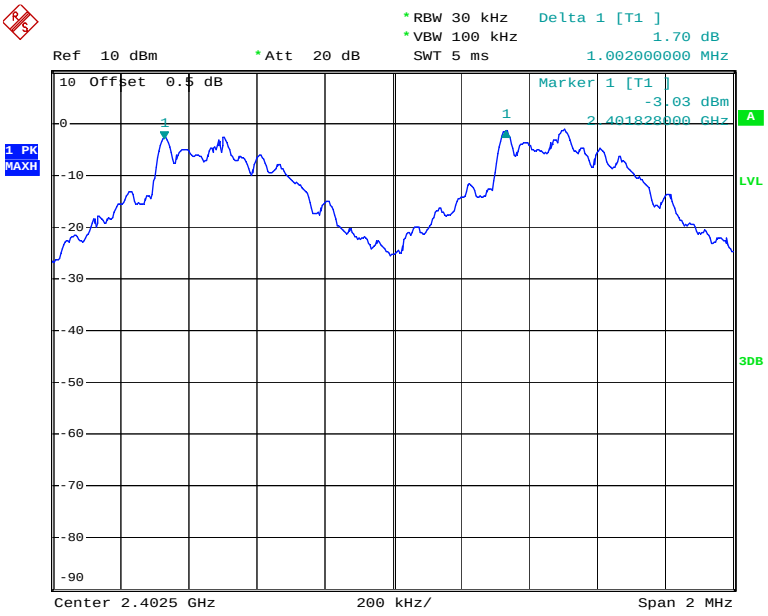
Please refer to following tables and plots

Test Mode: Transmitting

Mode	Channel	Frequency (MHz)	Channel Separation (MHz)	Limit (MHz)	Result
BDR Mode (GFSK)	Low	2402	1.002	0.59	Pass
	Adjacent	2403			
	Middle	2441	1.004	0.59	Pass
	Adjacent	2440			
	High	2480	1.000	0.60	Pass
	Adjacent	2479			
EDR Mode (π/4-DQPSK):	Low	2402	1.000	0.82	Pass
	Adjacent	2403			
	Middle	2441	1.002	0.82	Pass
	Adjacent	2440			
	High	2480	1.002	0.82	Pass
	Adjacent	2479			
EDR Mode (8-DPSK):	Low	2402	1.002	0.81	Pass
	Adjacent	2403			
	Middle	2441	1.002	0.81	Pass
	Adjacent	2440			
	High	2480	1.000	0.90	Pass
	Adjacent	2479			

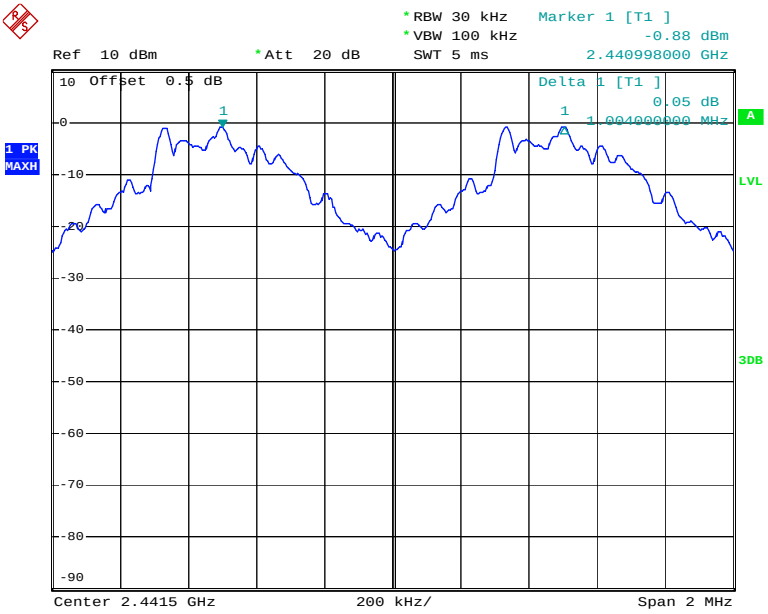
BDR Mode (GFSK):

Low Channel



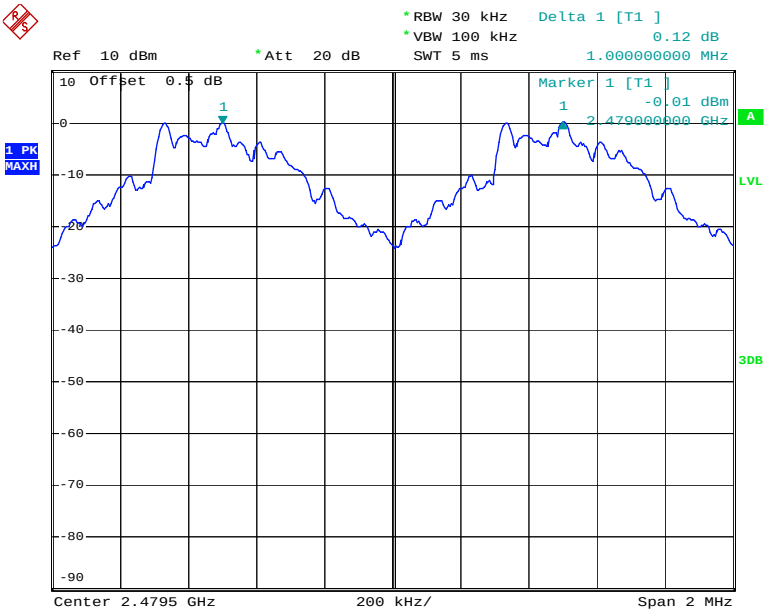
Date: 8.MAY.2014 13:59:31

Middle Channel



Date: 8.MAY.2014 14:00:57

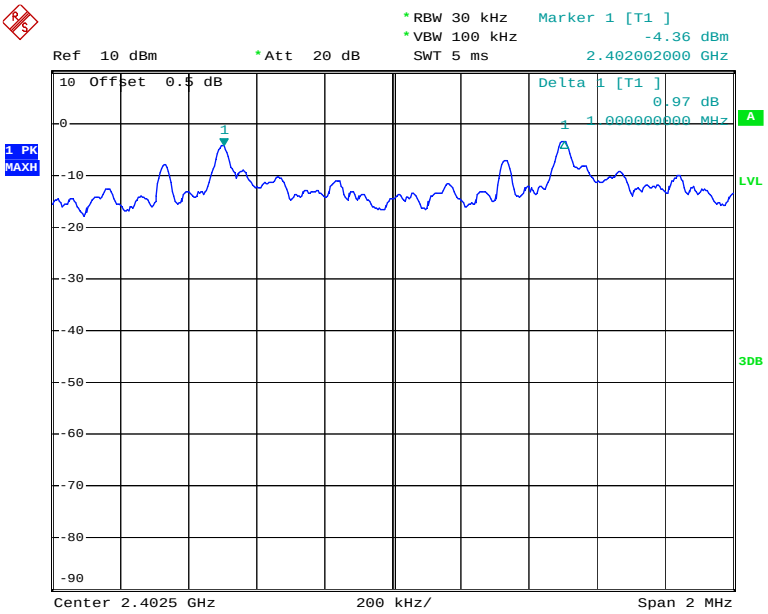
High Channel



Date: 8.MAY.2014 14:01:58

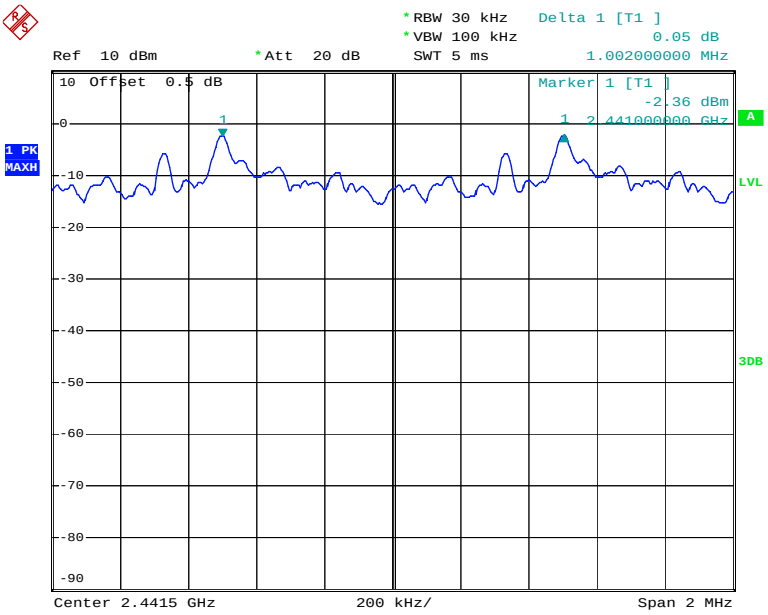
EDR Mode (π/4-DQPSK):

Low Channel



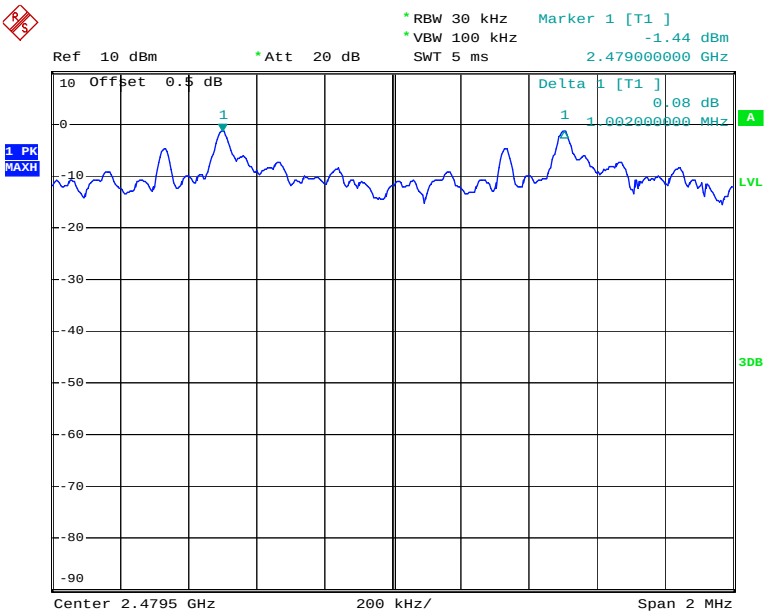
Date: 8.MAY.2014 14:05:37

Middle Channel



Date: 8.MAY.2014 14:04:45

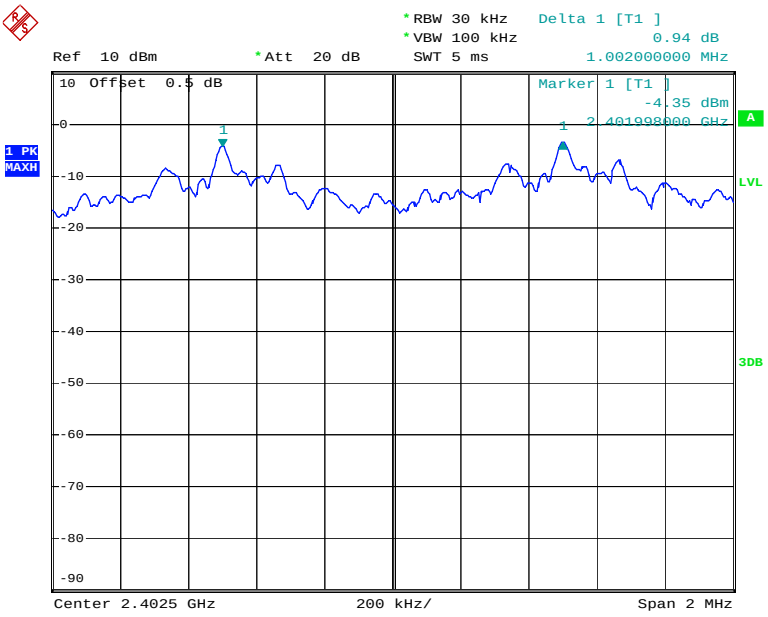
High Channel



Date: 8.MAY.2014 14:03:22

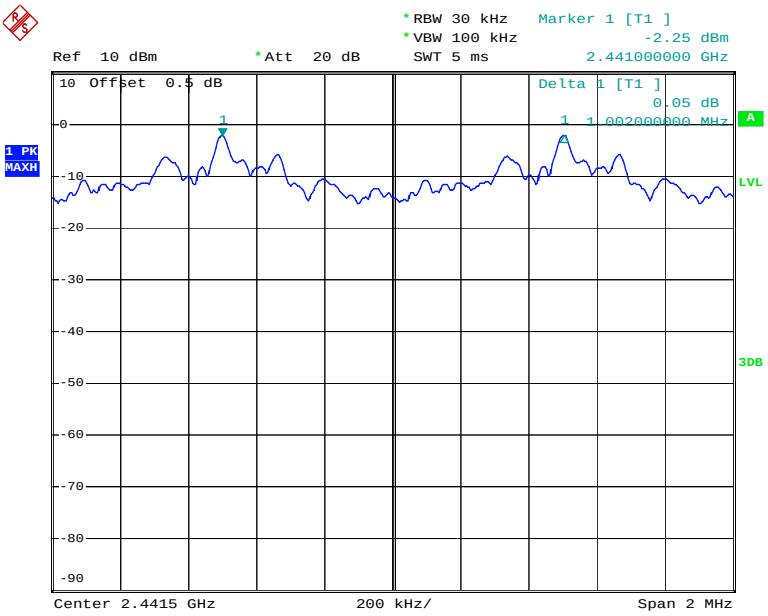
EDR Mode (8-DPSK):

Low Channel



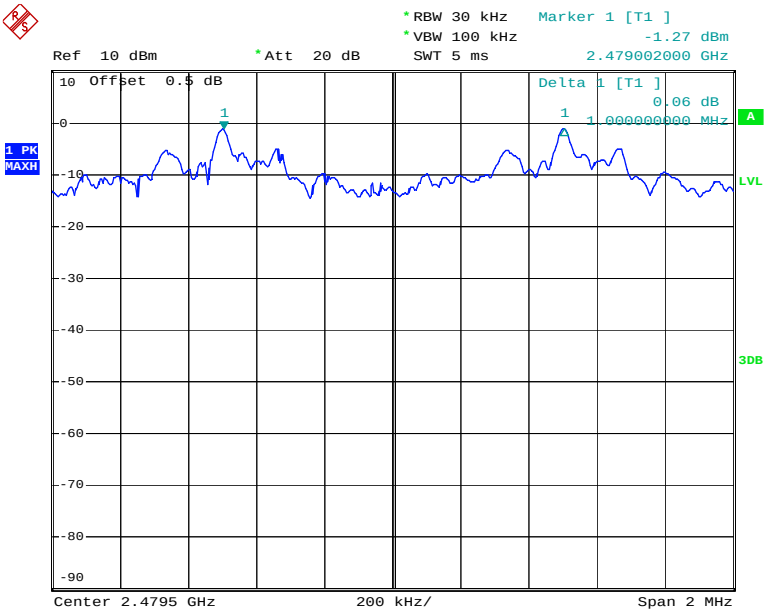
Date: 8.MAY.2014 14:07:15

Middle Channel



Date: 8.MAY.2014 14:08:42

High Channel



Date: 8.MAY.2014 14:10:43

FCC §15.247(a) (1) – 20 dB BANDWIDTH TESTING

Applicable Standard

Alternatively, frequency hopping systems operating in the 2400–2483.5 MHz band may have hopping channel carrier frequencies that are separated by 25 kHz or two-thirds of the 20 dB bandwidth of the hopping channel, whichever is greater, provided the systems operate with an output power no greater than 125 mW.

Test Procedure

1. Check the calibration of the measuring instrument using either an internal calibrator or a known signal from an external generator.
2. Position the EUT on the test table without connection to measurement instrument. Turn on the EUT. Then set it to any one convenient frequency within its operating range. Set a reference level on the measuring instrument equal to the highest peak value.
3. Measure the frequency difference of two frequencies that were attenuated 20 dB from the reference level. Record the frequency difference as the emission bandwidth.
4. Repeat above procedures until all frequencies measured were complete.

Test Equipment List and Details

Manufacturer	Description	Model	Serial Number	Calibration Date	Calibration Due Date
R&S	Spectrum Analyzer	FSEM	DE31388	2014-05-07	2015-05-06

*** Statement of Traceability:** Bay Area Compliance Laboratories Corp. (Dongguan) attests that all calibrations have been performed, traceable to National Primary Standards and International System of Units (SI).

Test Data

Environmental Conditions

Temperature:	25.1 °C
Relative Humidity:	64 %
ATM Pressure:	100.6 kPa

** The testing was performed by Allen Qiao on 2014-05-08.*

Test Result: Compliance.

Please refer to following tables and plots

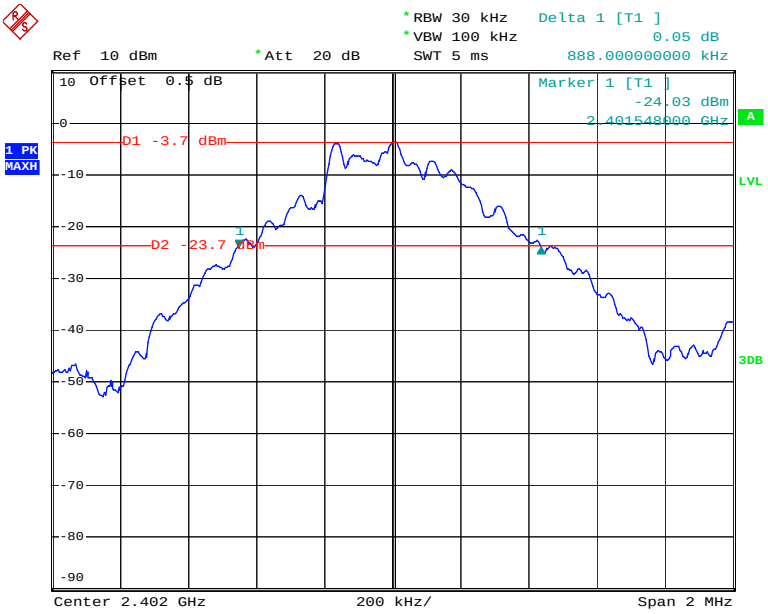
Test Mode: Transmitting

Mode	Channel	Frequency (MHz)	20 dB Bandwidth (MHz)
BDR Mode (GFSK)	Low	2402	0.888
	Middle	2441	0.892
	High	2480	0.898
EDR Mode (π/4-DQPSK):	Low	2402	1.224
	Middle	2441	1.224
	High	2480	1.230
EDR Mode (8-DPSK):	Low	2402	1.212
	Middle	2441	1.218
	High	2480	1.347

Please refer to the following plots.

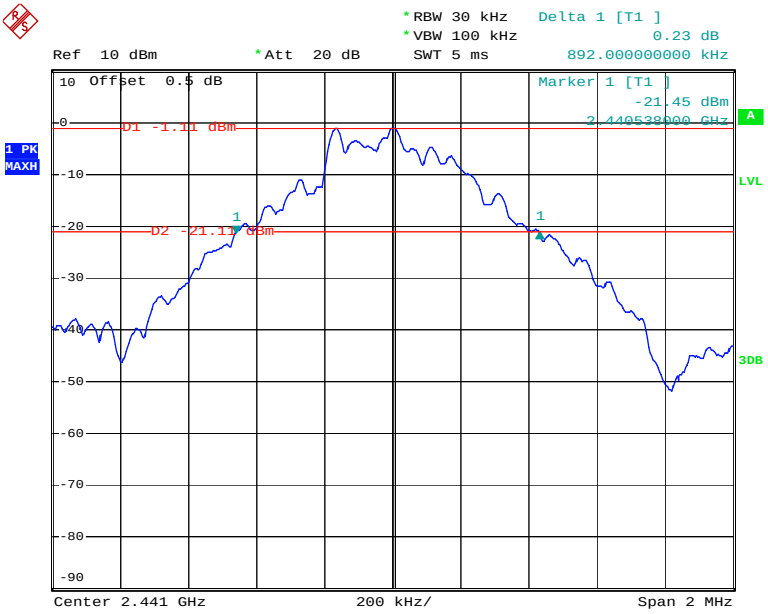
BDR Mode (GFSK):

Low Channel



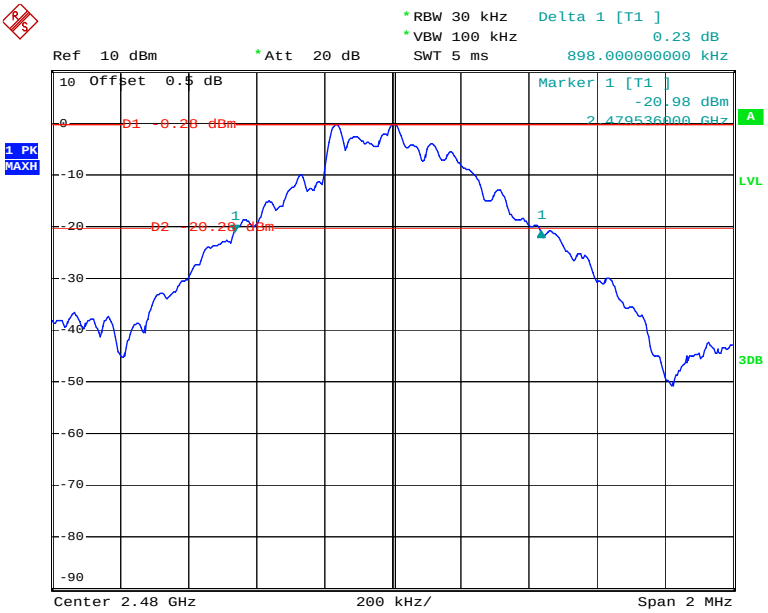
Date: 8.MAY.2014 10:55:07

Middle Channel



Date: 8.MAY.2014 10:53:18

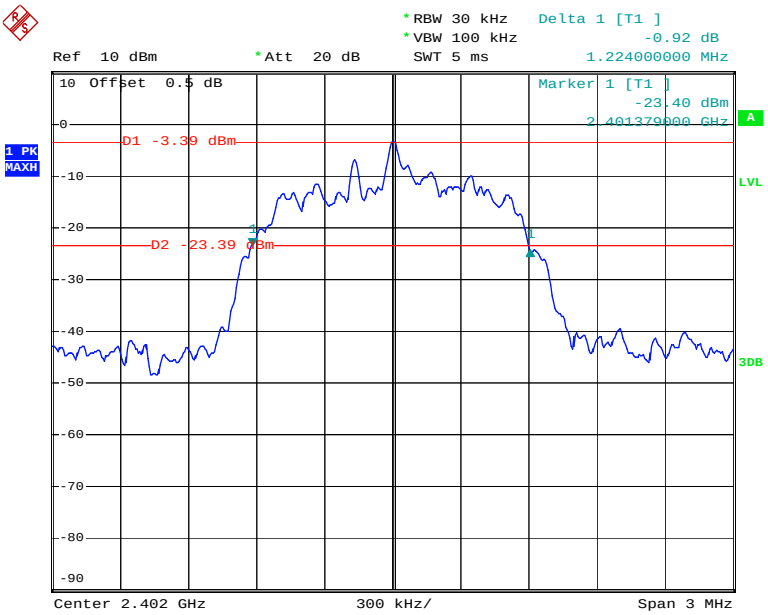
High Channel



Date: 8.MAY.2014 10:56:40

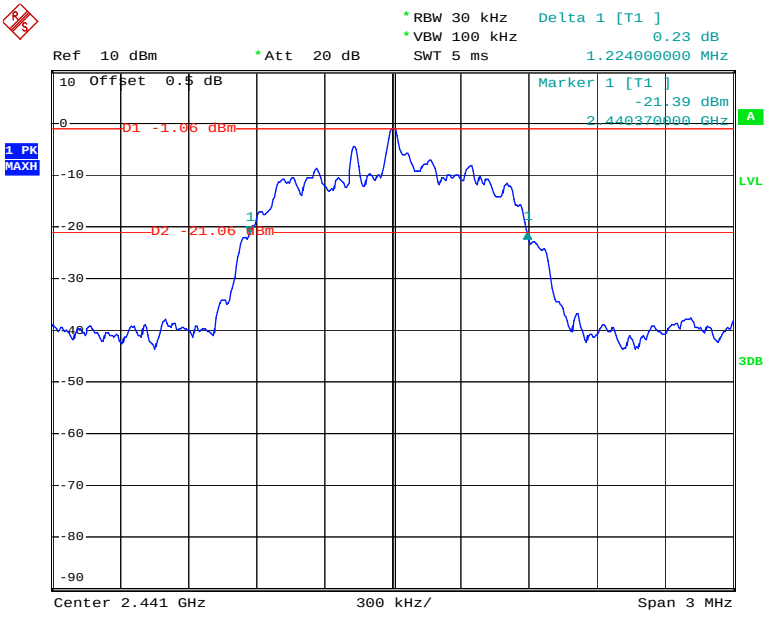
EDR Mode (π/4-DQPSK):

Low Channel



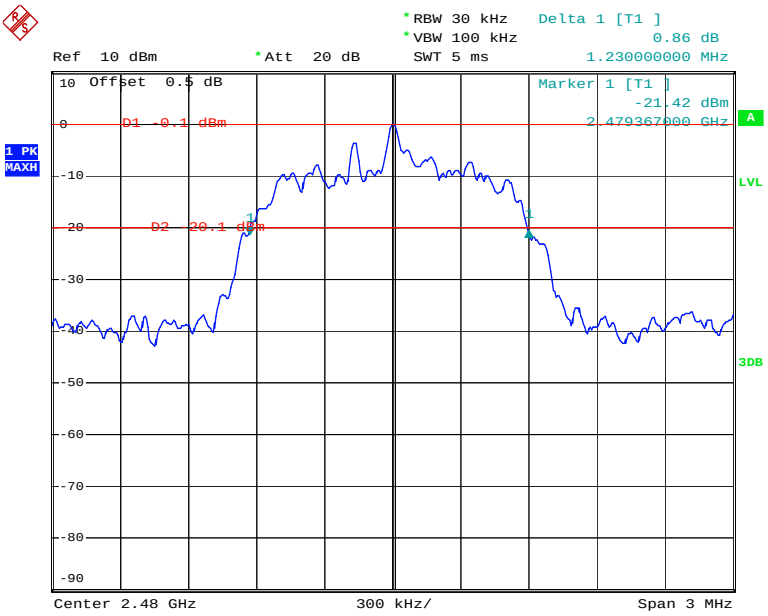
Date: 8.MAY.2014 11:01:42

Middle Channel



Date: 8.MAY.2014 11:00:15

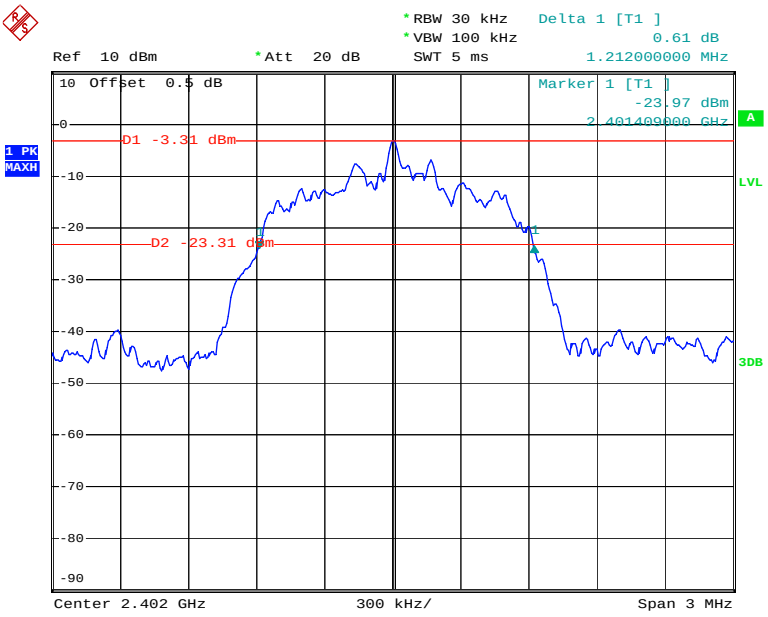
High Channel



Date: 8.MAY.2014 10:59:04

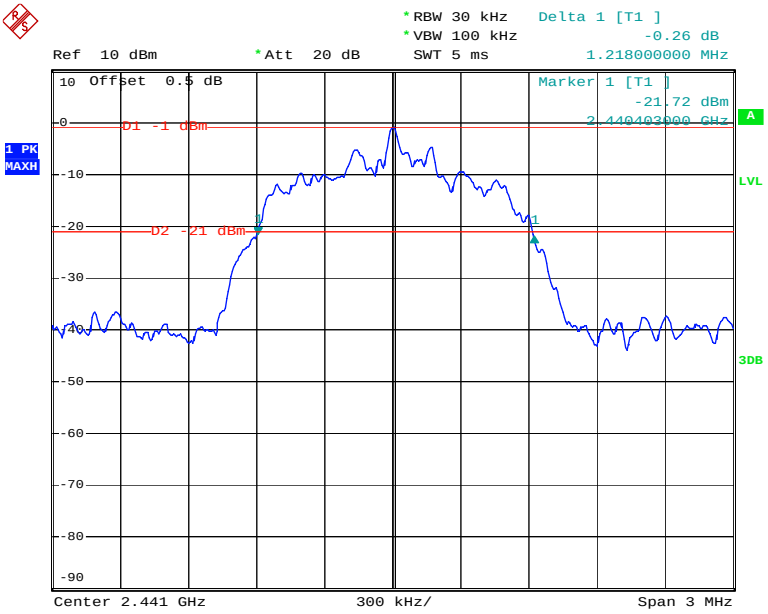
EDR Mode (8-DPSK):

Low Channel



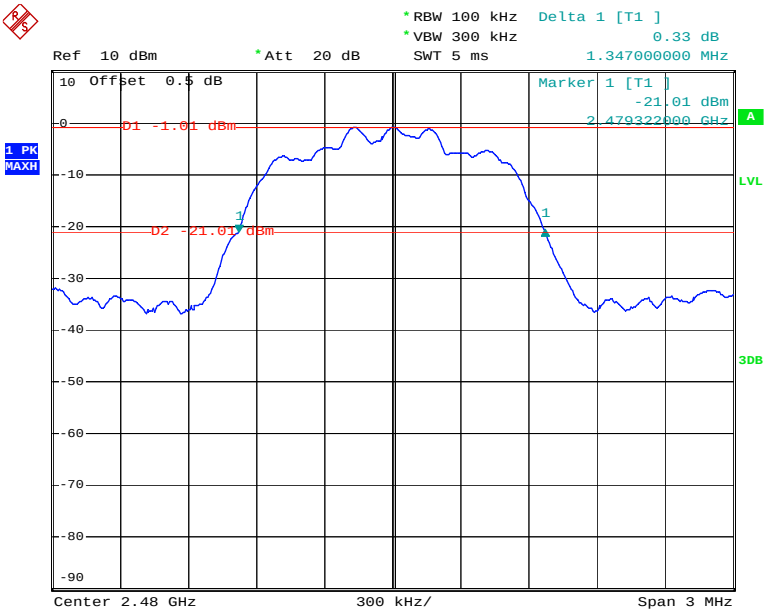
Date: 8.MAY.2014 11:02:59

Middle Channel



Date: 8.MAY.2014 11:04:22

High Channel



Date: 8.MAY.2014 16:02:20

FCC §15.247(a) (1) (iii) - QUANTITY OF HOPPING CHANNEL TEST

Applicable Standard

Frequency hopping systems in the 2400–2483.5 MHz band shall use at least 15 channels. The average time of occupancy on any channel shall not be greater than 0.4 seconds within a period of 0.4 seconds multiplied by the number of hopping channels employed. Frequency hopping systems may avoid or suppress transmissions on a particular hopping frequency provided that a minimum of 15 channels are used.

Test Procedure

1. Check the calibration of the measuring instrument (SA) using either an internal calibrator or a known signal from an external generator.
2. Set the EUT in hopping mode from first channel to last.
3. By using the Max-Hold function record the Quantity of the channel.

Test Equipment List and Details

Manufacturer	Description	Model	Serial Number	Calibration Date	Calibration Due Date
R&S	Spectrum Analyzer	FSEM	DE31388	2014-05-07	2015-05-06

*** Statement of Traceability:** Bay Area Compliance Laboratories Corp. (Dongguan) attests that all calibrations have been performed, traceable to National Primary Standards and International System of Units (SI).

Test Data

Environmental Conditions

Temperature:	25.1 °C
Relative Humidity:	64 %
ATM Pressure:	100.6 kPa

** The testing was performed by Allen Qiao on 2014-05-08.*

Test Result: Compliance.

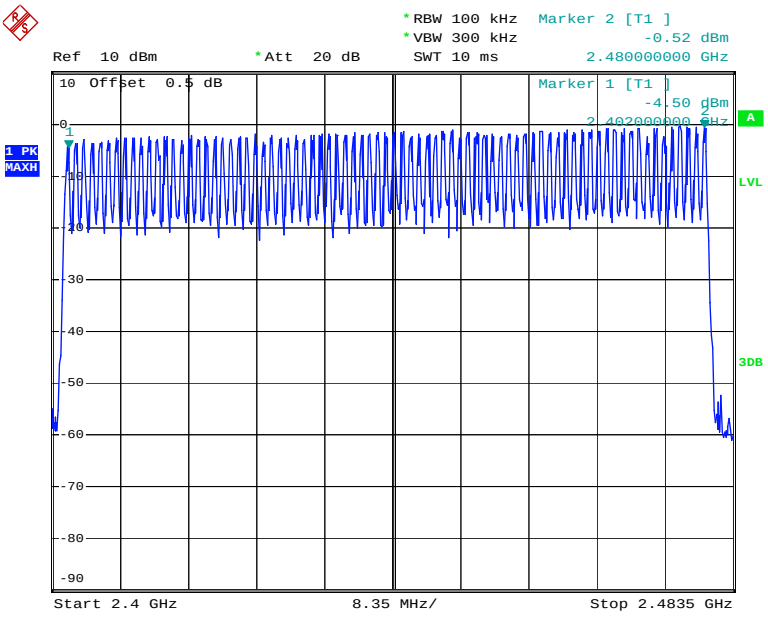
Please refer to following tables and plots

Test Mode: Transmitting

BDR Mode (GFSK):

Frequency Range (MHz)	Number of Hopping Channel	Limit
2400-2483.5	79	⩾15

Number of Hopping Channels

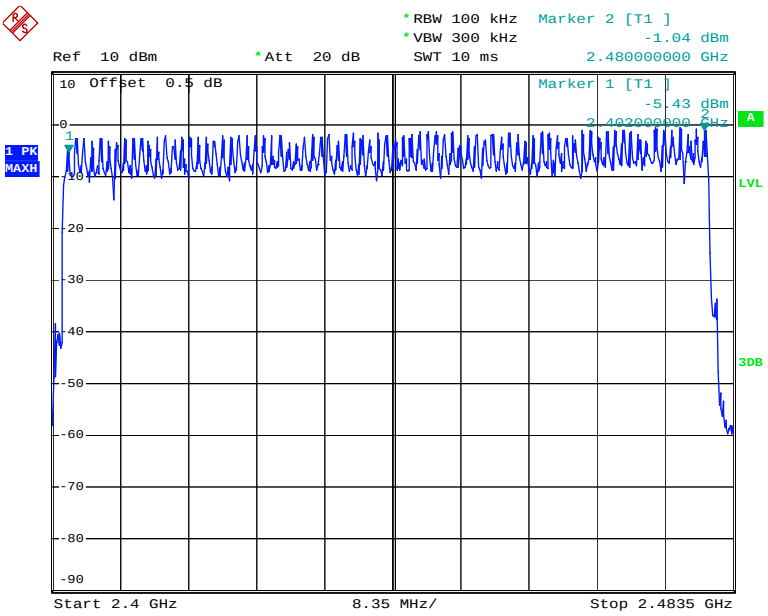


Date: 8.MAY.2014 14:21:12

EDR Mode (π/4-DQPSK):

Frequency Range (MHz)	Number of Hopping Channel	Limit
2400-2483.5	79	≥15

Number of Hopping Channels

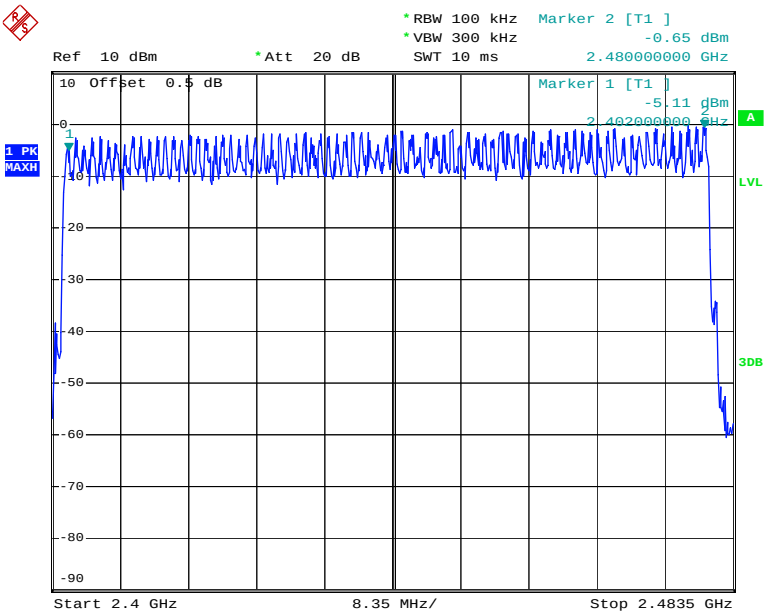


Date: 8.MAY.2014 14:19:07

EDR Mode (8-DPSK):

Frequency Range (MHz)	Number of Hopping Channel	Limit
2400-2483.5	79	≥15

Number of Hopping Channels



Date: 8.MAY.2014 14:16:07

FCC §15.247(a) (1) (iii) - TIME OF OCCUPANCY (DWELL TIME)

Applicable Standard

Frequency hopping systems in the 2400-2483.5 MHz shall use at least 15 channels. The average time of occupancy on any channel shall not be greater than 0.4 seconds within a period of 0.4 seconds multiplied by the number of hopping channels employed. Frequency hopping systems may avoid or suppress transmissions on a particular hopping frequency provided that a minimum of 15 channels are used.

Test Procedure

The EUT was worked in channel hopping; Spectrum SPAN was set as 0. Sweep was set as 0.4 * channel no. (s), the quantity of pulse was get from single sweep. In addition, the time of single pulses was tested.

Dwell Time= time slot length * hope rate/ number of hopping channels * 31.6s
Hop rate=1600/s

Test Equipment List and Details

Manufacturer	Description	Model	Serial Number	Calibration Date	Calibration Due Date
R&S	Spectrum Analyzer	FSEM	DE31388	2014-05-07	2015-05-06

*** Statement of Traceability:** Bay Area Compliance Laboratories Corp. (Dongguan) attests that all calibrations have been performed, traceable to National Primary Standards and International System of Units (SI).

Test Data

Environmental Conditions

Temperature:	25.1 °C
Relative Humidity:	64 %
ATM Pressure:	100.6 kPa

** The testing was performed by Allen Qiao on 2014-05-08.*

Test Result: Compliance.

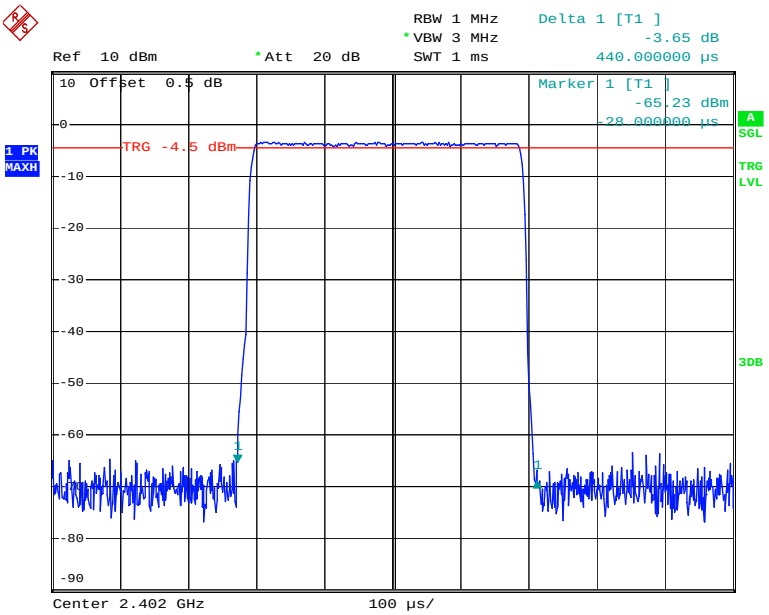
Please refer to following tables and plots

Test Mode: Transmitting

BDR Mode (GFSK):

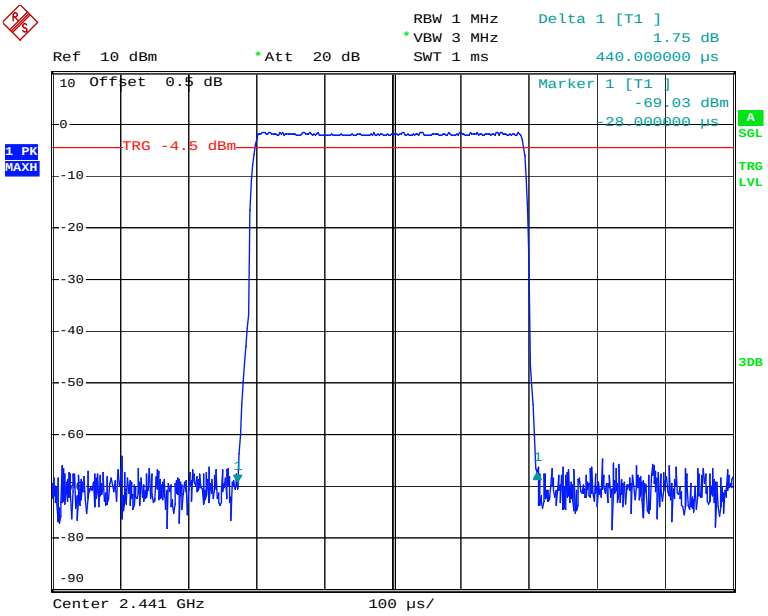
Mode	Channel	Pulse Width (ms)	Dwell Time (s)	Limit (s)	Result
DH1	Low	0.440	0.141	0.4	Pass
	Middle	0.440	0.141	0.4	Pass
	High	0.440	0.141	0.4	Pass
	Note: Dwell time=Pulse time (ms) × (1600/2/79) ×31.6 s				
DH3	Low	1.708	0.273	0.4	Pass
	Middle	1.708	0.273	0.4	Pass
	High	1.708	0.273	0.4	Pass
	Note: Dwell time=Pulse time (ms) × (1600/4/79) ×31.6 s				
DH5	Low	2.968	0.317	0.4	Pass
	Middle	2.968	0.317	0.4	Pass
	High	2.968	0.317	0.4	Pass
	Note: Dwell time=Pulse time (ms) × (1600/6/79) ×31.6 s				

DH1: Low Channel



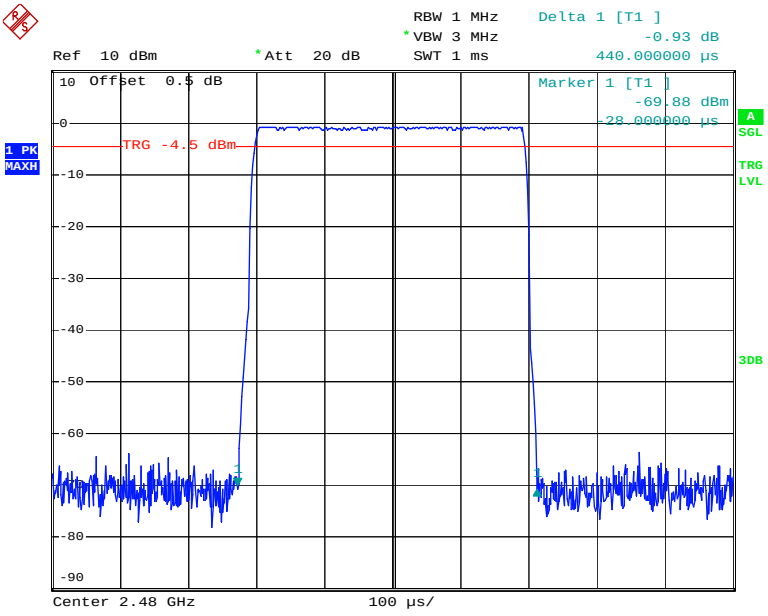
Date: 8.MAY.2014 14:55:23

DH1: Middle Channel



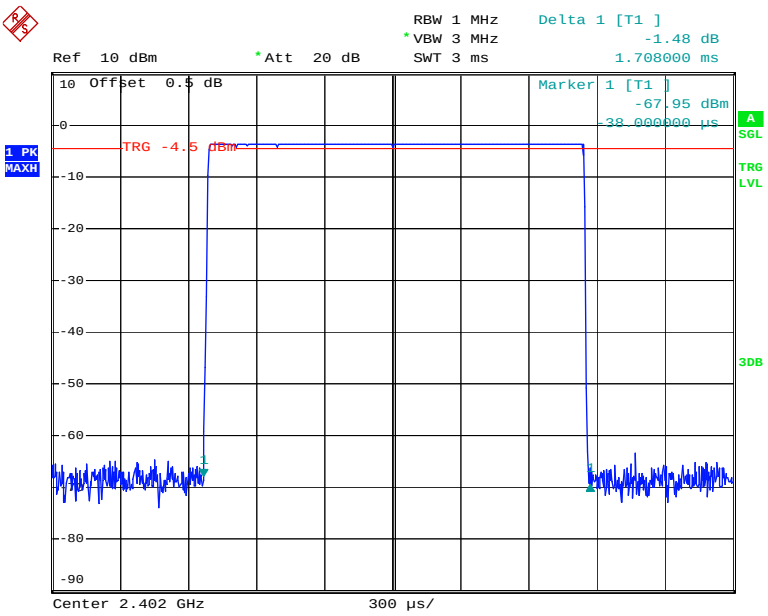
Date: 8.MAY.2014 14:55:12

DH1: High Channel



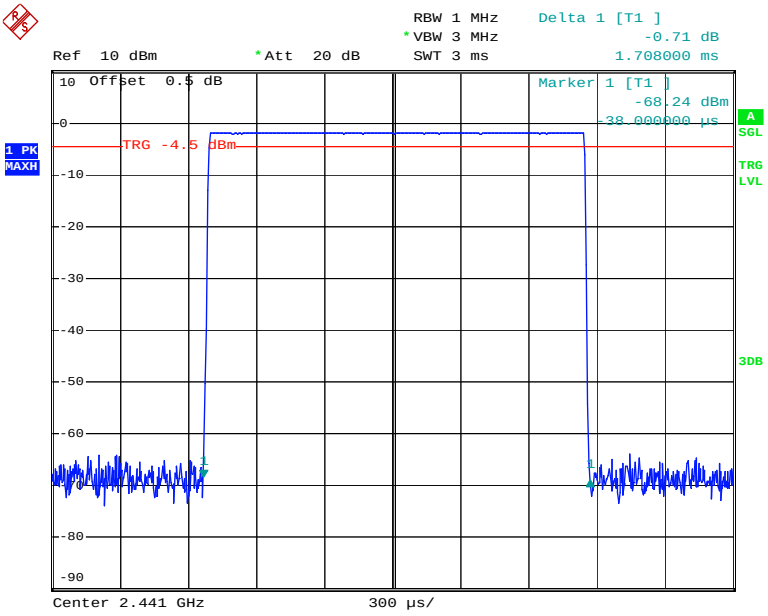
Date: 8.MAY.2014 14:55:02

DH3: Low Channel



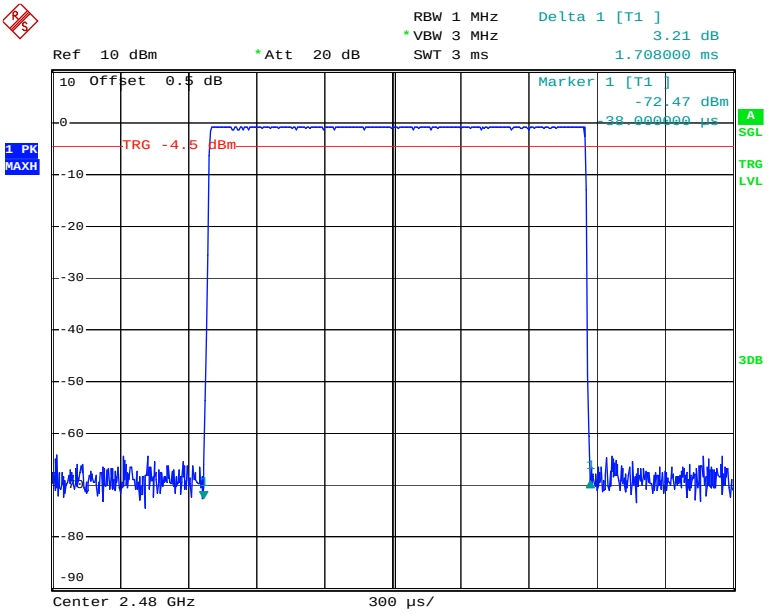
Date: 8.MAY.2014 14:46:48

DH3: Middle Channel



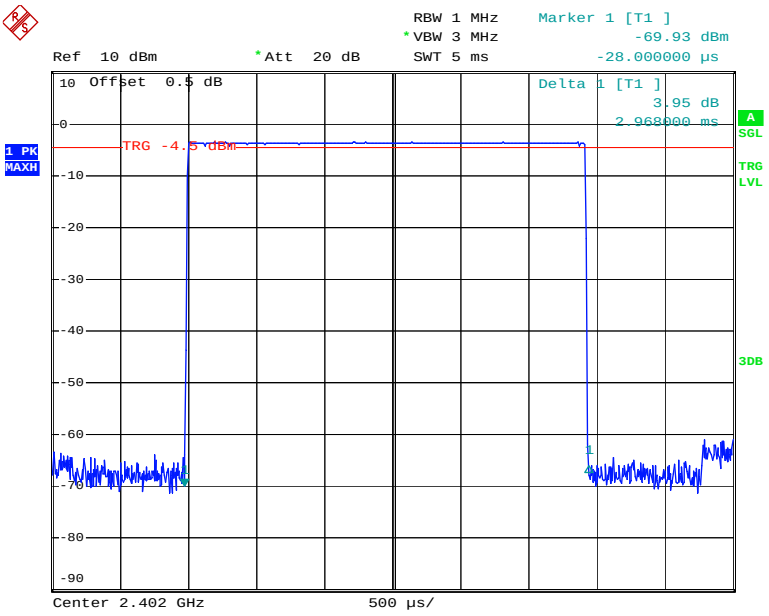
Date: 8.MAY.2014 14:46:25

DH3: High Channel



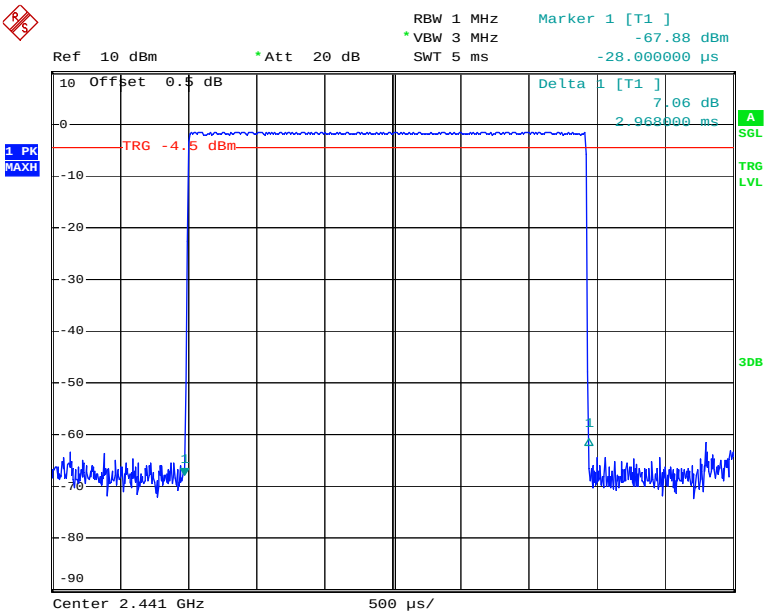
Date: 8.MAY.2014 14:46:13

DH5: Low Channel



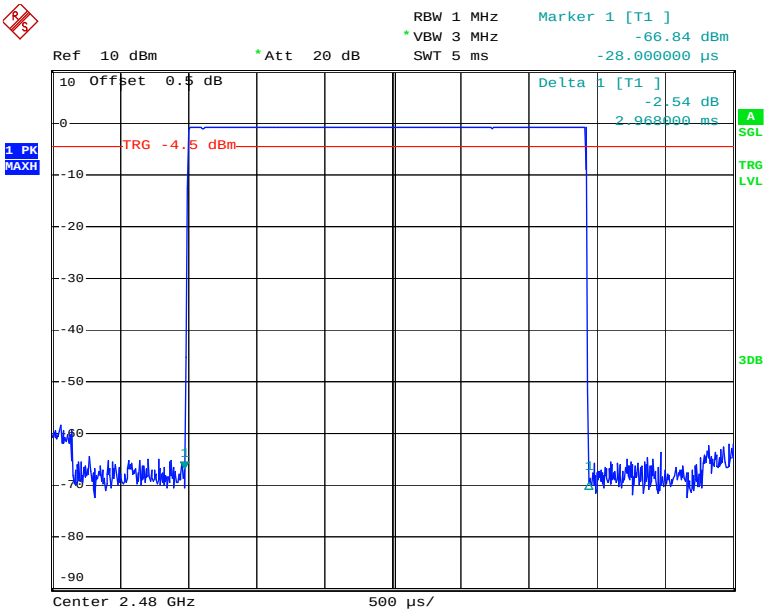
Date: 8.MAY.2014 14:48:42

DH5: Middle Channel



Date: 8.MAY.2014 14:48:54

DH5: High Channel

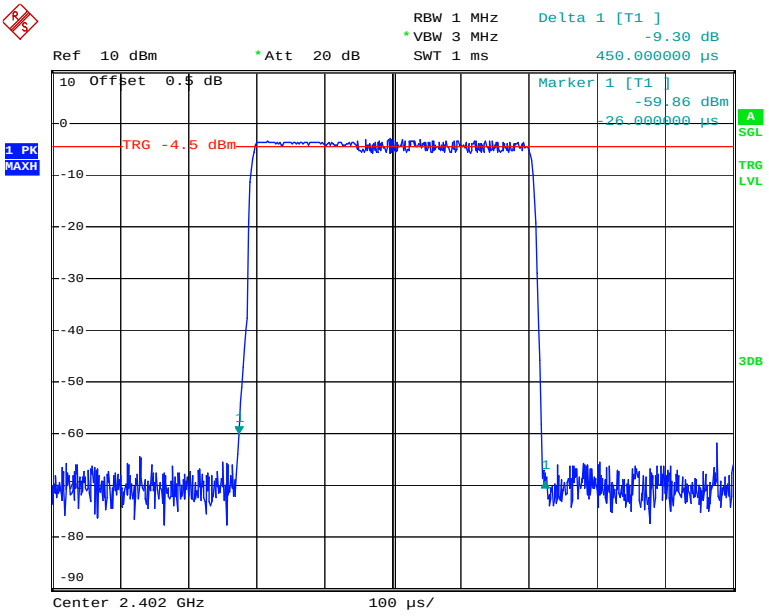


Date: 8.MAY.2014 14:49:09

EDR Mode (π/4-DQPSK):

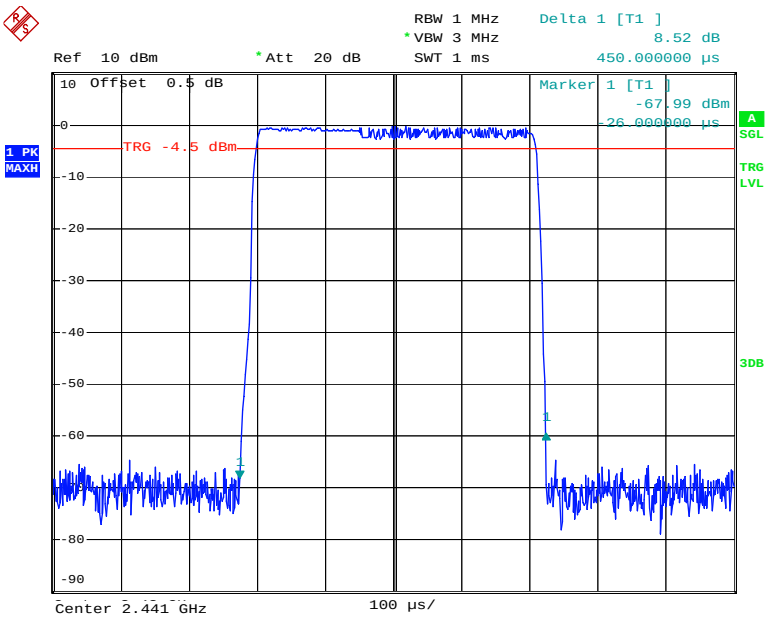
Mode	Channel	Pulse Width (ms)	Dwell Time (s)	Limit (s)	Result
DH1	Low	0.450	0.144	0.4	Pass
	Middle	0.450	0.144	0.4	Pass
	High	0.450	0.144	0.4	Pass
	Note: Dwell time=Pulse time (ms) × (1600/2/79) ×31.6 s				
DH3	Low	1.714	0.274	0.4	Pass
	Middle	1.714	0.274	0.4	Pass
	High	1.714	0.274	0.4	Pass
	Note: Dwell time=Pulse time (ms) × (1600/4/79) ×31.6 s				
DH5	Low	2.978	0.318	0.4	Pass
	Middle	2.978	0.318	0.4	Pass
	High	2.978	0.318	0.4	Pass
	Note: Dwell time=Pulse time (ms) × (1600/6/79) ×31.6 s				

DH1: Low Channel



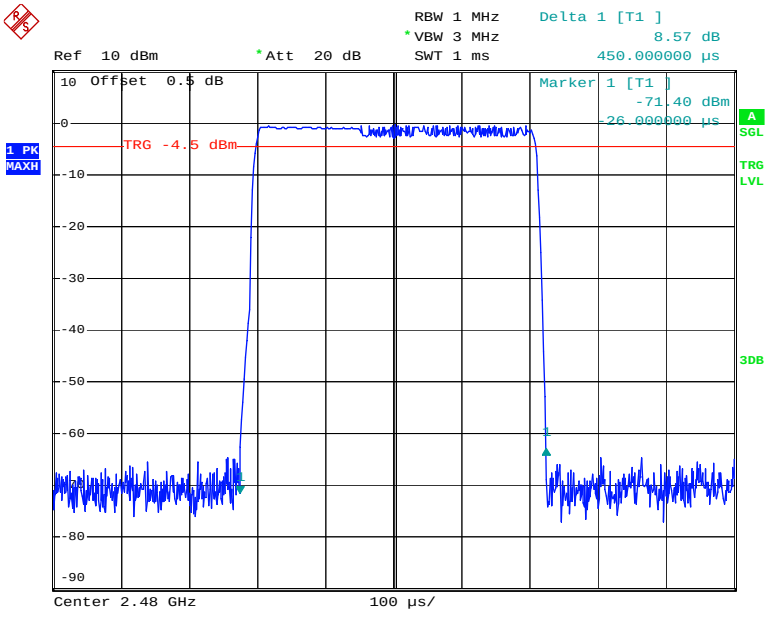
Date: 8.MAY.2014 14:36:01

DH1: Middle Channel



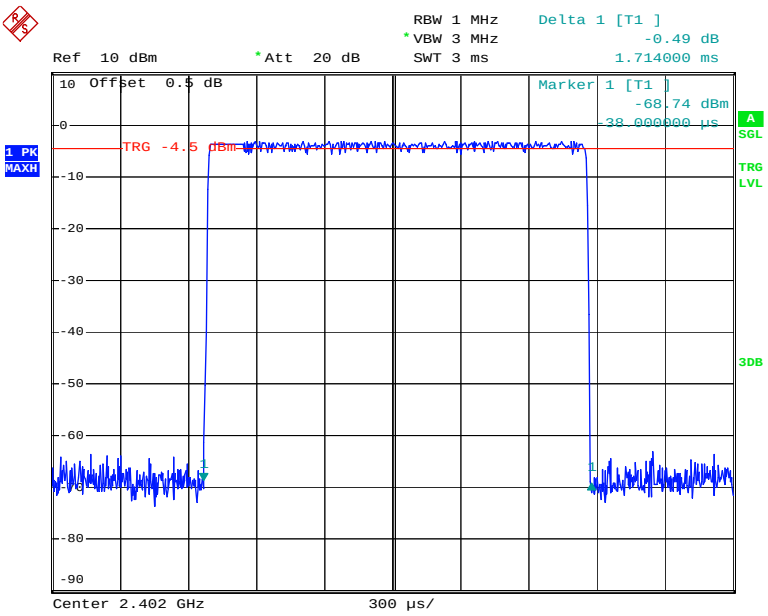
Date: 8.MAY.2014 14:35:50

DH1: High Channel



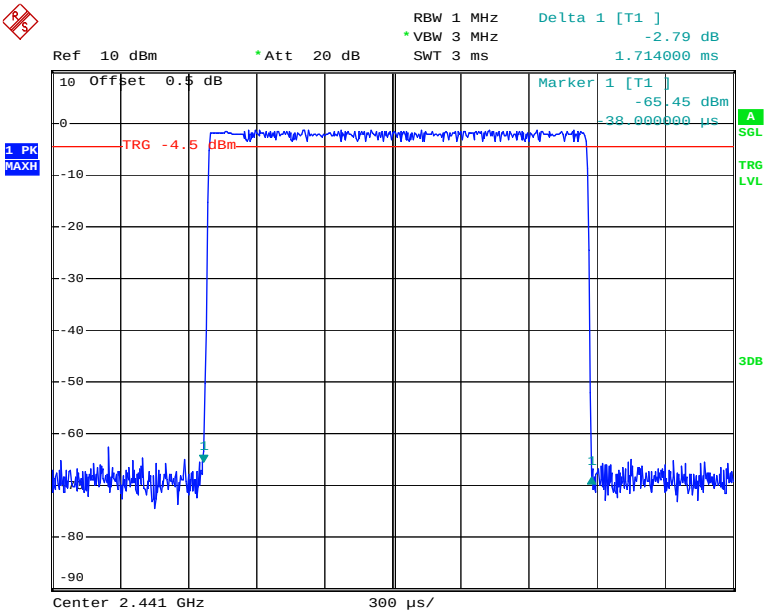
Date: 8.MAY.2014 14:35:35

DH3: Low Channel



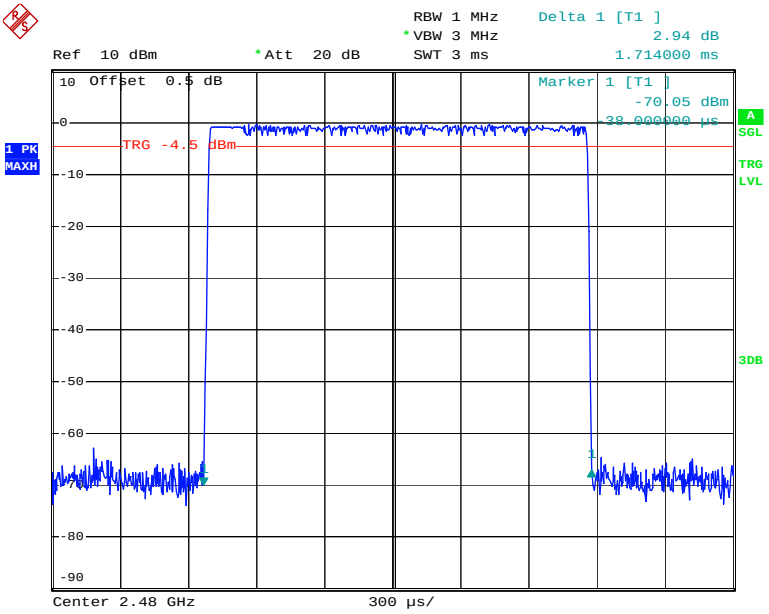
Date: 8.MAY.2014 14:43:41

DH3: Middle Channel



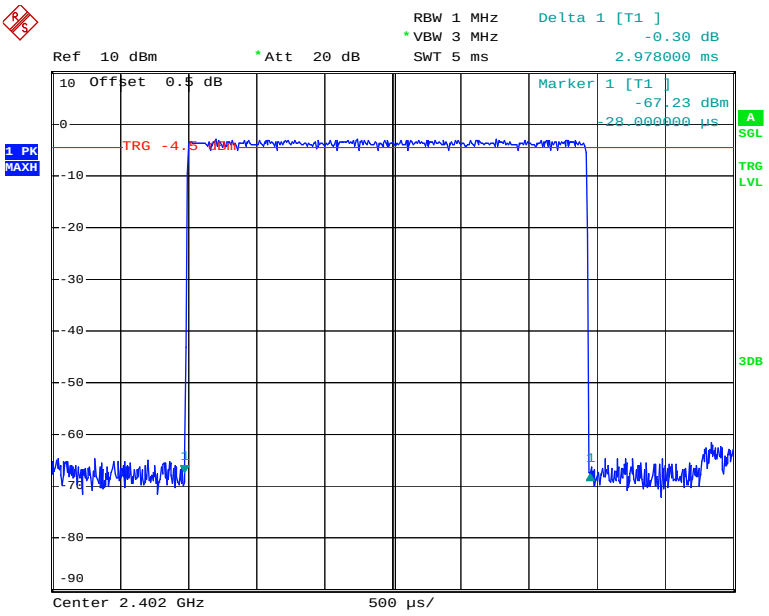
Date: 8.MAY.2014 14:43:52

DH3: High Channel



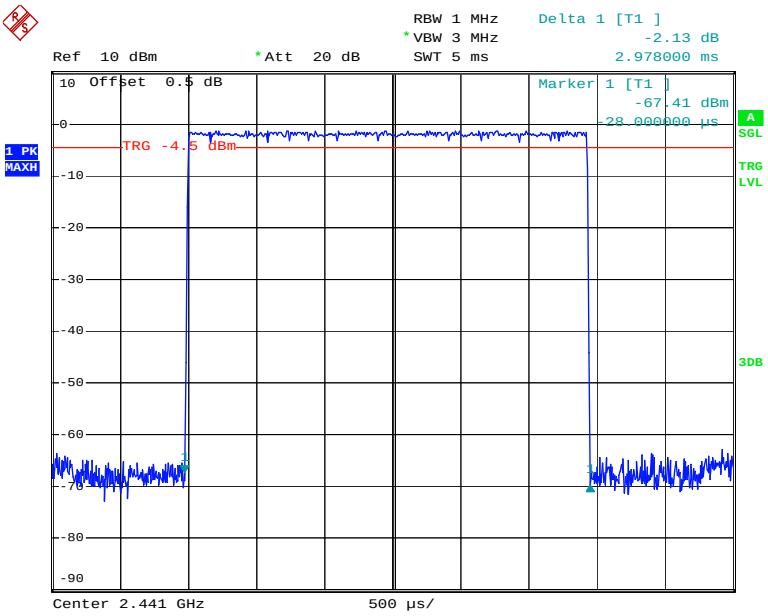
Date: 8.MAY.2014 14:44:07

DH5: Low Channel



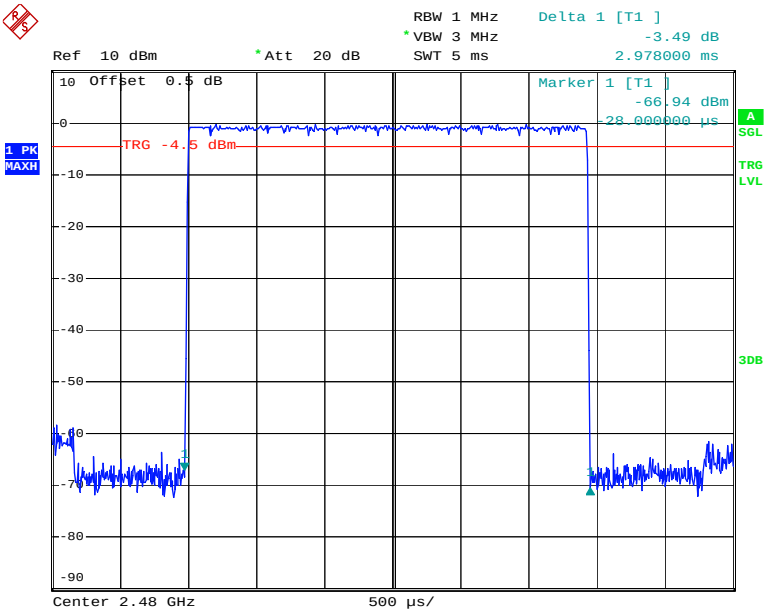
Date: 8.MAY.2014 14:51:26

DH5: Middle Channel



Date: 8.MAY.2014 14:51:12

DH5: High Channel

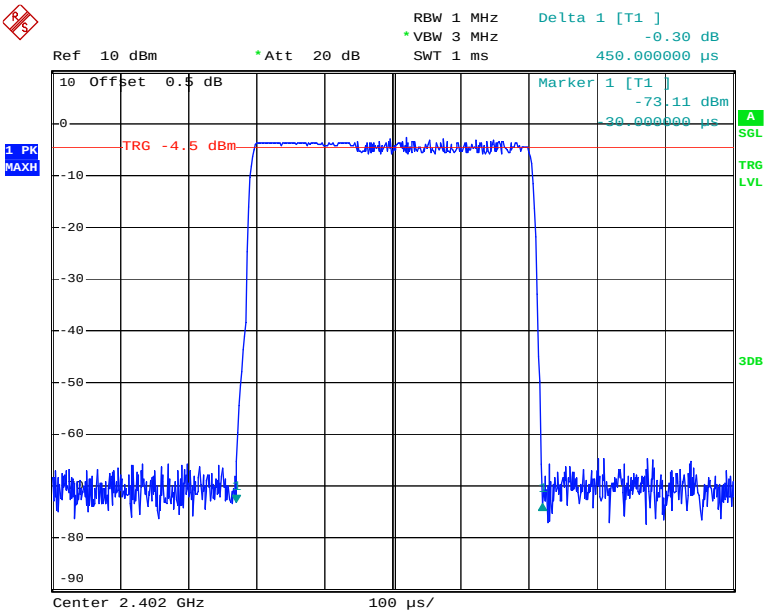


Date: 8.MAY.2014 14:50:29

EDR Mode (8-DPSK):

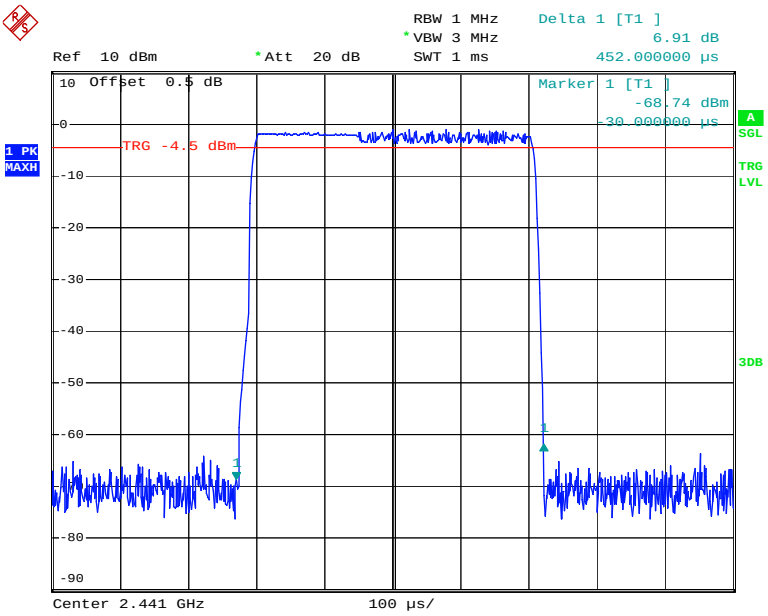
Mode	Channel	Pulse Width (ms)	Dwell Time (s)	Limit (s)	Result
DH1	Low	0.450	0.144	0.4	Pass
	Middle	0.452	0.145	0.4	Pass
	High	0.450	0.144	0.4	Pass
	Note: Dwell time=Pulse time (ms) × (1600/2/79) ×31.6 s				
DH3	Low	1.708	0.273	0.4	Pass
	Middle	1.708	0.273	0.4	Pass
	High	1.708	0.273	0.4	Pass
	Note: Dwell time=Pulse time (ms) × (1600/4/79) ×31.6 s				
DH5	Low	2.978	0.318	0.4	Pass
	Middle	2.978	0.318	0.4	Pass
	High	2.978	0.318	0.4	Pass
	Note: Dwell time=Pulse time (ms) × (1600/6/79) ×31.6 s				

DH1: Low Channel



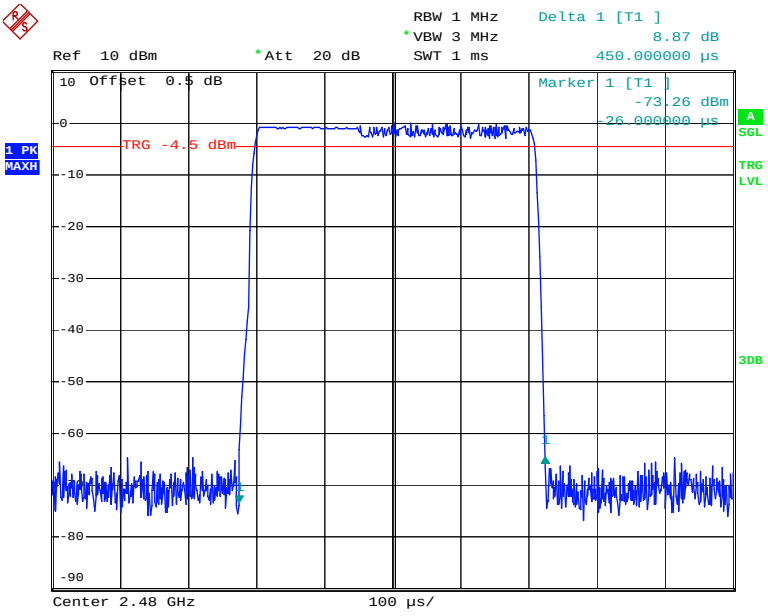
Date: 8.MAY.2014 14:37:37

DH1: Middle Channel



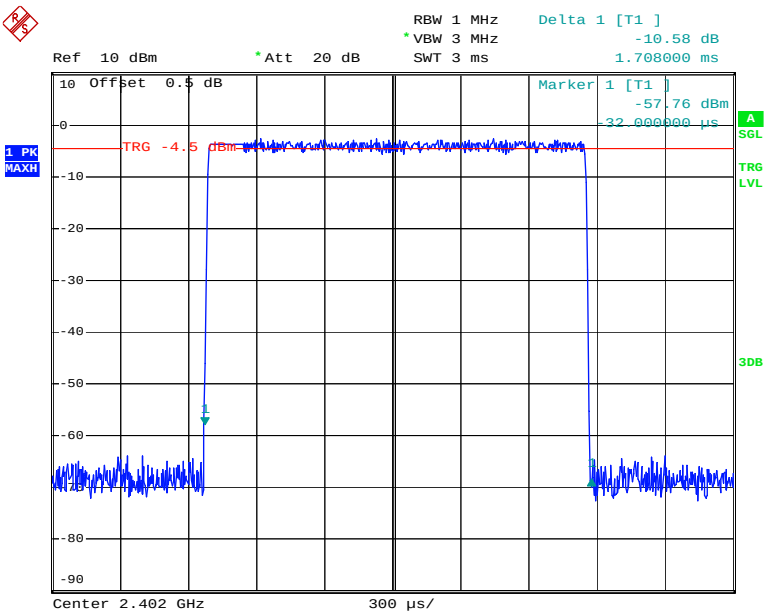
Date: 8.MAY.2014 14:38:00

DH1: High Channel



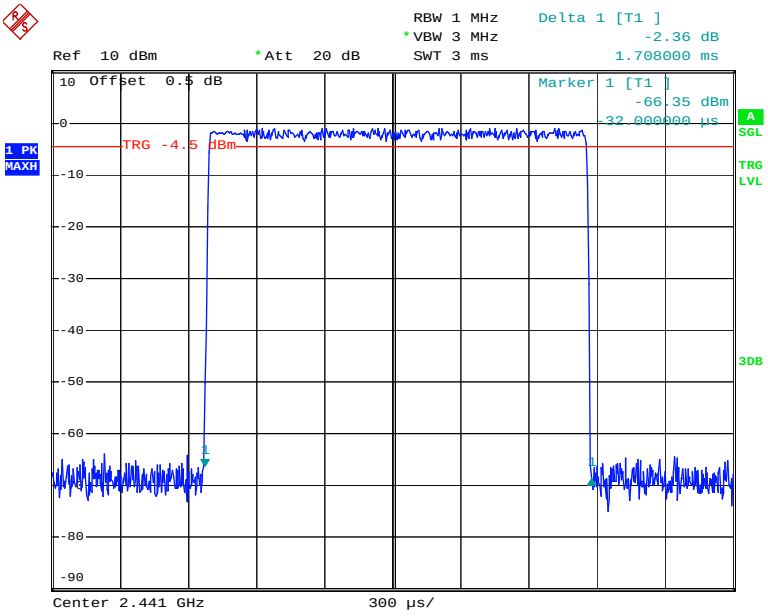
Date: 8.MAY.2014 14:38:43

DH3: Low Channel



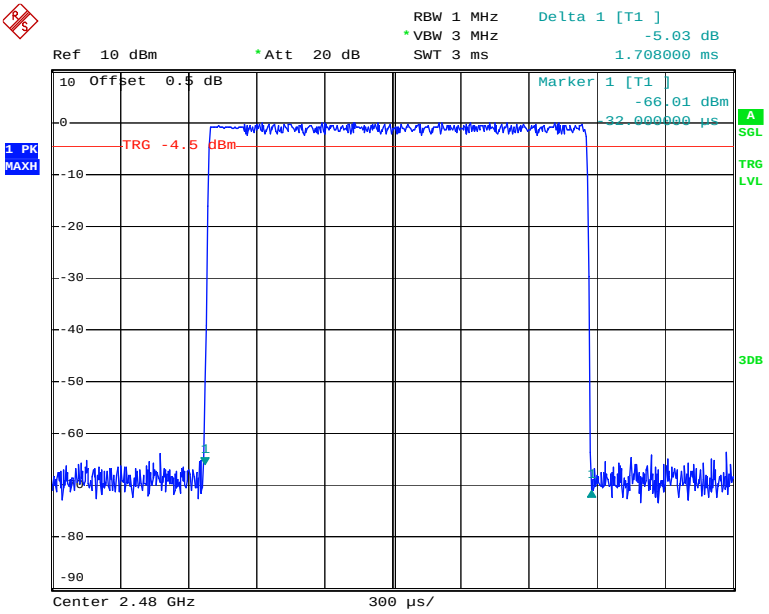
Date: 8.MAY.2014 14:42:22

DH3: Middle Channel



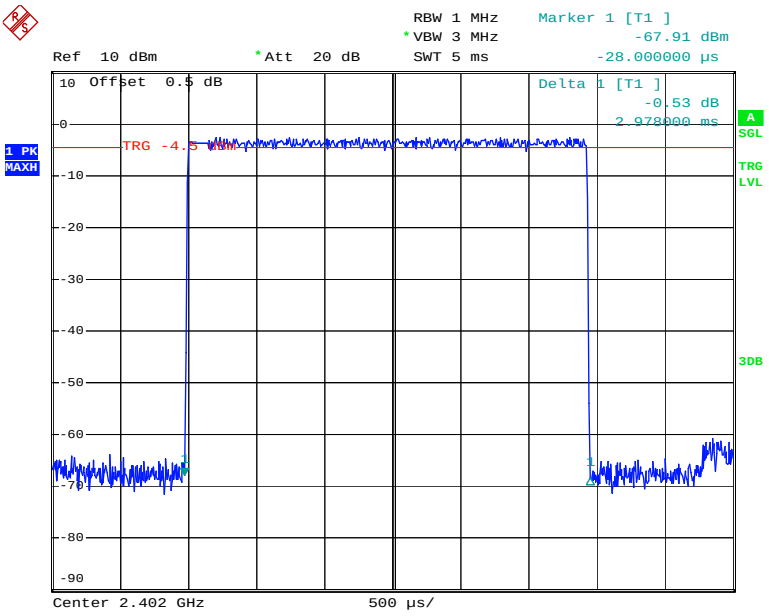
Date: 8.MAY.2014 14:42:08

DH3: High Channel



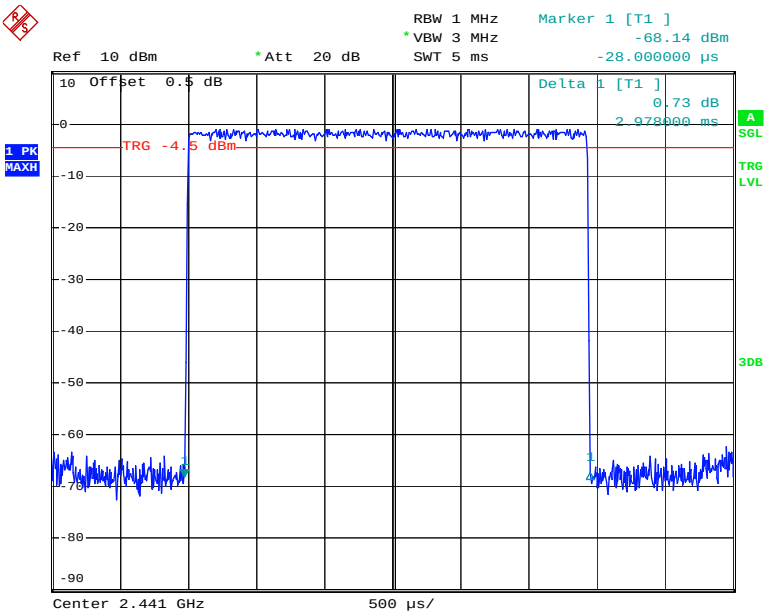
Date: 8.MAY.2014 14:41:55

DH5: Low Channel



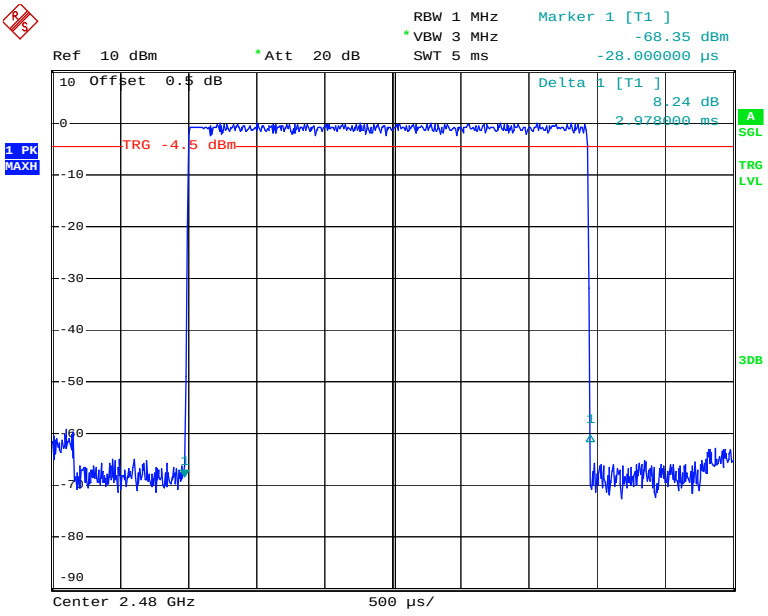
Date: 8.MAY.2014 14:52:58

DH5: Middle Channel



Date: 8.MAY.2014 14:53:12

DH5: High Channel



Date: 8.MAY.2014 14:53:27

FCC §15.247(b) (1) - PEAK OUTPUT POWER MEASUREMENT

Applicable Standard

According to §15.247(b) (1), for frequency hopping systems operating in the 2400–2483.5 MHz band employing at least 75 non-overlapping hopping channels, and all frequency hopping systems in the 5725-5850 MHz band: 1 watt. For all other frequency hopping systems in the 2400–2483.5 MHz band: 0.125 watts

Test Procedure

1. Place the EUT on a bench and set in transmitting mode.
2. Remove the antenna from the EUT and then connect a low loss RF cable from the antenna port to an EMI test receiver.
3. Add a correction factor to the display.

Test Equipment List and Details

Manufacturer	Description	Model	Serial Number	Calibration Date	Calibration Due Date
R&S	Spectrum Analyzer	FSEM	DE31388	2014-05-07	2015-05-06

*** Statement of Traceability:** Bay Area Compliance Laboratories Corp. (Dongguan) attests that all calibrations have been performed, traceable to National Primary Standards and International System of Units (SI).

Test Data

Environmental Conditions

Temperature:	25.1 °C
Relative Humidity:	64 %
ATM Pressure:	100.6 kPa

** The testing was performed by Allen Qiao on 2014-05-08.*

Test Result: Compliance.

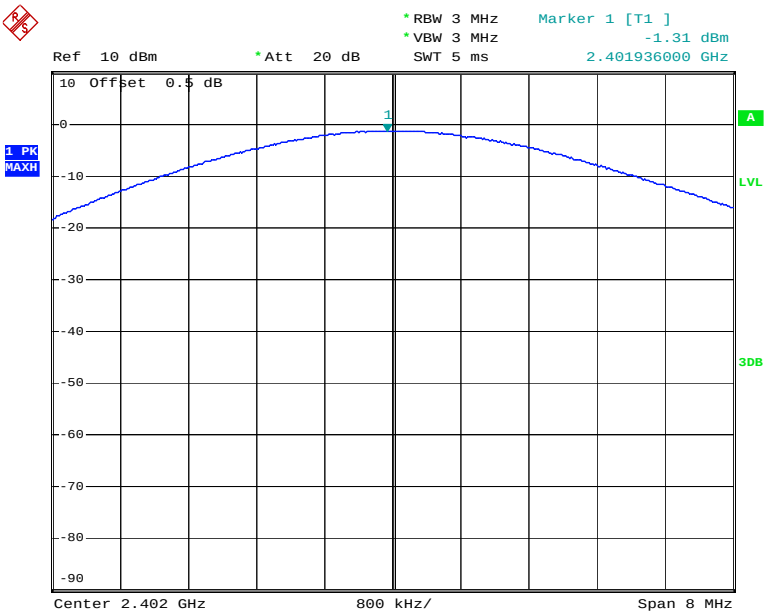
Test Mode: Transmitting

Mode	Channel	Frequency (MHz)	Output power (dBm)	Limit (dBm)
BDR Mode (GFSK)	Low	2402	-1.31	30
	Middle	2441	0.67	30
	High	2480	-0.13	30
EDR Mode (π/4-DQPSK)	Low	2402	-1.56	30
	Middle	2441	0.53	30
	High	2480	-0.30	30
EDR Mode (8-DPSK)	Low	2402	-2.51	30
	Middle	2441	-0.38	30
	High	2480	-0.02	30

Note: The data above was tested in conducted mode.

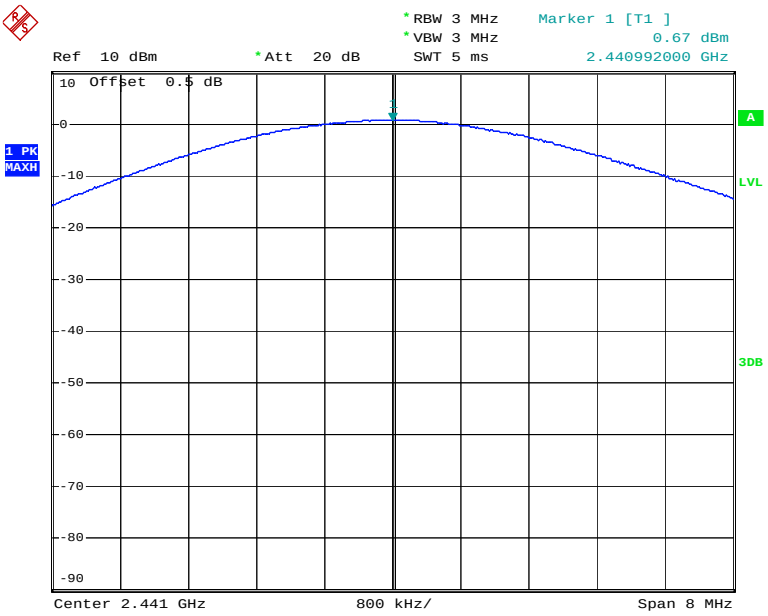
BDR Mode (GFSK):

Output Power, Low Channel



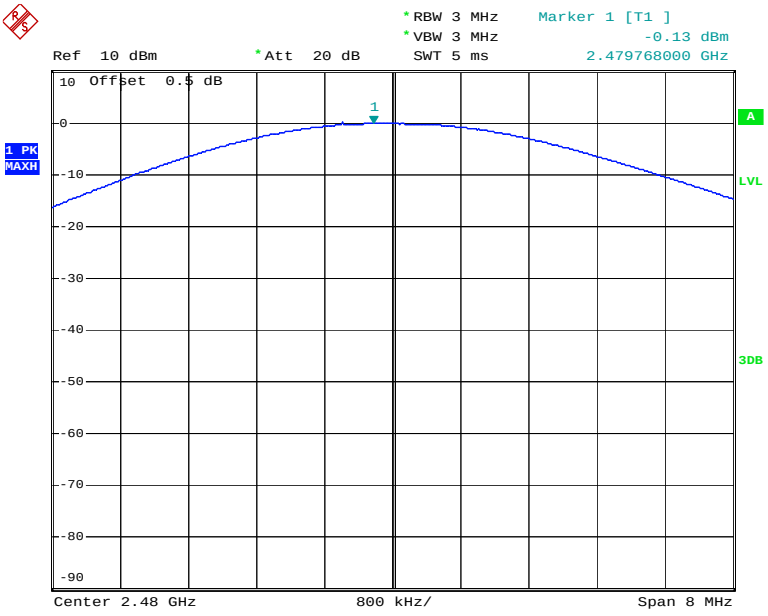
Date: 8.MAY.2014 11:06:52

Output Power, Middle Channel



Date: 8.MAY.2014 11:07:16

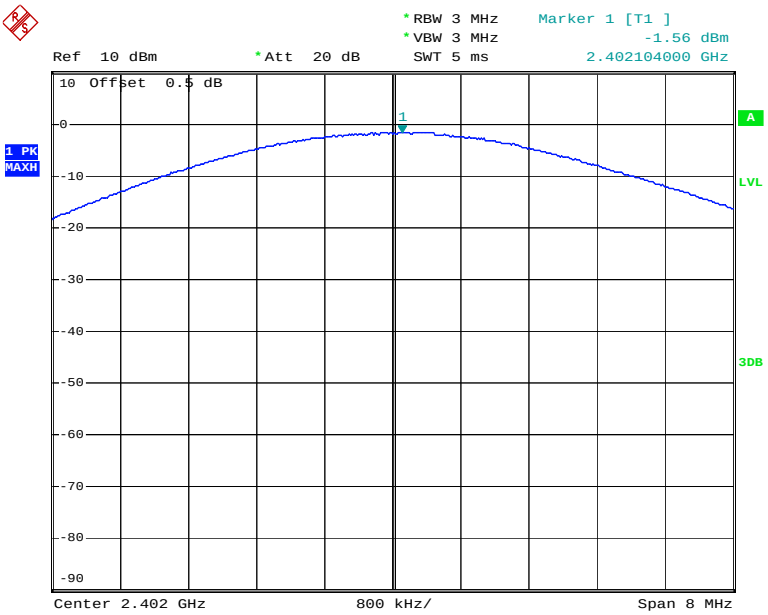
Output Power, High Channel



Date: 8.MAY.2014 13:27:50

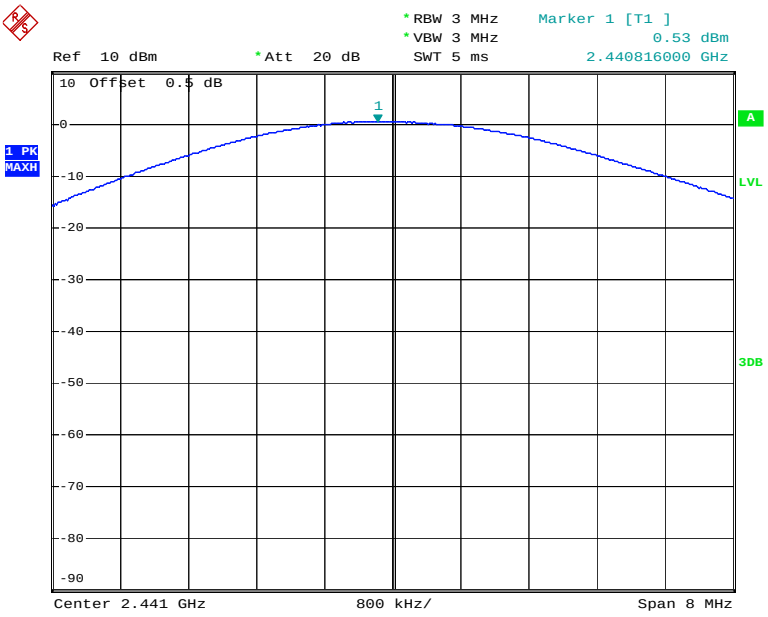
EDR Mode (π/4-DQPSK):

Output Power, Low Channel



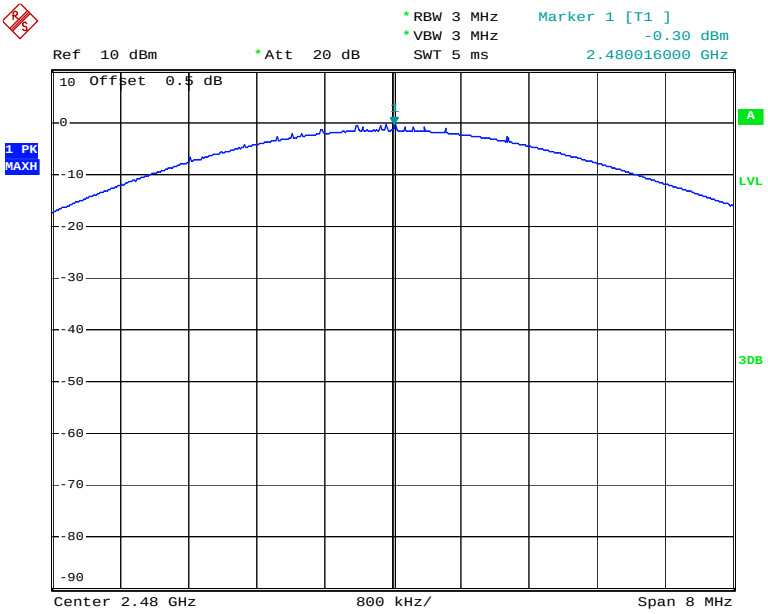
Date: 8.MAY.2014 11:08:01

Output Power, Middle Channel



Date: 8.MAY.2014 11:08:20

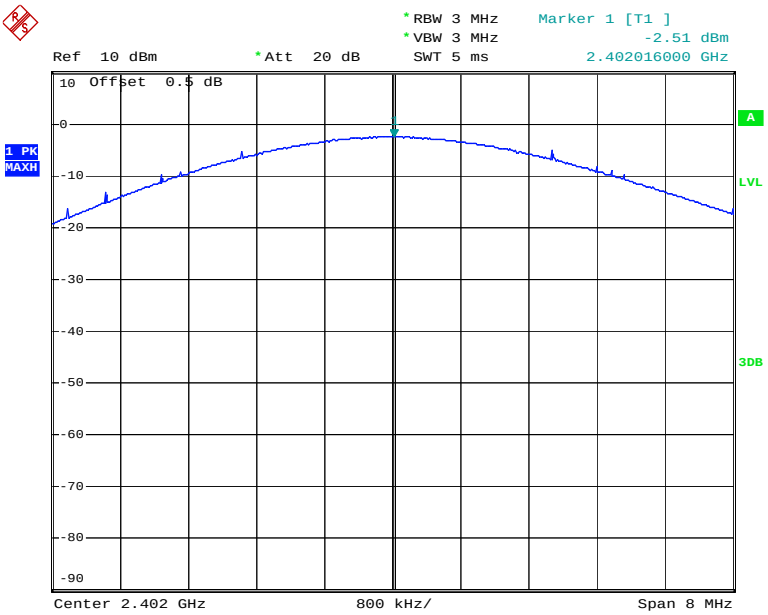
Output Power, High Channel



Date: 8.MAY.2014 13:29:54

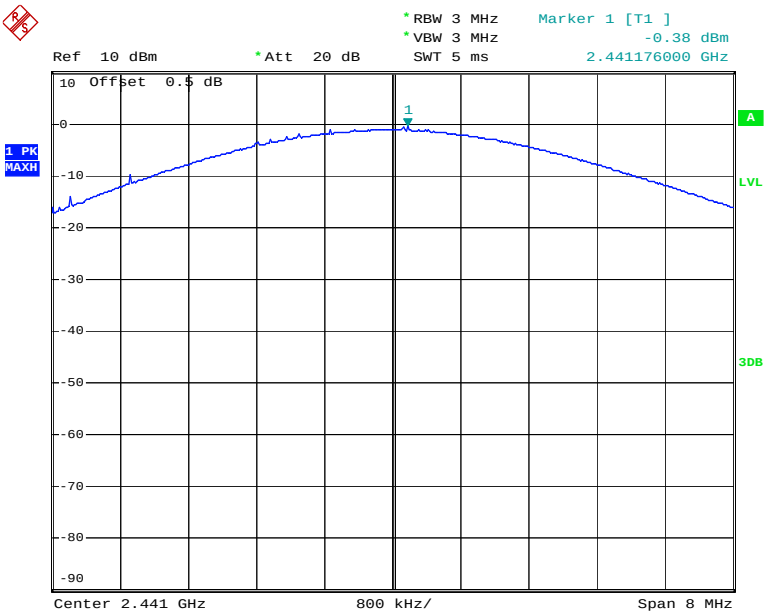
EDR Mode (8-DPSK):

Output Power, Low Channel



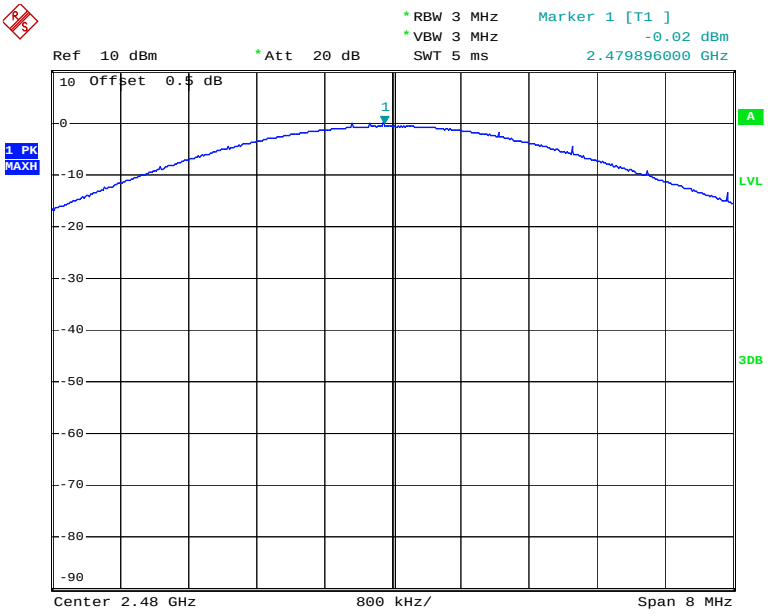
Date: 8.MAY.2014 13:37:27

Output Power, Middle Channel



Date: 8.MAY.2014 13:36:04

Output Power, High Channel



Date: 8.MAY.2014 13:36:57

FCC §15.247(d) - BAND EDGES TESTING

Applicable Standard

In any 100 kHz bandwidth outside the frequency band in which the spread spectrum or digitally modulated intentional radiator is operating, the radio frequency power that is produced by the intentional radiator shall be at least 20 dB below that in the 100 kHz bandwidth within the band that contains the highest level of the desired power, based on either an RF conducted or a radiated measurement, provided the transmitter demonstrates compliance with the peak conducted power limits. If the transmitter complies with the conducted power limits based on the use of RMS averaging over a time interval, as permitted under paragraph (b)(3) of this section, the attenuation required under this paragraph shall be 30 dB instead of 20 dB. Attenuation below the general limits specified in §15.209(a) is not required. In addition, radiated emissions which fall in the restricted bands, as defined in §15.205(a), must also comply with the radiated emission limits specified in §15.209(a) (see §15.205(c)).

Test Procedure

1. Check the calibration of the measuring instrument using either an internal calibrator or a known signal from an external generator.
2. Remove the antenna from the EUT and then connect to a low loss RF cable from the antenna port to a EMI test receiver, then turn on the EUT and make it operate in transmitting mode. Then set it to Low Channel and High Channel within its operating range, and make sure the instrument is operated in its linear range.
3. Set both RBW and VBW of spectrum analyzer to 100 kHz with a convenient frequency span including 100 kHz bandwidth from band edge.
4. Measure the highest amplitude appearing on spectral display and set it as a reference level. Plot the graph with marking the highest point and edge frequency.
5. Repeat above procedures until all measured frequencies were complete.

Test Equipment List and Details

Manufacturer	Description	Model	Serial Number	Calibration Date	Calibration Due Date
R&S	Spectrum Analyzer	FSEM	DE31388	2014-05-07	2015-05-06

*** Statement of Traceability:** Bay Area Compliance Laboratories Corp. (Dongguan) attests that all calibrations have been performed, traceable to National Primary Standards and International System of Units (SI).

Test Data

Environmental Conditions

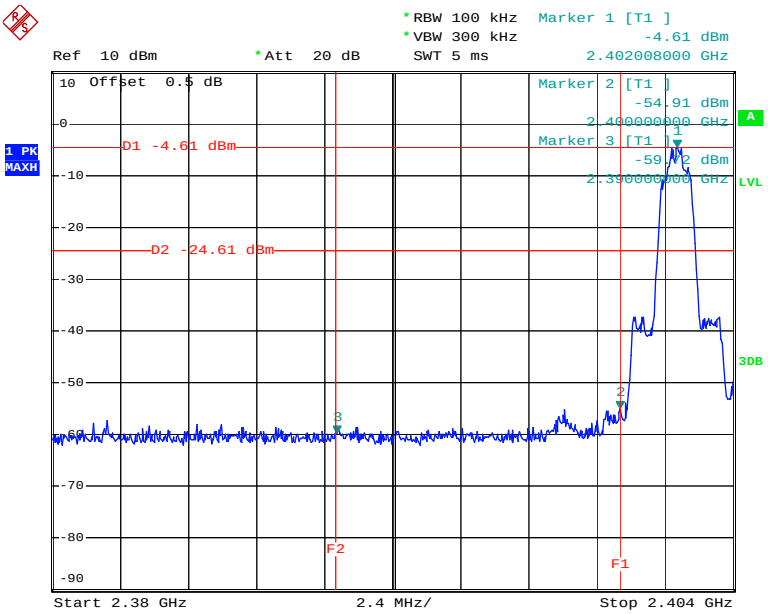
Temperature:	25.1 °C
Relative Humidity:	64 %
ATM Pressure:	100.6 kPa

** The testing was performed by Allen Qiao on 2014-05-08.*

Test Result: Compliance

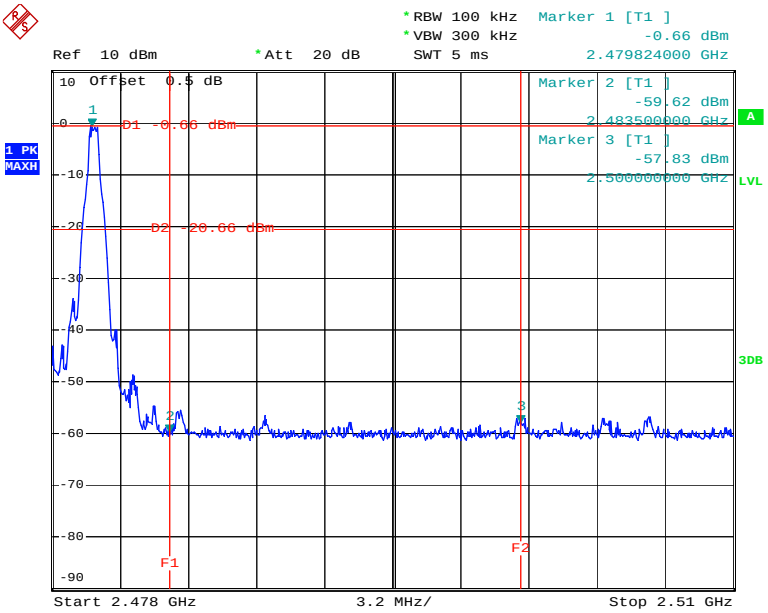
BDR Mode (GFSK):

Band Edge, Left Side



Date: 8.MAY.2014 13:46:55

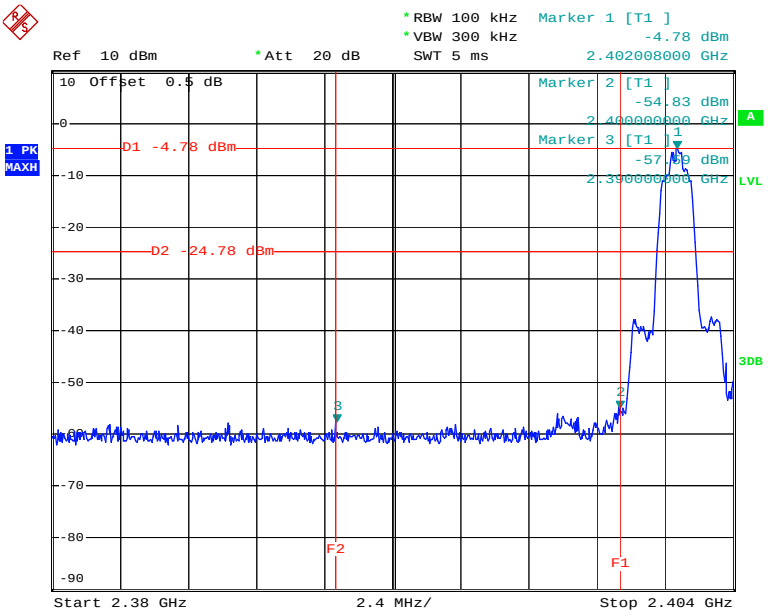
Band Edge, Right Side



Date: 8.MAY.2014 13:56:05

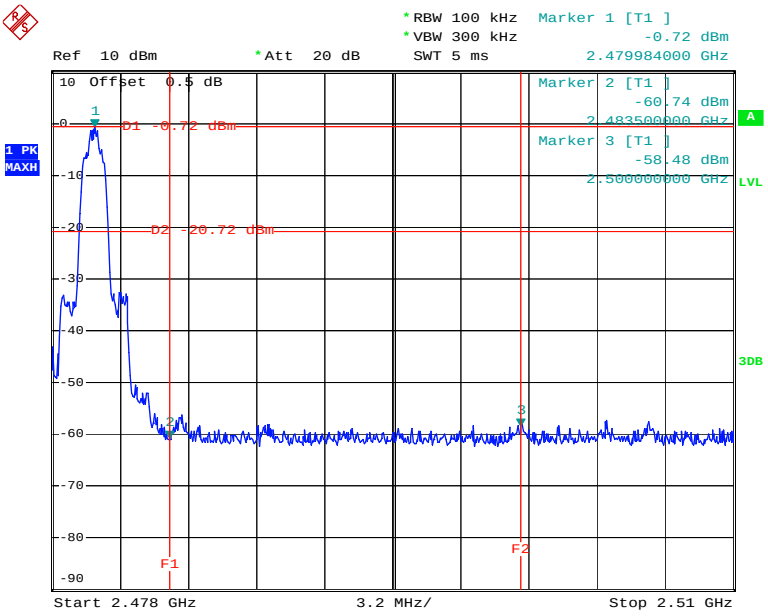
EDR Mode (π/4-DQPSK):

Band Edge, Left Side



Date: 8.MAY.2014 13:48:06

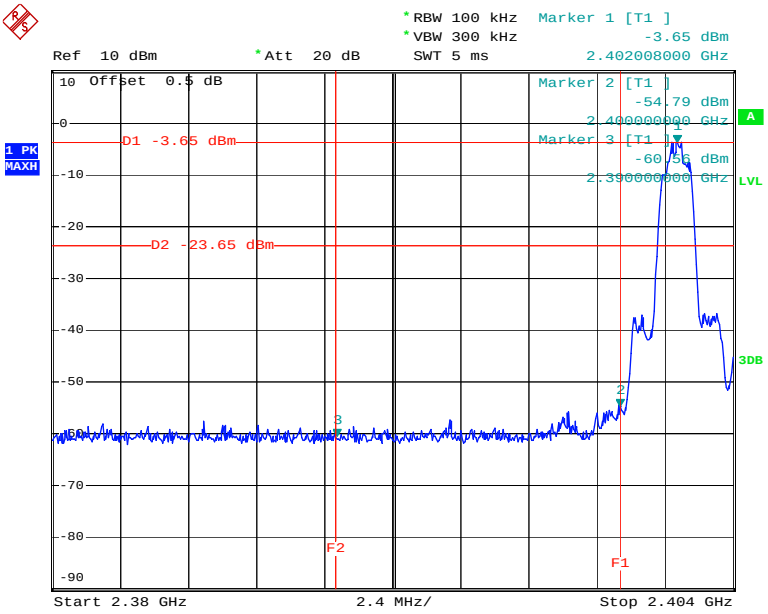
Band Edge, Right Side



Date: 8.MAY.2014 13:53:31

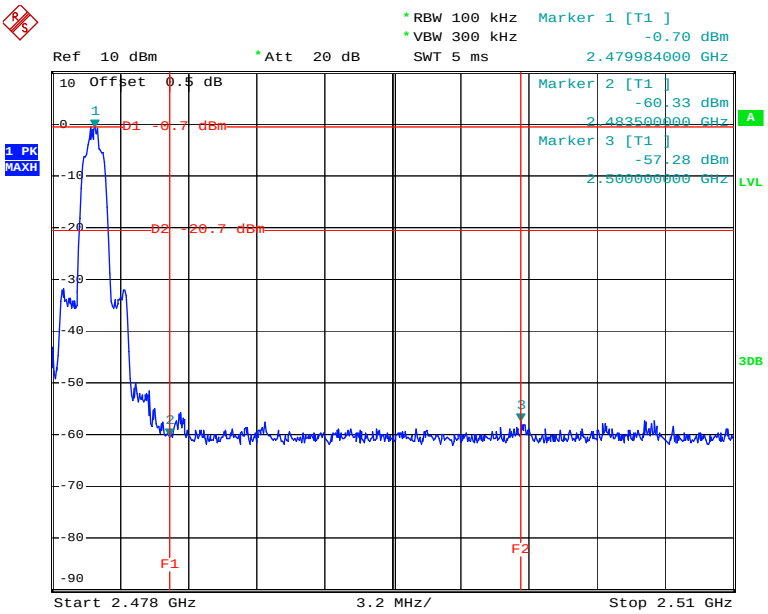
EDR Mode (8-DPSK):

Band Edge, Left Side



Date: 8.MAY.2014 13:49:07

Band Edge, Right Side



Date: 8.MAY.2014 13:52:42

******* END OF REPORT *******