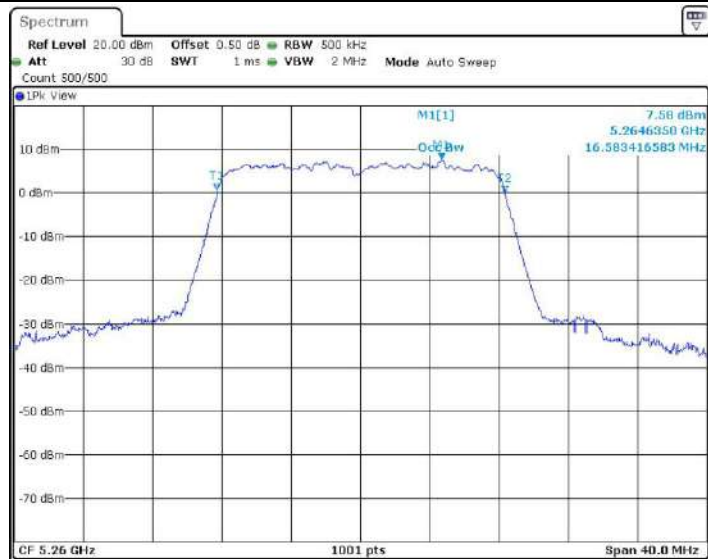
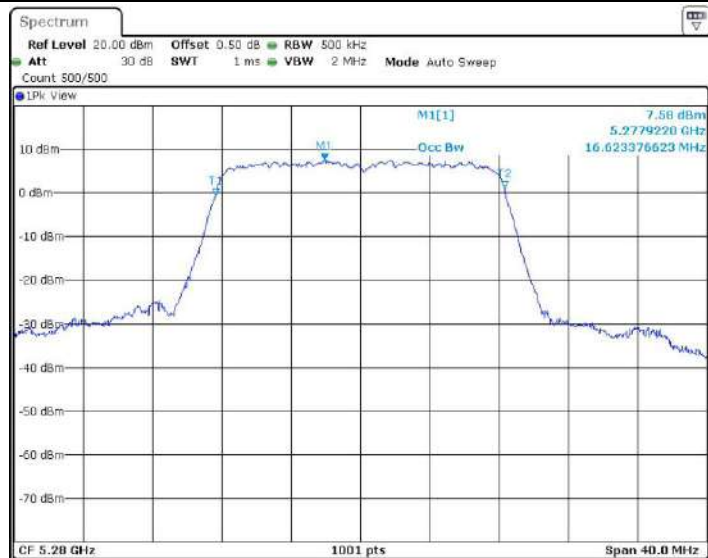
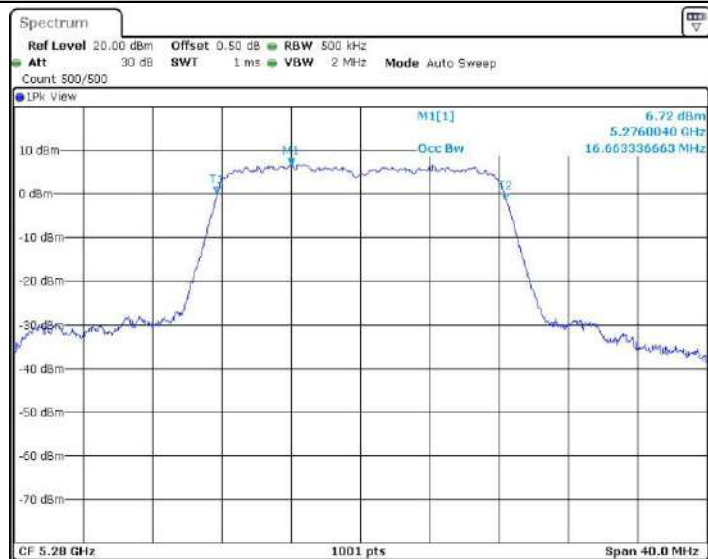




11A_Ant1_5260



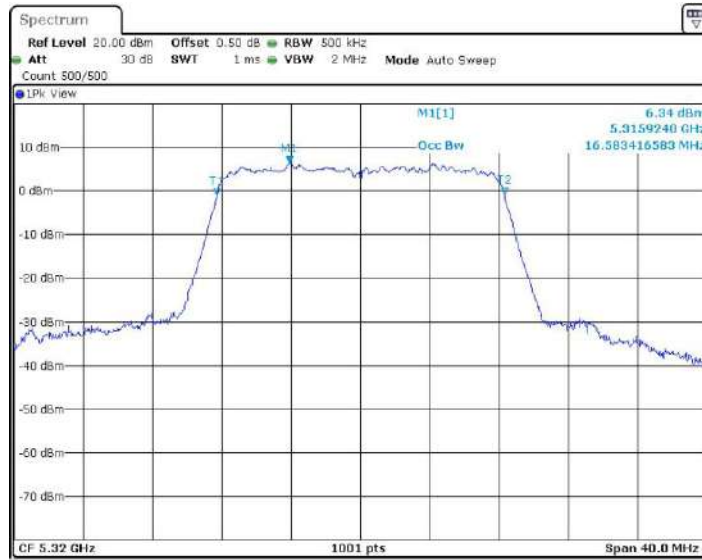
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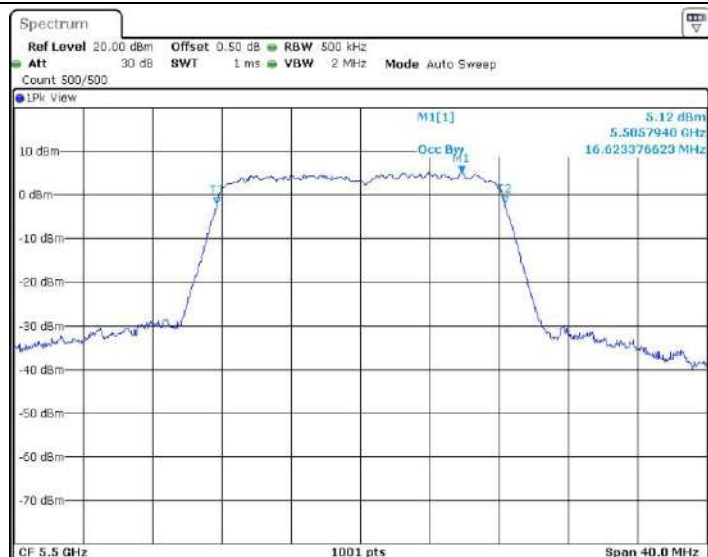
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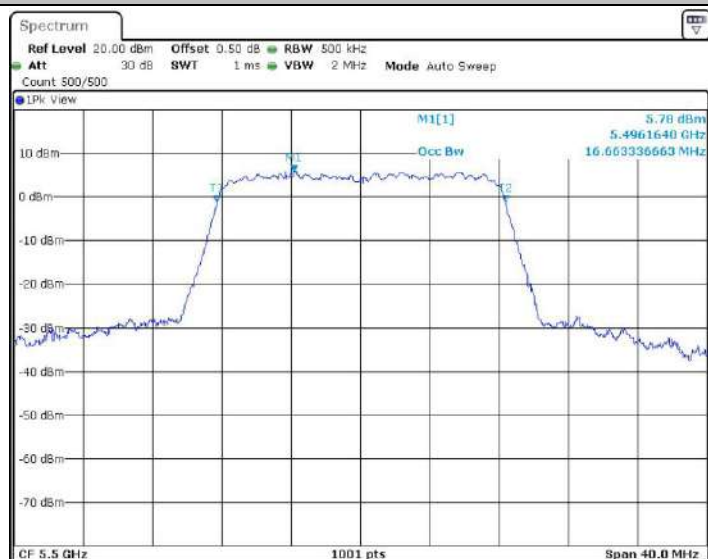
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11A_Ant1_5320



11A_Ant0_5500



11A_Ant1_5500



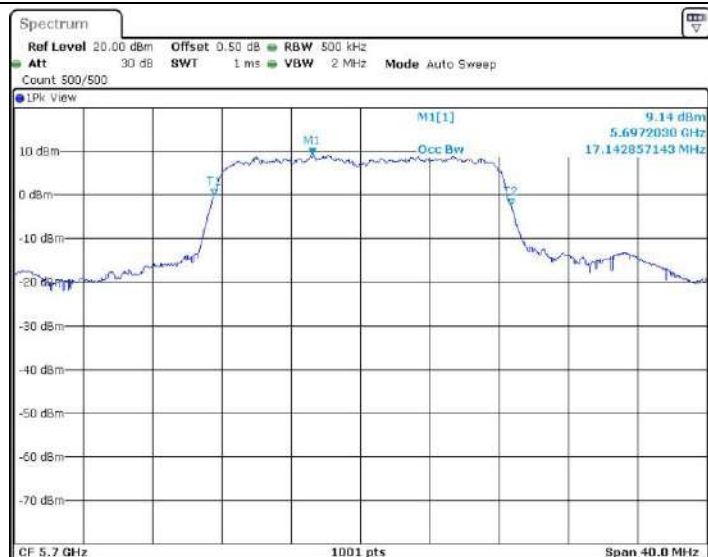
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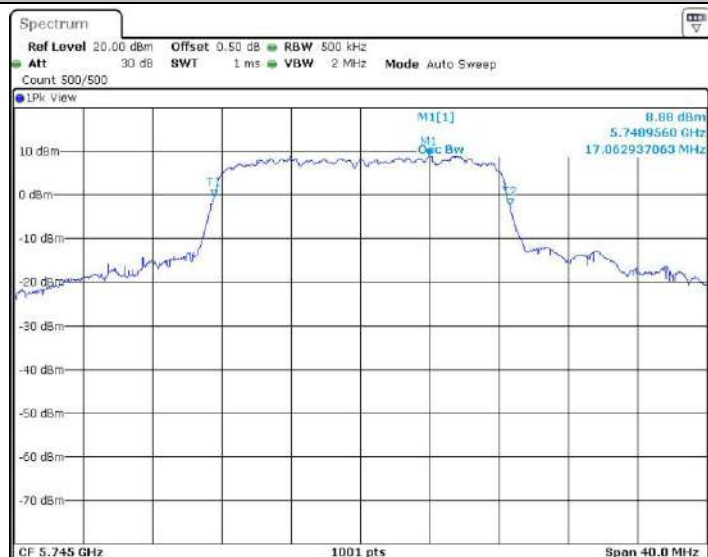
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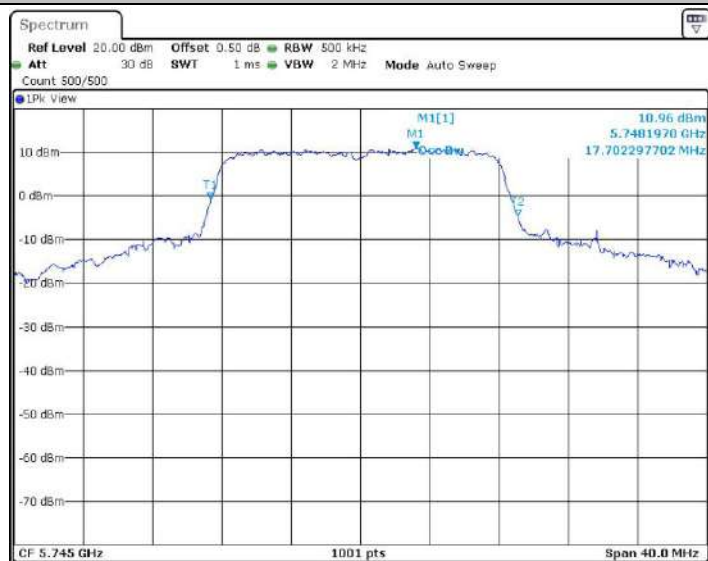
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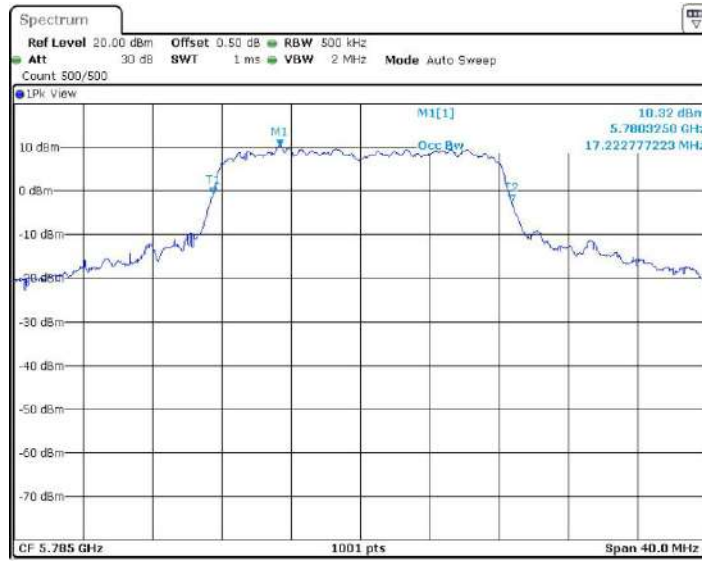
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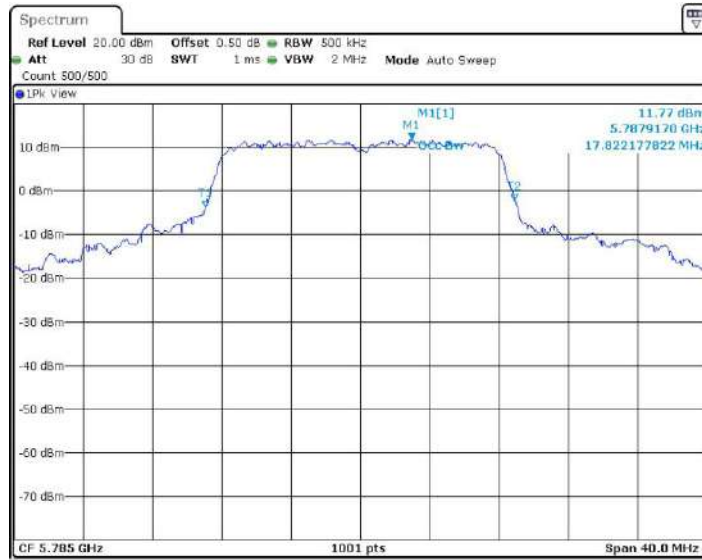
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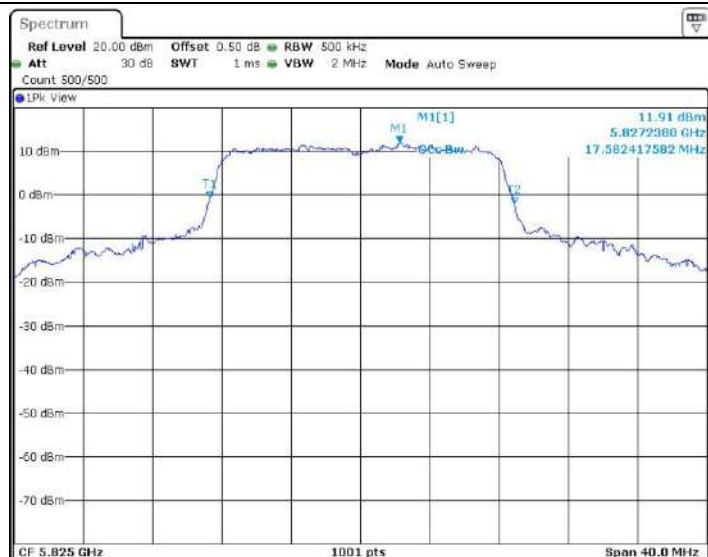
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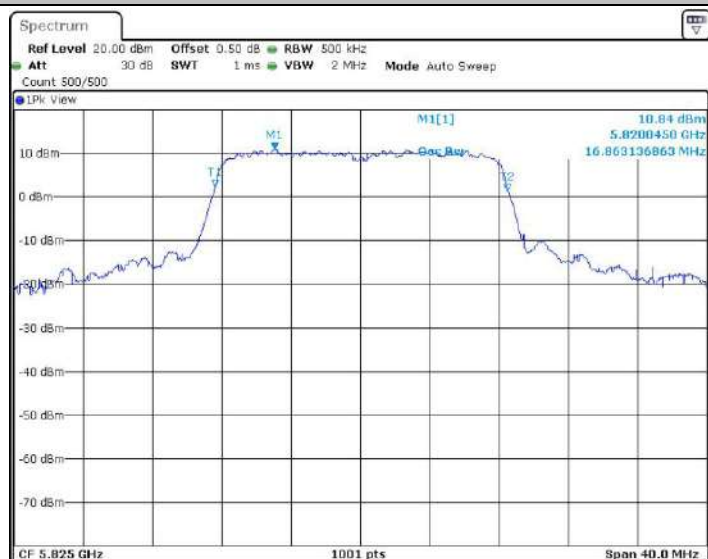
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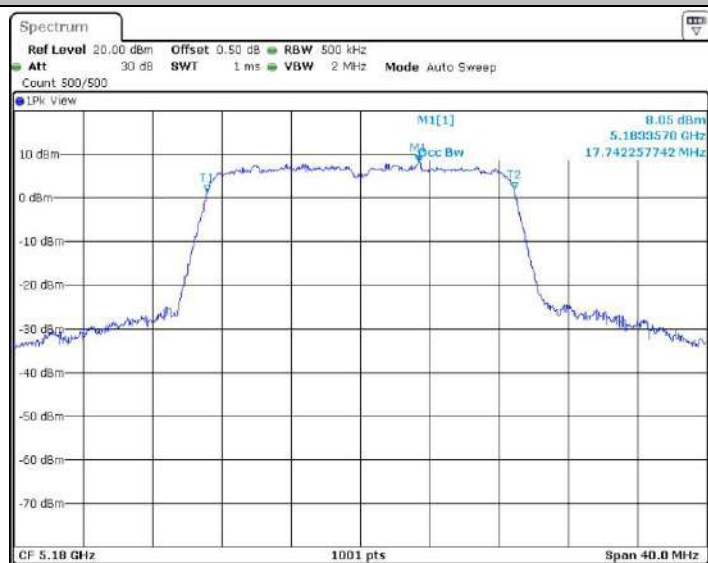
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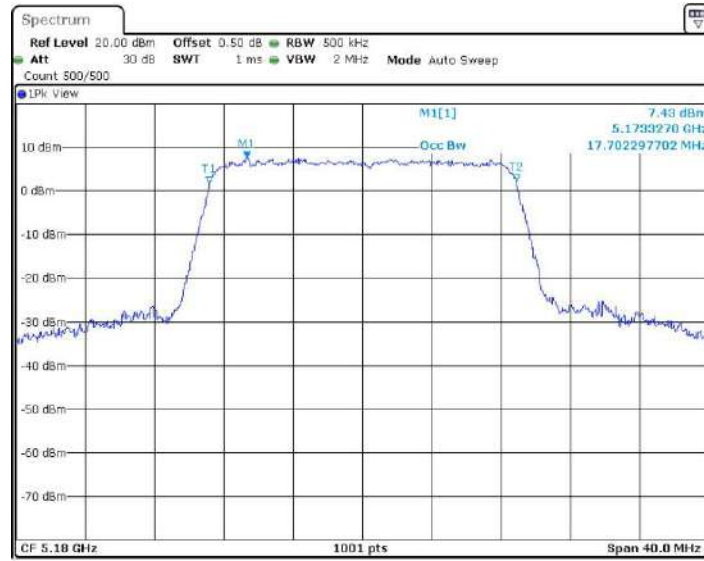
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11A_Ant1_5825



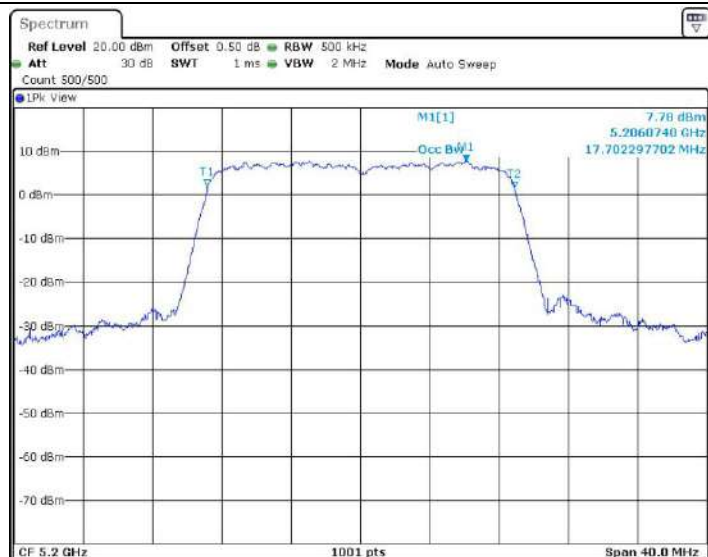
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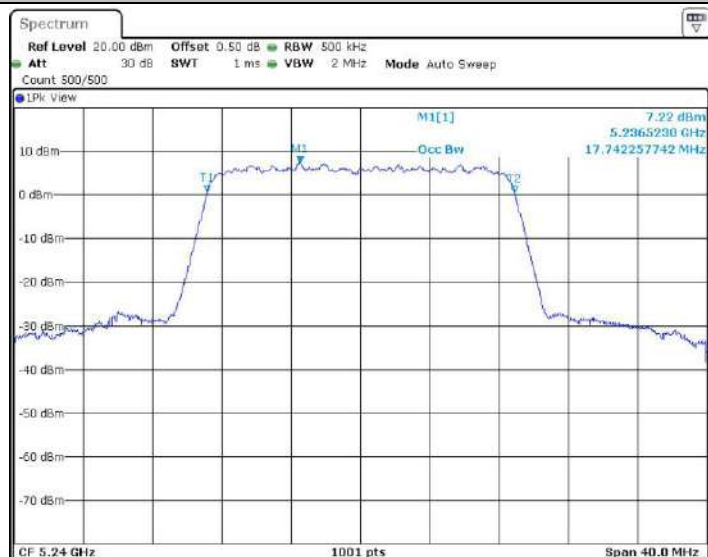
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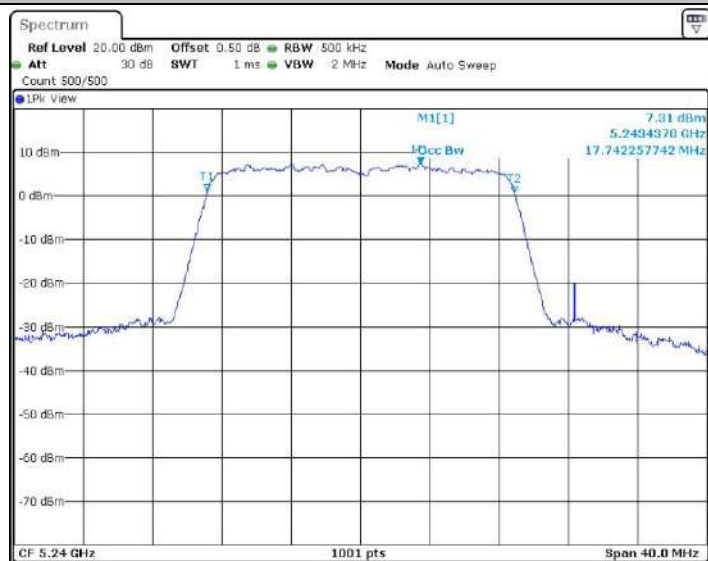
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11N20SISO_Ant1_5200



11N20SISO_Ant0_5240



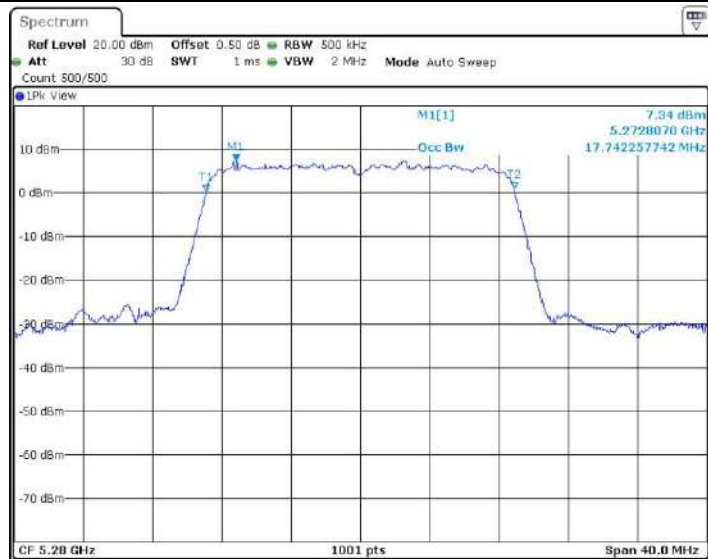
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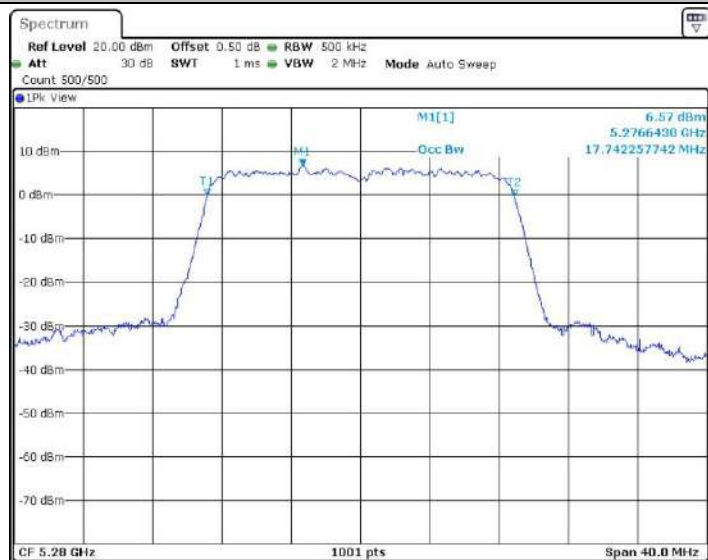
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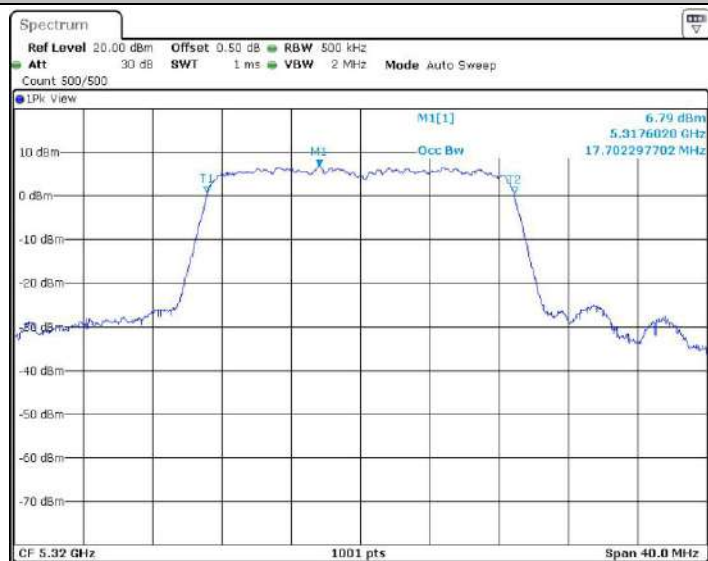
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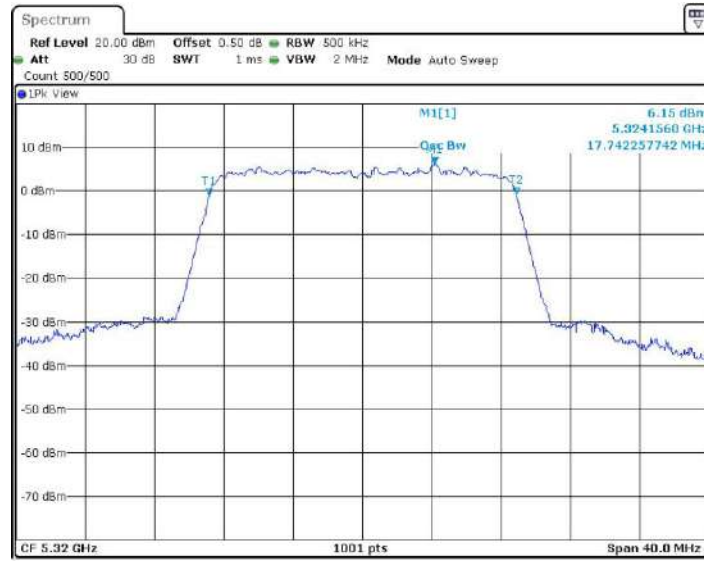
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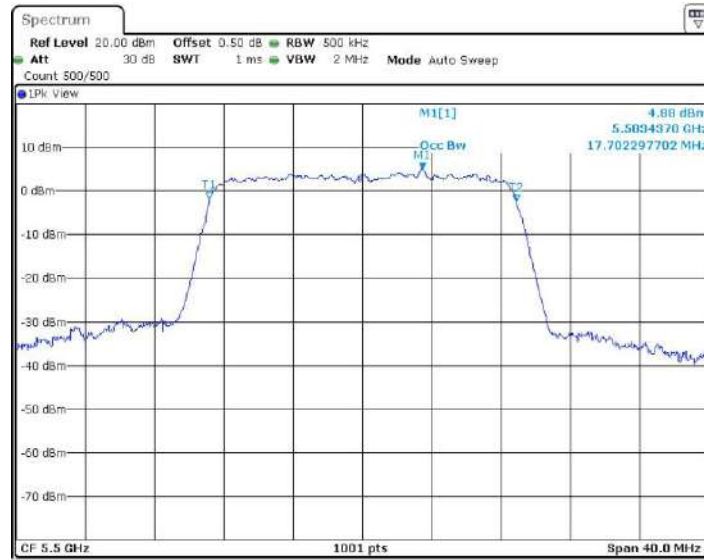
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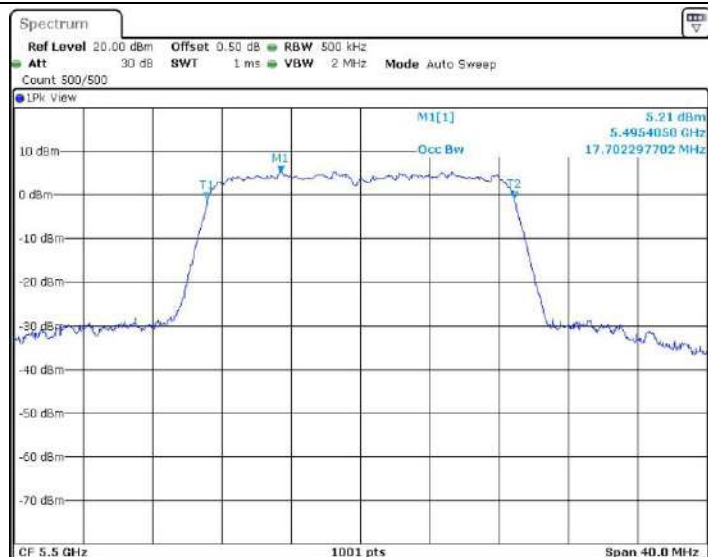
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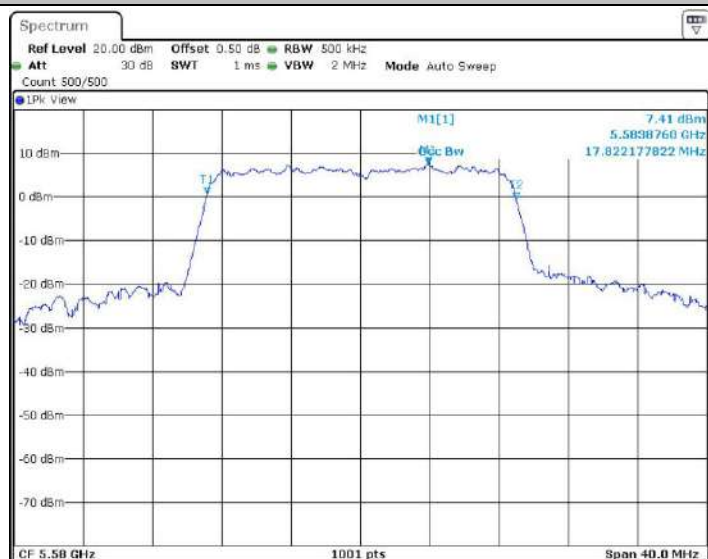
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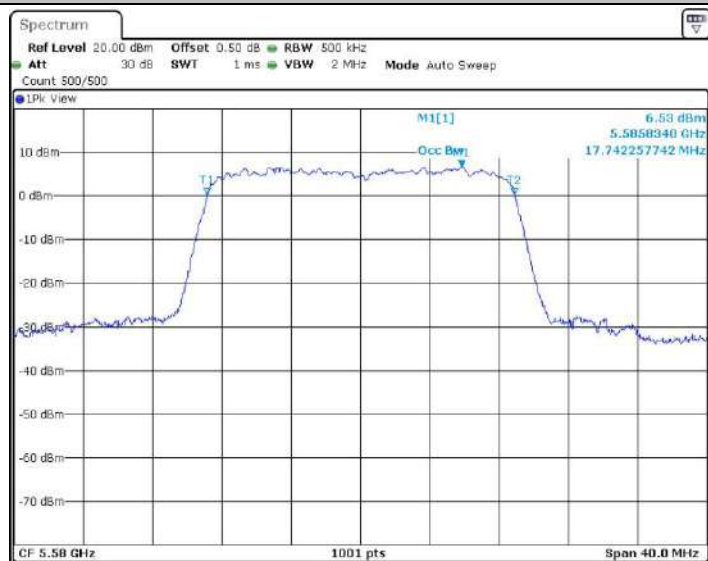
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11N20SISO_Ant1_5500



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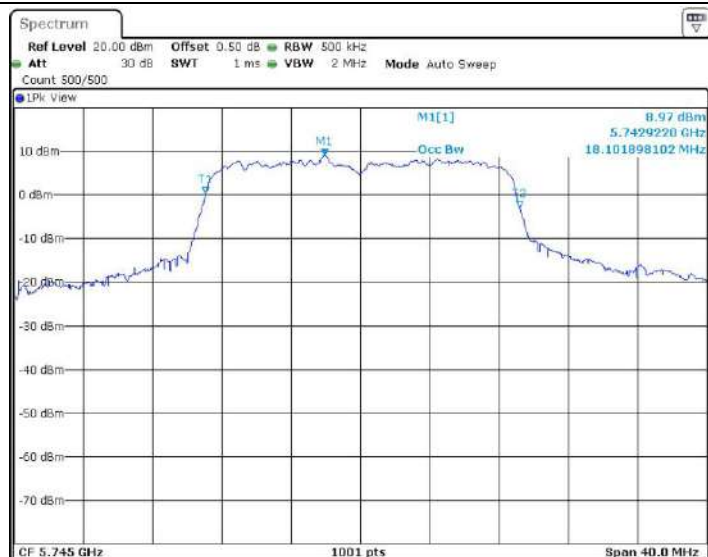
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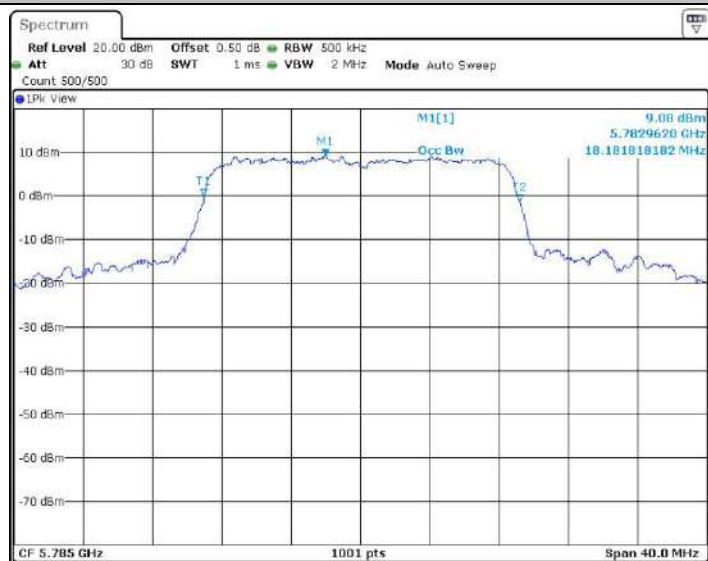
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11N20SISO_Ant0_5745



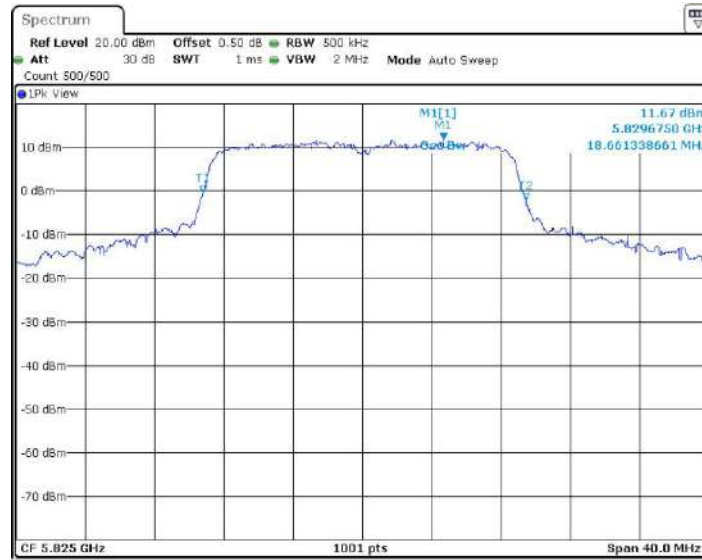
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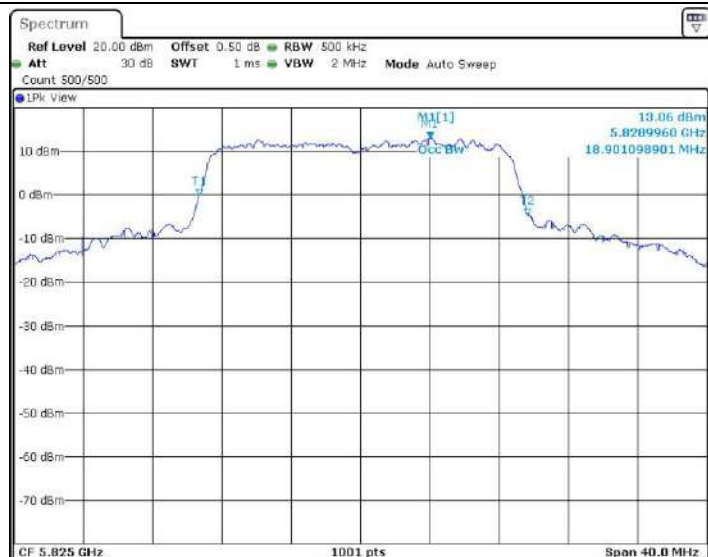
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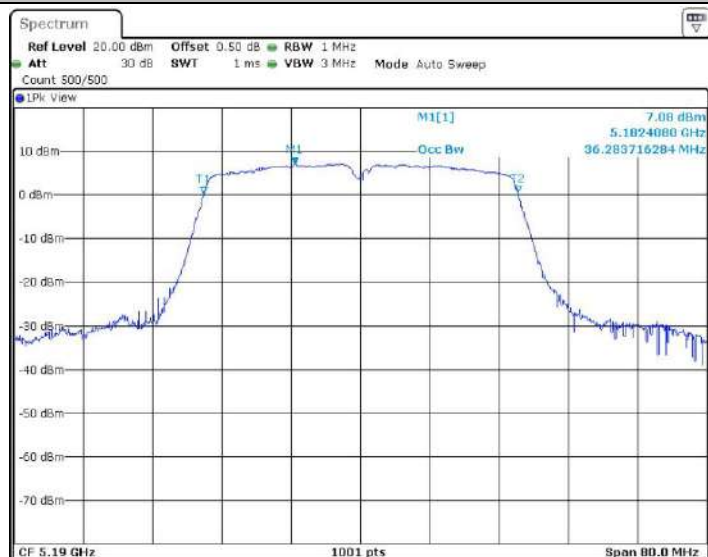
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11N20SISO_Ant0_5825



11N20SISO_Ant1_5825



11N40SISO_Ant0_5190



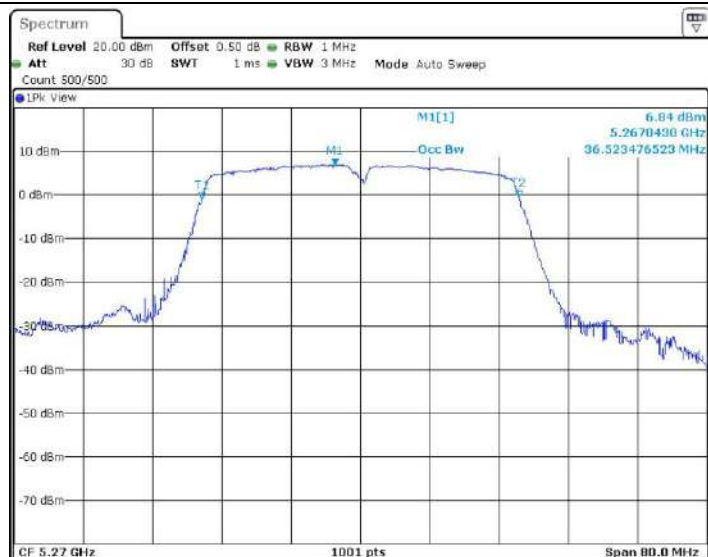
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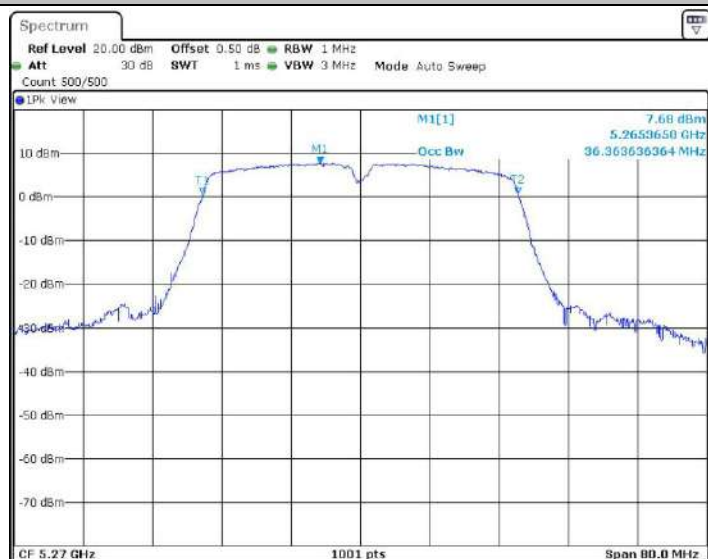
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11N40SISO_Ant1_5230



11N40SISO_Ant0_5270



11N40SISO_Ant1_5270



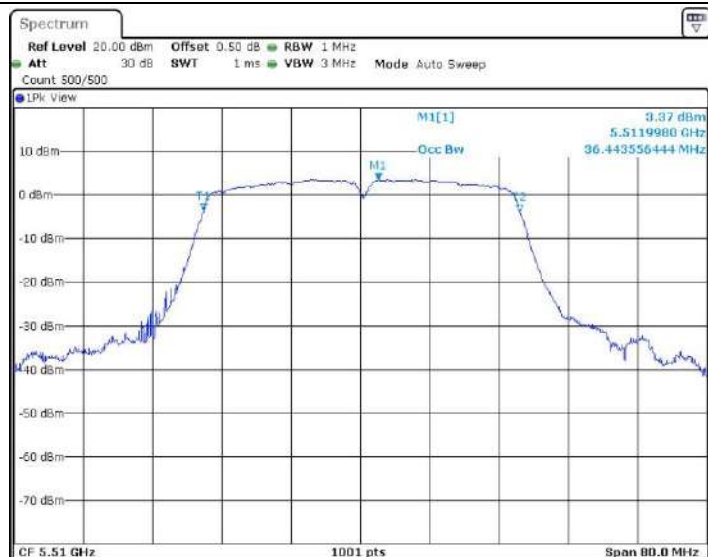
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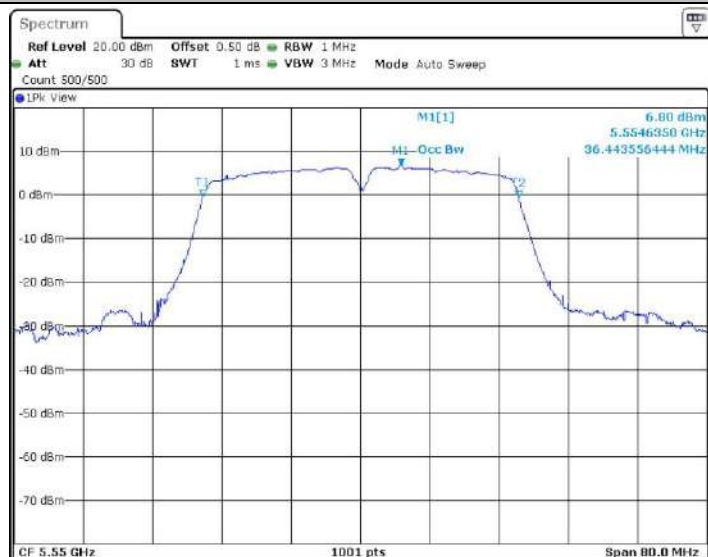
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11N40SISO_Ant0_5510



11N40SISO_Ant1_5510



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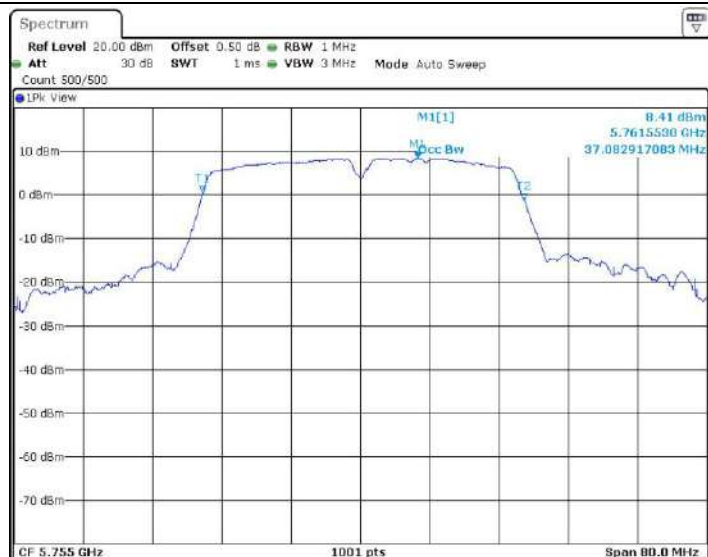
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11N40SISO_Ant0_5670



11N40SISO_Ant1_5670



11N40SISO_Ant0_5755



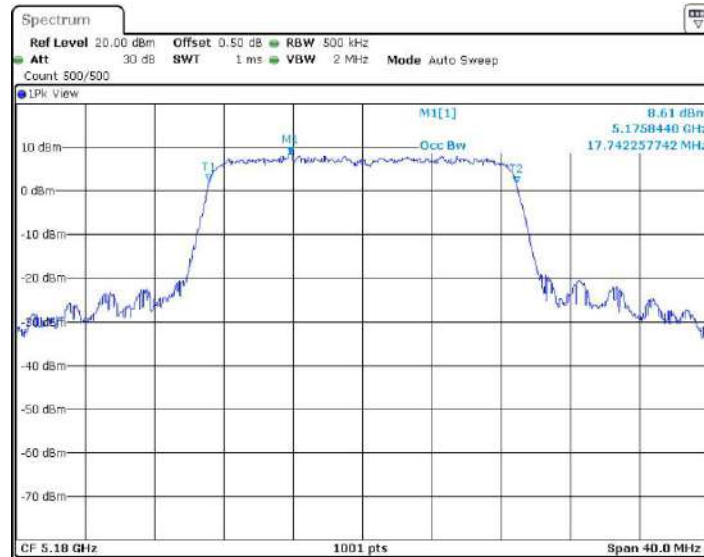
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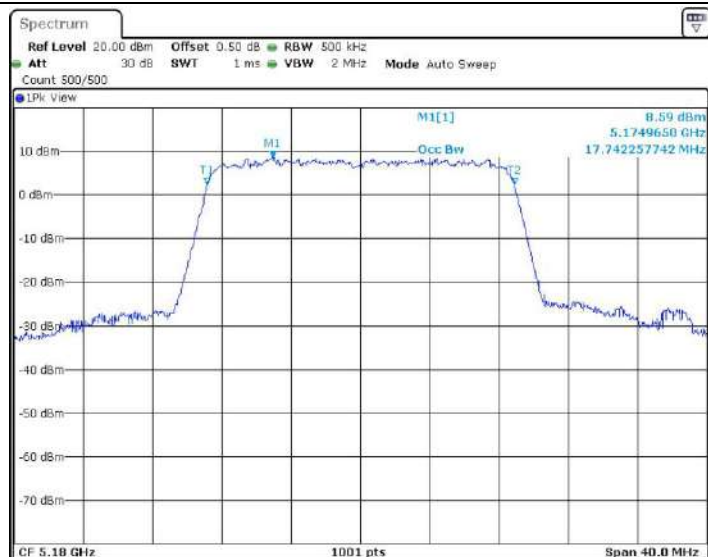
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11N40SISO_Ant1_5795



11AC20SISO_Ant0_5180



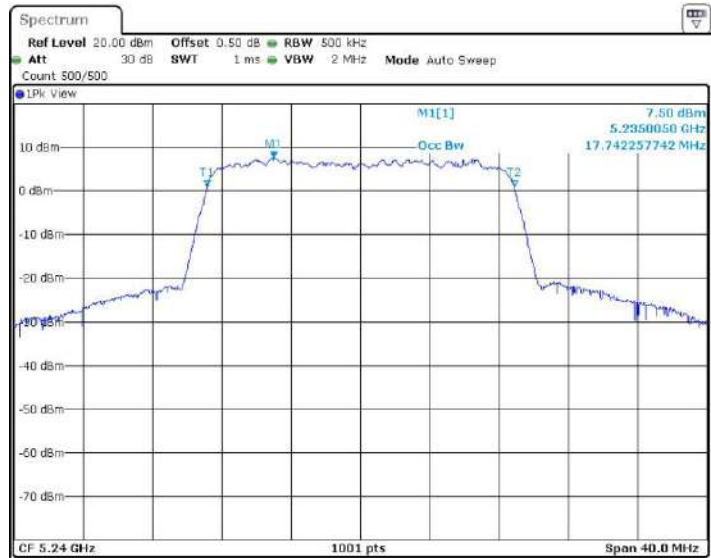
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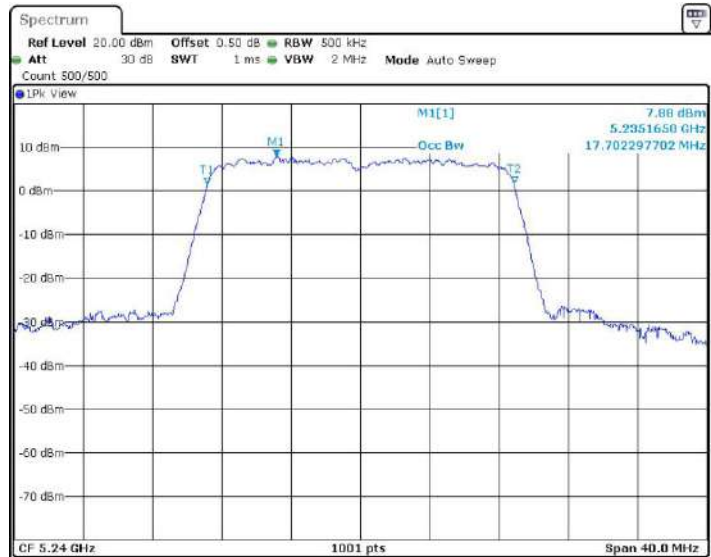
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11AC20SISO_Ant1_5200



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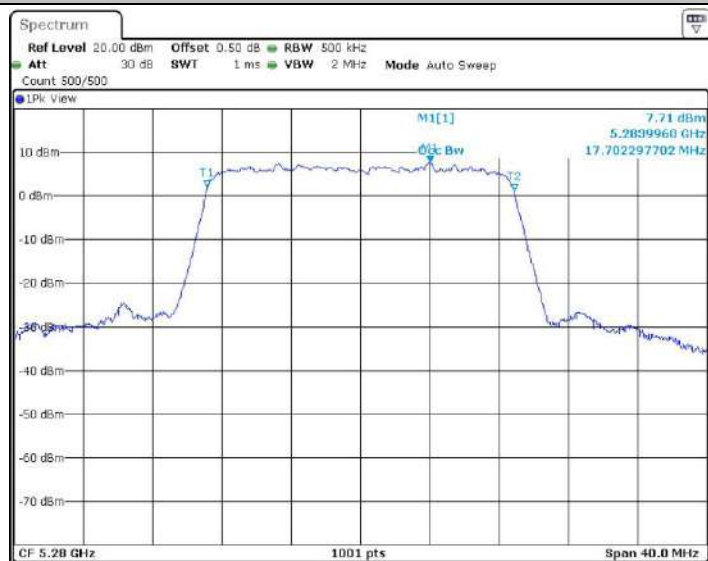
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11AC20SISO_Ant0_5260



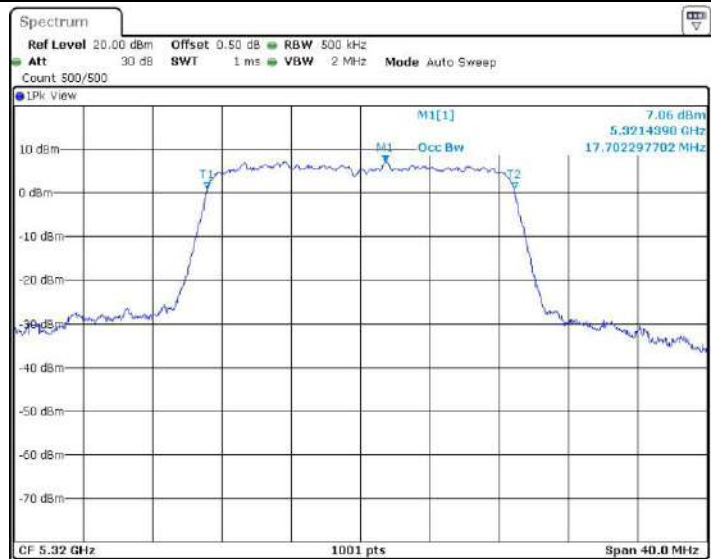
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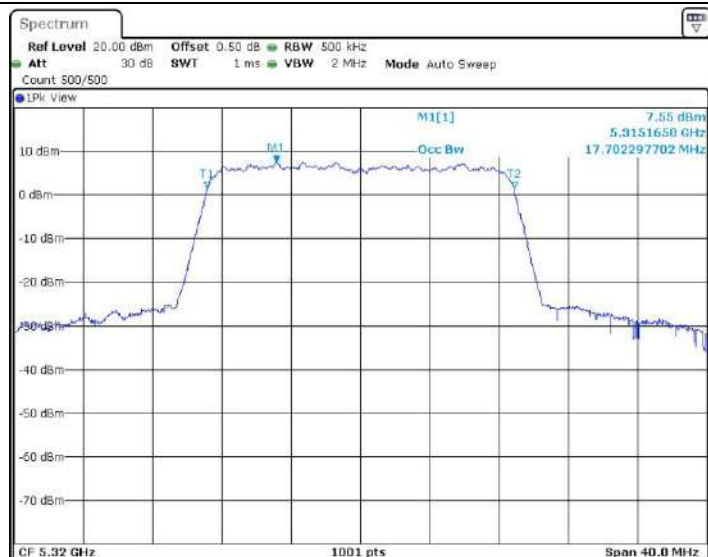
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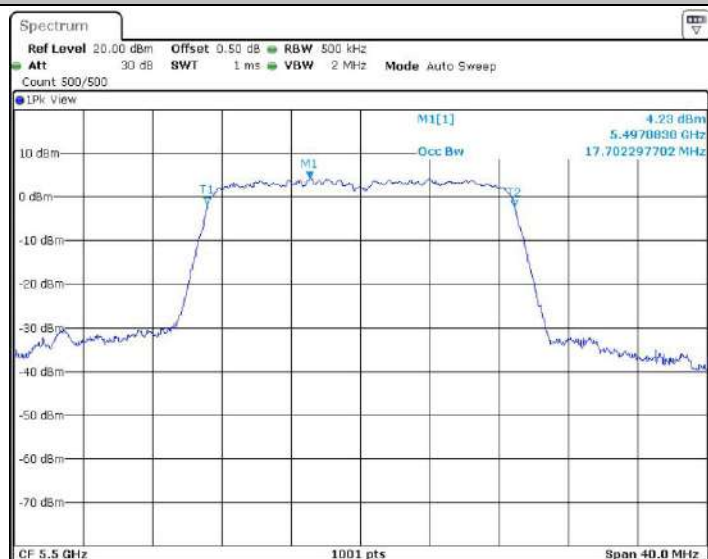
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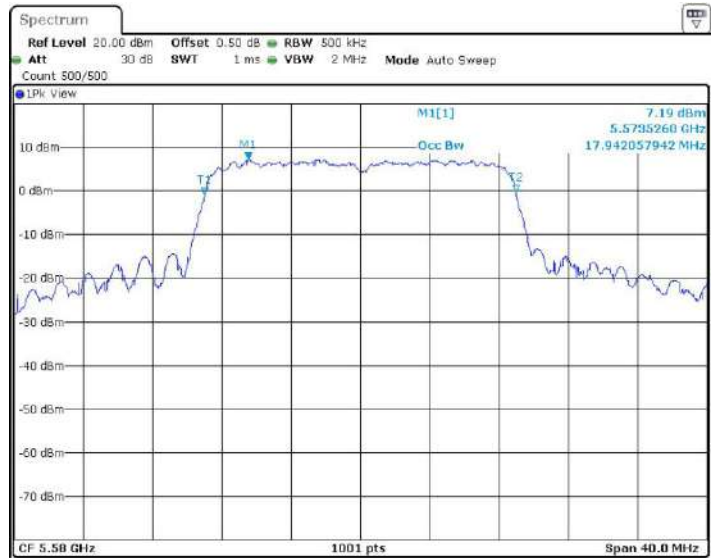
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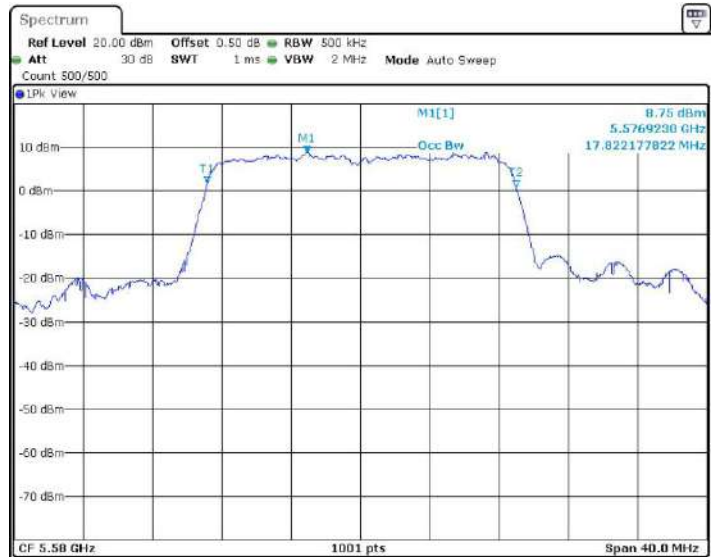
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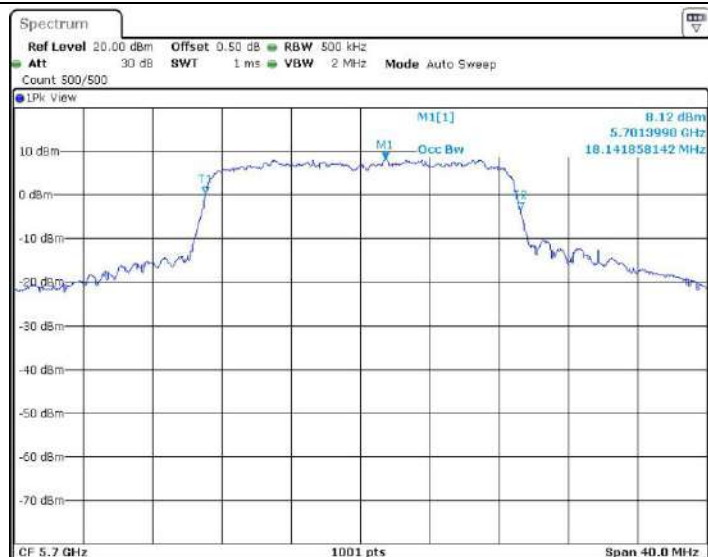
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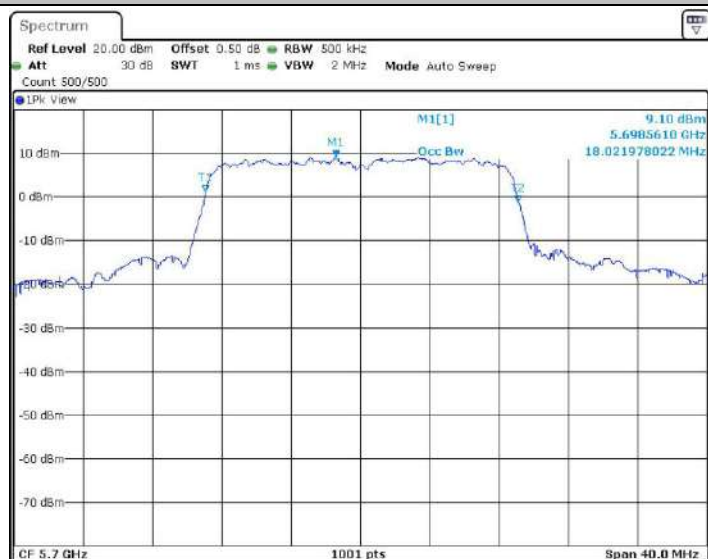
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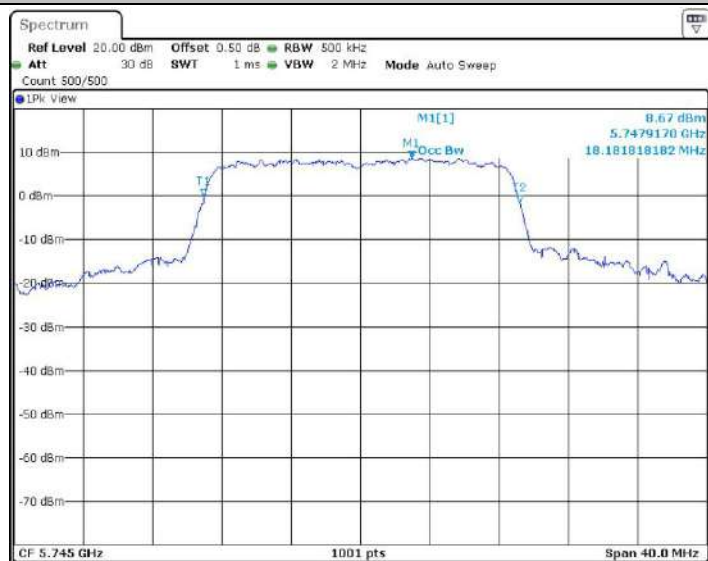
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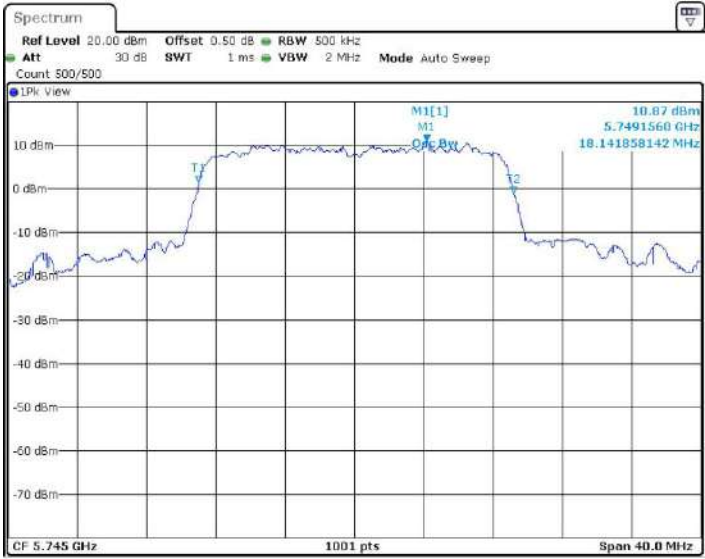
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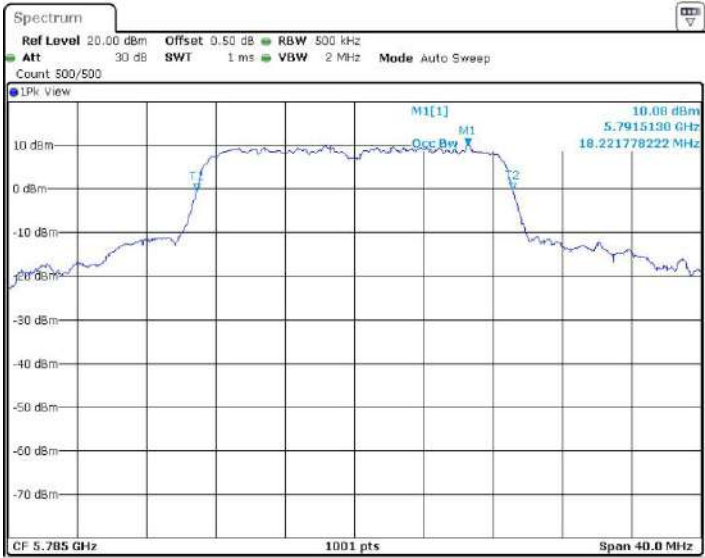
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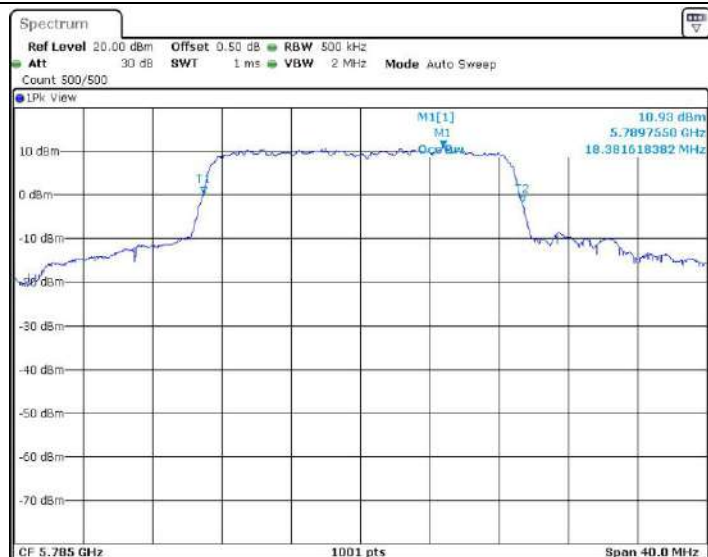
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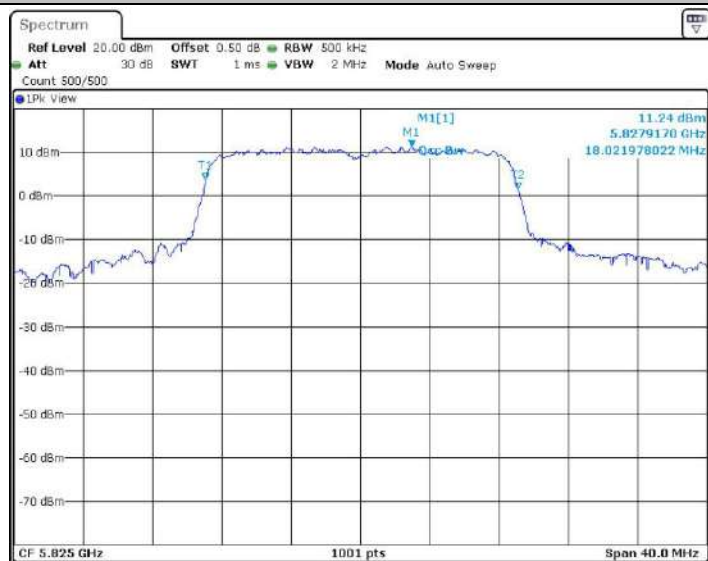
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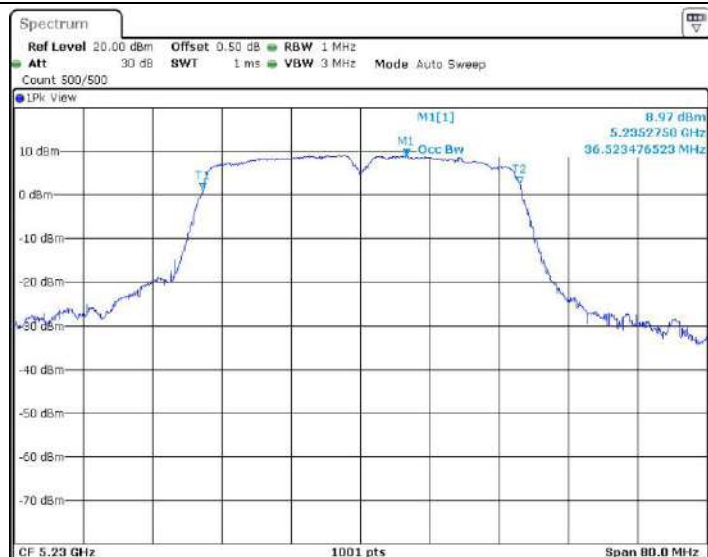


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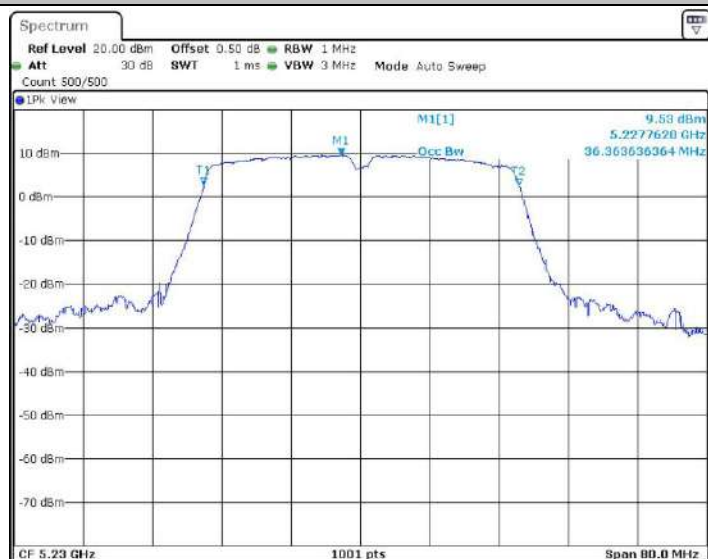


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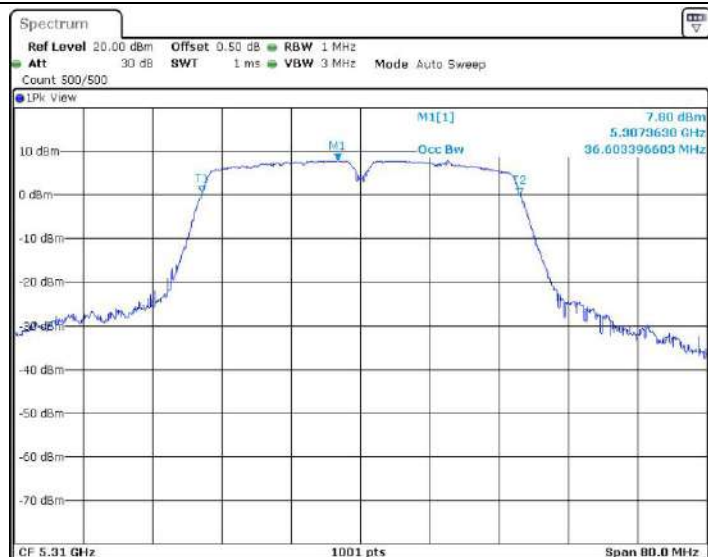


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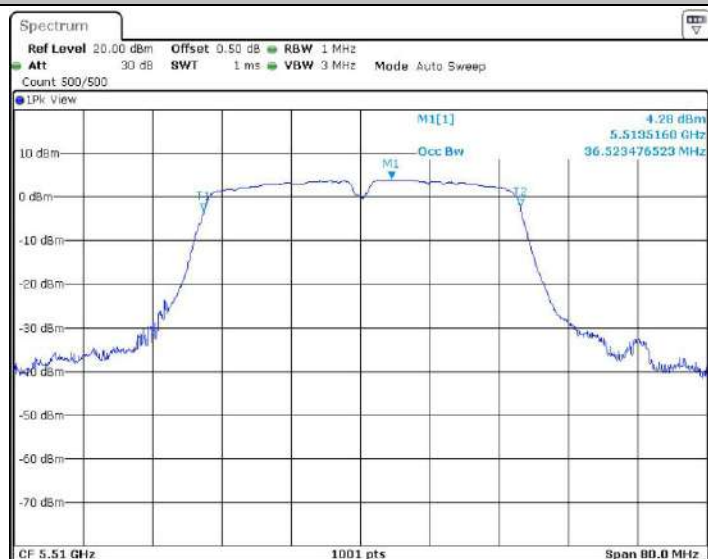


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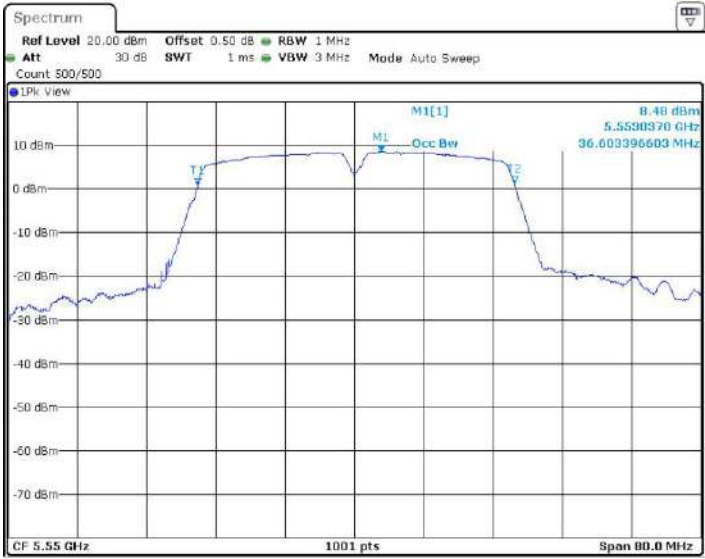
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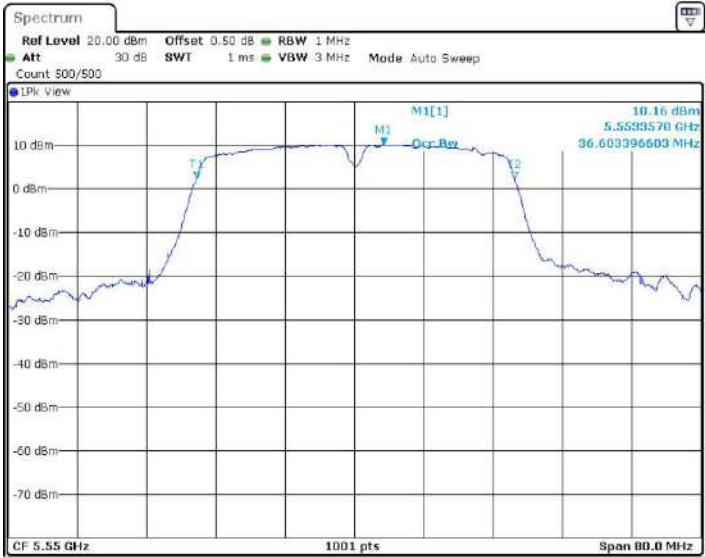
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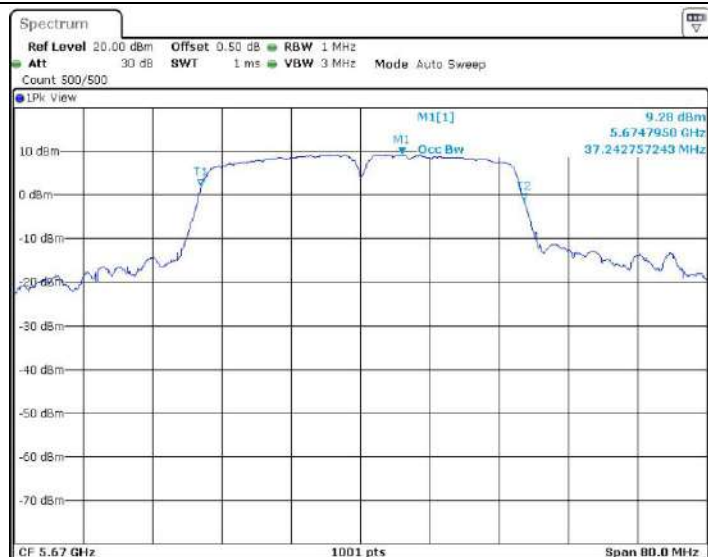
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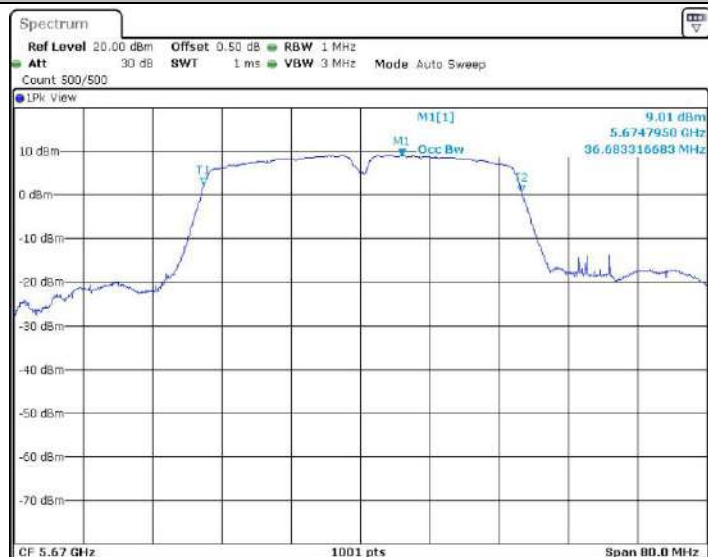
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11AC40SISO_Ant1_5550



11AC40SISO_Ant0_5670



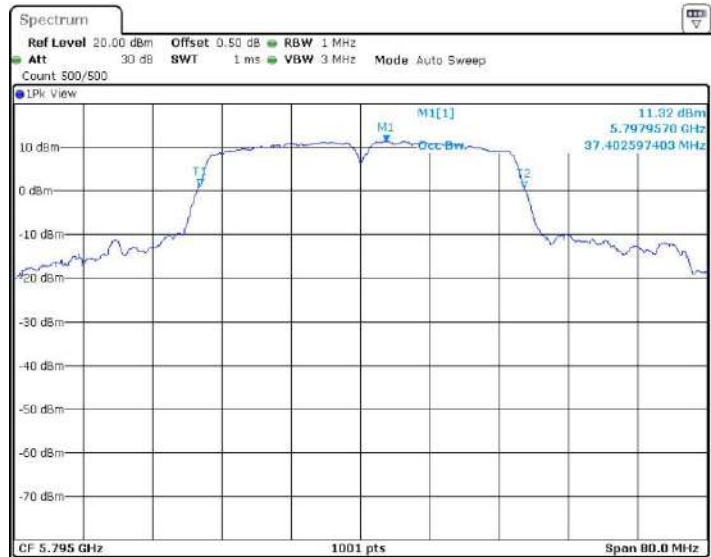
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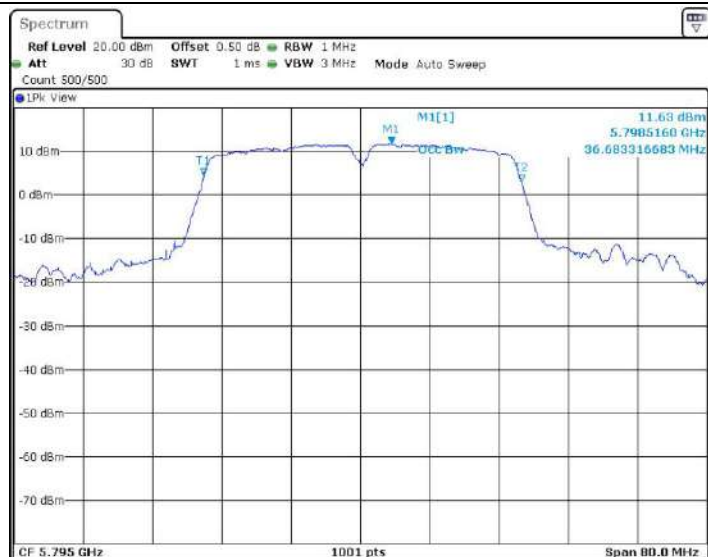
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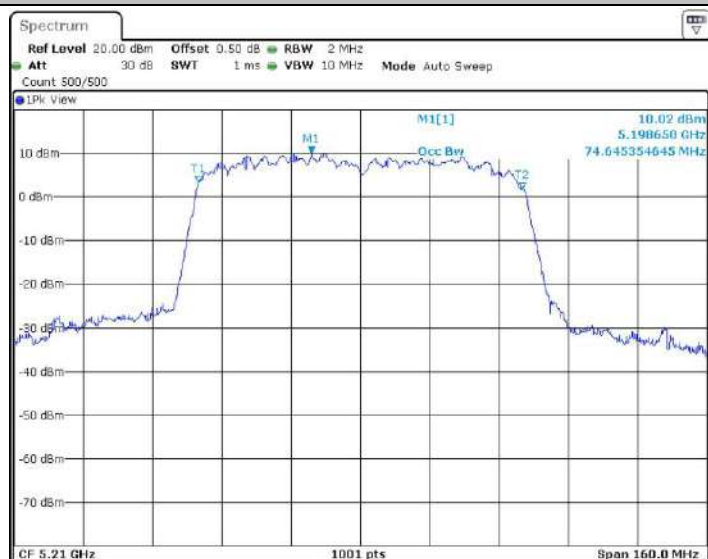
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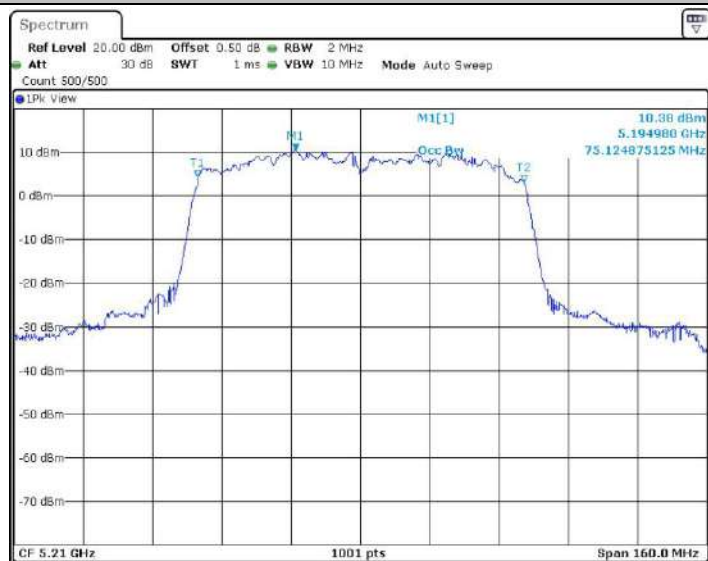
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11AC40SISO_Ant1_5795



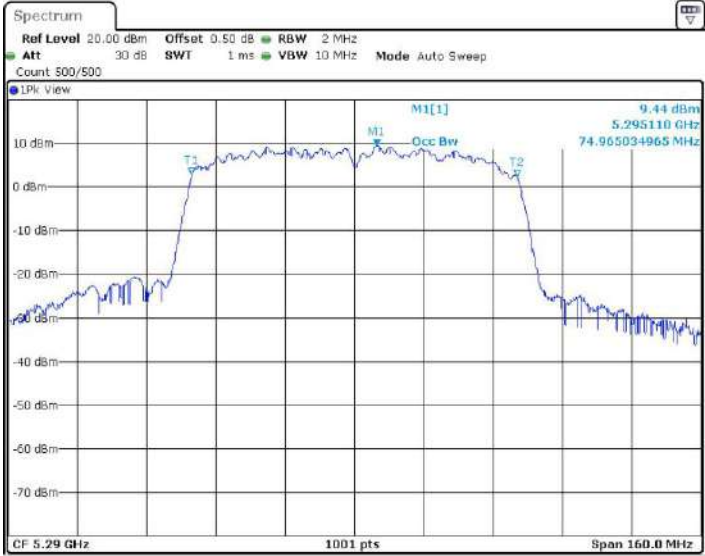
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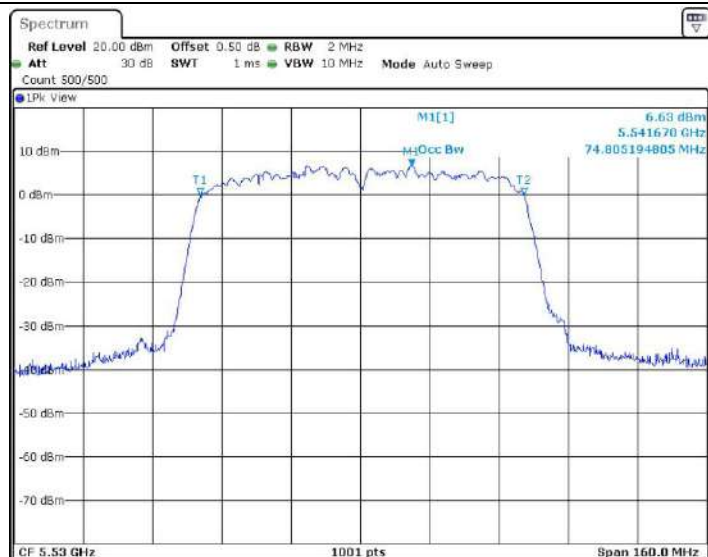
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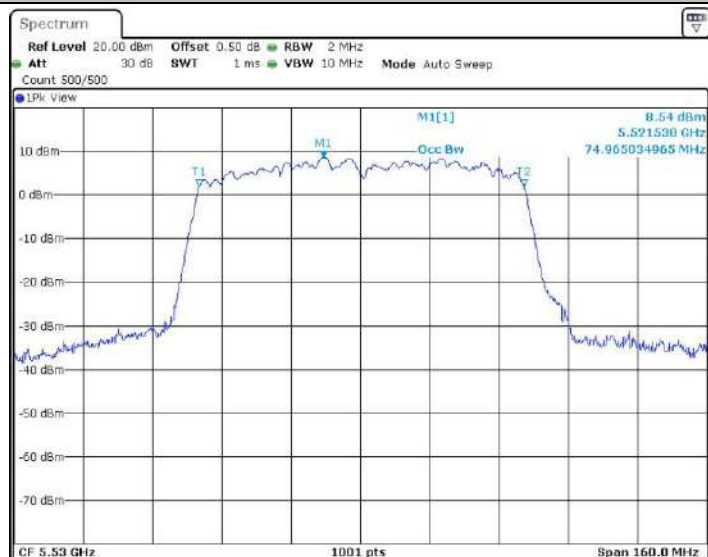
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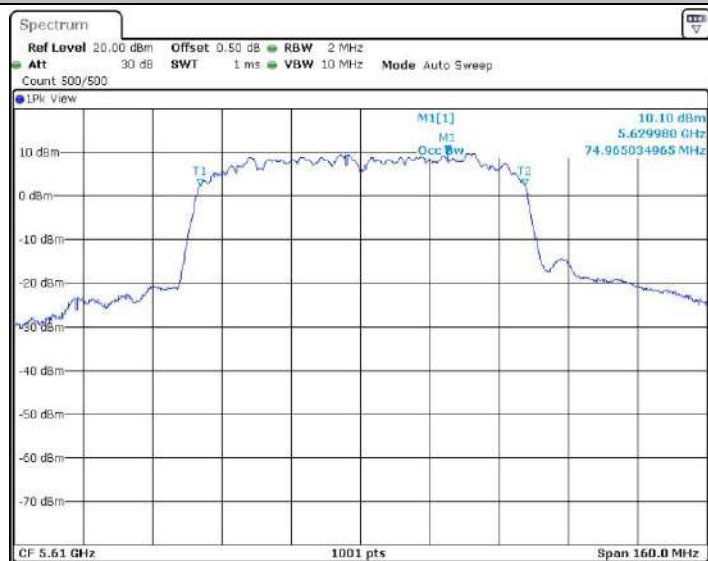
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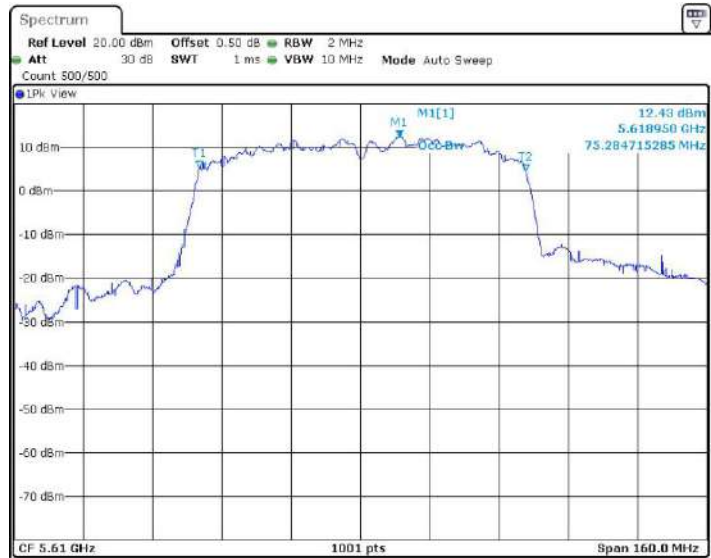
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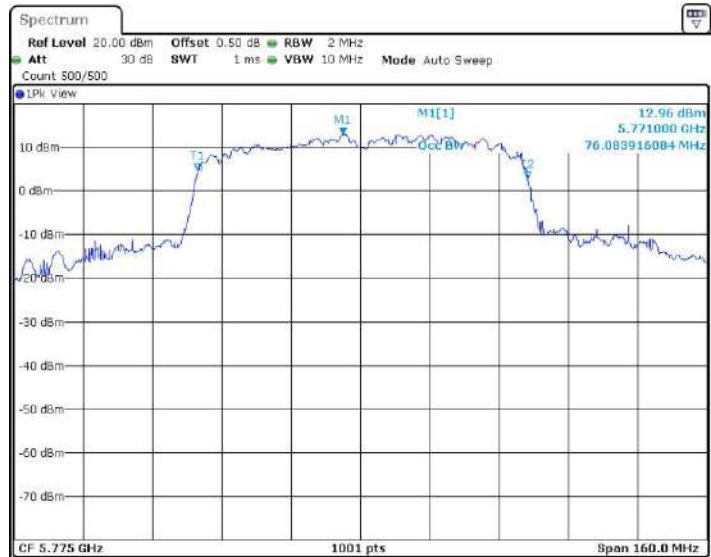
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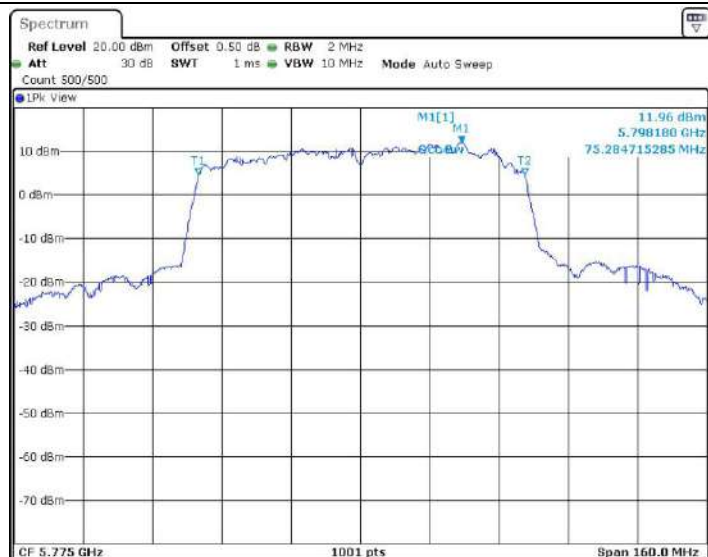
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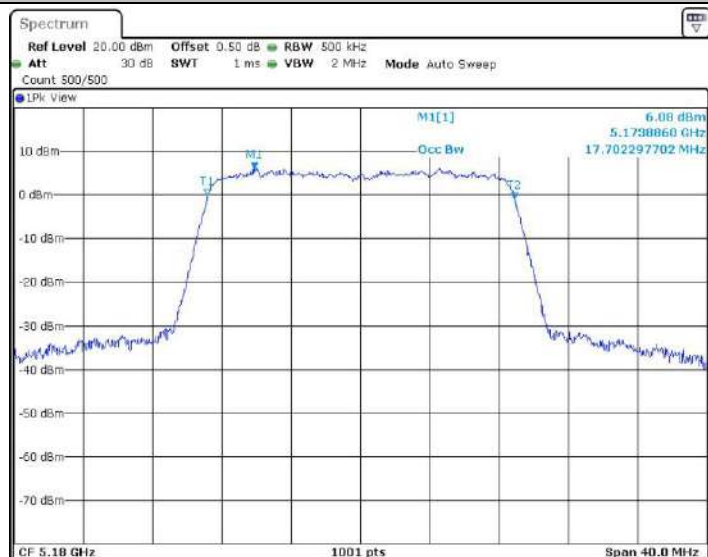
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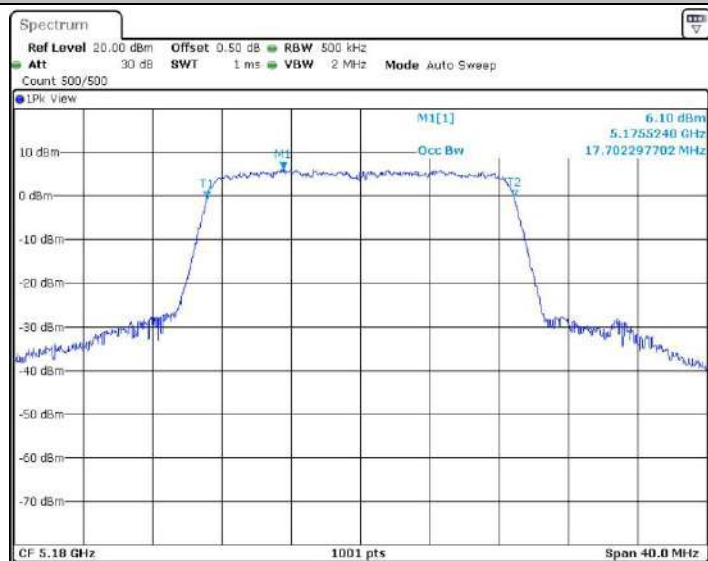
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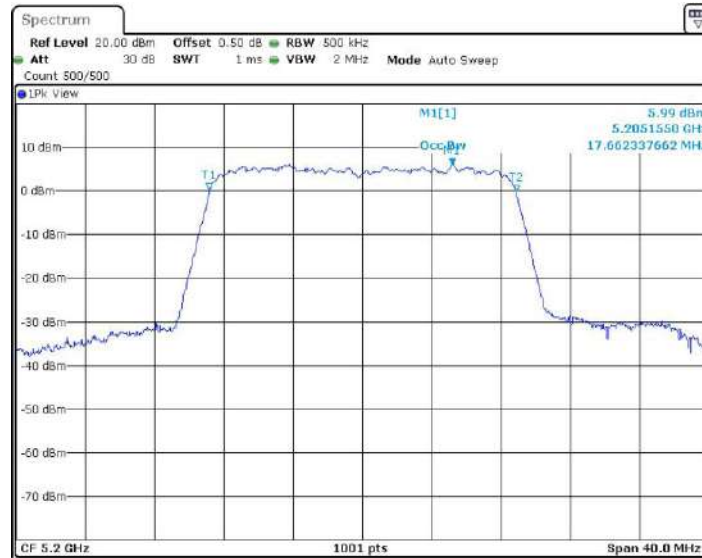
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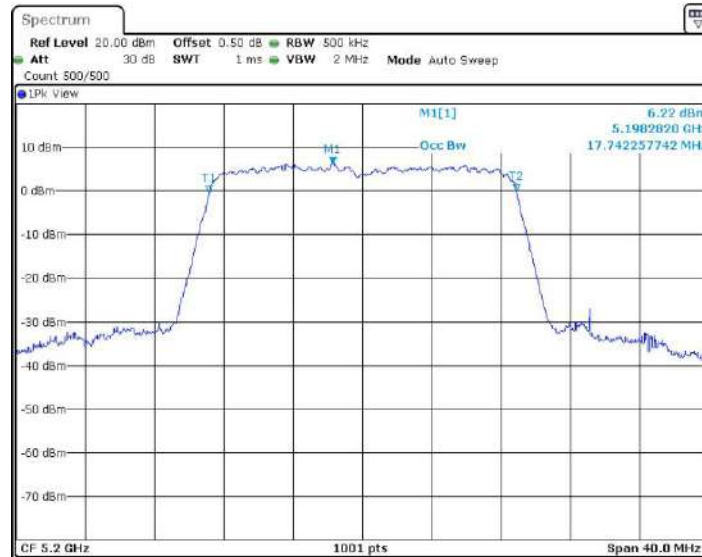
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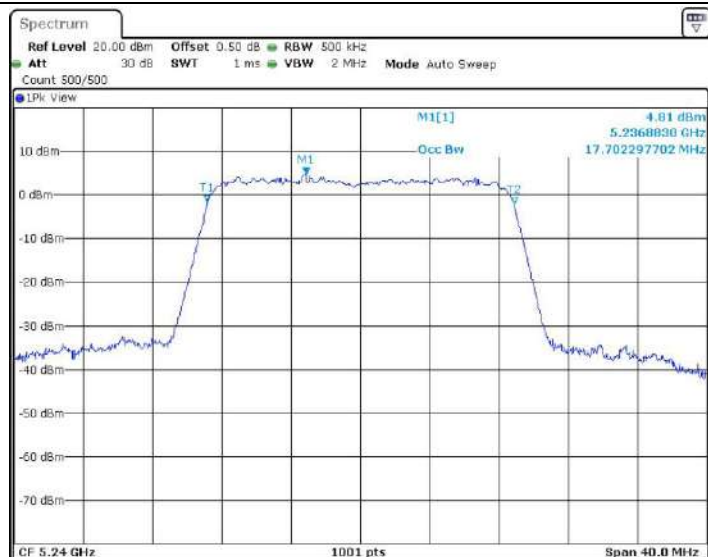
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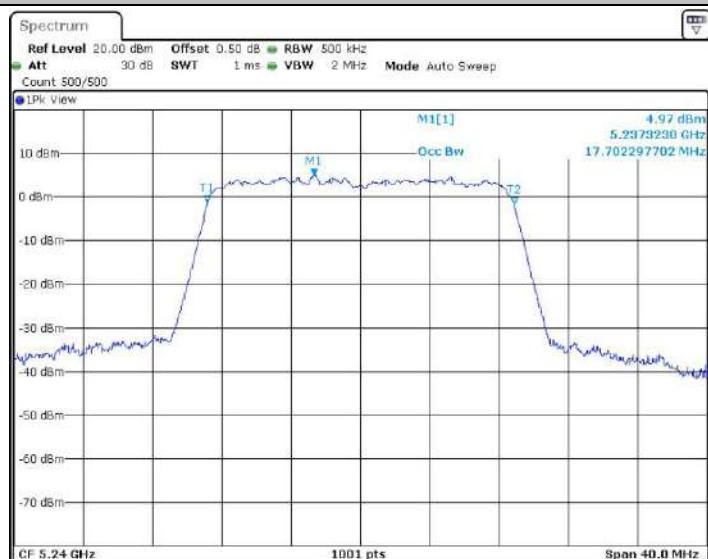
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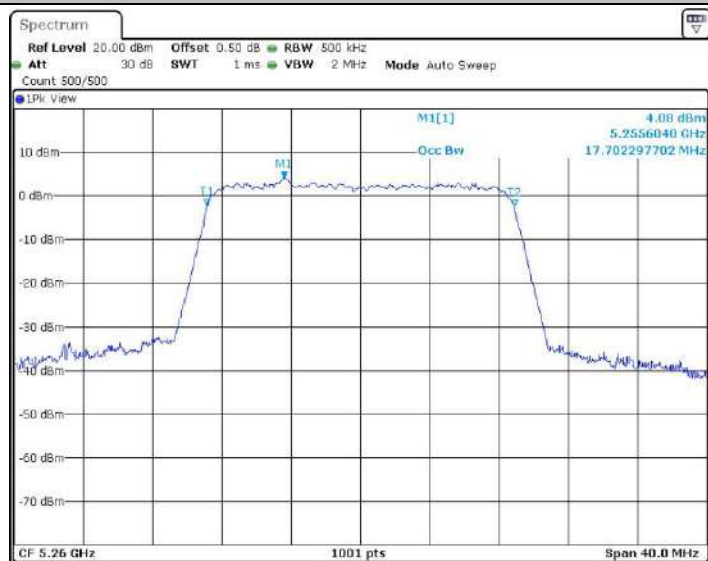
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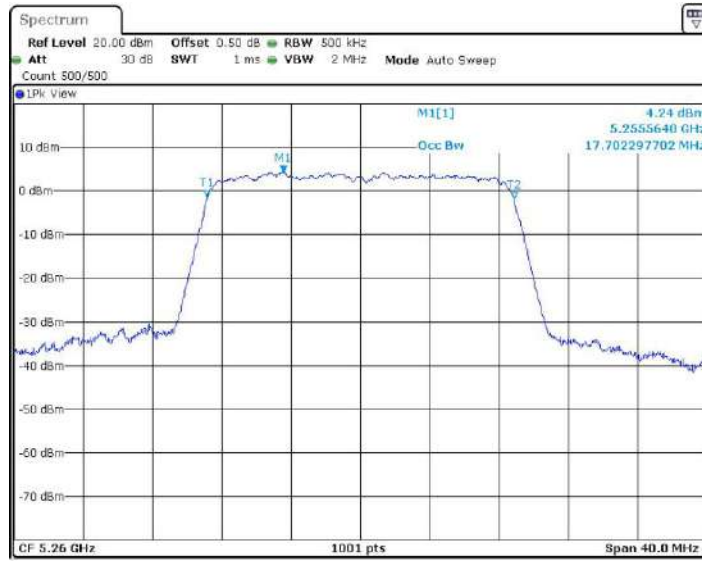
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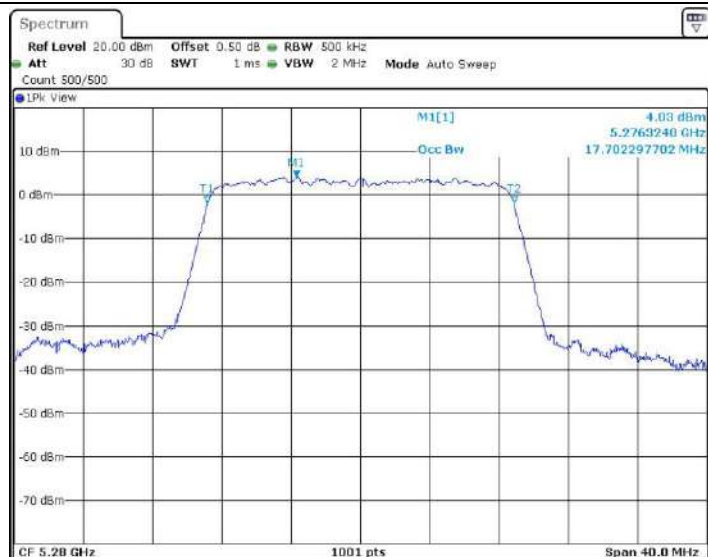
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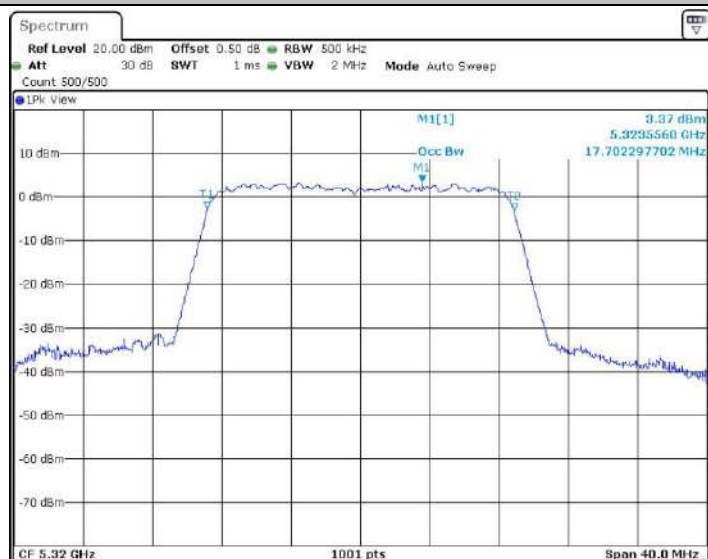
11N20MIMO_Ant1_5260



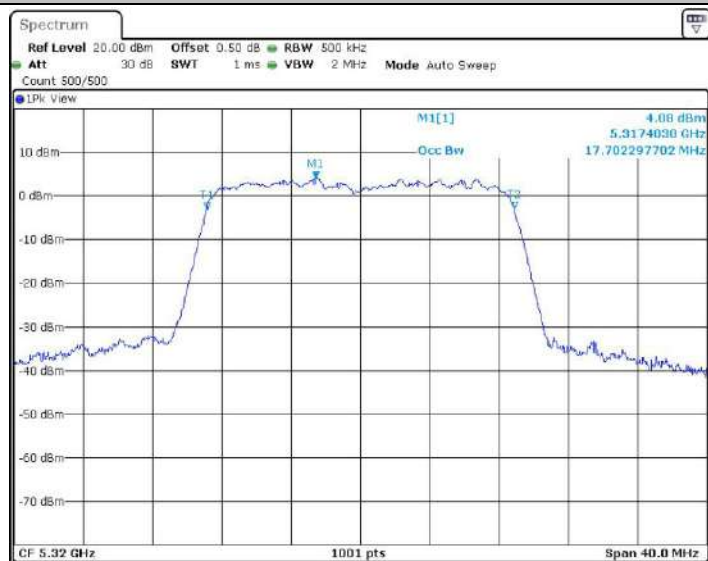
11N20MIMO_Ant0_5280



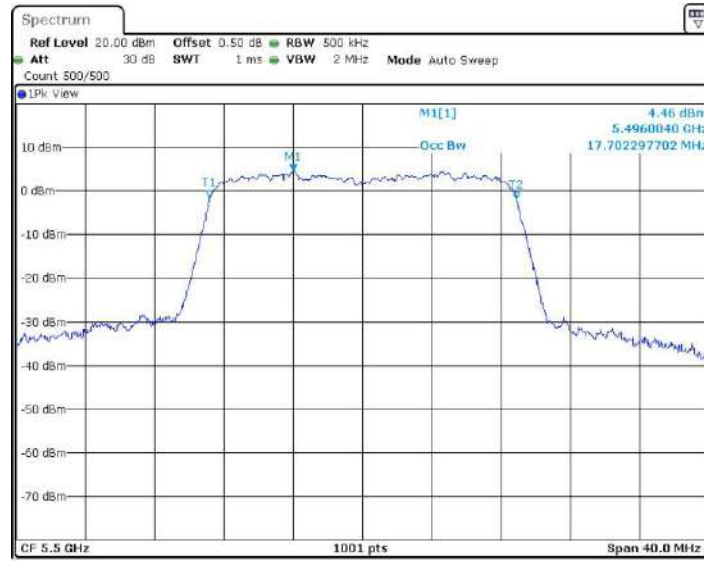
11N20MIMO_Ant1_5280



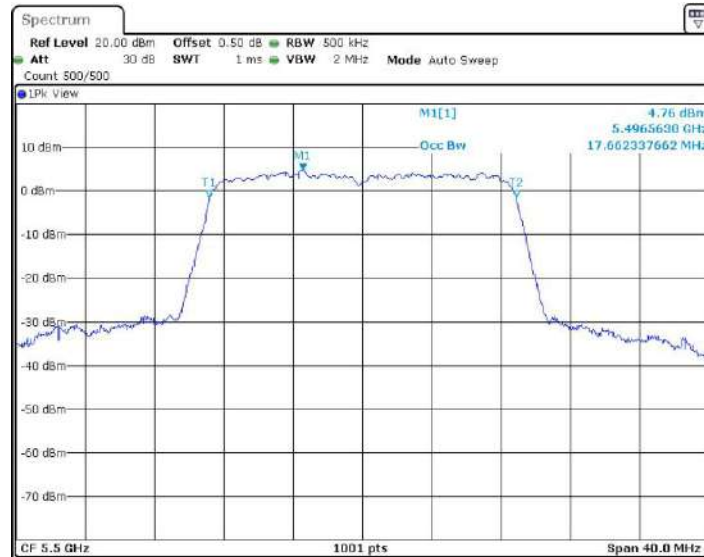
11N20MIMO_Ant0_5320



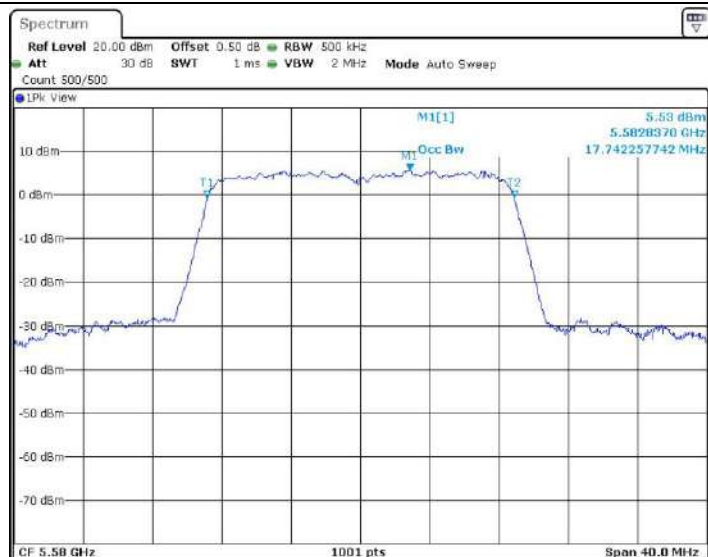
11N20MIMO_Ant1_5320



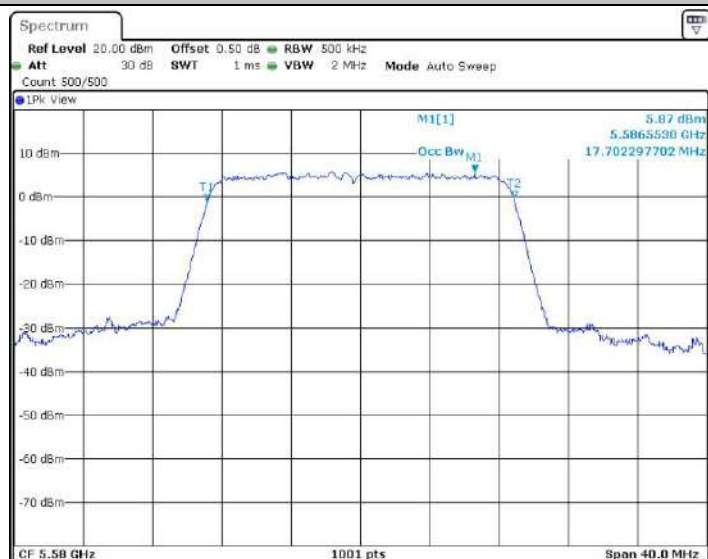
11N20MIMO_Ant0_5500



11N20MIMO_Ant1_5500



11N20MIMO_Ant0_5580



11N20MIMO_Ant1_5580



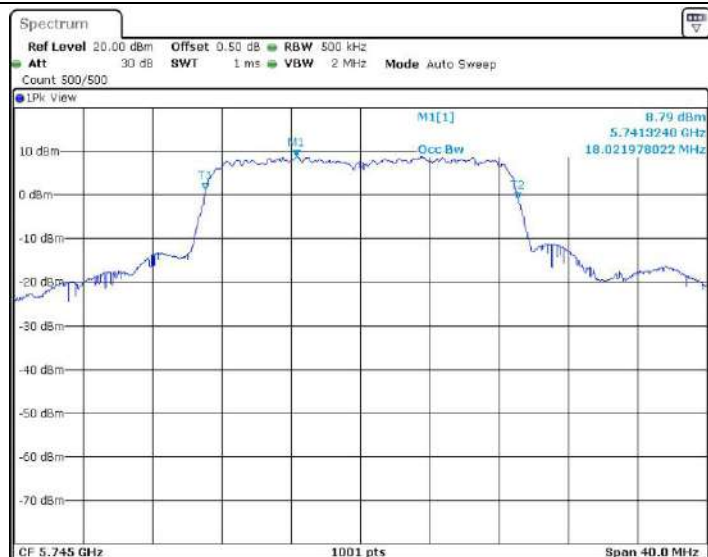
11N20MIMO_Ant0_5700



11N20MIMO_Ant1_5700



11N20MIMO_Ant0_5745



11N20MIMO_Ant1_5745



11N20MIMO_Ant0_5785



11N20MIMO_Ant1_5785



11N20MIMO_Ant0_5825



11N20MIMO_Ant1_5825



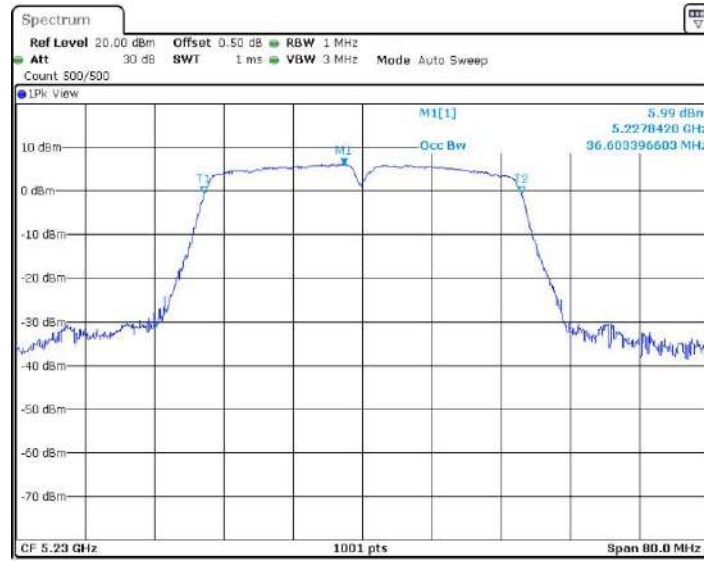
11N40MIMO_Ant0_5190



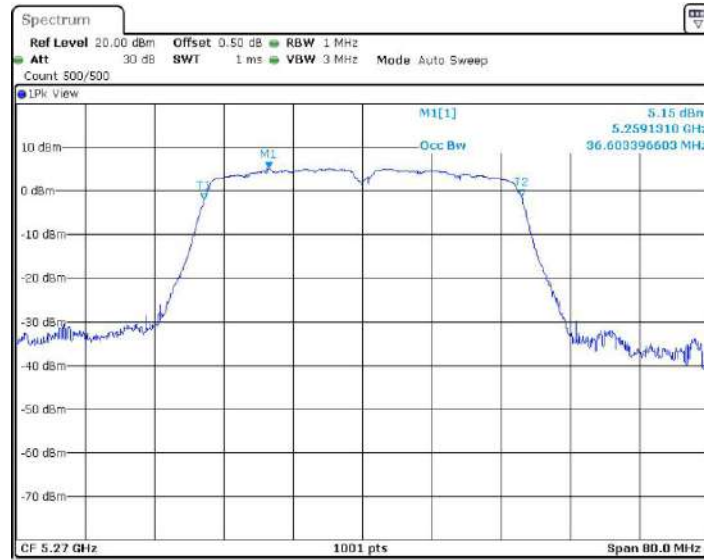
11N40MIMO_Ant1_5190



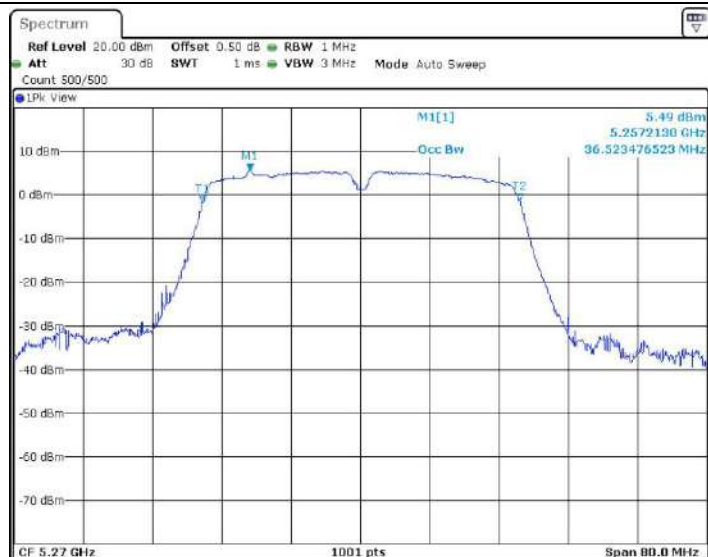
11N40MIMO_Ant0_5230



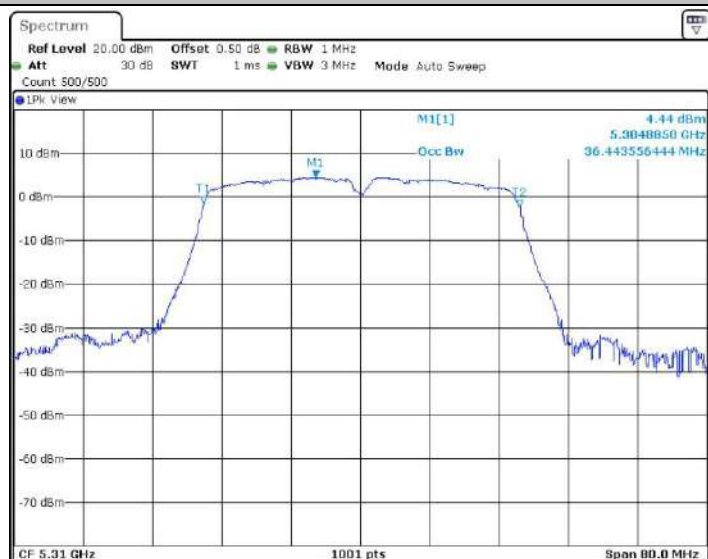
11N40MIMO_Ant1_5230



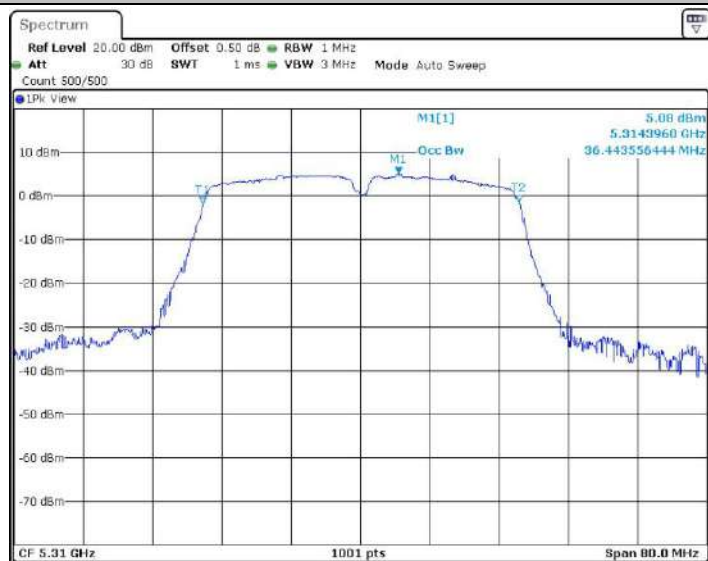
11N40MIMO_Ant0_5270



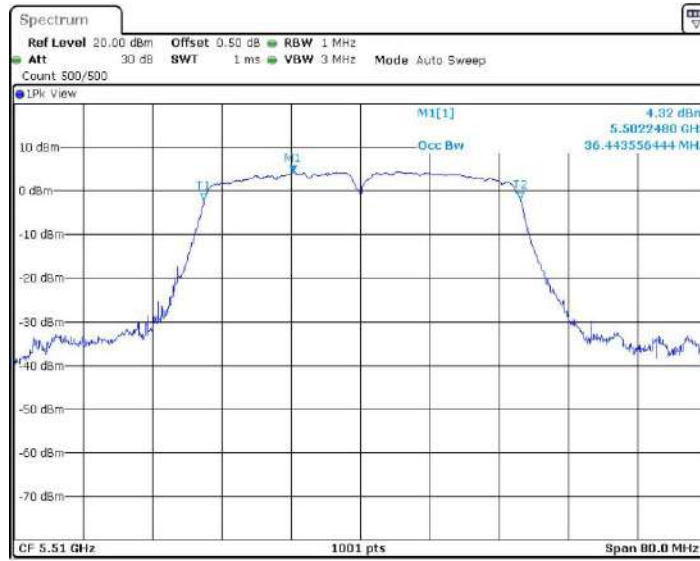
11N40MIMO_Ant1_5270



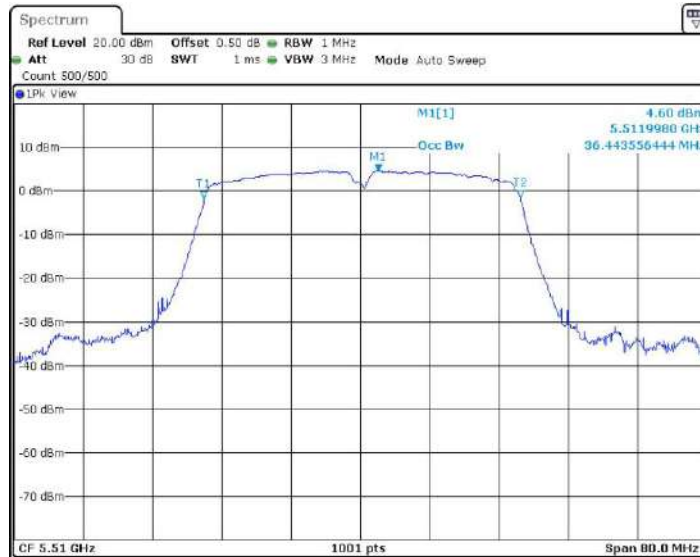
11N40MIMO_Ant0_5310



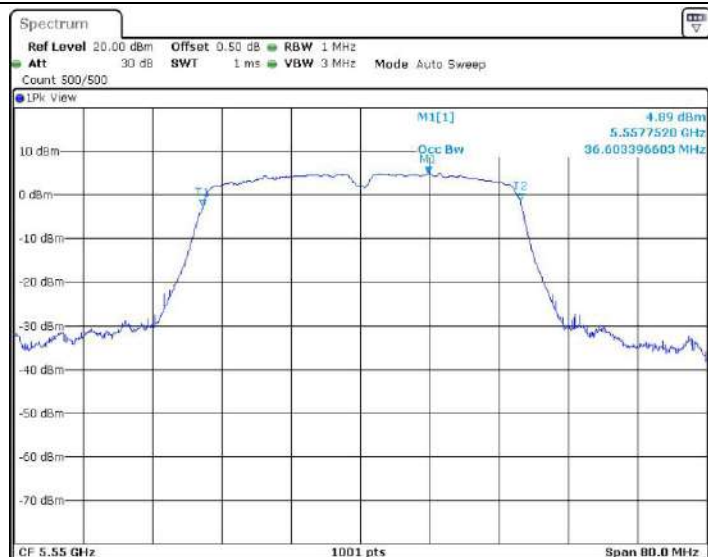
11N40MIMO_Ant1_5310



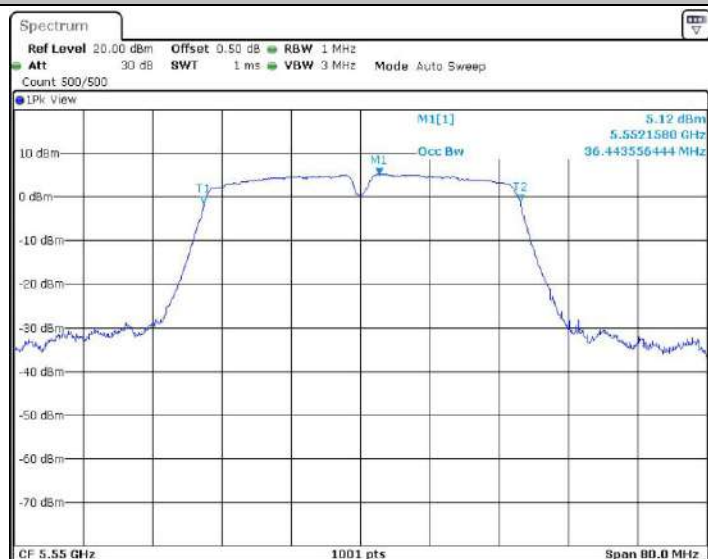
11N40MIMO_Ant0_5510



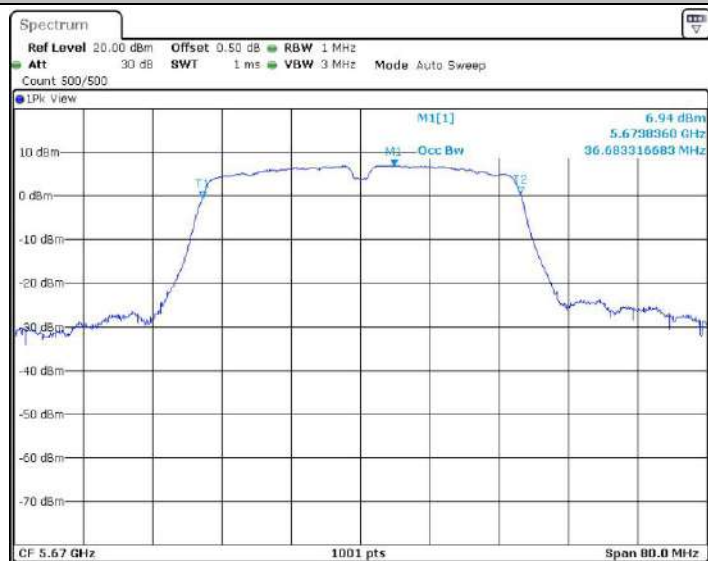
11N40MIMO_Ant1_5510



11N40MIMO_Ant0_5550



11N40MIMO_Ant1_5550



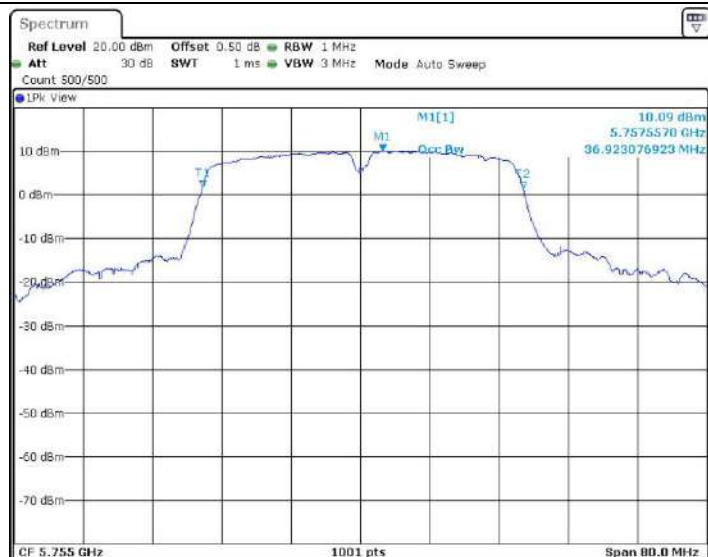
11N40MIMO_Ant0_5670



11N40MIMO_Ant1_5670



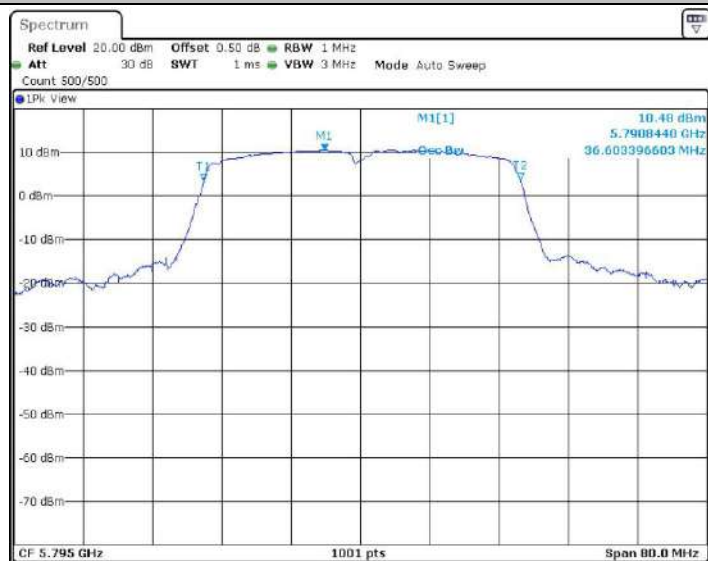
11N40MIMO_Ant0_5755



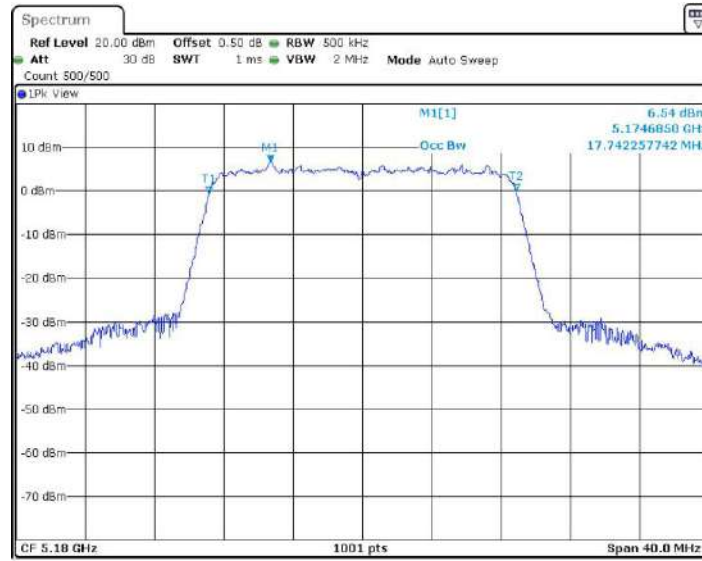
11N40MIMO_Ant1_5755



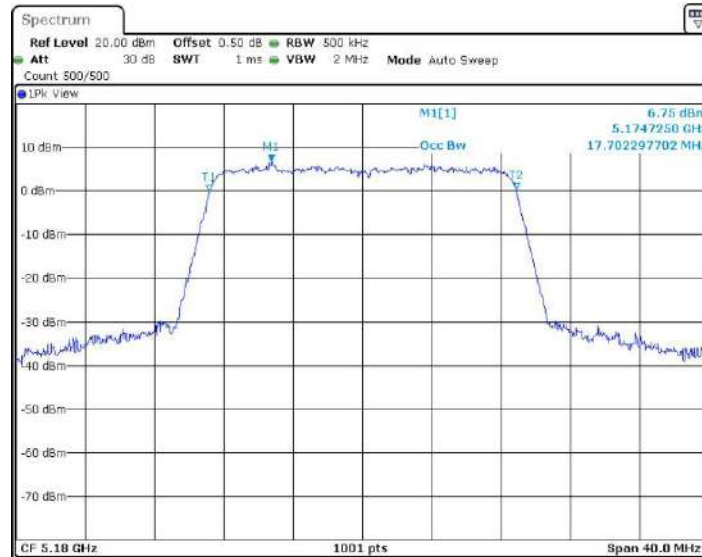
11N40MIMO_Ant0_5795



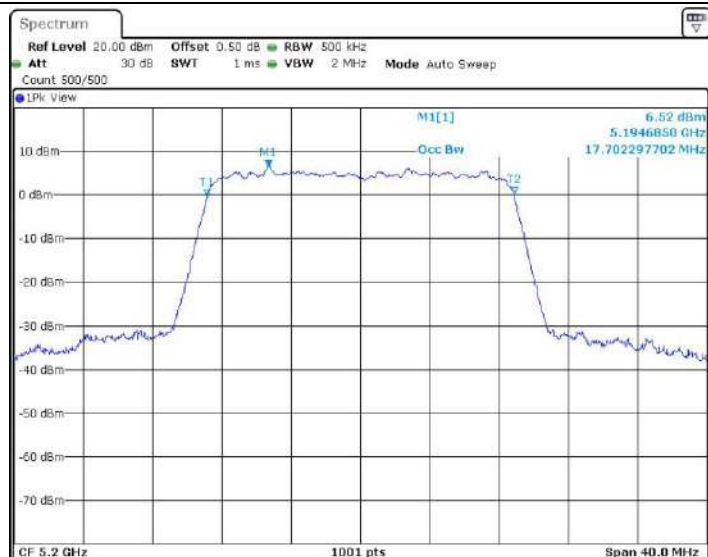
11N40MIMO_Ant1_5795



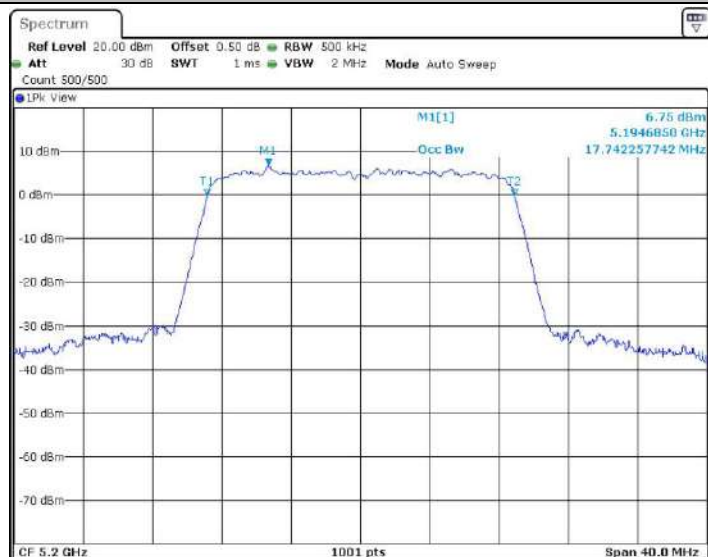
11AC20MIMO_Ant0_5180



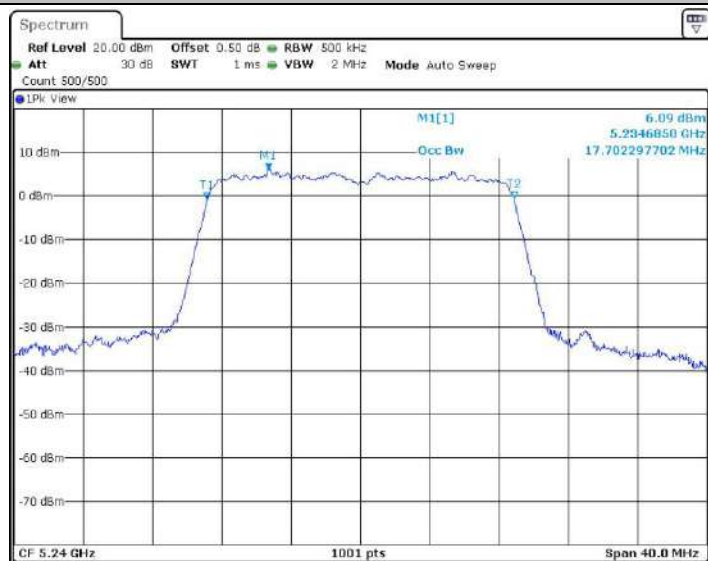
11AC20MIMO_Ant1_5180



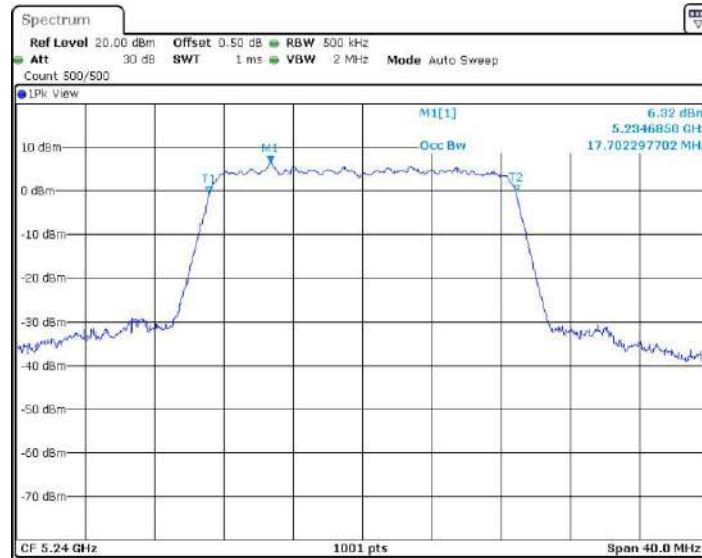
11AC20MIMO_Ant0_5200



11AC20MIMO_Ant1_5200



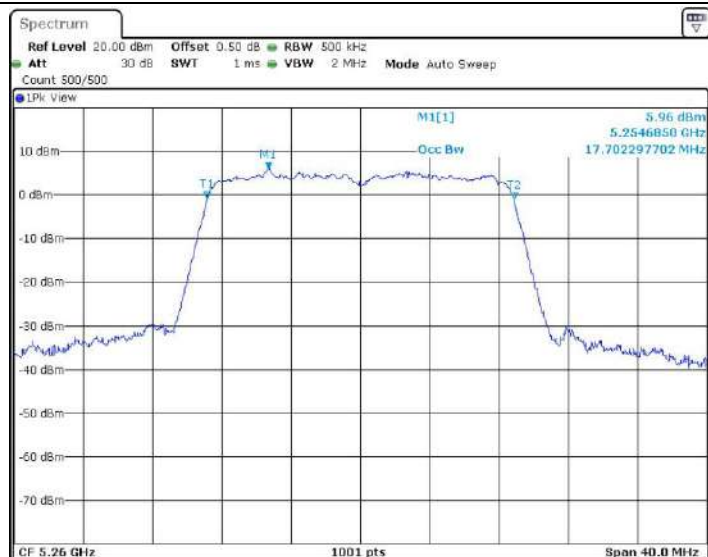
11AC20MIMO_Ant0_5240



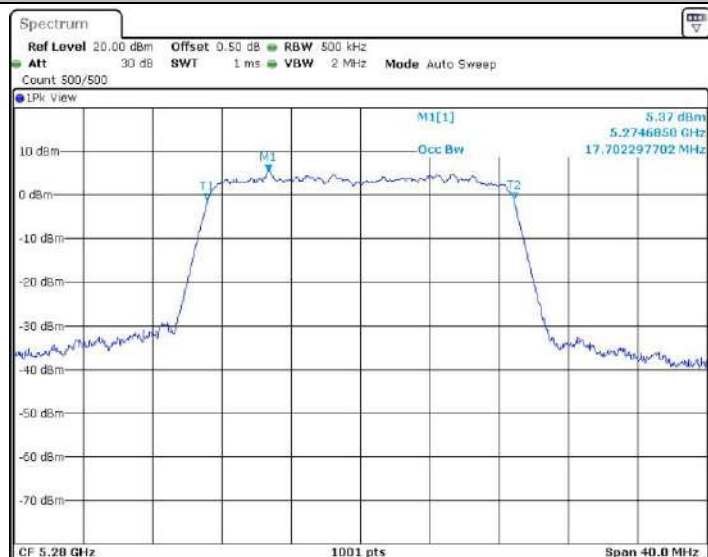
11AC20MIMO_Ant1_5240



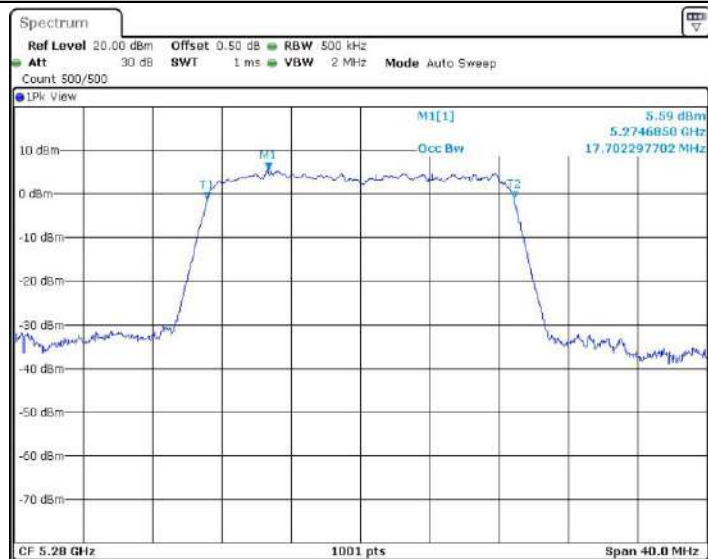
11AC20MIMO_Ant0_5260



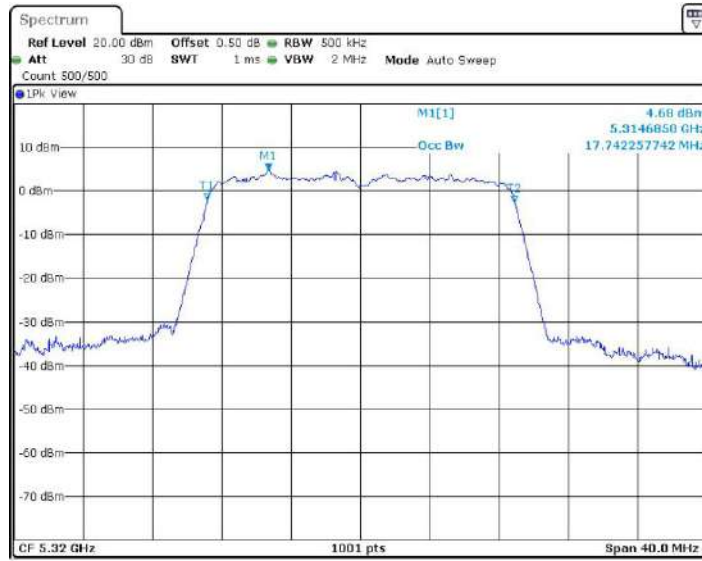
11AC20MIMO_Ant1_5260



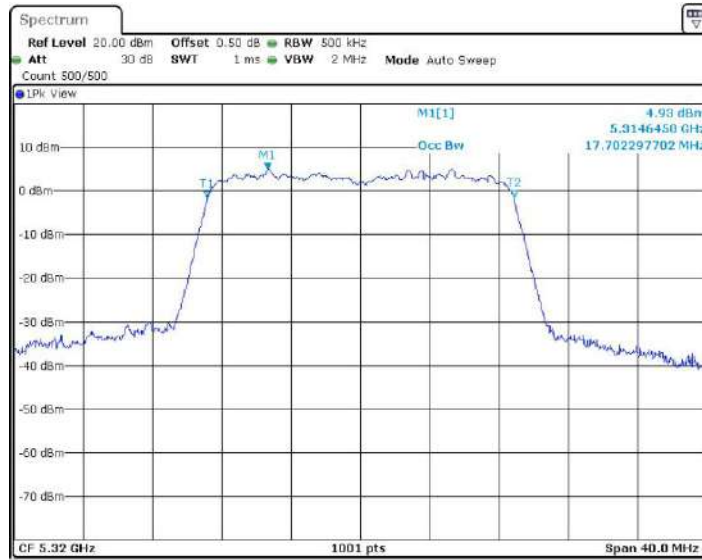
11AC20MIMO_Ant0_5280



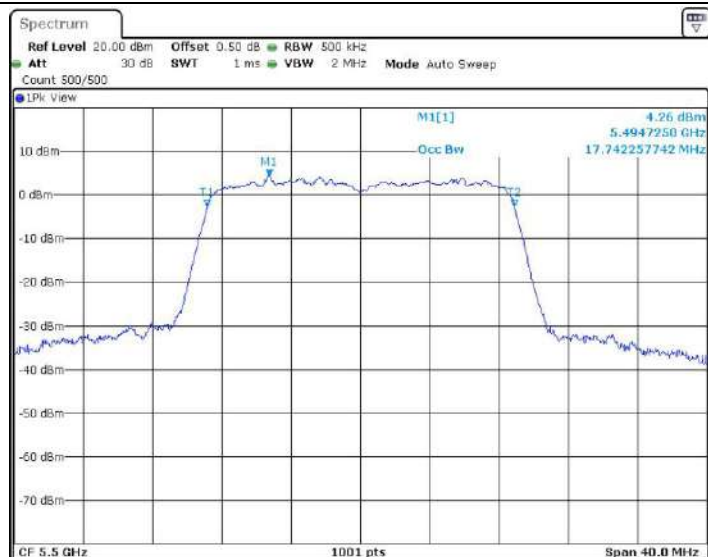
11AC20MIMO_Ant1_5280



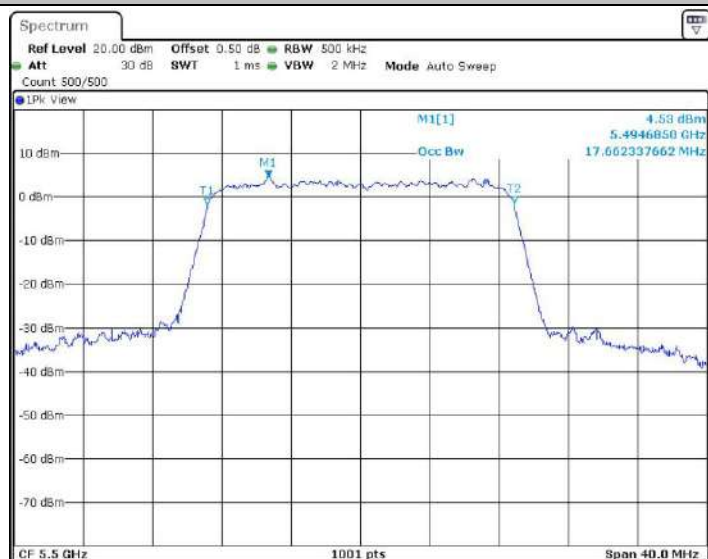
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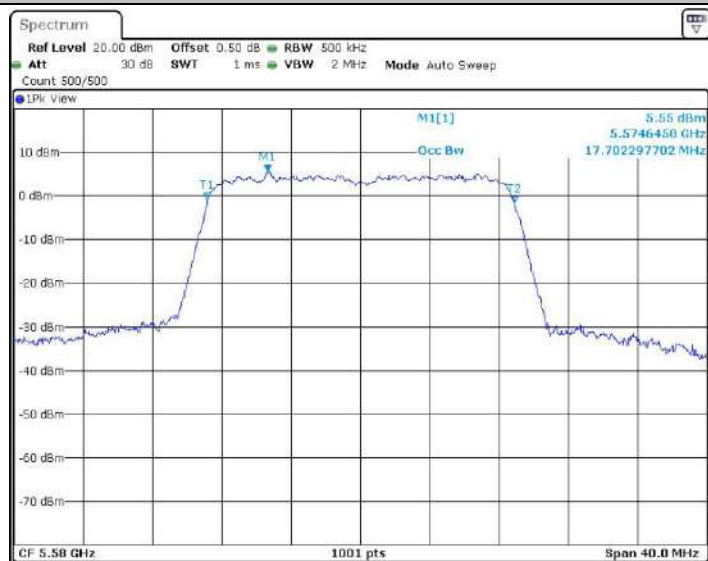
11AC20MIMO_Ant1_5320



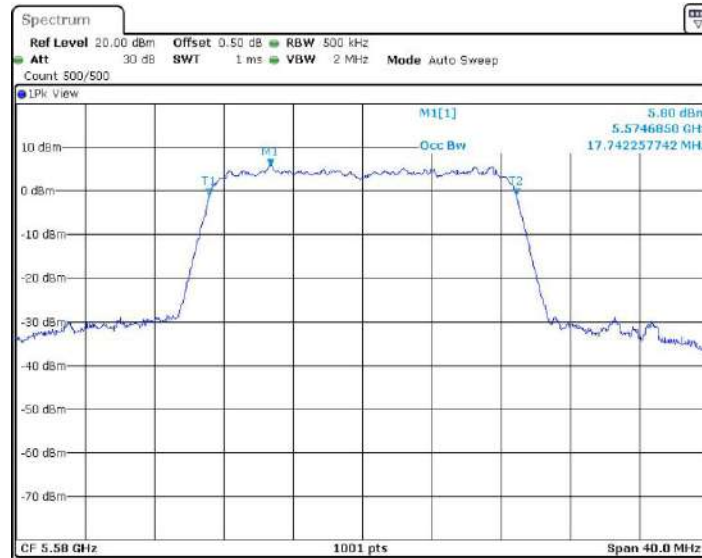
11AC20MIMO_Ant0_5500



11AC20MIMO_Ant1_5500



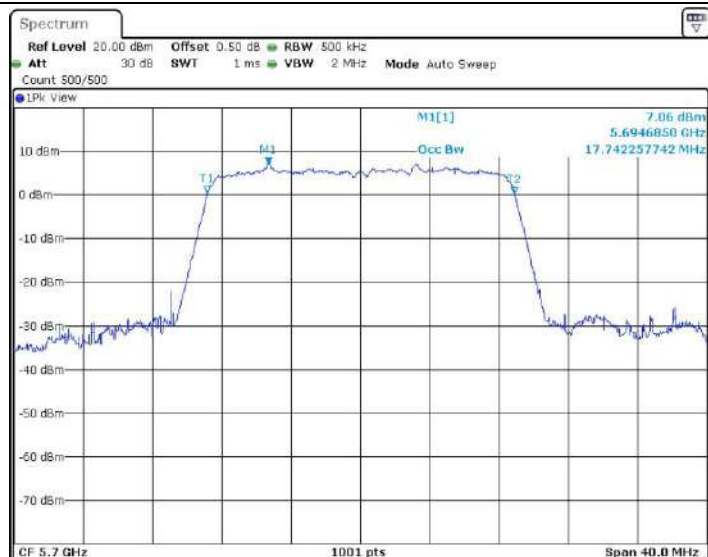
11AC20MIMO_Ant0_5580



11AC20MIMO_Ant1_5580



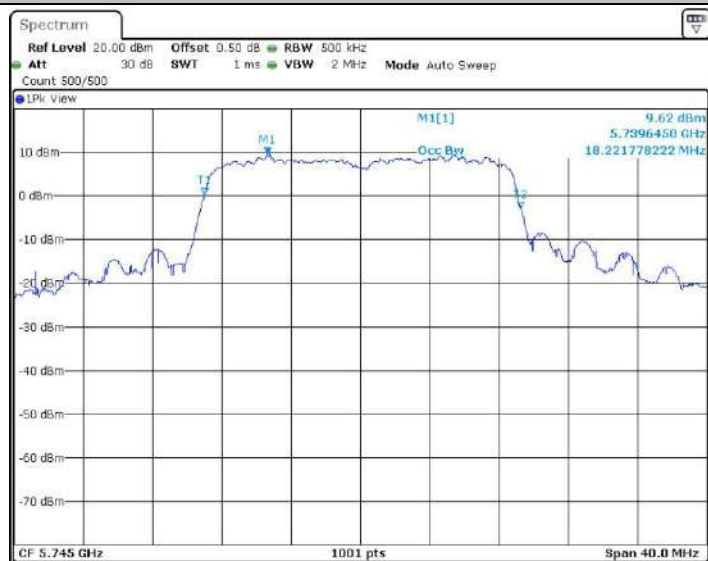
11AC20MIMO_Ant0_5700



11AC20MIMO_Ant1_5700



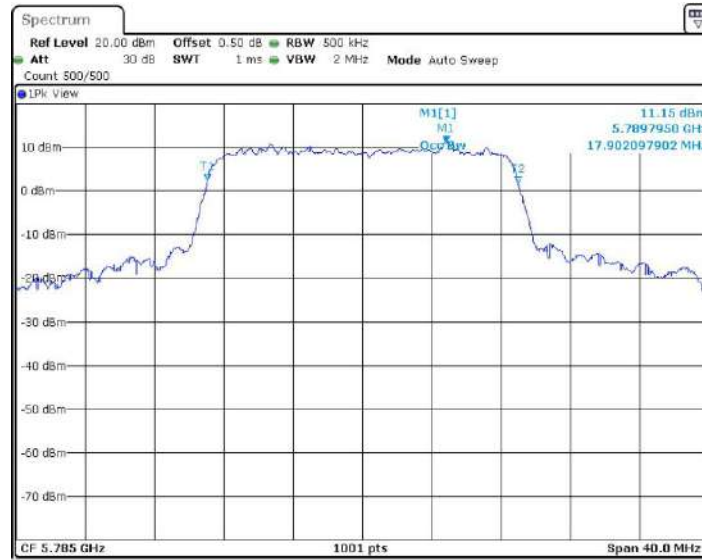
11AC20MIMO_Ant0_5745



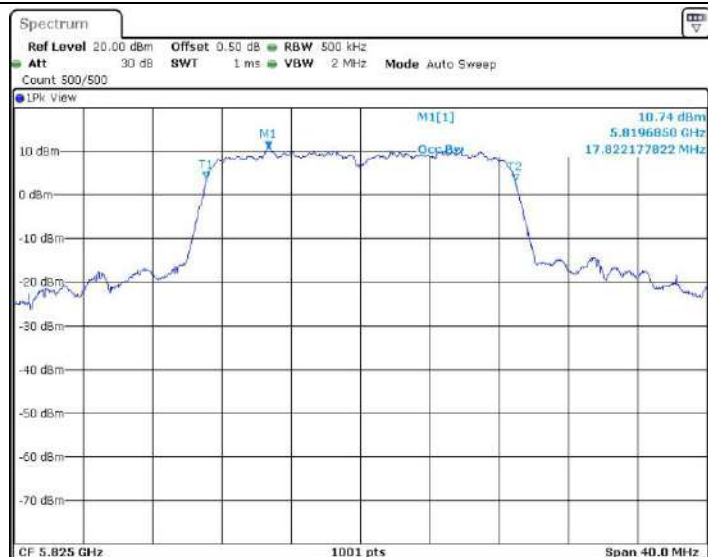
11AC20MIMO_Ant1_5745



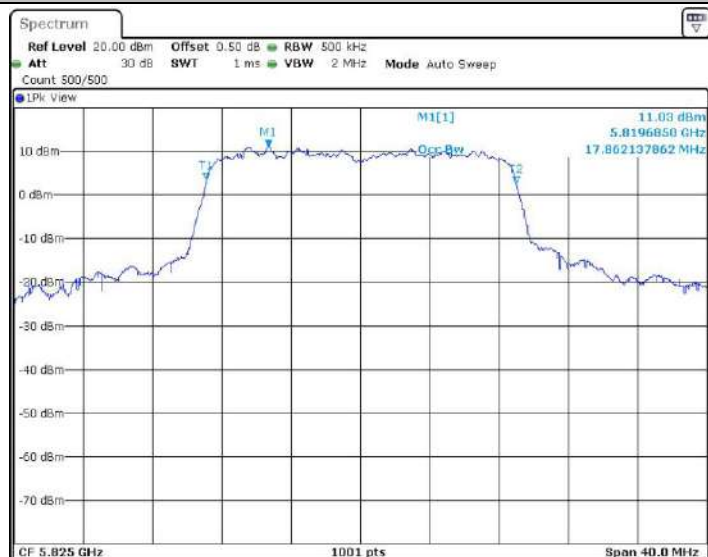
11AC20MIMO_Ant0_5785



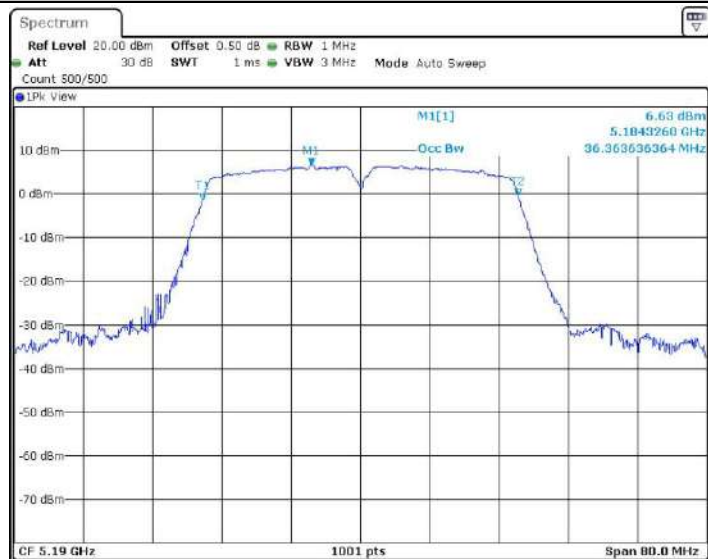
11AC20MIMO_Ant1_5785



11AC20MIMO_Ant0_5825



11AC20MIMO_Ant1_5825



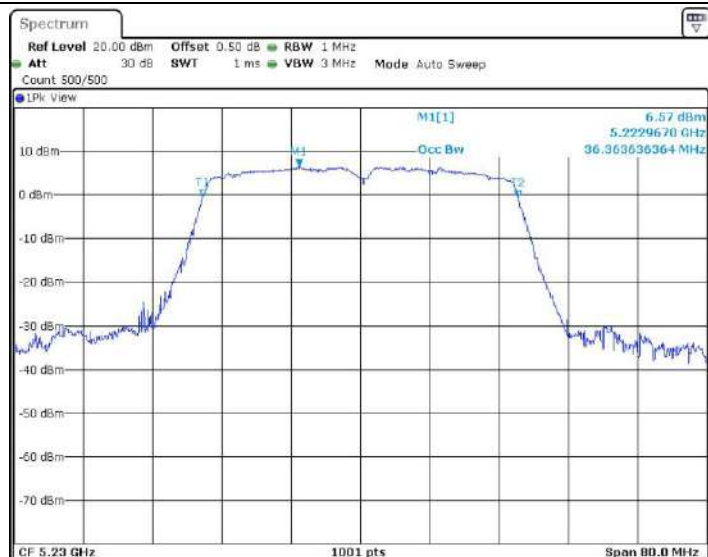
11AC40MIMO_Ant0_5190



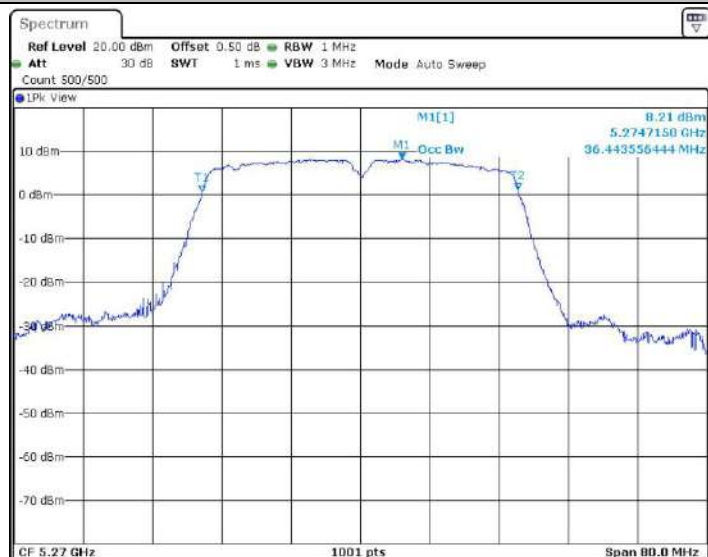
11AC40MIMO_Ant1_5190



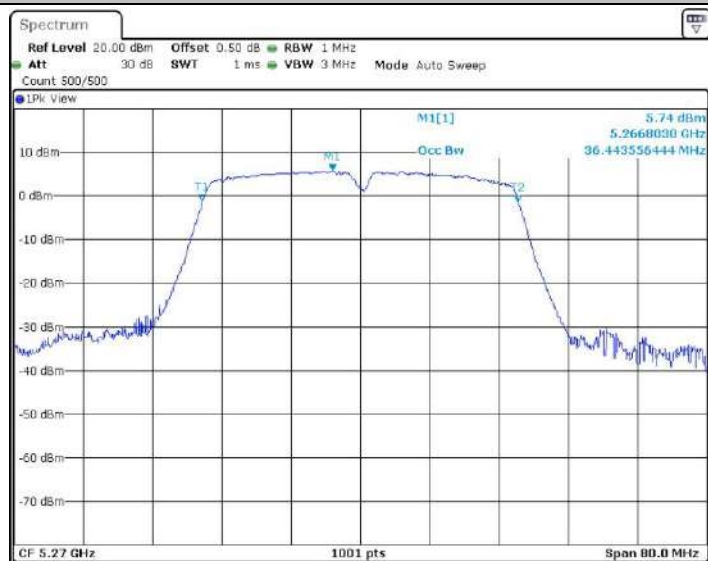
11AC40MIMO_Ant0_5230



11AC40MIMO_Ant1_5230



11AC40MIMO_Ant0_5270



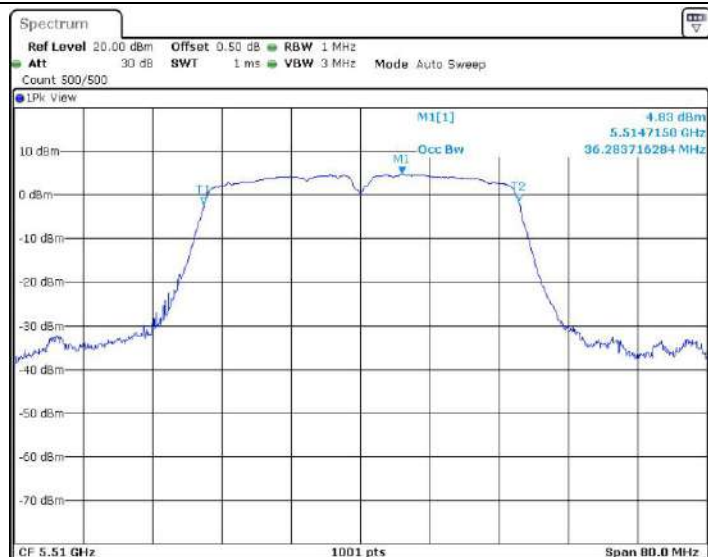
11AC40MIMO_Ant1_5270



11AC40MIMO_Ant0_5310



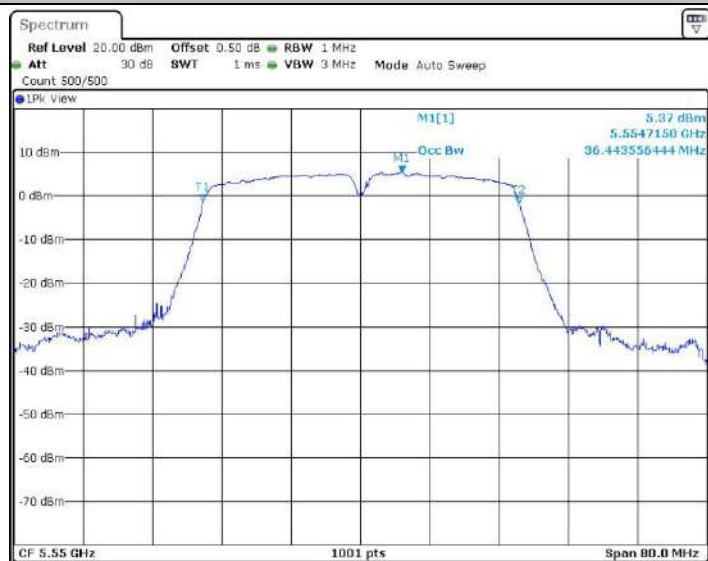
11AC40MIMO_Ant1_5310



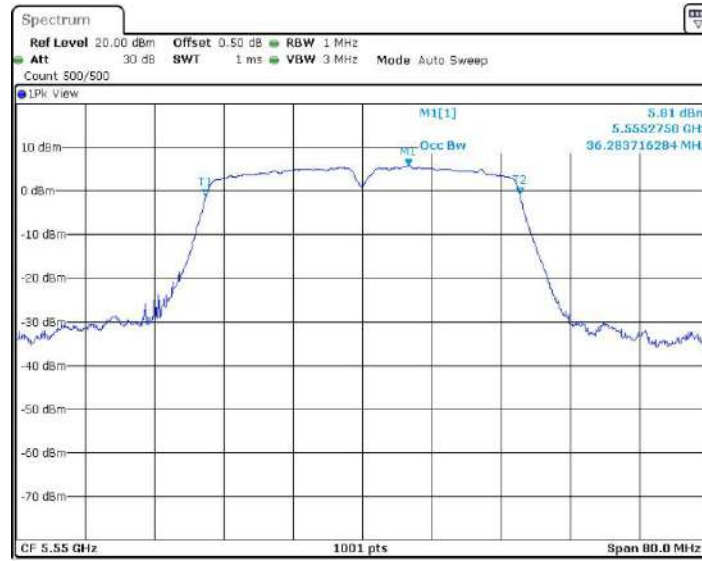
11AC40MIMO_Ant0_5510



11AC40MIMO_Ant1_5510



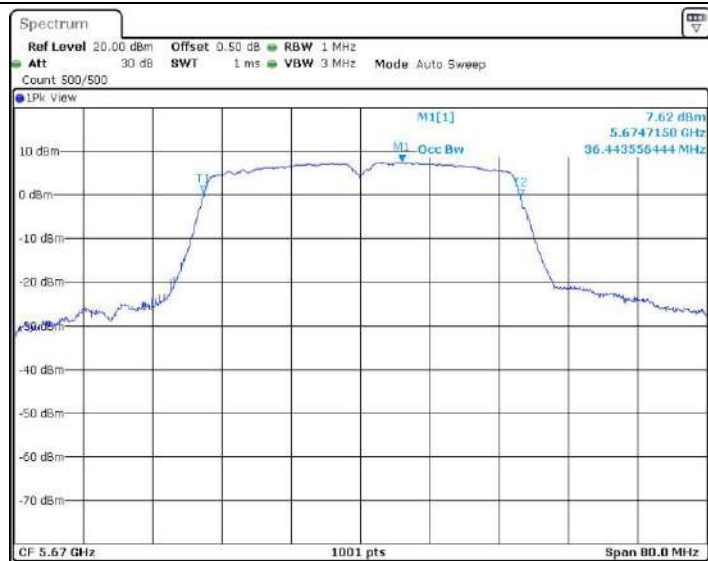
11AC40MIMO_Ant0_5550



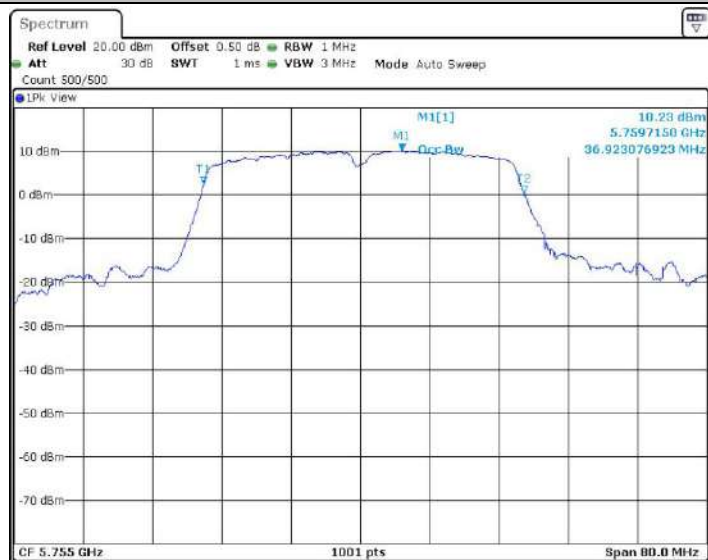
11AC40MIMO_Ant1_5550



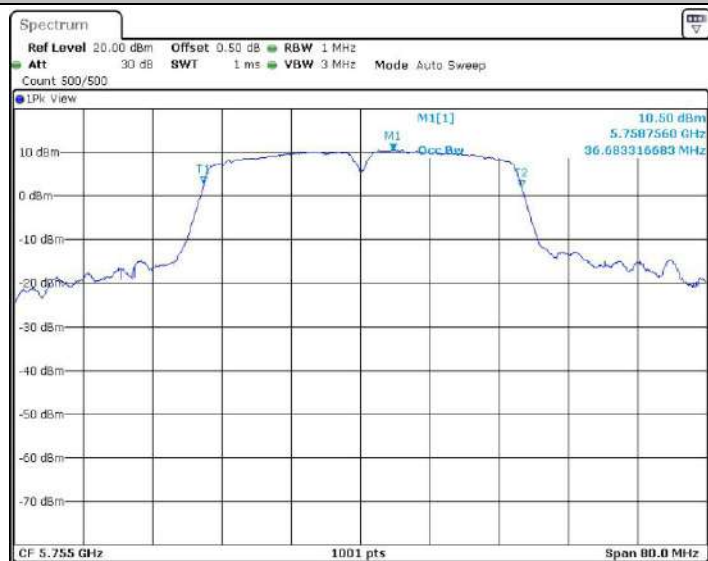
11AC40MIMO_Ant0_5670



11AC40MIMO_Ant1_5670



11AC40MIMO_Ant0_5755



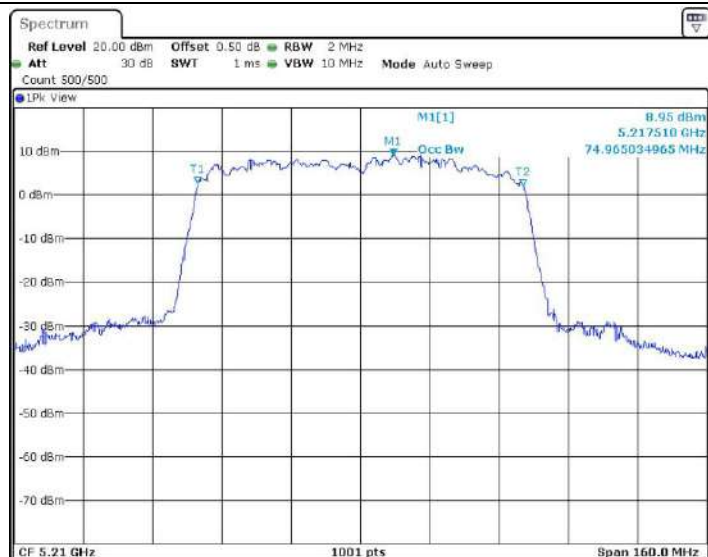
11AC40MIMO_Ant1_5755



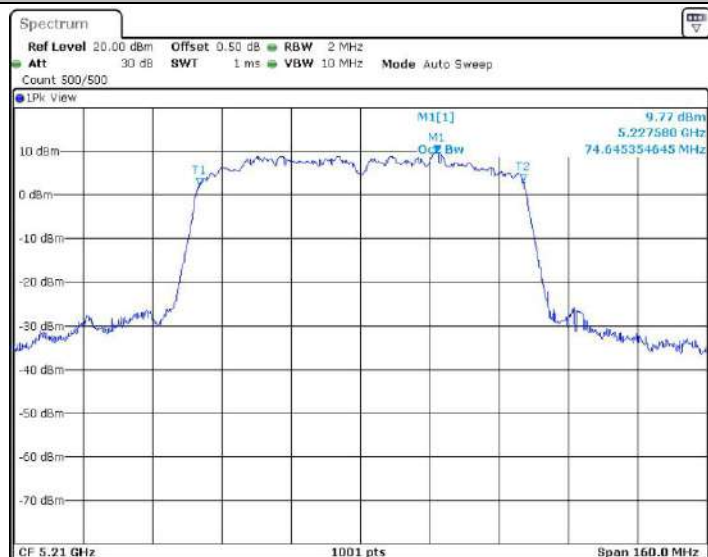
11AC40MIMO_Ant0_5795



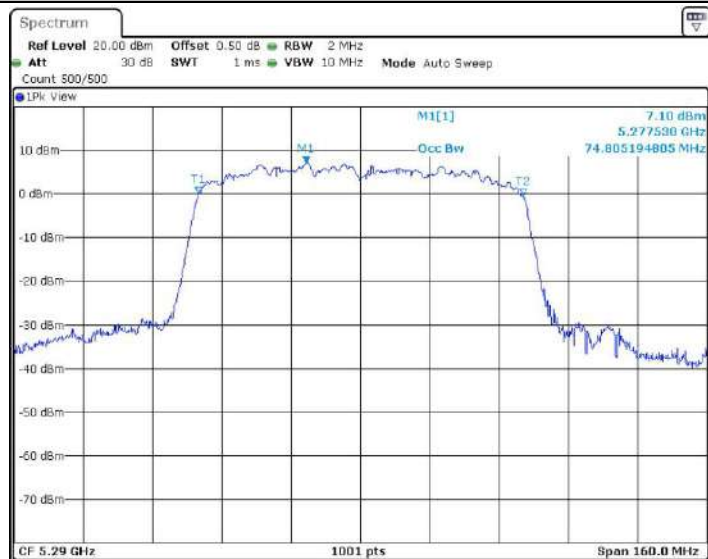
11AC40MIMO_Ant1_5795



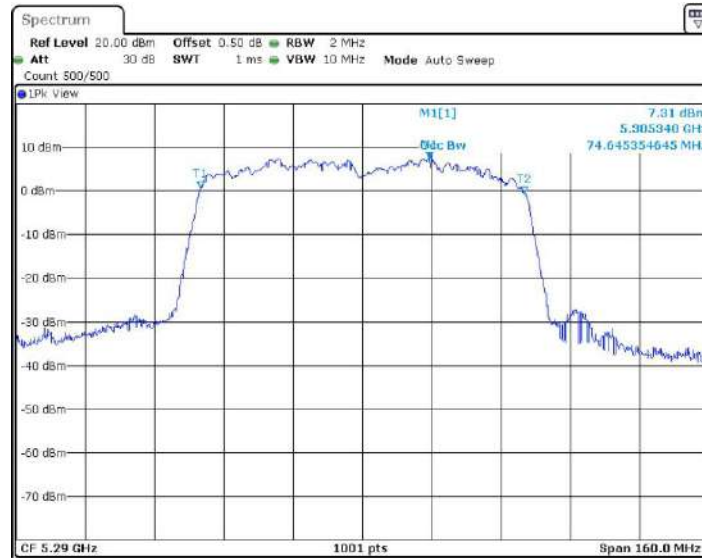
11AC80MIMO_Ant0_5210



11AC80MIMO_Ant1_5210



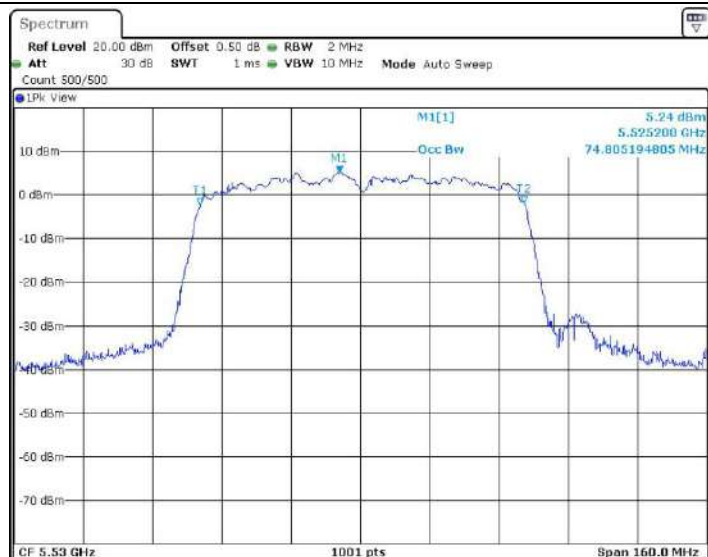
11AC80MIMO_Ant0_5290



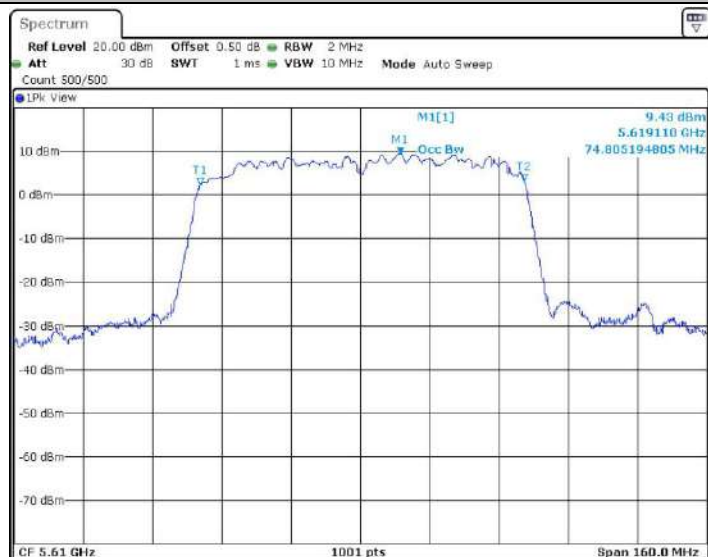
11AC80MIMO_Ant1_5290



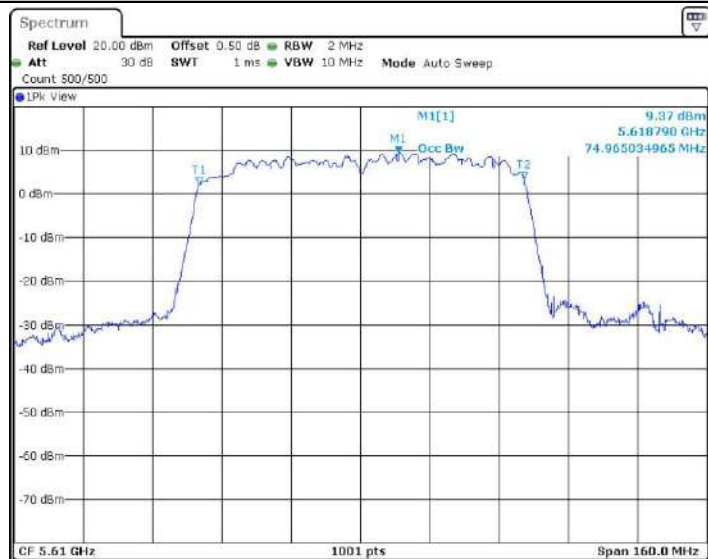
11AC80MIMO_Ant0_5530



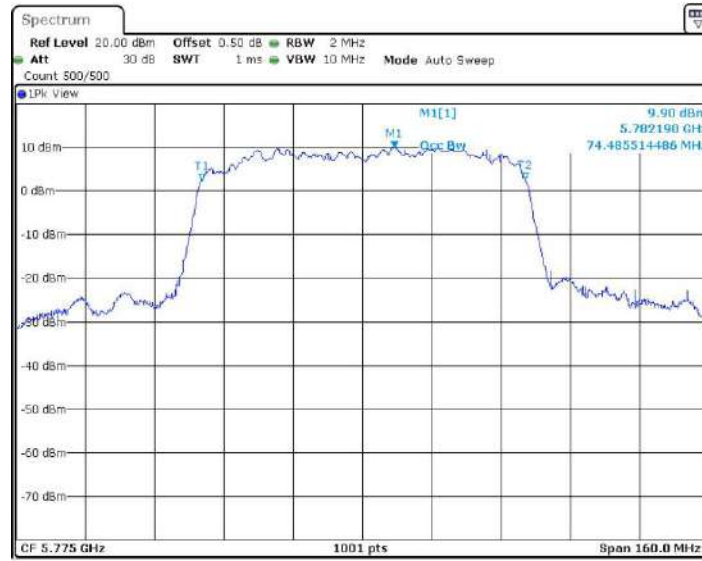
11AC80MIMO_Ant1_5530



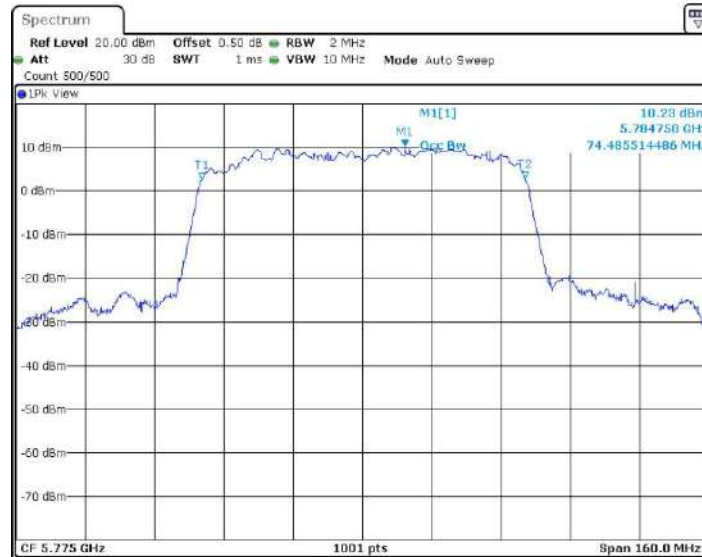
11AC80MIMO_Ant0_5610



11AC80MIMO_Ant1_5610



11AC80MIMO_Ant0_5775



11AC80MIMO_Ant1_5775

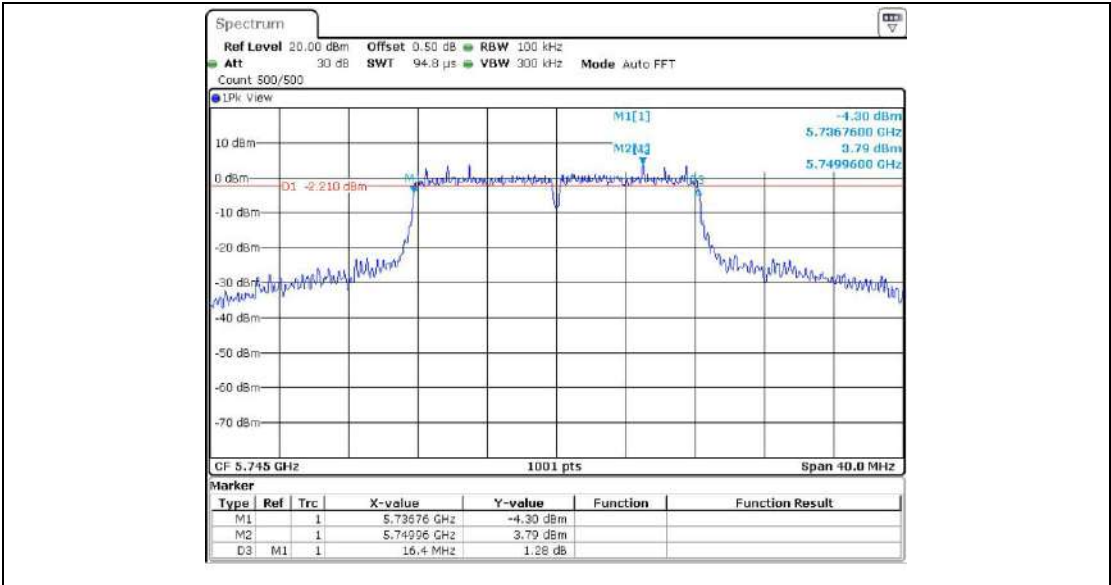
Appendix A3: Min emission bandwidth

Test Result

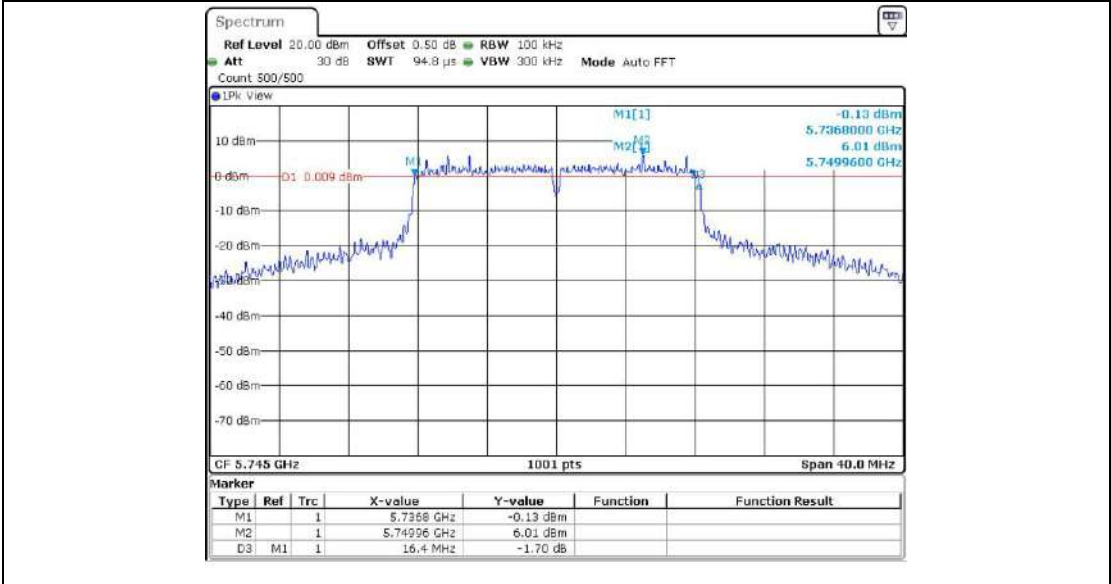
TestMode	Antenna	Channel	6db EBW [MHz]	FL[MHz]	FH[MHz]	Limit[MHz]	Verdict
11A	Ant0	5745	16.400	5736.760	5753.160	0.5	PASS
	Ant1	5745	16.400	5736.800	5753.200	0.5	PASS
	Ant0	5785	16.400	5776.760	5793.160	0.5	PASS
	Ant1	5785	16.400	5776.760	5793.160	0.5	PASS
	Ant0	5825	16.400	5816.760	5833.160	0.5	PASS
	Ant1	5825	16.400	5816.760	5833.160	0.5	PASS
11N20SISO	Ant0	5745	17.720	5736.120	5753.840	0.5	PASS
	Ant1	5745	17.360	5736.440	5753.800	0.5	PASS
	Ant0	5785	17.680	5776.120	5793.800	0.5	PASS
	Ant1	5785	17.640	5776.160	5793.800	0.5	PASS
	Ant0	5825	17.600	5816.160	5833.760	0.5	PASS
	Ant1	5825	17.640	5816.160	5833.800	0.5	PASS
11N40SISO	Ant0	5755	35.280	5737.320	5772.600	0.5	PASS
	Ant1	5755	35.280	5737.320	5772.600	0.5	PASS
	Ant0	5795	35.280	5777.400	5812.680	0.5	PASS
	Ant1	5795	35.280	5777.400	5812.680	0.5	PASS
11AC20SISO	Ant0	5745	17.280	5736.520	5753.800	0.5	PASS
	Ant1	5745	17.600	5736.200	5753.800	0.5	PASS
	Ant0	5785	17.600	5776.160	5793.760	0.5	PASS
	Ant1	5785	17.400	5776.400	5793.800	0.5	PASS
	Ant0	5825	17.600	5816.160	5833.760	0.5	PASS
	Ant1	5825	17.640	5816.160	5833.800	0.5	PASS
11AC40SISO	Ant0	5755	35.280	5737.400	5772.680	0.5	PASS
	Ant1	5755	35.280	5737.400	5772.680	0.5	PASS
	Ant0	5795	35.280	5777.400	5812.680	0.5	PASS
	Ant1	5795	35.280	5777.400	5812.680	0.5	PASS
11AC80SISO	Ant0	5775	70.560	5742.200	5812.760	0.5	PASS
	Ant1	5775	66.720	5746.040	5812.760	0.5	PASS
11N20MIMO	Ant0	5745	17.640	5736.160	5753.800	0.5	PASS
	Ant1	5745	17.240	5736.520	5753.760	0.5	PASS
	Ant0	5785	17.640	5776.160	5793.800	0.5	PASS
	Ant1	5785	17.680	5776.120	5793.800	0.5	PASS
	Ant0	5825	17.600	5816.160	5833.760	0.5	PASS
	Ant1	5825	17.640	5816.160	5833.800	0.5	PASS
11N40MIMO	Ant0	5755	35.280	5737.320	5772.600	0.5	PASS
	Ant1	5755	35.280	5737.320	5772.600	0.5	PASS

	Ant0	5795	35.280	5777.400	5812.680	0.5	PASS
	Ant1	5795	35.280	5777.400	5812.680	0.5	PASS
11AC20MIMO	Ant0	5745	17.640	5736.160	5753.800	0.5	PASS
	Ant1	5745	17.640	5736.160	5753.800	0.5	PASS
	Ant0	5785	17.240	5776.520	5793.760	0.5	PASS
	Ant1	5785	17.640	5776.160	5793.800	0.5	PASS
	Ant0	5825	17.640	5816.160	5833.800	0.5	PASS
	Ant1	5825	17.640	5816.160	5833.800	0.5	PASS
11AC40MIMO	Ant0	5755	35.280	5737.320	5772.600	0.5	PASS
	Ant1	5755	35.280	5737.320	5772.600	0.5	PASS
	Ant0	5795	35.280	5777.400	5812.680	0.5	PASS
	Ant1	5795	35.280	5777.400	5812.680	0.5	PASS
11AC80MIMO	Ant0	5775	66.560	5746.040	5812.600	0.5	PASS
	Ant1	5775	72.960	5739.800	5812.760	0.5	PASS

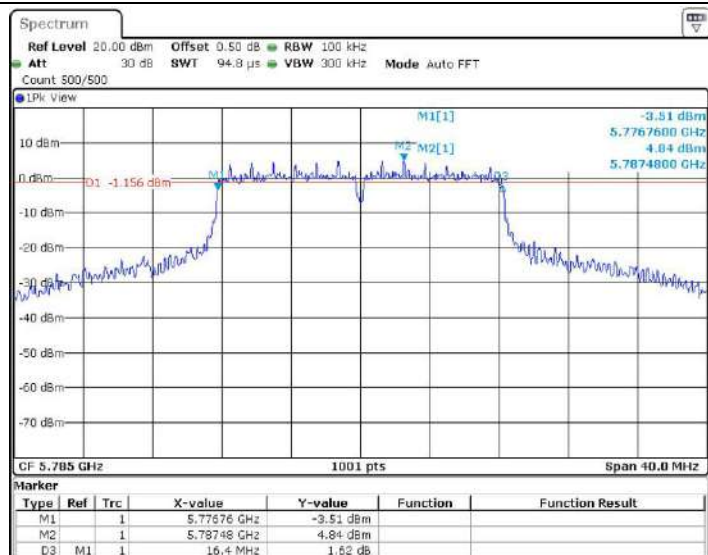
Test Graphs



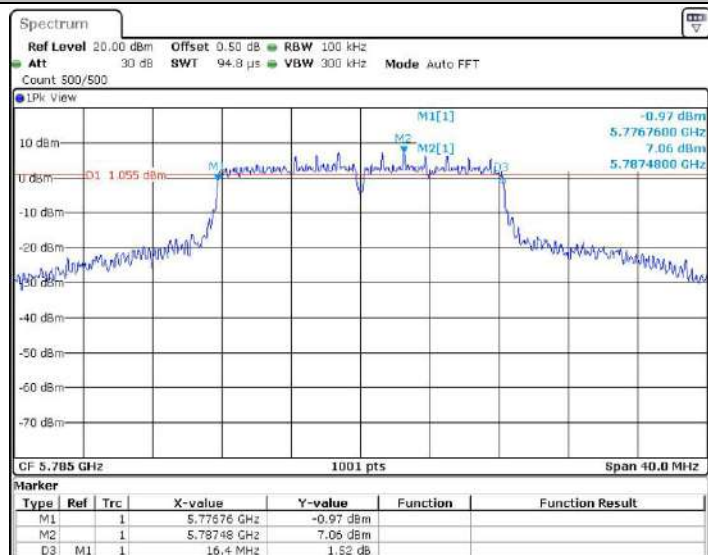
11A_Ant0_5745



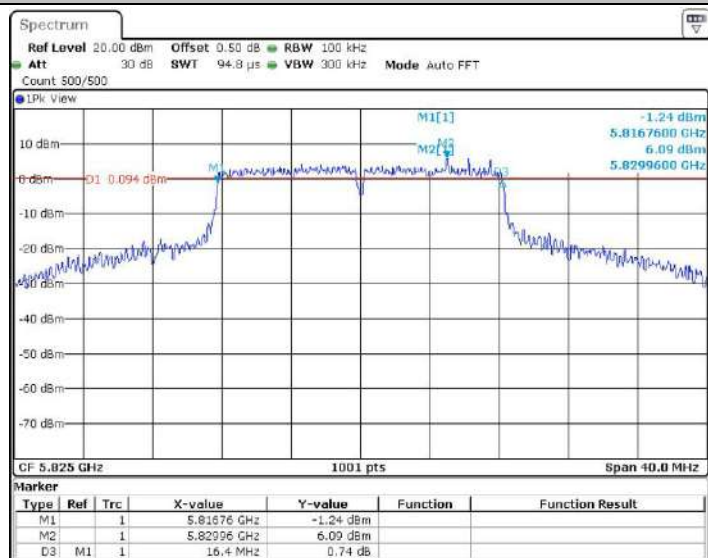
11A_Ant1_5745



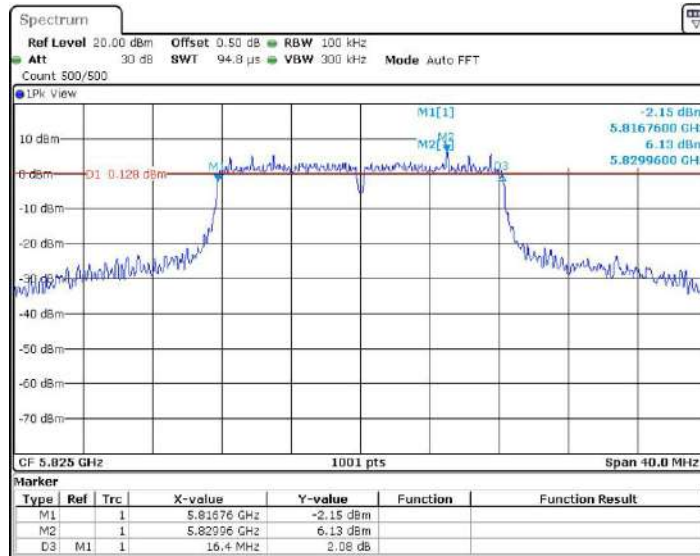
11A_Ant0_5785



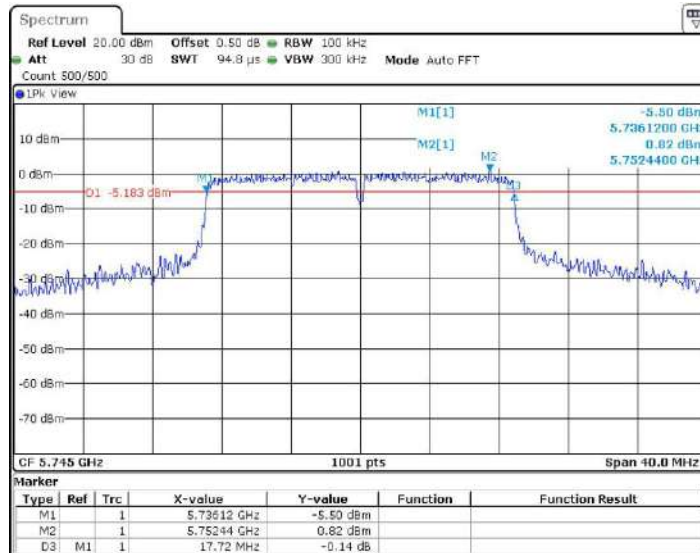
11A_Ant1_5785



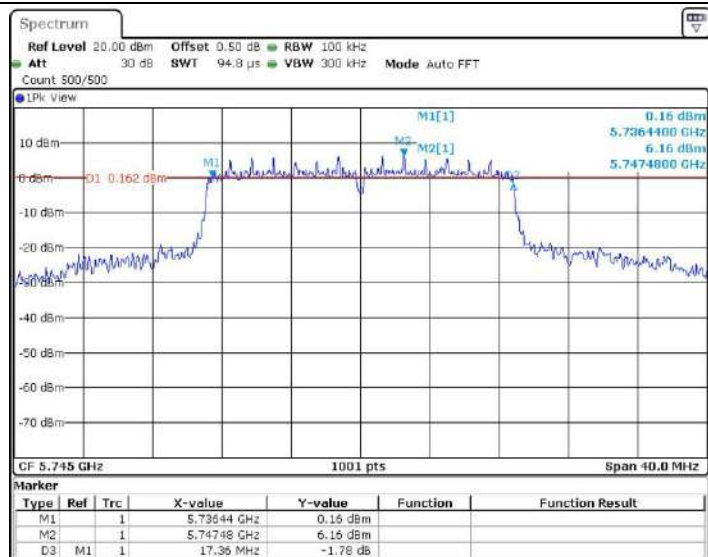
11A_Ant0_5825



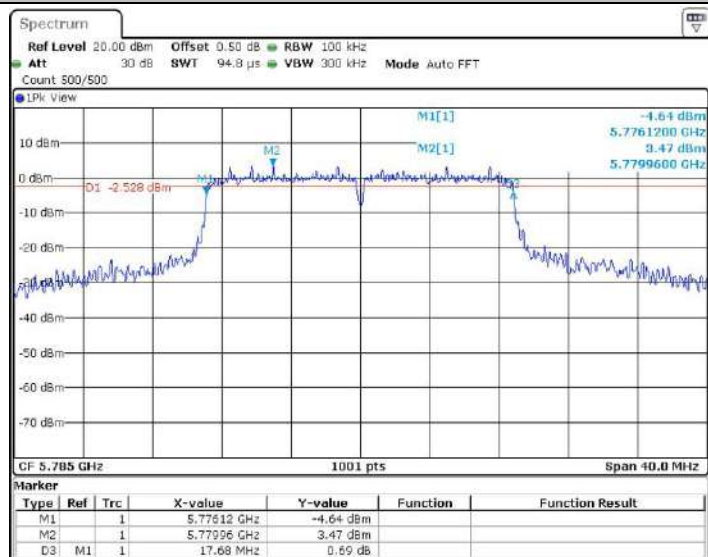
11A_Ant1_5825



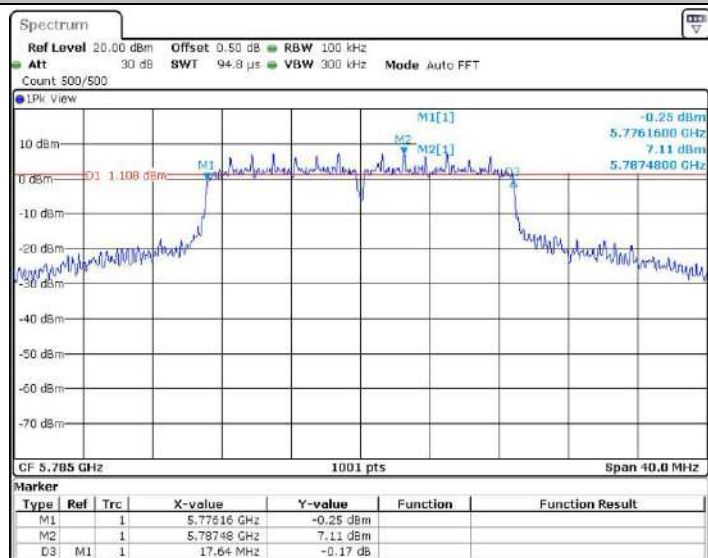
11N20SISO_Ant0_5745



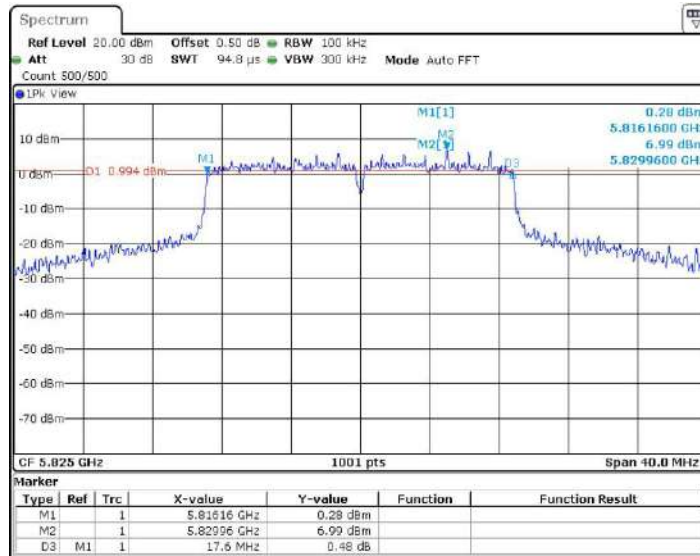
11N20SISO_Ant1_5745



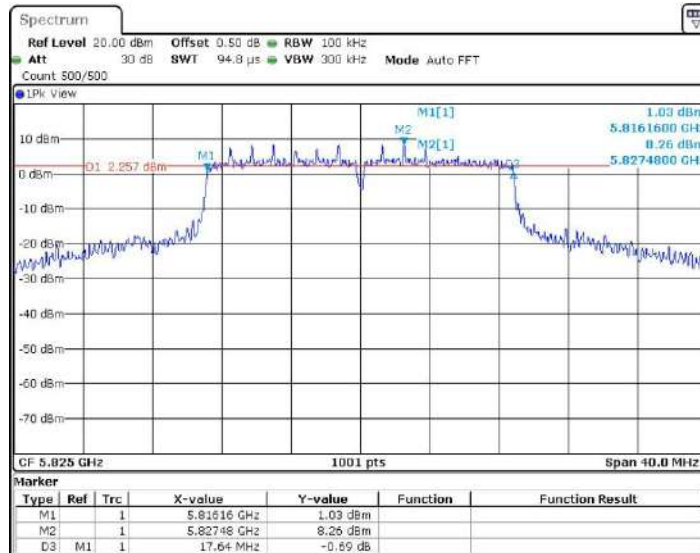
11N20SISO_Ant0_5785



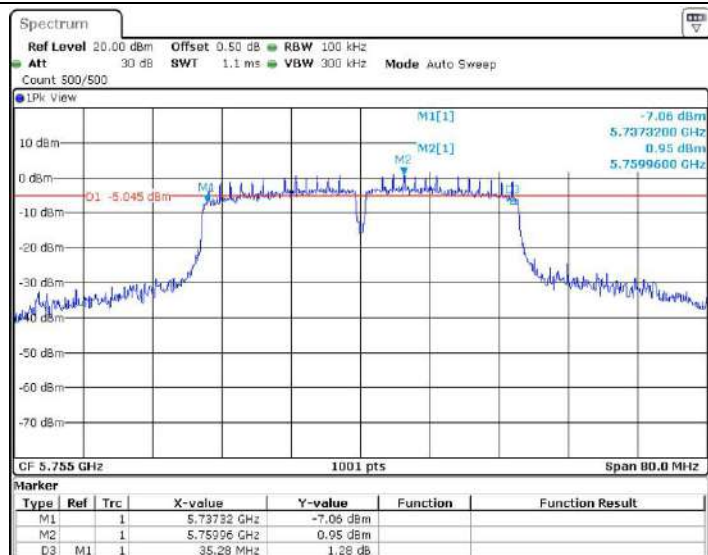
11N20SISO_Ant1_5785



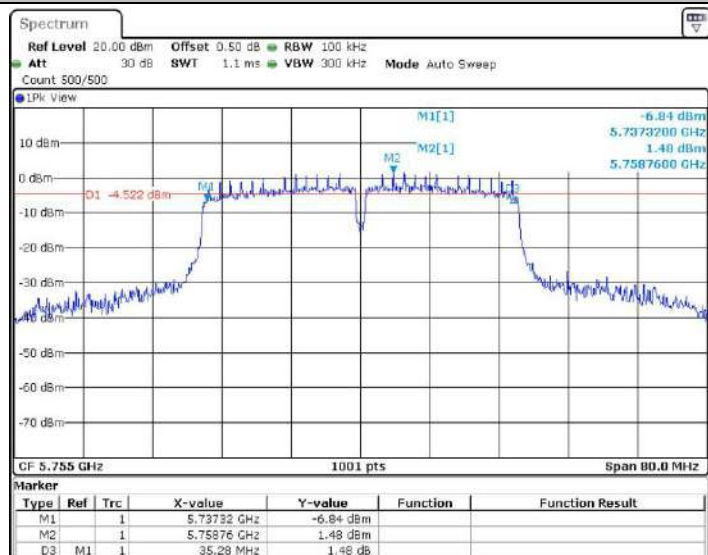
11N20SISO_Ant0_5825



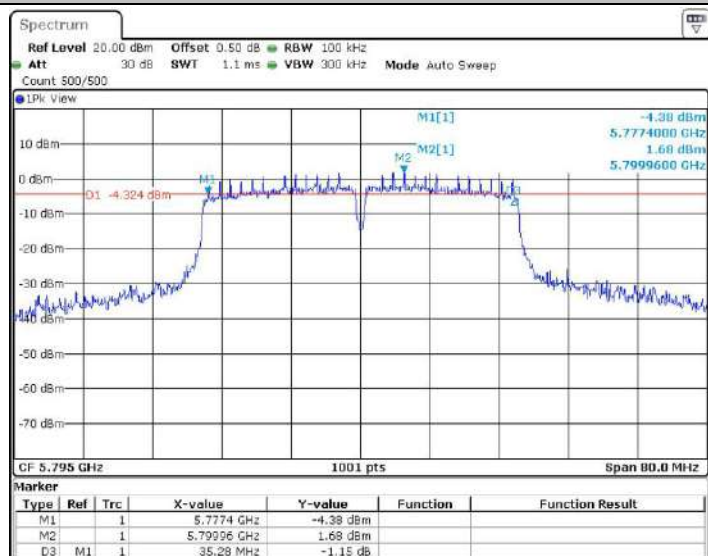
11N20SISO_Ant1_5825



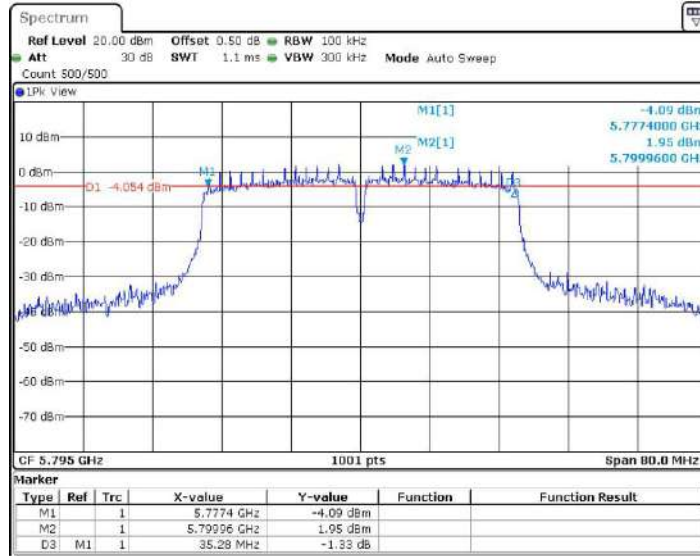
11N40SISO_Ant0_5755



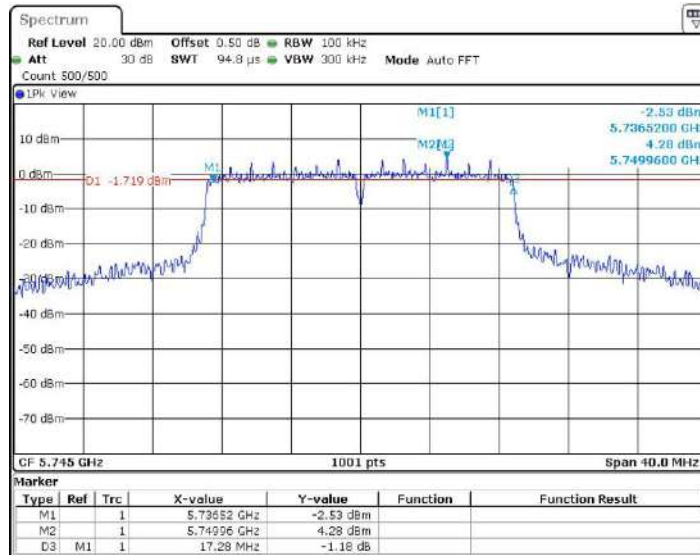
11N40SISO_Ant1_5755



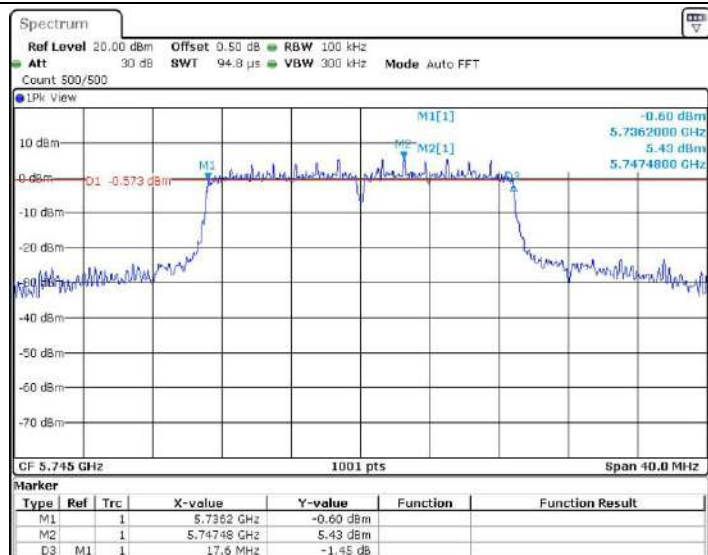
11N40SISO_Ant0_5795



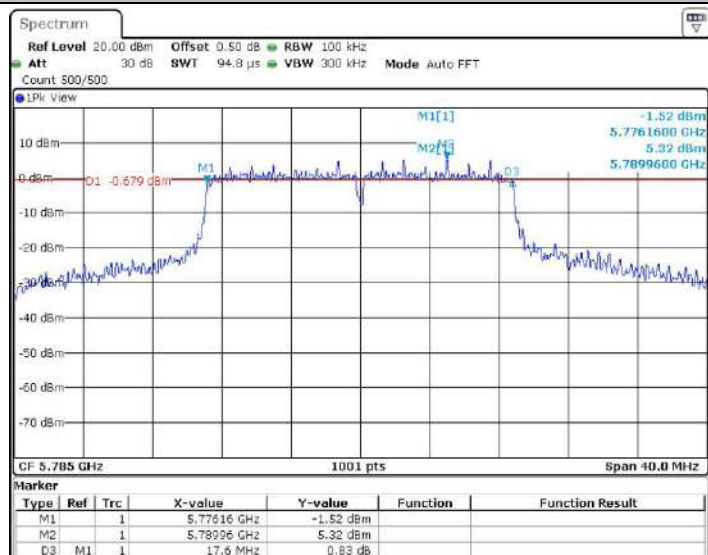
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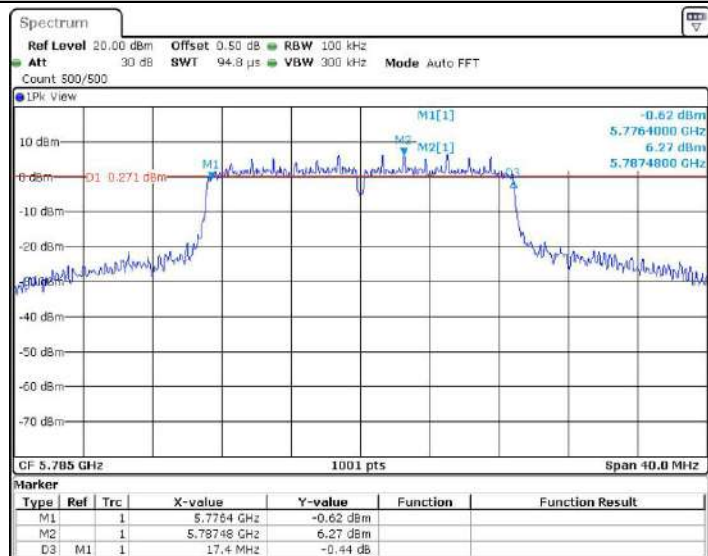
11AC20SISO_Ant0_5745



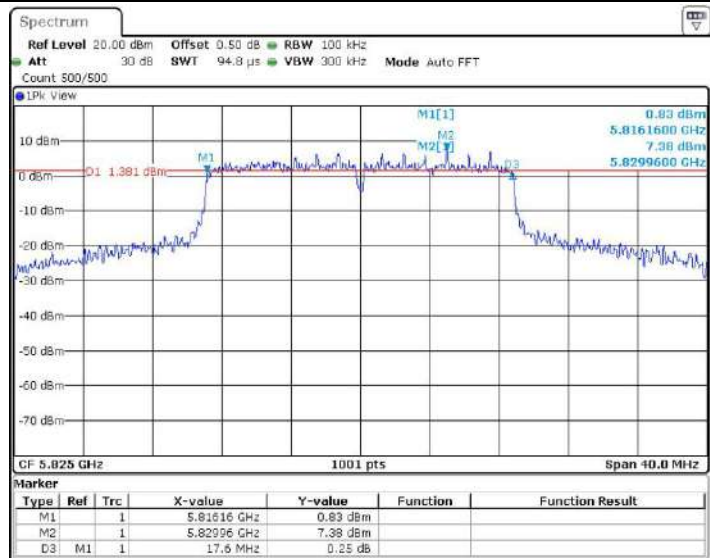
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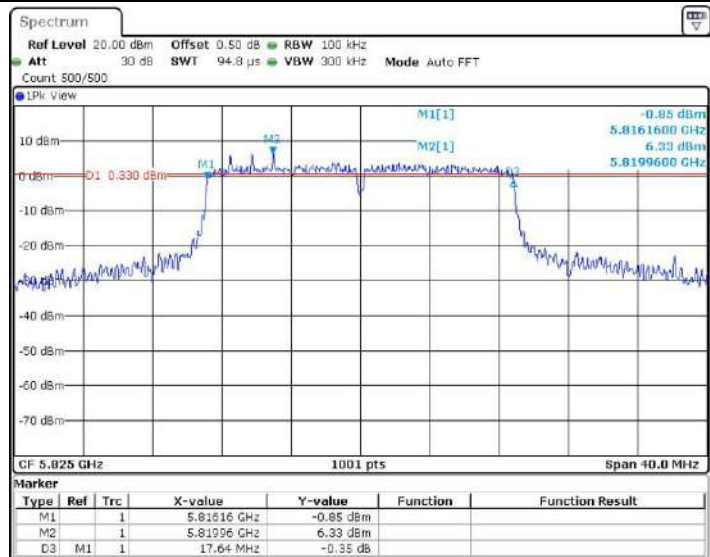
11AC20SISO_Ant0_5785



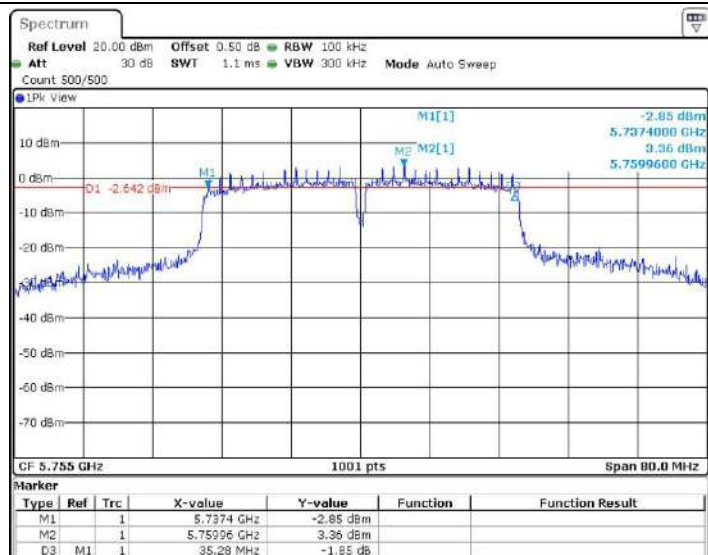
11AC20SISO_Ant1_5785



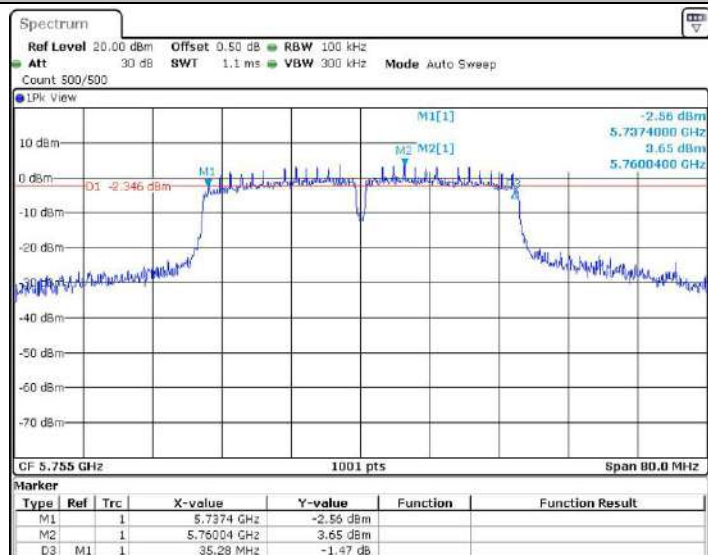
11AC20SISO_Ant0_5825



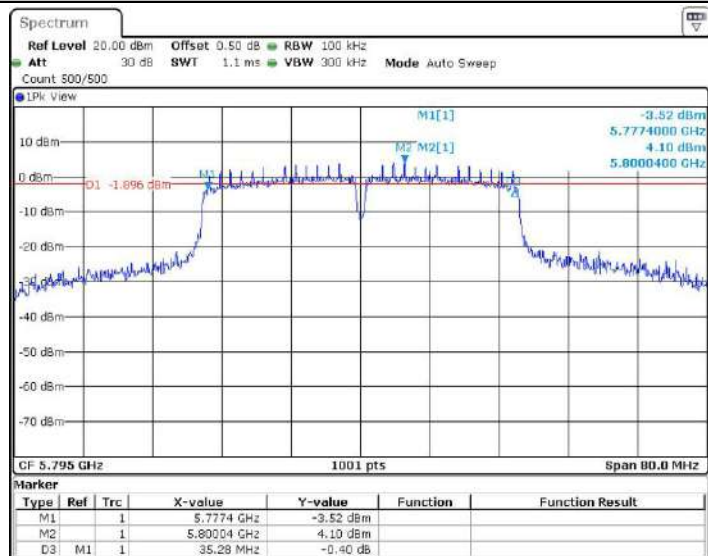
11AC20SISO_Ant1_5825



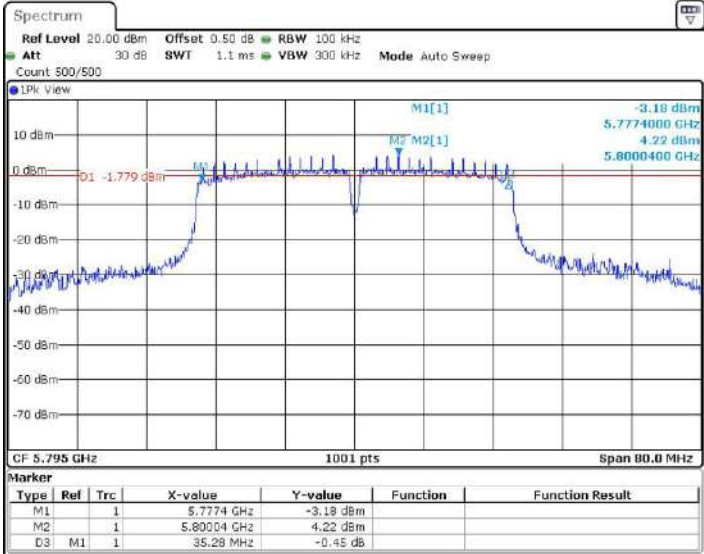
11AC40SISO_Ant0_5755



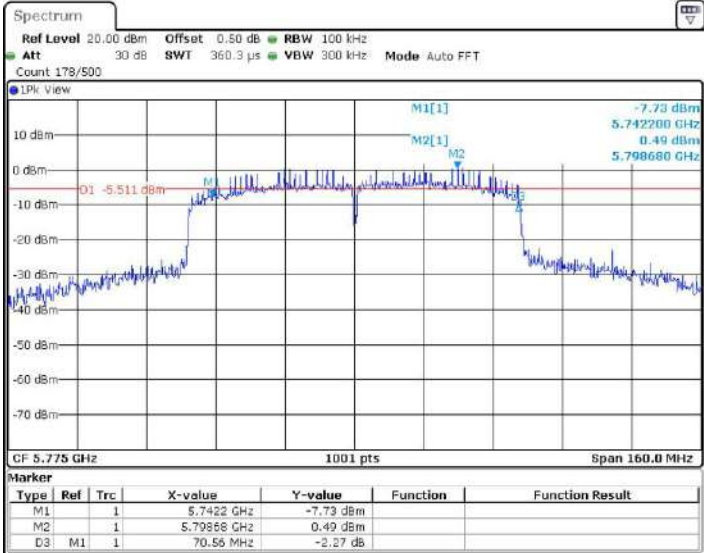
11AC40SISO_Ant1_5755



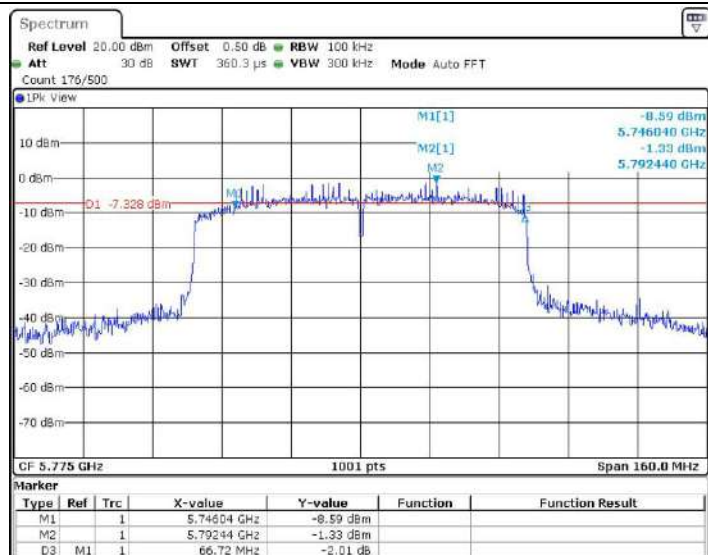
11AC40SISO_Ant0_5795



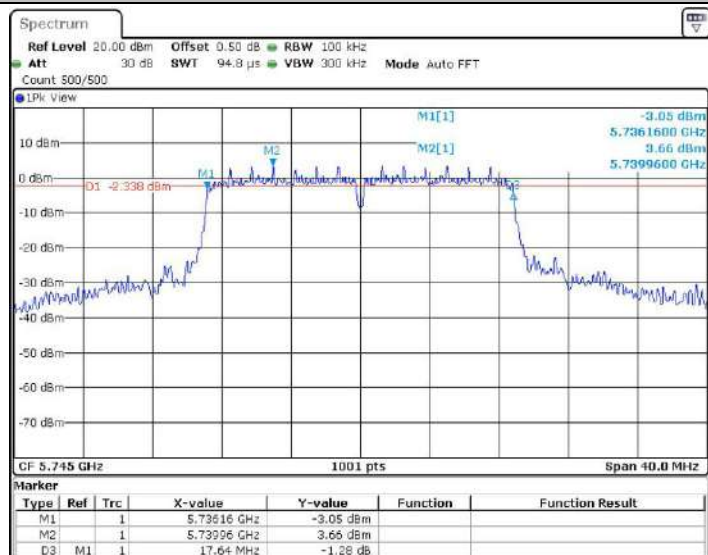
11AC40SISO_Ant1_5795



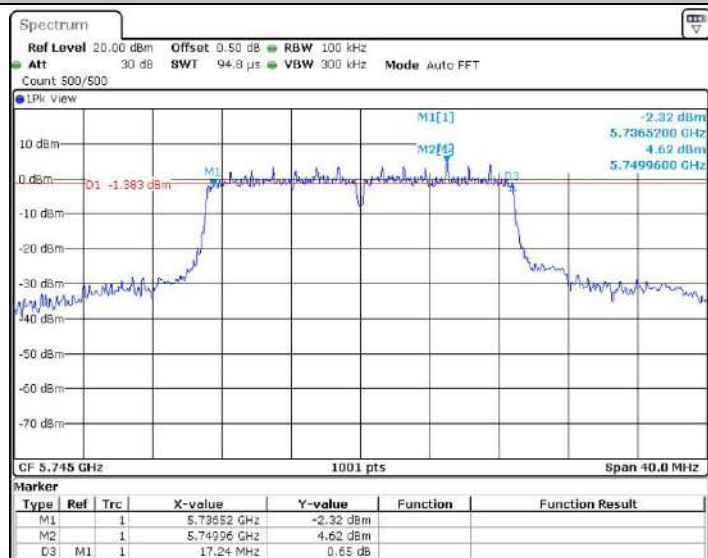
11AC80SISO_Ant0_5775



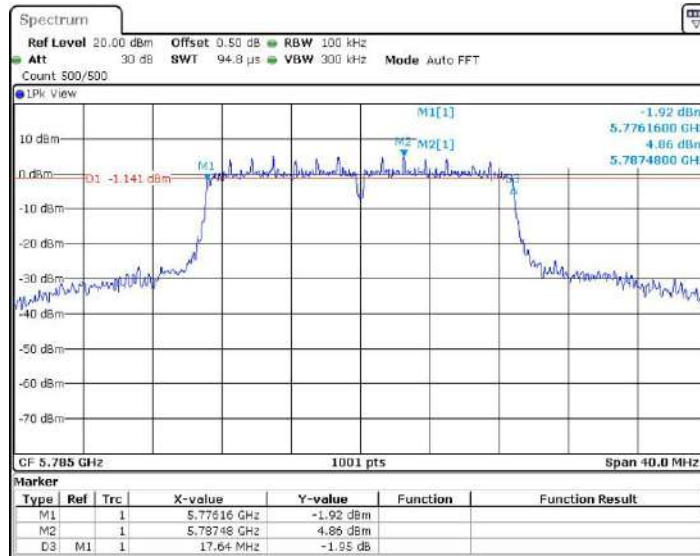
11AC80SISO_Ant1_5775



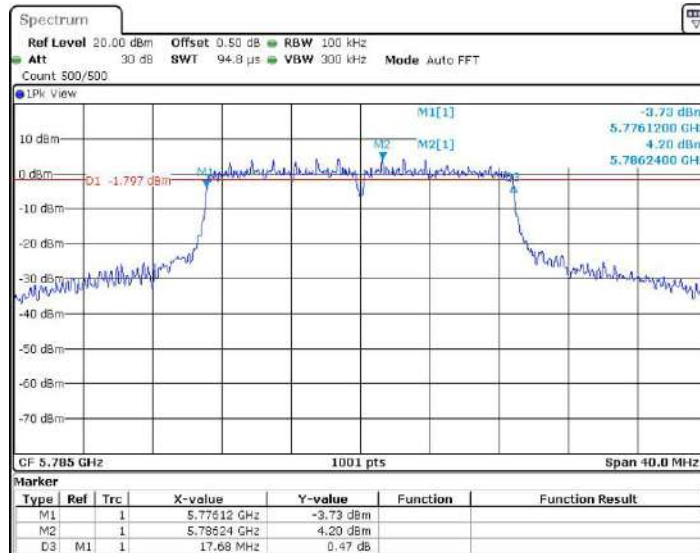
11N20MIMO_Ant0_5745



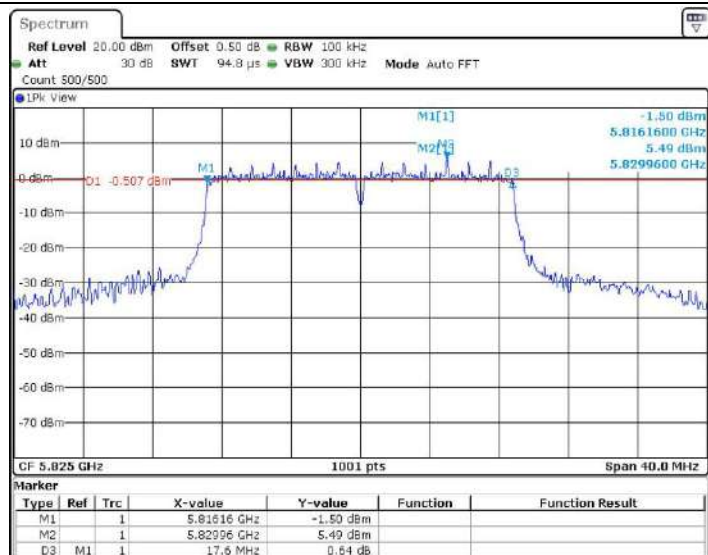
11N20MIMO_Ant1_5745



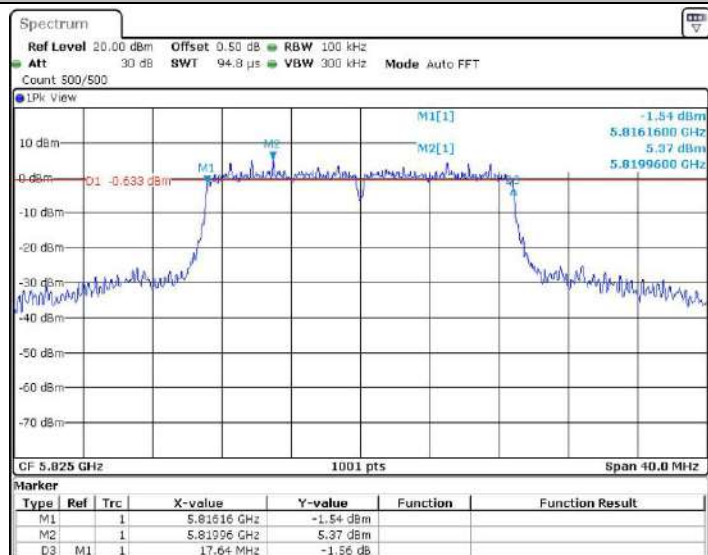
11N20MIMO_Ant0_5785



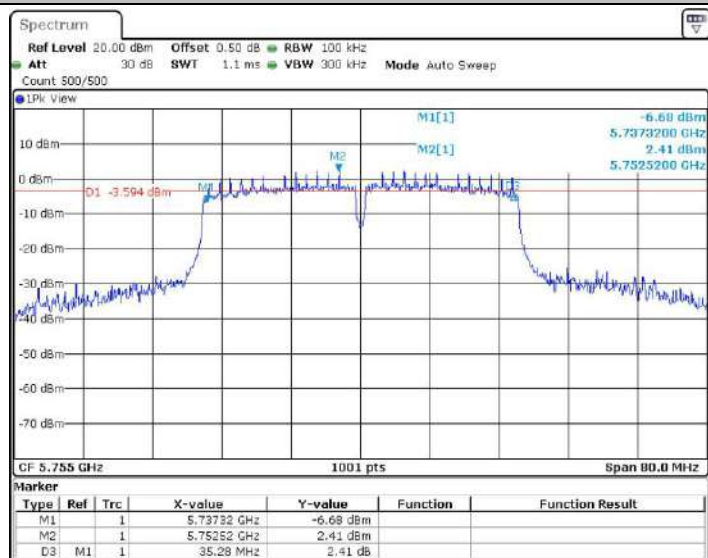
11N20MIMO_Ant1_5785



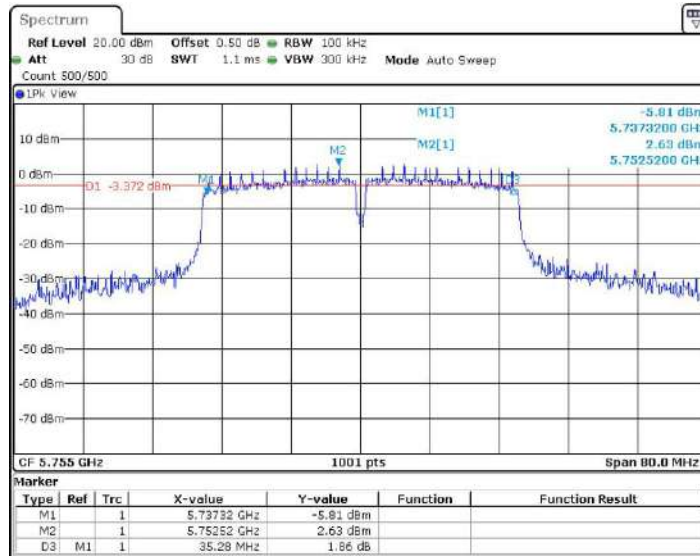
11N20MIMO_Ant0_5825



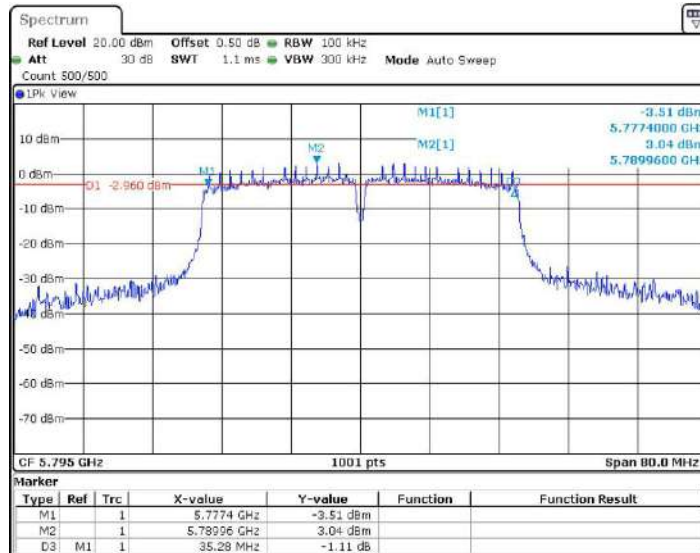
11N20MIMO_Ant1_5825



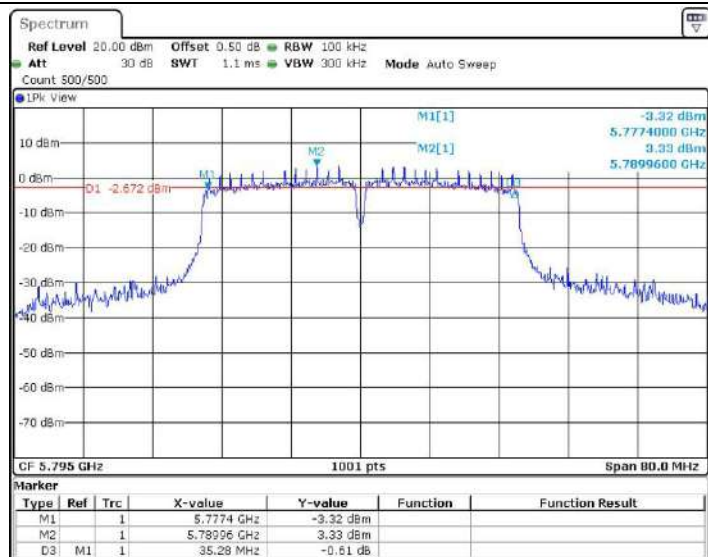
11N40MIMO_Ant0_5755



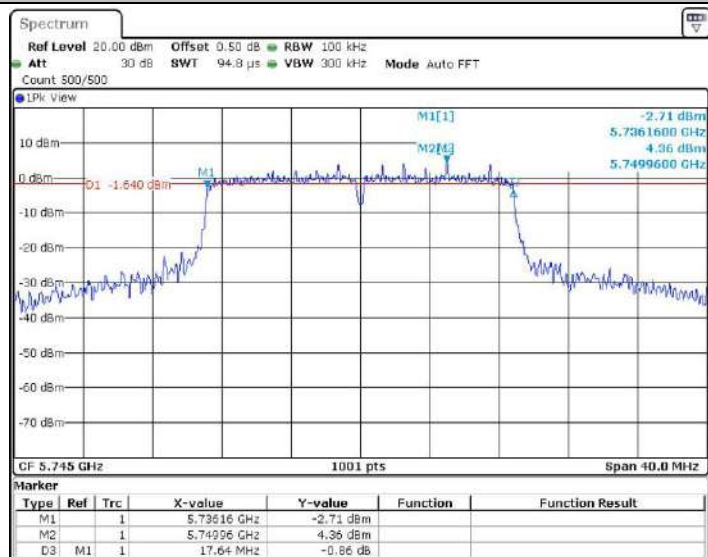
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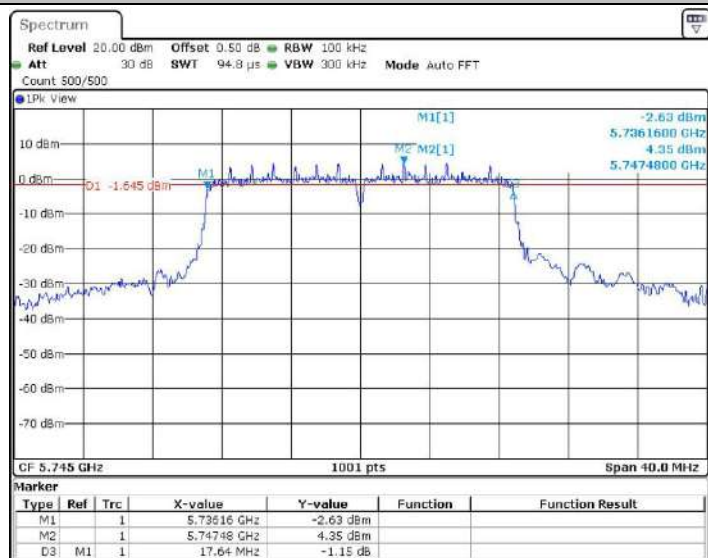
11N40MIMO_Ant0_5795



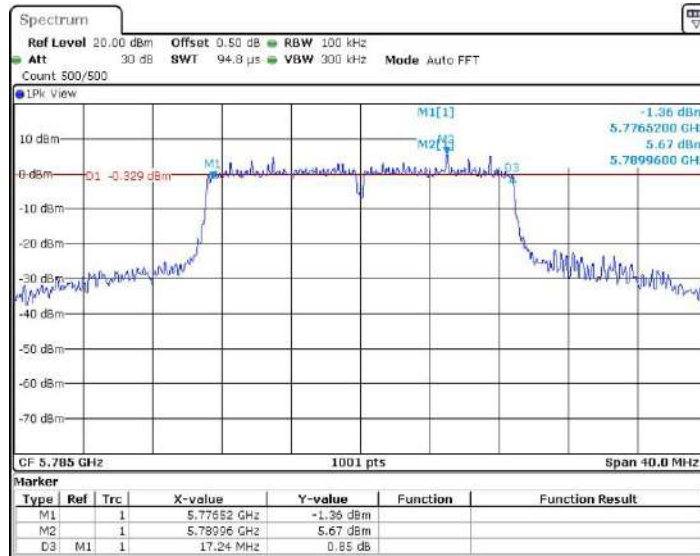
11N40MIMO_Ant1_5795



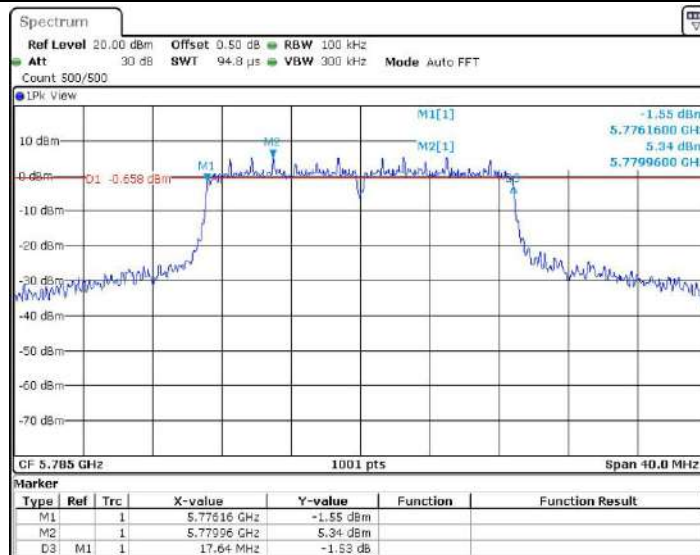
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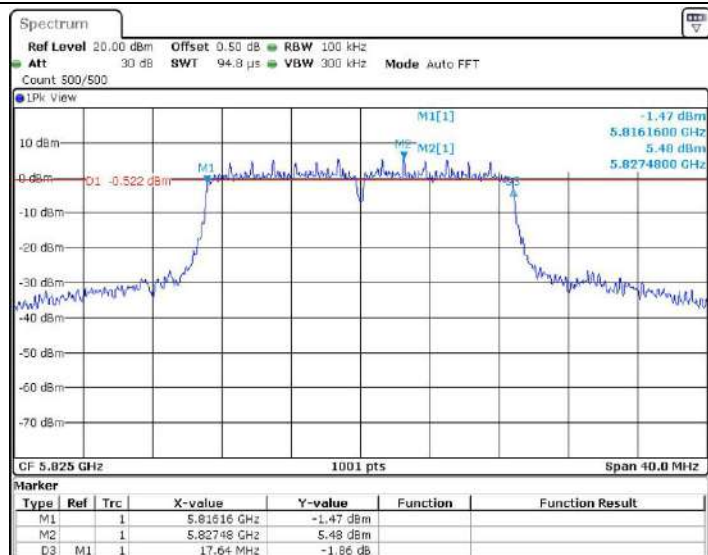
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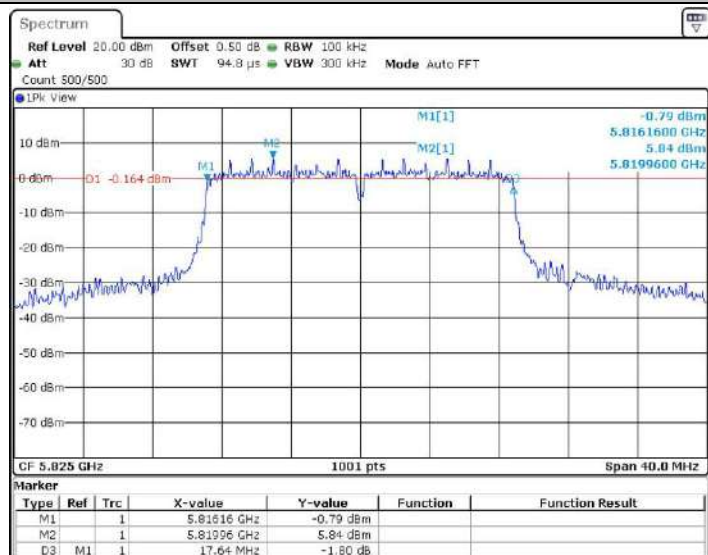
11AC20MIMO_Ant0_5785



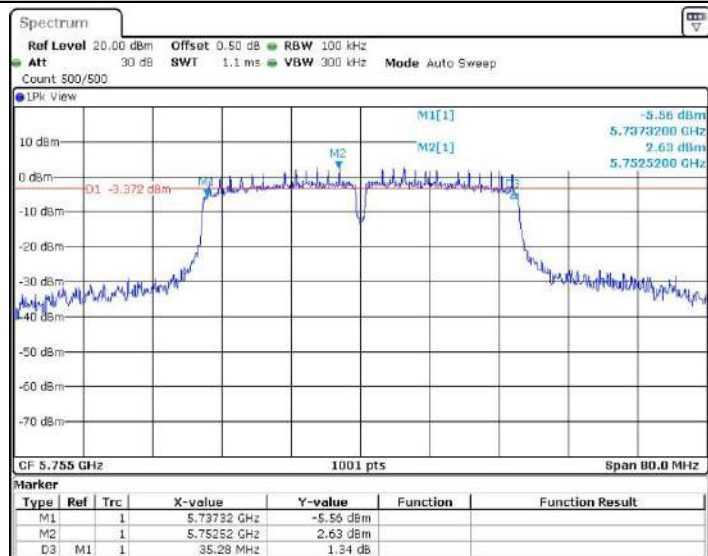
11AC20MIMO_Ant1_5785



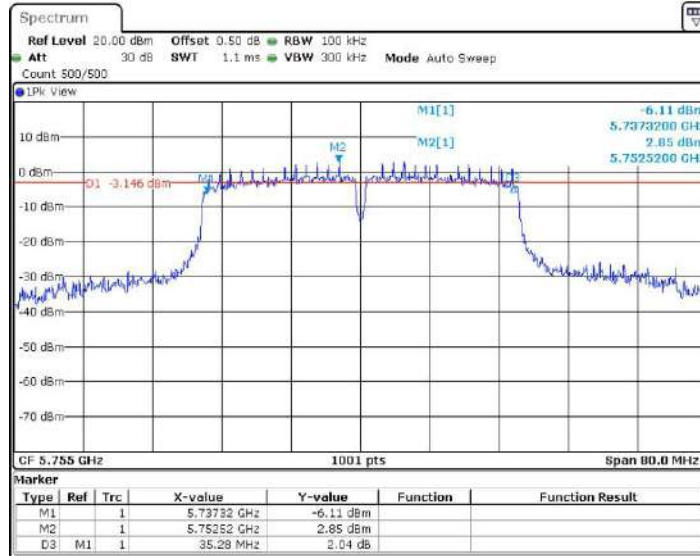
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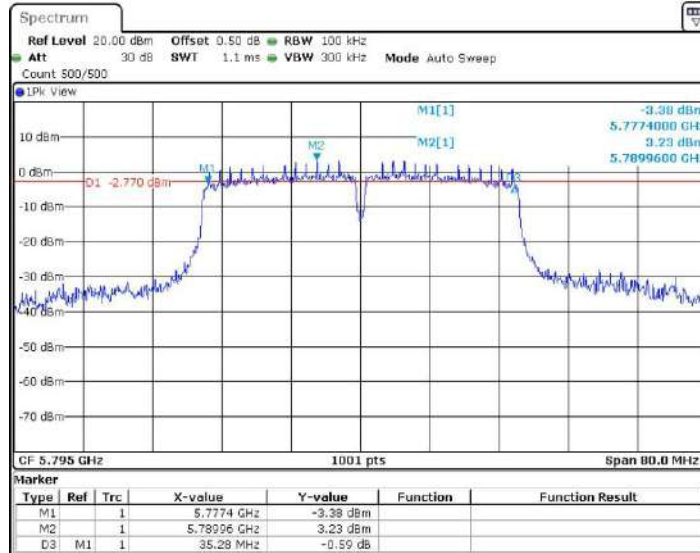
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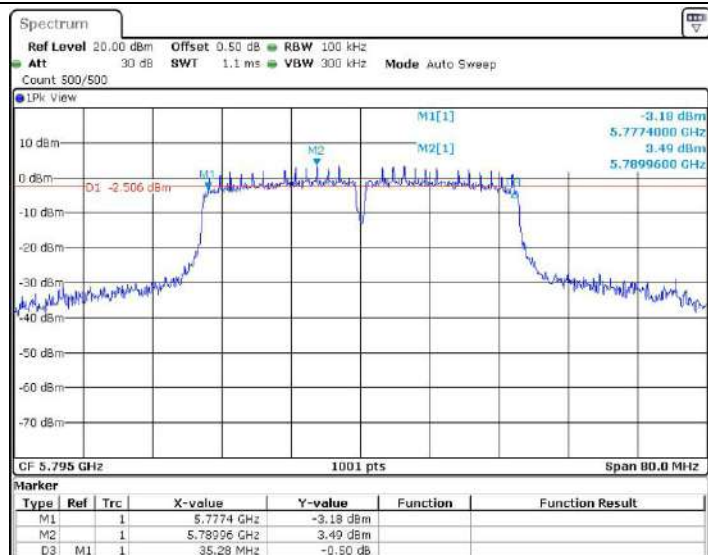
11AC40MIMO_Ant0_5755



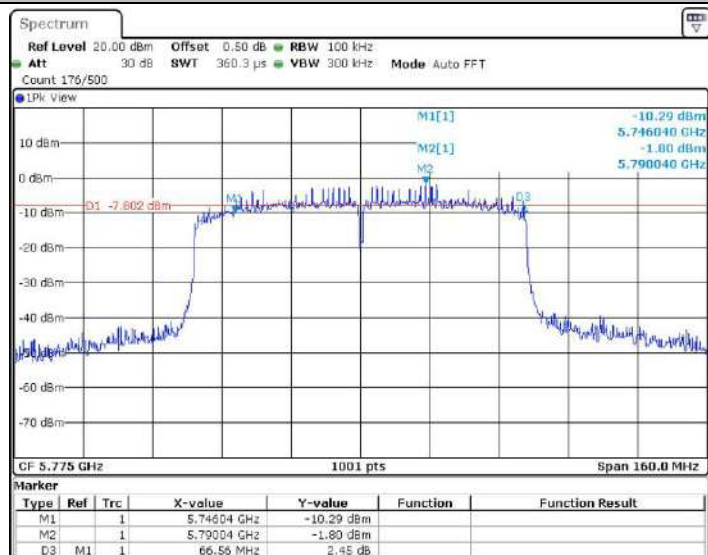
11AC40MIMO_Ant1_5755



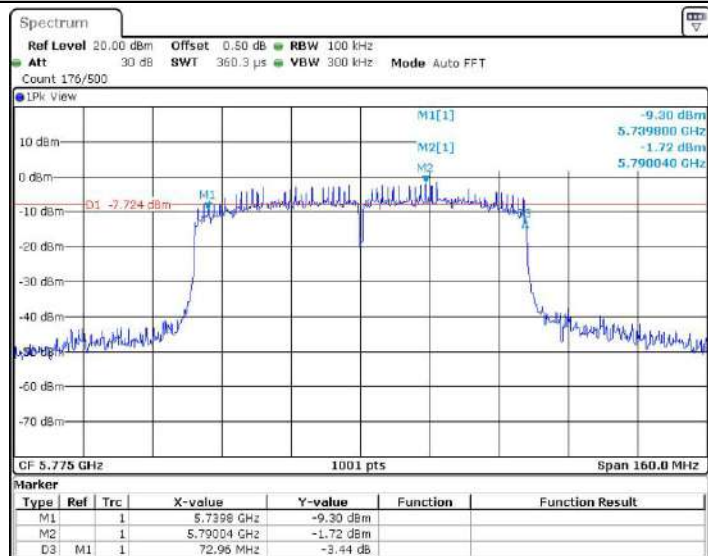
11AC40MIMO_Ant0_5795



11AC40MIMO_Ant1_5795



11AC80MIMO_Ant0_5775



11AC80MIMO_Ant1_5775

Appendix B: Maximum conducted output power

Test Result

TestMode	Antenna	Channel	Result[dBm]	Limit[dBm]	Verdict
11A	Ant0	5180	15.54	<=23.98	PASS
	Ant1	5180	14.90	<=23.98	PASS
	Ant0	5200	15.87	<=23.98	PASS
	Ant1	5200	14.85	<=23.98	PASS
	Ant0	5240	13.92	<=23.98	PASS
	Ant1	5240	14.43	<=23.98	PASS
	Ant0	5260	14.50	<=23.65	PASS
	Ant1	5260	13.67	<=23.61	PASS
	Ant0	5280	14.10	<=23.70	PASS
	Ant1	5280	13.22	<=23.70	PASS
	Ant0	5320	13.65	<=23.69	PASS
	Ant1	5320	12.59	<=23.67	PASS
	Ant0	5500	11.51	<=23.68	PASS
	Ant1	5500	12.96	<=23.69	PASS
	Ant0	5580	14.24	<=23.71	PASS
	Ant1	5580	14.33	<=23.72	PASS
	Ant0	5700	15.19	<=23.98	PASS
	Ant1	5700	15.70	<=23.98	PASS
	Ant0	5745	15.15	<=30	PASS
	Ant1	5745	17.58	<=30	PASS
	Ant0	5785	16.10	<=30	PASS
	Ant1	5785	18.49	<=30	PASS
	Ant0	5825	18.17	<=30	PASS
	Ant1	5825	17.48	<=30	PASS
11N20SISO	Ant0	5180	14.61	<=23.98	PASS
	Ant1	5180	14.61	<=23.98	PASS
	Ant0	5200	15.62	<=23.98	PASS
	Ant1	5200	14.60	<=23.98	PASS
	Ant0	5240	13.87	<=23.98	PASS
	Ant1	5240	14.09	<=23.98	PASS
	Ant0	5260	14.13	<=23.88	PASS
	Ant1	5260	13.35	<=23.85	PASS
	Ant0	5280	13.98	<=23.90	PASS
	Ant1	5280	12.98	<=23.88	PASS
	Ant0	5320	13.53	<=23.90	PASS
	Ant1	5320	12.29	<=23.88	PASS
	Ant0	5500	10.79	<=23.87	PASS
	Ant1	5500	12.00	<=23.89	PASS

	Ant0	5580	13.91	<=23.92	PASS
	Ant1	5580	13.44	<=23.87	PASS
	Ant0	5700	14.42	<=23.98	PASS
	Ant1	5700	15.49	<=23.98	PASS
	Ant0	5745	14.94	<=30	PASS
	Ant1	5745	17.44	<=30	PASS
	Ant0	5785	16.02	<=30	PASS
	Ant1	5785	18.47	<=30	PASS
	Ant0	5825	18.04	<=30	PASS
	Ant1	5825	19.43	<=30	PASS
11N40SISO	Ant0	5190	13.37	<=23.98	PASS
	Ant1	5190	15.05	<=23.98	PASS
	Ant0	5230	13.19	<=23.98	PASS
	Ant1	5230	14.02	<=23.98	PASS
	Ant0	5270	12.87	<=23.98	PASS
	Ant1	5270	13.84	<=23.98	PASS
	Ant0	5310	10.59	<=23.98	PASS
	Ant1	5310	11.73	<=23.98	PASS
	Ant0	5510	7.59	<=23.98	PASS
	Ant1	5510	9.65	<=23.98	PASS
	Ant0	5550	12.25	<=23.98	PASS
	Ant1	5550	14.12	<=23.98	PASS
	Ant0	5670	12.82	<=23.98	PASS
	Ant1	5670	15.23	<=23.98	PASS
	Ant0	5755	14.49	<=30	PASS
	Ant1	5755	15.36	<=30	PASS
	Ant0	5795	15.07	<=30	PASS
	Ant1	5795	15.36	<=30	PASS
11AC20SISO	Ant0	5180	15.01	<=23.98	PASS
	Ant1	5180	15.26	<=23.98	PASS
	Ant0	5200	15.77	<=23.98	PASS
	Ant1	5200	15.92	<=23.98	PASS
	Ant0	5240	14.23	<=23.98	PASS
	Ant1	5240	14.41	<=23.98	PASS
	Ant0	5260	14.55	<=23.89	PASS
	Ant1	5260	14.98	<=23.90	PASS
	Ant0	5280	14.22	<=23.86	PASS
	Ant1	5280	14.92	<=23.88	PASS
	Ant0	5320	13.57	<=23.90	PASS
	Ant1	5320	14.17	<=23.88	PASS
	Ant0	5500	10.90	<=23.87	PASS
	Ant1	5500	14.10	<=23.90	PASS
	Ant0	5580	14.20	<=23.95	PASS

	Ant1	5580	15.50	<=23.98	PASS
	Ant0	5700	14.90	<=23.98	PASS
	Ant1	5700	16.10	<=23.98	PASS
	Ant0	5745	15.50	<=30	PASS
	Ant1	5745	16.85	<=30	PASS
	Ant0	5785	16.62	<=30	PASS
	Ant1	5785	17.67	<=30	PASS
	Ant0	5825	18.67	<=30	PASS
	Ant1	5825	17.94	<=30	PASS
11AC40SISO	Ant0	5190	15.33	<=23.98	PASS
	Ant1	5190	15.90	<=23.98	PASS
	Ant0	5230	15.02	<=23.98	PASS
	Ant1	5230	15.59	<=23.98	PASS
	Ant0	5270	14.95	<=23.98	PASS
	Ant1	5270	16.03	<=23.98	PASS
	Ant0	5310	13.25	<=23.98	PASS
	Ant1	5310	14.05	<=23.98	PASS
	Ant0	5510	9.87	<=23.98	PASS
	Ant1	5510	11.49	<=23.98	PASS
	Ant0	5550	14.66	<=23.98	PASS
	Ant1	5550	16.00	<=23.98	PASS
	Ant0	5670	15.30	<=23.98	PASS
	Ant1	5670	15.10	<=23.98	PASS
	Ant0	5755	16.68	<=30	PASS
	Ant1	5755	17.10	<=30	PASS
	Ant0	5795	17.44	<=30	PASS
	Ant1	5795	17.65	<=30	PASS
11AC80SISO	Ant0	5210	14.41	<=23.98	PASS
	Ant1	5210	14.48	<=23.98	PASS
	Ant0	5290	12.51	<=23.98	PASS
	Ant1	5290	13.42	<=23.98	PASS
	Ant0	5530	10.92	<=23.98	PASS
	Ant1	5530	12.27	<=23.98	PASS
	Ant0	5610	13.74	<=23.98	PASS
	Ant1	5610	15.66	<=23.98	PASS
	Ant0	5775	17.63	<=30	PASS
	Ant1	5775	15.23	<=30	PASS
11N20MIMO	Ant0	5180	12.80	<=23.98	PASS
	Ant1	5180	13.01	<=23.98	PASS
	total	5180	15.9	<=23.98	PASS
	Ant0	5200	12.61	<=23.98	PASS
	Ant1	5200	12.97	<=23.98	PASS
	total	5200	15.8	<=23.98	PASS

	Ant0	5240	11.00	<=23.98	PASS
	Ant1	5240	11.28	<=23.98	PASS
	total	5240	14.2	<=23.98	PASS
	Ant0	5260	10.38	<=23.87	PASS
	Ant1	5260	11.03	<=23.86	PASS
	total	5260	13.7	<=23.86	PASS
	Ant0	5280	10.73	<=23.84	PASS
	Ant1	5280	10.94	<=23.87	PASS
	total	5280	13.8	<=23.87	PASS
	Ant0	5320	10.13	<=23.88	PASS
	Ant1	5320	10.43	<=23.89	PASS
	total	5320	13.3	<=23.89	PASS
	Ant0	5500	10.88	<=23.88	PASS
	Ant1	5500	11.02	<=23.86	PASS
	total	5500	14.0	<=23.86	PASS
	Ant0	5580	12.32	<=23.89	PASS
	Ant1	5580	12.51	<=23.87	PASS
	total	5580	15.4	<=23.87	PASS
	Ant0	5700	12.54	<=23.88	PASS
	Ant1	5700	12.89	<=23.94	PASS
	total	5700	15.7	<=23.94	PASS
	Ant0	5745	15.32	<=30	PASS
	Ant1	5745	15.60	<=30	PASS
	total	5745	18.5	<=30	PASS
	Ant0	5785	16.30	<=30	PASS
	Ant1	5785	16.54	<=30	PASS
	total	5785	19.4	<=30	PASS
	Ant0	5825	16.60	<=30	PASS
	Ant1	5825	16.81	<=30	PASS
	total	5825	19.7	<=30	PASS
11N40MIMO	Ant0	5190	12.10	<=23.98	PASS
	Ant1	5190	12.39	<=23.98	PASS
	total	5190	15.3	<=23.98	PASS
	Ant0	5230	11.65	<=23.98	PASS
	Ant1	5230	12.22	<=23.98	PASS
	total	5230	15.0	<=23.98	PASS
	Ant0	5270	11.53	<=23.98	PASS
	Ant1	5270	11.32	<=23.98	PASS
	total	5270	14.4	<=23.98	PASS
	Ant0	5310	10.68	<=23.98	PASS
	Ant1	5310	11.08	<=23.98	PASS
	total	5310	13.9	<=23.98	PASS
	Ant0	5510	10.14	<=23.98	PASS

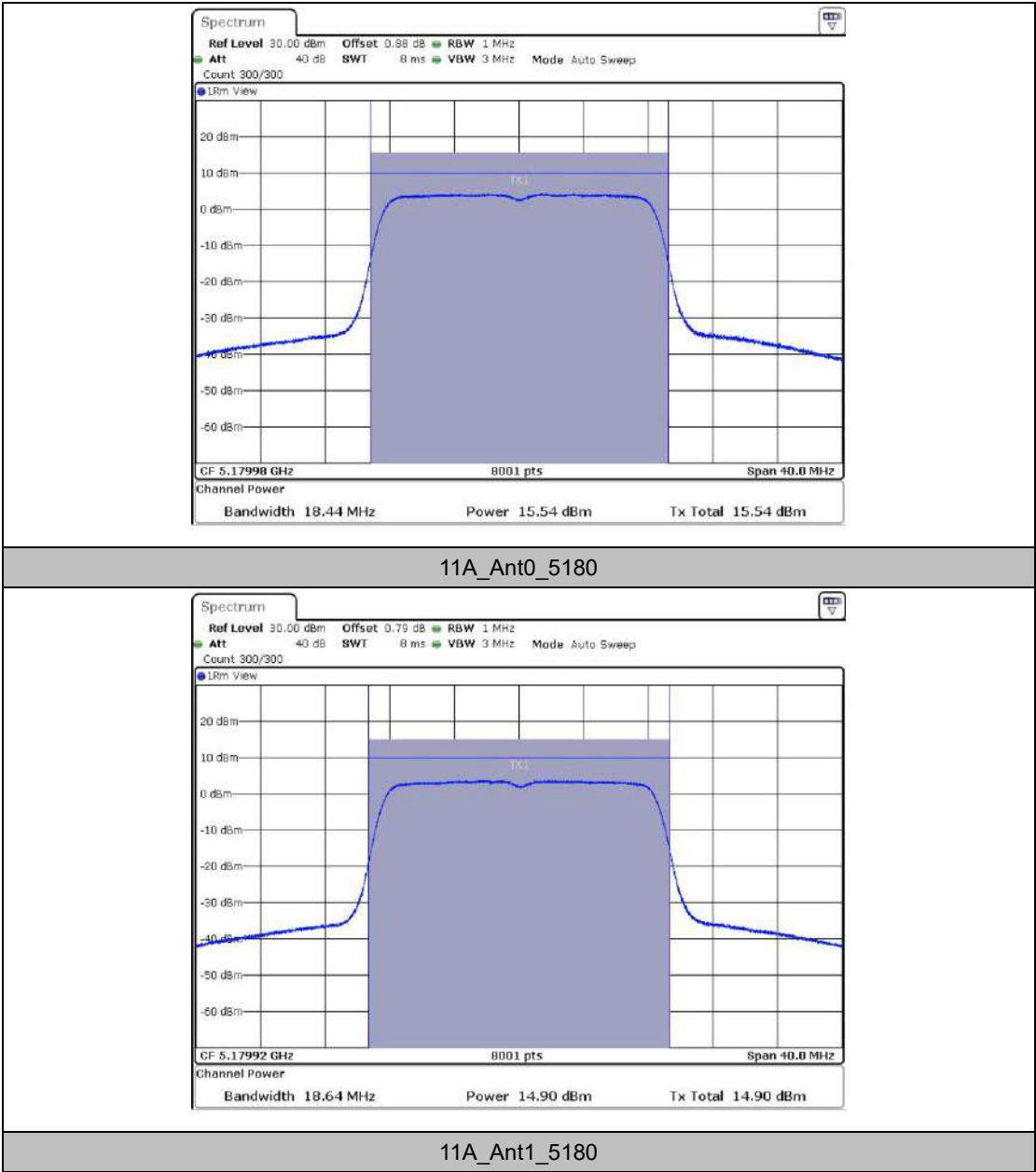
	Ant1	5510	10.91	<=23.98	PASS
	total	5510	13.6	<=23.98	PASS
	Ant0	5550	10.96	<=23.98	PASS
	Ant1	5550	11.30	<=23.98	PASS
	total	5550	14.1	<=23.98	PASS
	Ant0	5670	13.09	<=23.98	PASS
	Ant1	5670	13.52	<=23.98	PASS
	total	5670	16.3	<=23.98	PASS
	Ant0	5755	16.11	<=30	PASS
	Ant1	5755	15.84	<=30	PASS
	total	5755	19.0	<=30	PASS
	Ant0	5795	16.43	<=30	PASS
	Ant1	5795	16.85	<=30	PASS
	total	5795	19.7	<=30	PASS
11AC20MIMO	Ant0	5180	12.77	<=23.98	PASS
	Ant1	5180	13.07	<=23.98	PASS
	total	5180	15.9	<=23.98	PASS
	Ant0	5200	12.52	<=23.98	PASS
	Ant1	5200	12.86	<=23.98	PASS
	total	5200	15.7	<=23.98	PASS
	Ant0	5240	12.12	<=23.98	PASS
	Ant1	5240	12.30	<=23.98	PASS
	total	5240	15.2	<=23.98	PASS
	Ant0	5260	11.82	<=23.87	PASS
	Ant1	5260	11.96	<=23.89	PASS
	total	5260	14.9	<=23.89	PASS
	Ant0	5280	11.49	<=23.85	PASS
	Ant1	5280	11.82	<=23.87	PASS
	total	5280	14.7	<=23.87	PASS
	Ant0	5320	10.98	<=23.89	PASS
	Ant1	5320	11.16	<=23.89	PASS
	total	5320	14.1	<=23.89	PASS
	Ant0	5500	10.43	<=23.88	PASS
	Ant1	5500	10.68	<=23.84	PASS
	total	5500	13.6	<=23.84	PASS
	Ant0	5580	11.67	<=23.89	PASS
	Ant1	5580	12.04	<=23.89	PASS
	total	5580	14.9	<=23.89	PASS
	Ant0	5700	13.04	<=23.90	PASS
	Ant1	5700	13.43	<=23.87	PASS
	total	5700	16.2	<=23.87	PASS
	Ant0	5745	15.83	<=30	PASS
	Ant1	5745	16.00	<=30	PASS

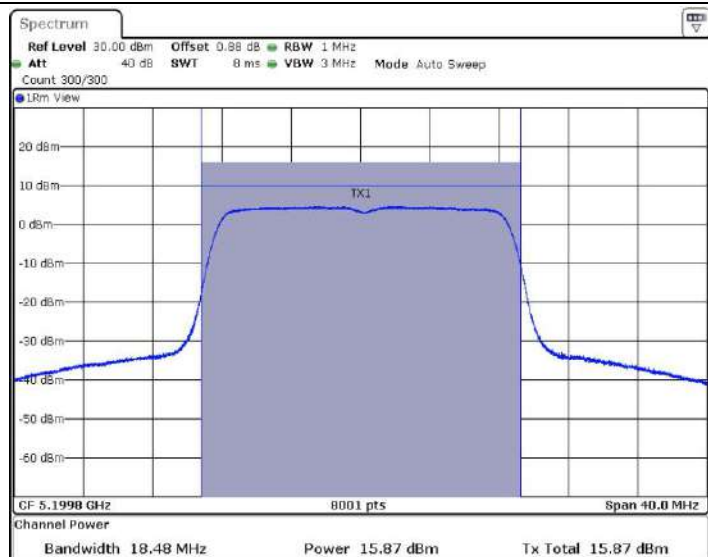
	total	5745	18.9	<=30	PASS
	Ant0	5785	16.55	<=30	PASS
	Ant1	5785	16.90	<=30	PASS
	total	5785	19.7	<=30	PASS
	Ant0	5825	16.86	<=30	PASS
	Ant1	5825	17.26	<=30	PASS
	total	5825	20.1	<=30	PASS
11AC40MIMO	Ant0	5190	12.67	<=23.98	PASS
	Ant1	5190	12.95	<=23.98	PASS
	total	5190	15.8	<=23.98	PASS
	Ant0	5230	12.29	<=23.98	PASS
	Ant1	5230	12.64	<=23.98	PASS
	total	5230	15.5	<=23.98	PASS
	Ant0	5270	14.13	<=23.98	PASS
	Ant1	5270	11.86	<=23.98	PASS
	total	5270	16.2	<=23.98	PASS
	Ant0	5310	11.15	<=23.98	PASS
	Ant1	5310	11.36	<=23.98	PASS
	total	5310	14.3	<=23.98	PASS
	Ant0	5510	10.74	<=23.98	PASS
	Ant1	5510	10.66	<=23.98	PASS
	total	5510	13.7	<=23.98	PASS
	Ant0	5550	11.39	<=23.98	PASS
	Ant1	5550	11.23	<=23.98	PASS
	total	5550	14.3	<=23.98	PASS
	Ant0	5670	13.03	<=23.98	PASS
	Ant1	5670	13.46	<=23.98	PASS
	total	5670	16.3	<=23.98	PASS
	Ant0	5755	16.16	<=30	PASS
	Ant1	5755	16.08	<=30	PASS
	total	5755	19.1	<=30	PASS
	Ant0	5795	16.81	<=30	PASS
	Ant1	5795	16.81	<=30	PASS
	total	5795	19.8	<=30	PASS
11AC80MIMO	Ant0	5210	11.28	<=23.98	PASS
	Ant1	5210	11.15	<=23.98	PASS
	total	5210	14.2	<=23.98	PASS
	Ant0	5290	10.86	<=23.98	PASS
	Ant1	5290	10.37	<=23.98	PASS
	total	5290	13.6	<=23.98	PASS
	Ant0	5530	9.29	<=23.98	PASS
	Ant1	5530	9.52	<=23.98	PASS
	total	5530	12.4	<=23.98	PASS

	Ant0	5610	13.70	≤ 23.98	PASS
	Ant1	5610	13.34	≤ 23.98	PASS
	total	5610	16.5	≤ 23.98	PASS
	Ant0	5775	14.74	≤ 30	PASS
	Ant1	5775	14.87	≤ 30	PASS
	total	5775	17.8	≤ 30	PASS

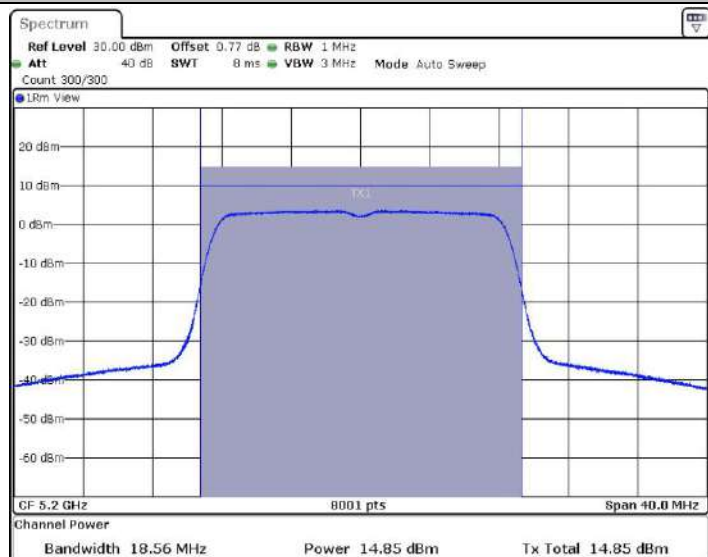
Note: The Duty Cycle Factor is compensated in the graph.

Test Graphs

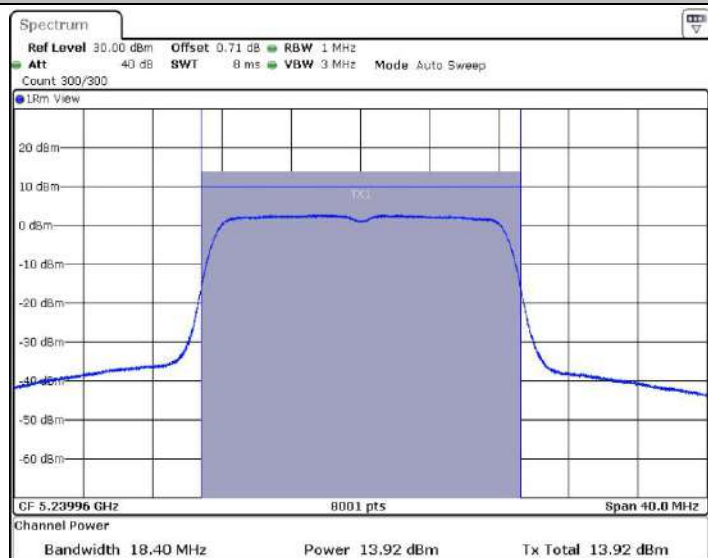




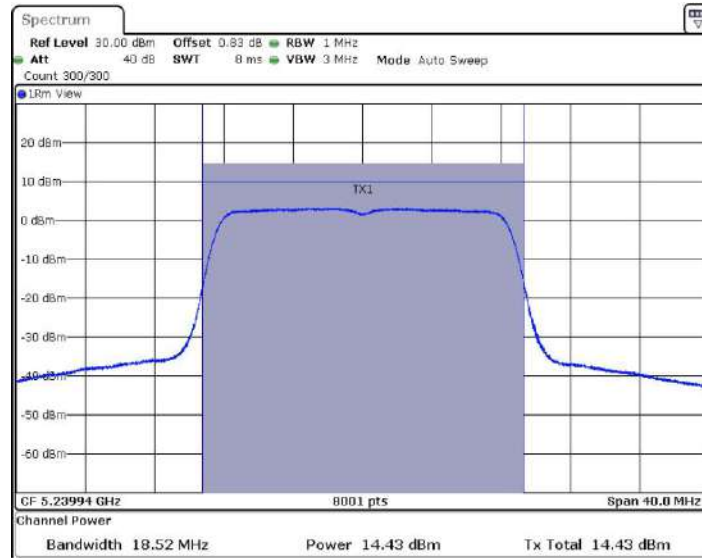
11A_Ant0_5200



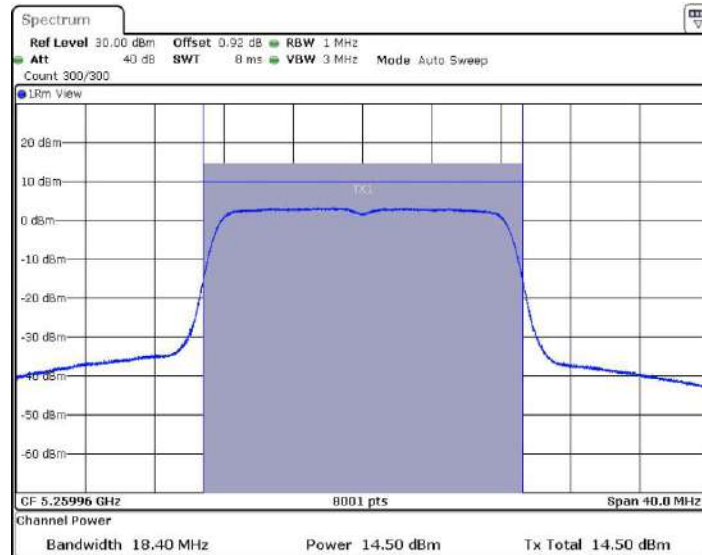
11A_Ant1_5200



11A_Ant0_5240



11A_Ant1_5240



11A_Ant0_5260