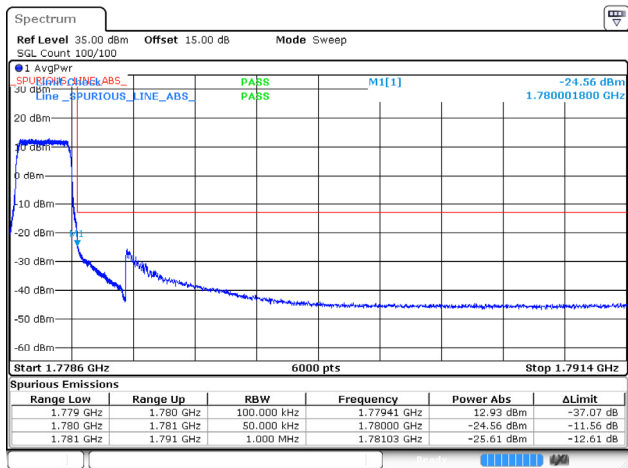
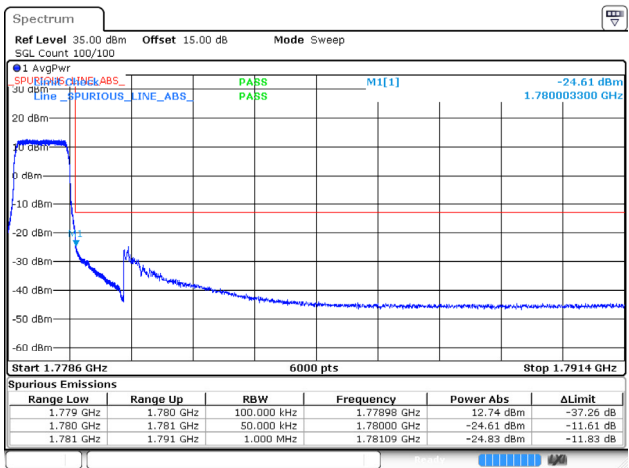


1.4MHz_High_16QAM_1@5



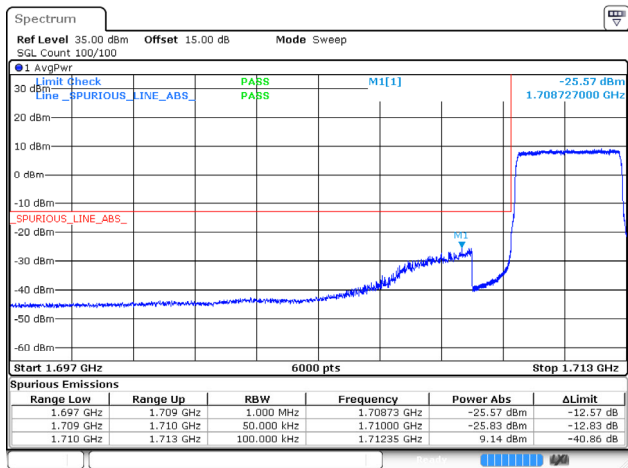
ProjectNo.:RKSA250319001 Tester:Jason Lu
Date: 22 MAY 2025 19:04:31

1.4MHz_High_16QAM_6@0



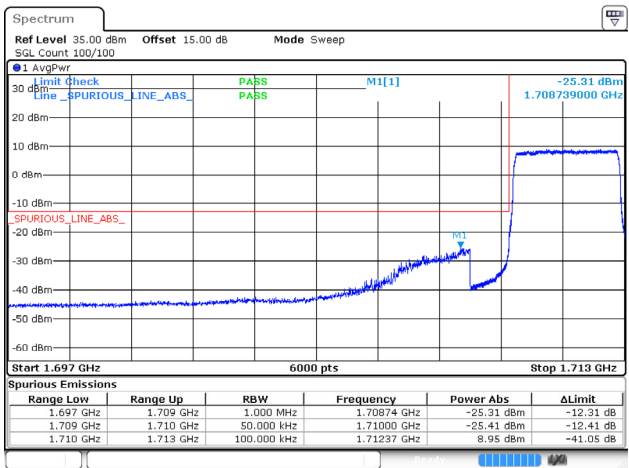
ProjectNo.:RKSA250319001 Tester:Jason Lu
Date: 22 MAY 2025 19:05:01

3MHz_Low_QPSK_1@0



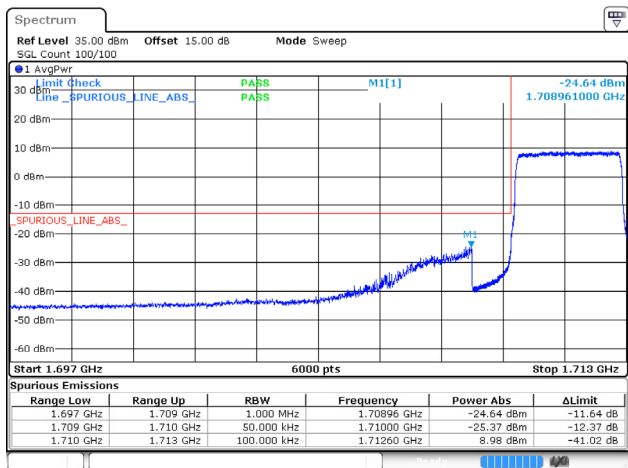
ProjectNo.:RKSA250319001 Tester:Jason Lu
Date: 22 MAY 2025 19:06:12

3MHz_Low_QPSK_15@0



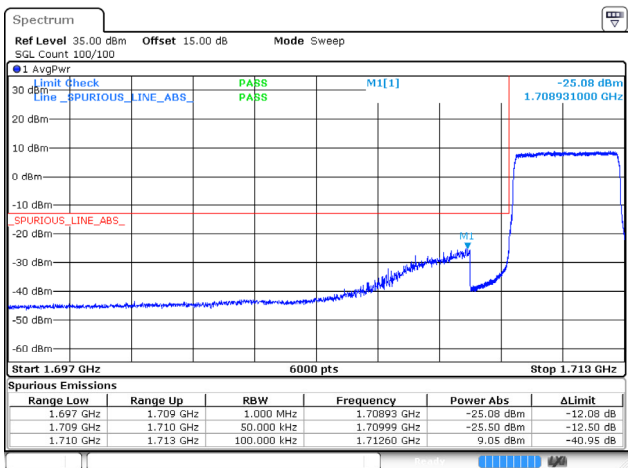
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Date: 22 MAY 2025 19:06:46

3MHz_Low_16QAM_1@0



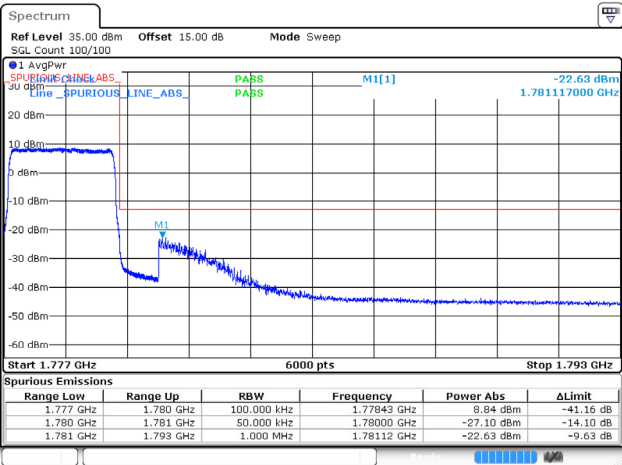
ProjectNo.:RKSA250319001 Tester:Jason Lu
Date: 22 MAY 2025 19:07:19

3MHz_Low_16QAM_15@0



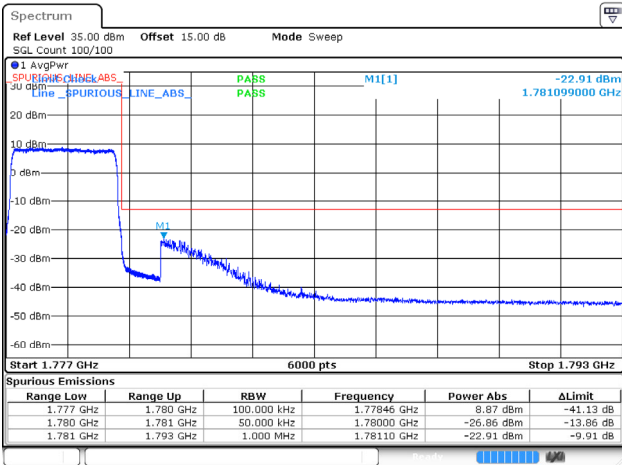
ProjectNo.:RKSA250319001 Tester:Jason Lu
Date: 22 MAY 2025 19:07:52

3MHz_High_QPSK_1@14



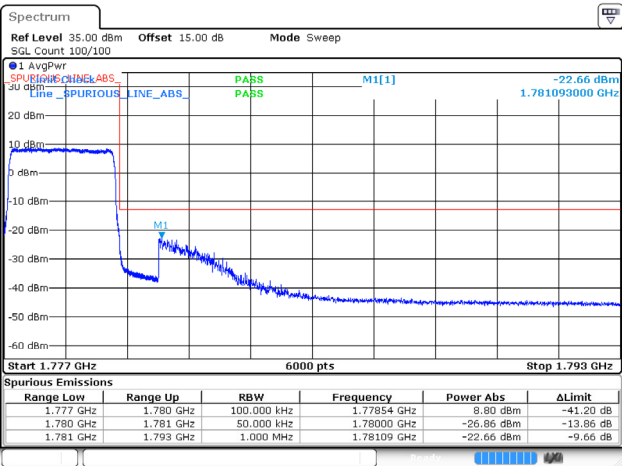
ProjectNo.:RKSA250319001 Tester:Jason Lu
Date: 22 MAY 2025 19:08:25

3MHz_High_QPSK_15@0



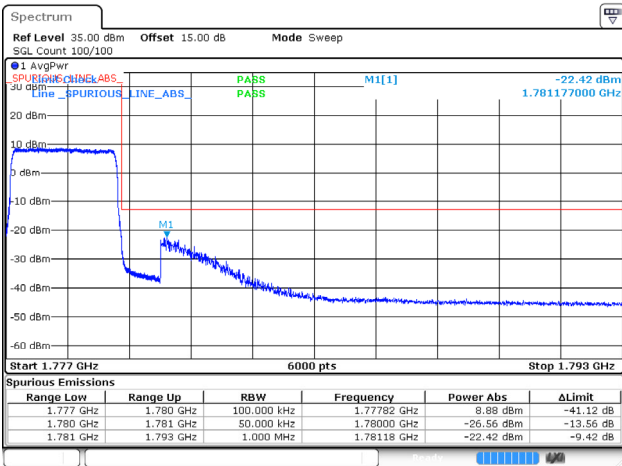
ProjectNo.:RKSA250319001 Tester:Jason Lu
Date: 22 MAY 2025 19:08:59

3MHz_High_16QAM_1@14



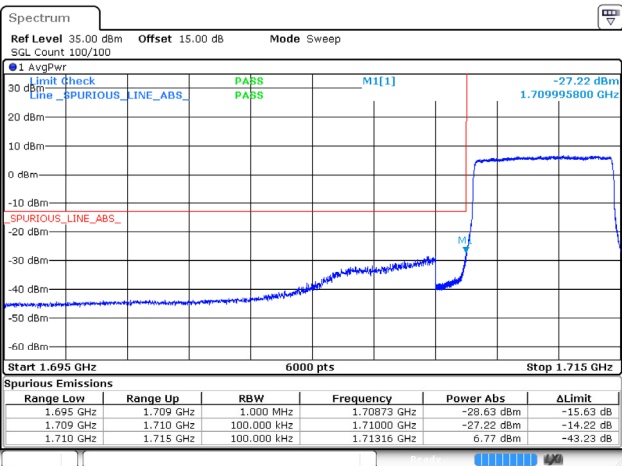
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Date: 22 MAY 2025 19:09:32

3MHz_High_16QAM_15@0



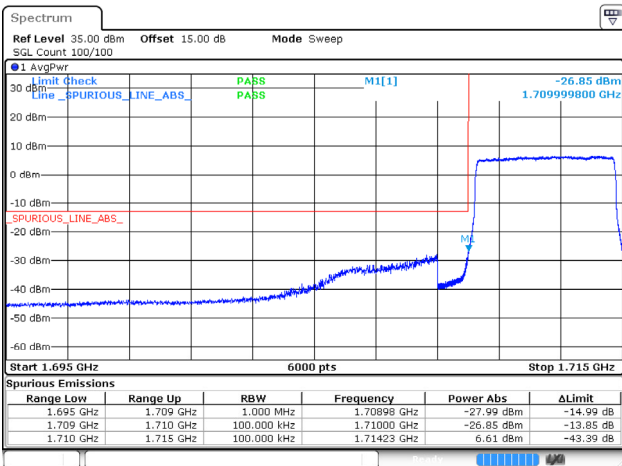
ProjectNo.:RKSA250319001 Tester:Jason Lu
Date: 22 MAY 2025 19:10:05

5MHz_Low_QPSK_1@0



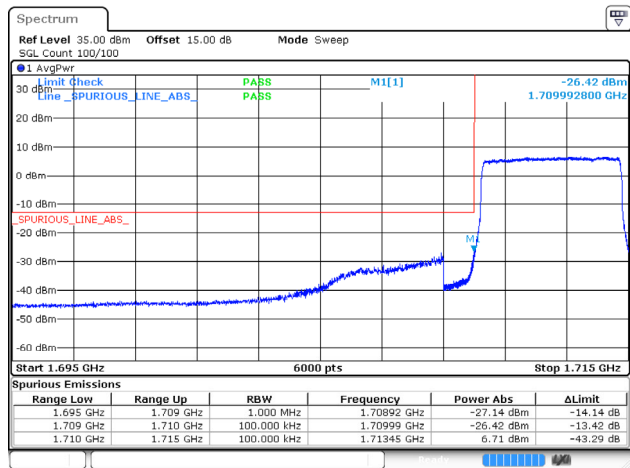
ProjectNo.:RKSA250319001 Tester:Jason Lu
Date: 22 MAY 2025 19:11:23

5MHz_Low_QPSK_25@0



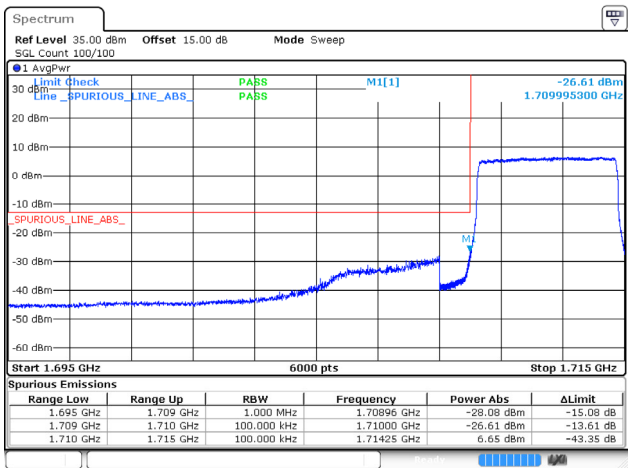
ProjectNo.:RKSA250319001 Tester:Jason Lu
Date: 22 MAY 2025 19:11:53

5MHz_Low_16QAM_1@0



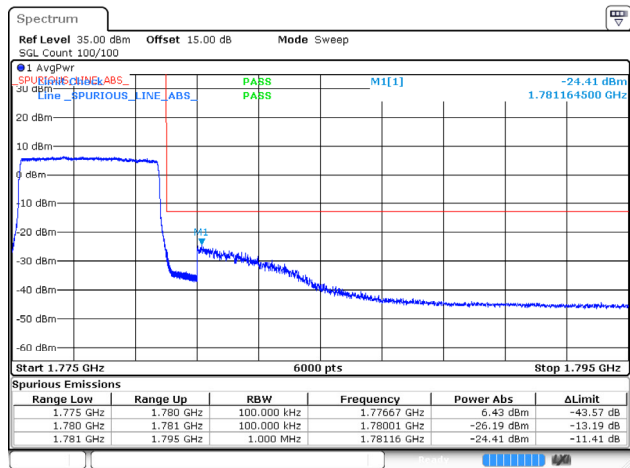
ProjectNo.:RKSA250319001 Tester:Jason Lu
Date: 22 MAY 2025 19:12:24

5MHz_Low_16QAM_25@0



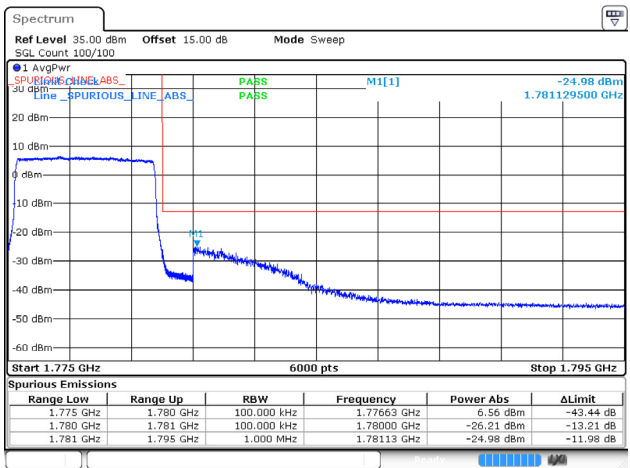
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Date: 22 MAY 2025 19:12:56

5MHz_High_QPSK_1@24



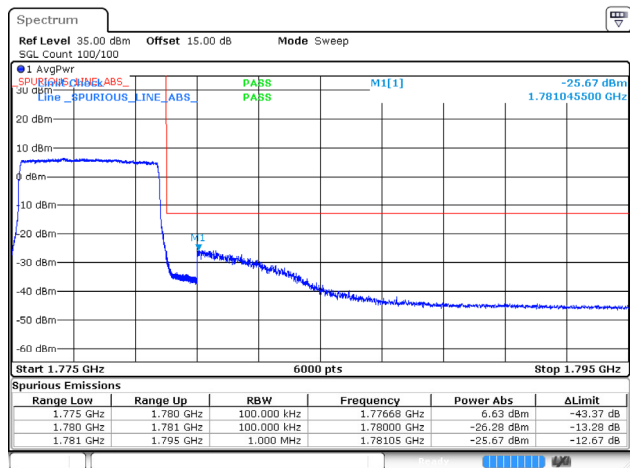
ProjectNo.:RKSA250319001 Tester:Jason Lu
Date: 22 MAY 2025 19:13:28

5MHz_High_QPSK_25@0



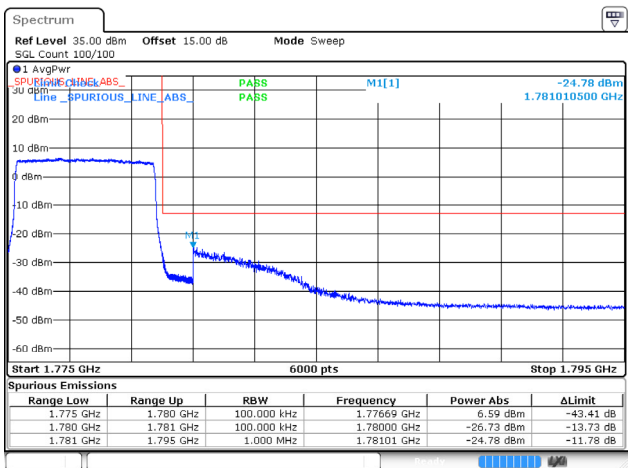
ProjectNo.:RKSA250319001 Tester:Jason Lu
Date: 22 MAY 2025 19:13:59

5MHz_High_16QAM_1@24



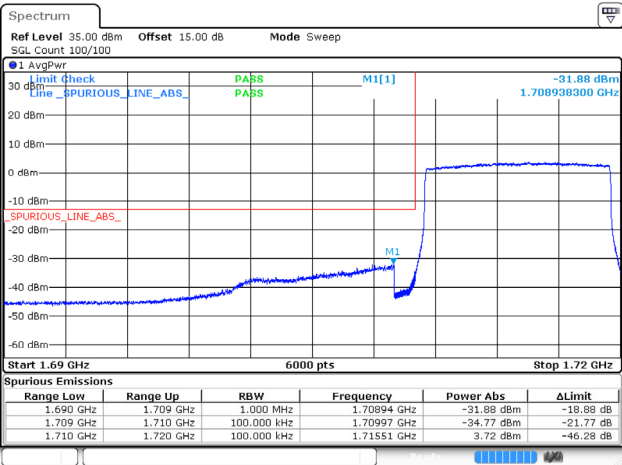
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Date: 22 MAY 2025 19:14:33

5MHz_High_16QAM_25@0



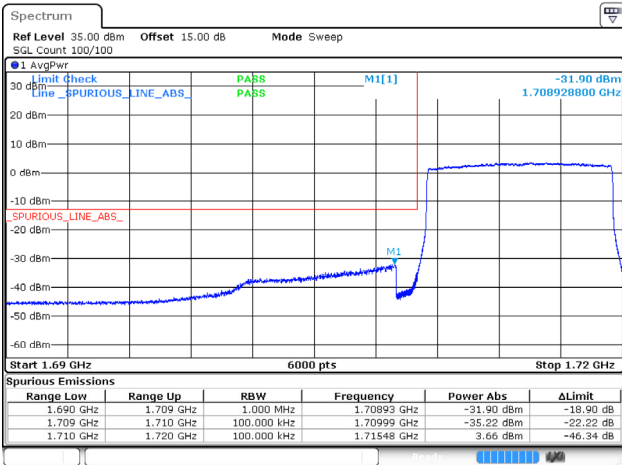
ProjectNo.:RKSA250319001 Tester:Jason Lu
Date: 22 MAY 2025 19:15:07

10MHz_Low_QPSK_1@0



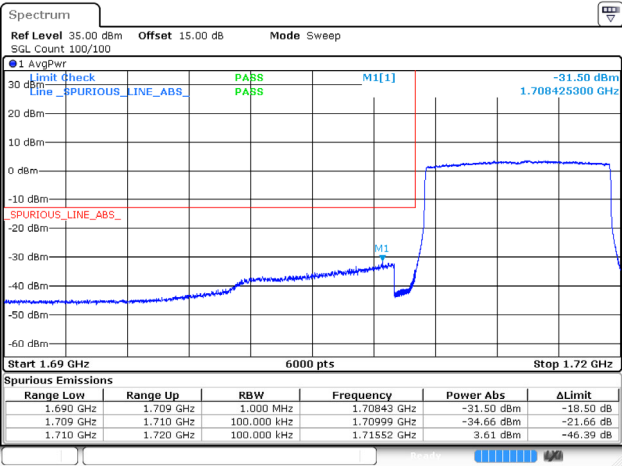
ProjectNo.:RKSA250319001 Tester:Jason Lu
Date: 22 MAY 2025 19:16:24

10MHz_Low_QPSK_50@0



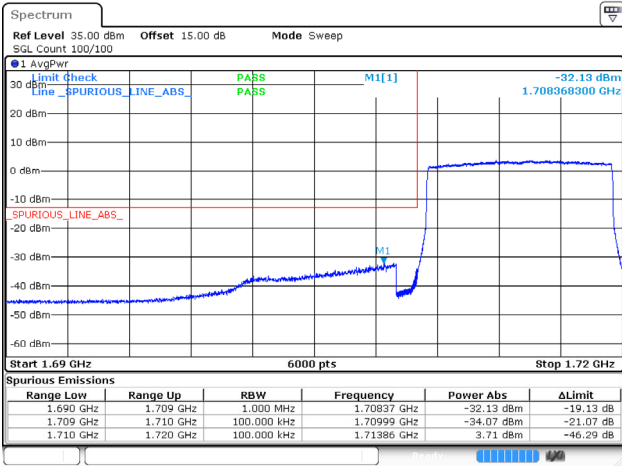
ProjectNo.:RKSA250319001 Tester:Jason Lu
Date: 22 MAY 2025 19:17:08

10MHz_Low_16QAM_1@0



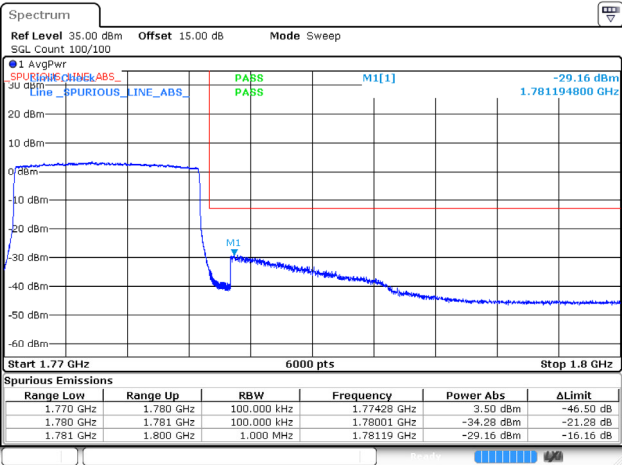
ProjectNo.:RKSA250319001 Tester:Jason Lu
Date: 22 MAY 2025 19:17:53

10MHz_Low_16QAM_50@0



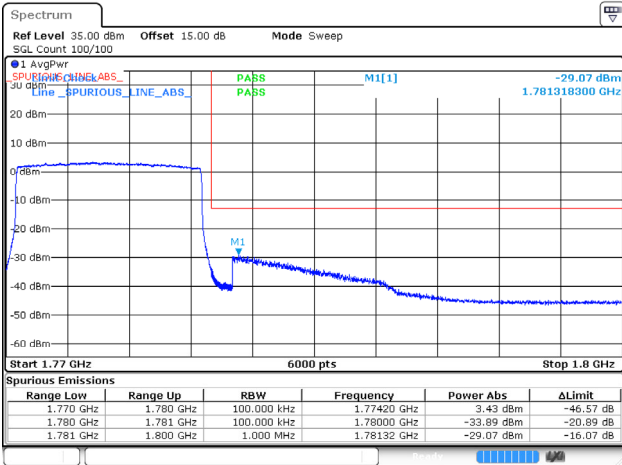
ProjectNo.:RKSA250319001 Tester:Jason Lu
Date: 22 MAY 2025 19:18:38

10MHz_High_QPSK_1@49



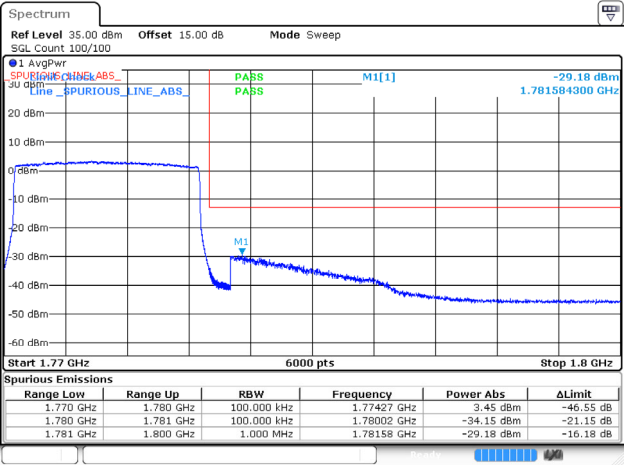
ProjectNo.:RKSA250319001 Tester:Jason Lu
Date: 22 MAY 2025 19:19:23

10MHz_High_QPSK_50@0



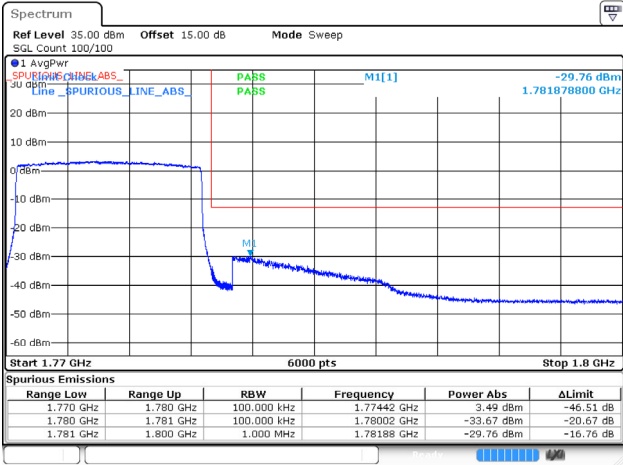
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Date: 22 MAY 2025 19:20:07

10MHz_High_16QAM_1@49



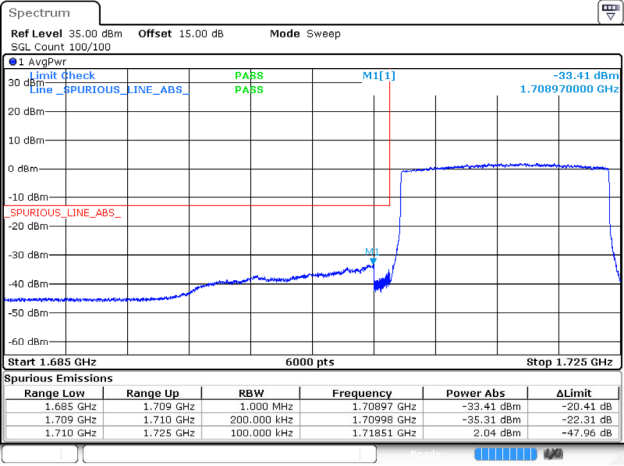
ProjectNo.:RKSA250319001 Tester:Jason Lu
Date: 22 MAY 2025 19:20:52

10MHz_High_16QAM_50@0



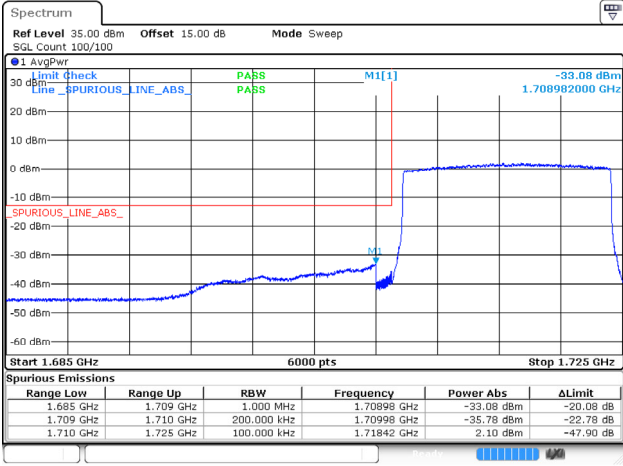
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Date: 22 MAY 2025 19:21:37

15MHz_Low_QPSK_1@0



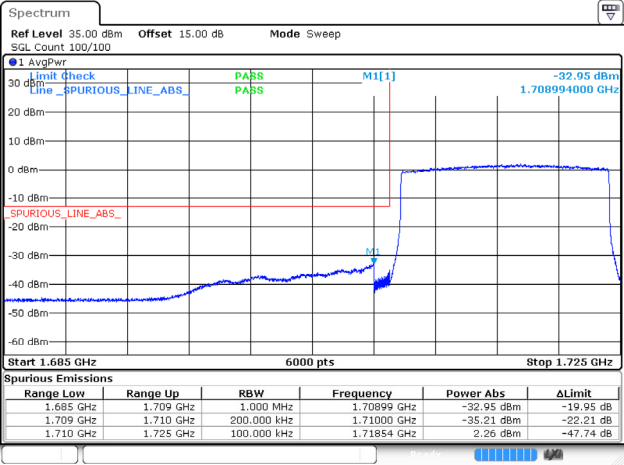
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Date: 22 MAY 2025 19:23:10

15MHz_Low_QPSK_75@0



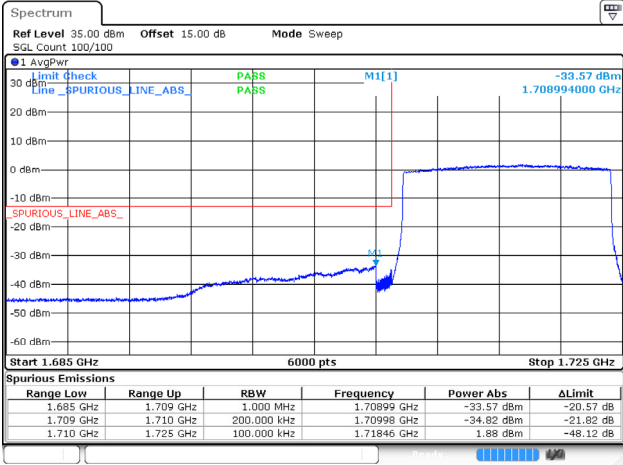
ProjectNo.:RKSA250319001 Tester:Jason Lu
Date: 22 MAY 2025 19:24:03

15MHz_Low_16QAM_1@0



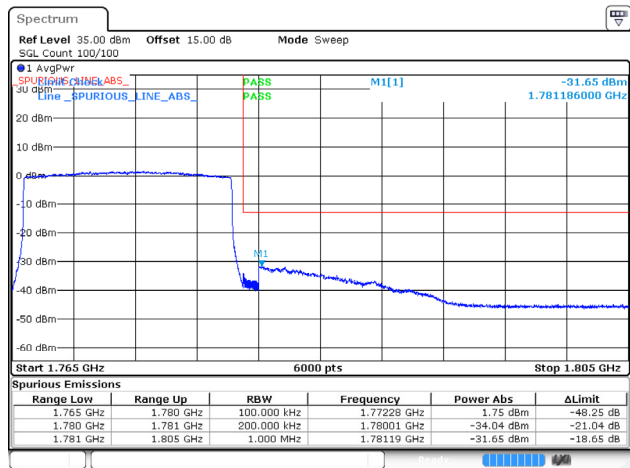
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Date: 22 MAY 2025 19:24:55

15MHz_Low_16QAM_75@0



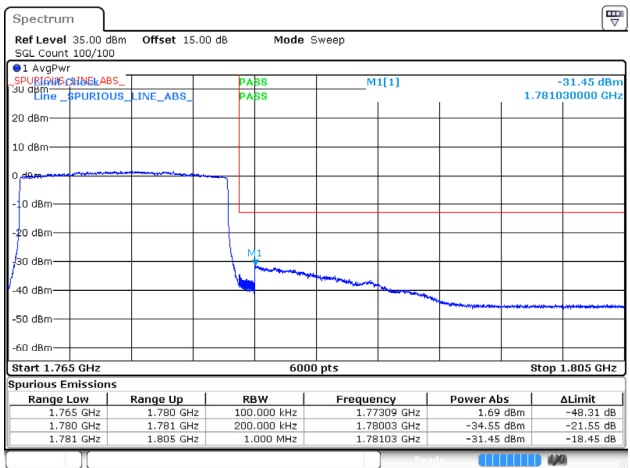
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Date: 22 MAY 2025 19:25:47

15MHz_High_QPSK_1@74



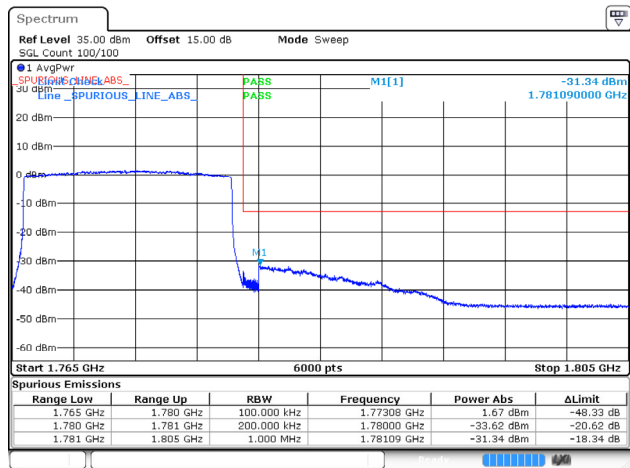
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Date: 22 MAY 2025 19:26:39

15MHz_High_QPSK_75@0



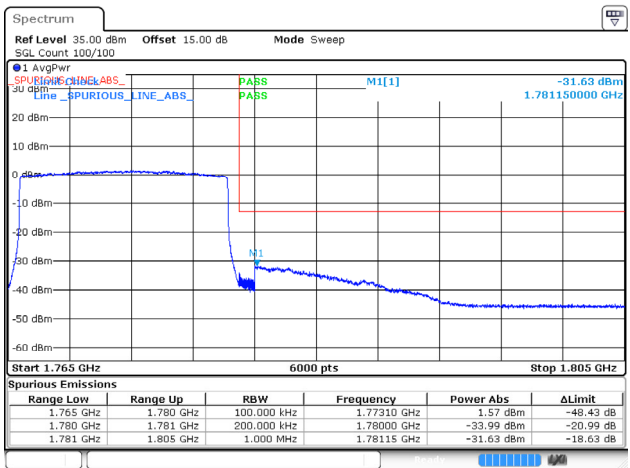
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Date: 22 MAY 2025 19:27:30

15MHz_High_16QAM_1@74



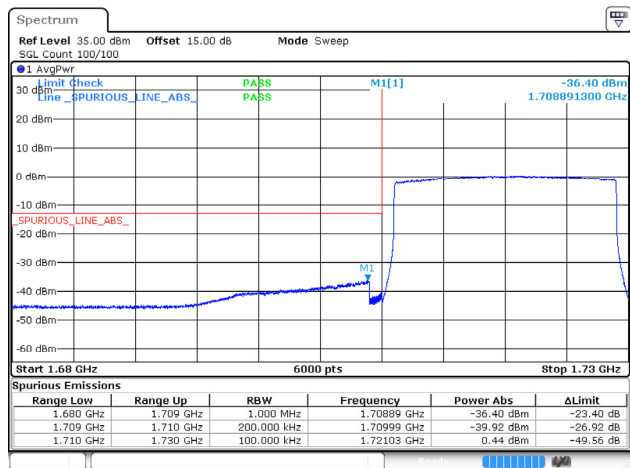
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Date: 22 MAY 2025 19:28:24

15MHz_High_16QAM_75@0



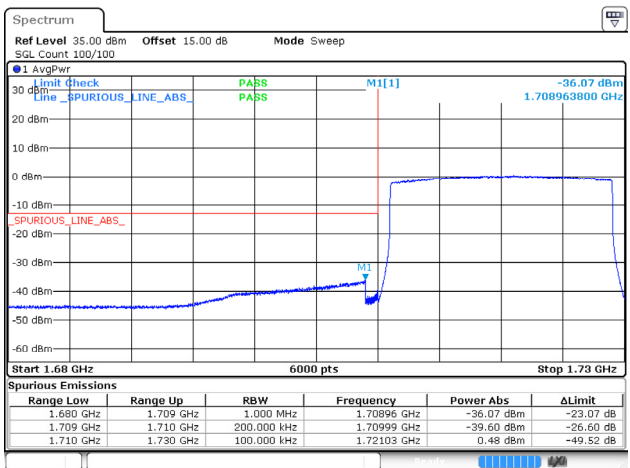
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Date: 22 MAY 2025 19:29:16

20MHz_Low_QPSK_1@0



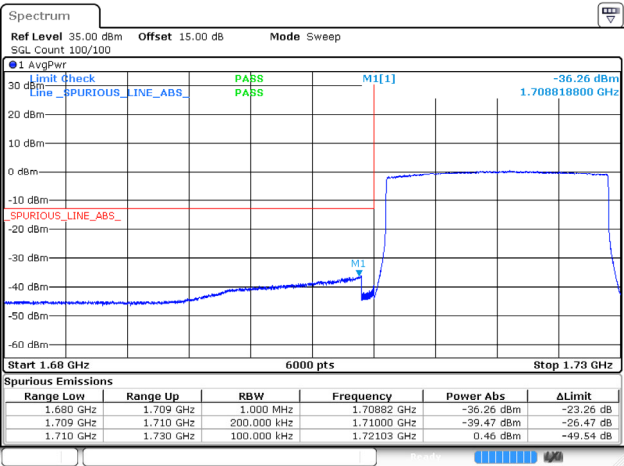
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Date: 22 MAY 2025 19:30:52

20MHz_Low_QPSK_100@0



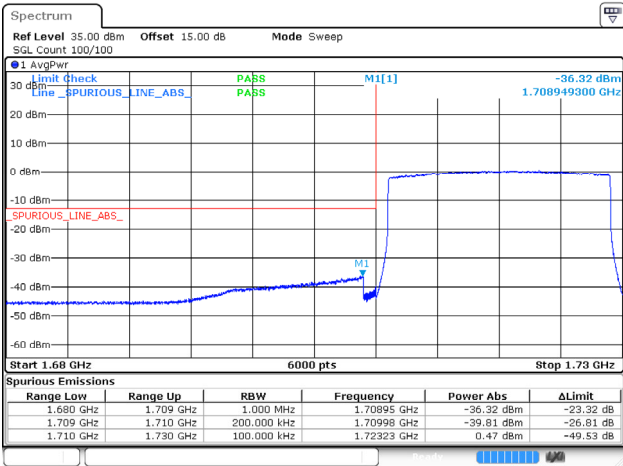
ProjectNo.:RKSA250319001 Tester:Jason Lu
Date: 22 MAY 2025 19:31:57

20MHz_Low_16QAM_1@0



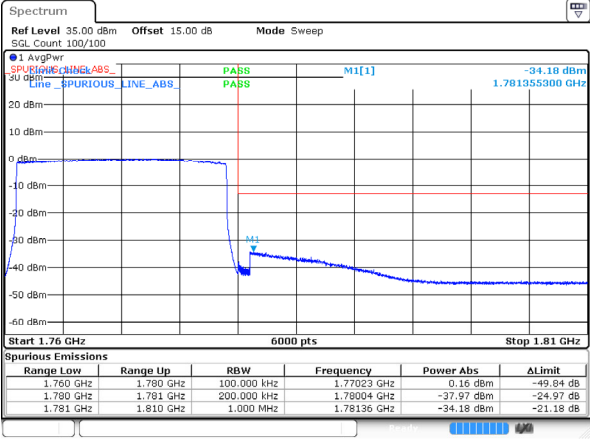
ProjectNo.:RKSA250319001 Tester:Jason Lu
Date: 22 MAY 2025 19:33:03

20MHz_Low_16QAM_100@0



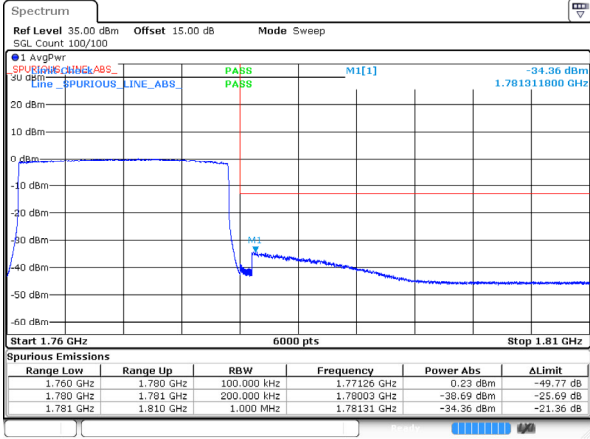
ProjectNo.:RKSA250319001 Tester:Jason Lu
Date: 22 MAY 2025 19:34:09

20MHz_High_QPSK_1@99



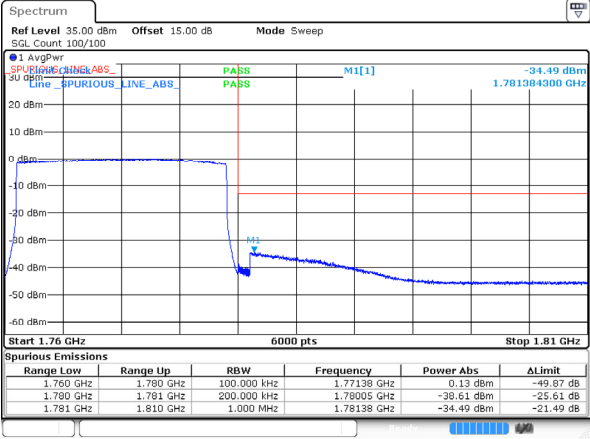
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Date: 22 MAY 2025 19:35:14

20MHz_High_QPSK_100@0



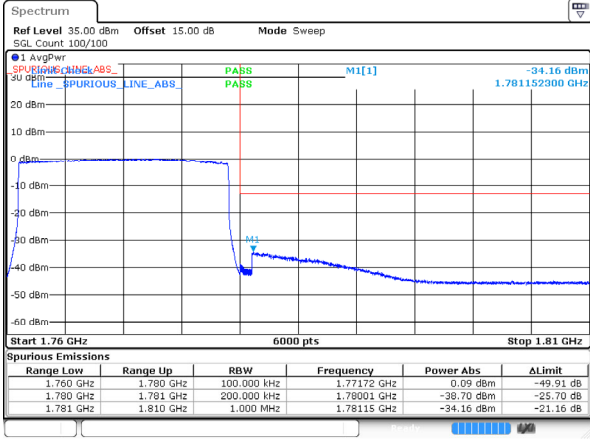
ProjectNo.:RKSA250319001 Tester:Jason Lu
Date: 22 MAY 2025 19:36:19

20MHz_High_16QAM_1@99



ProjectNo.:RKSA250319001 Tester:Jason Lu
Date: 22 MAY 2025 19:37:25

20MHz_High_16QAM_100@0



ProjectNo.:RKSA250319001 Tester:Jason Lu
Date: 22 MAY 2025 19:38:30

***** END *****