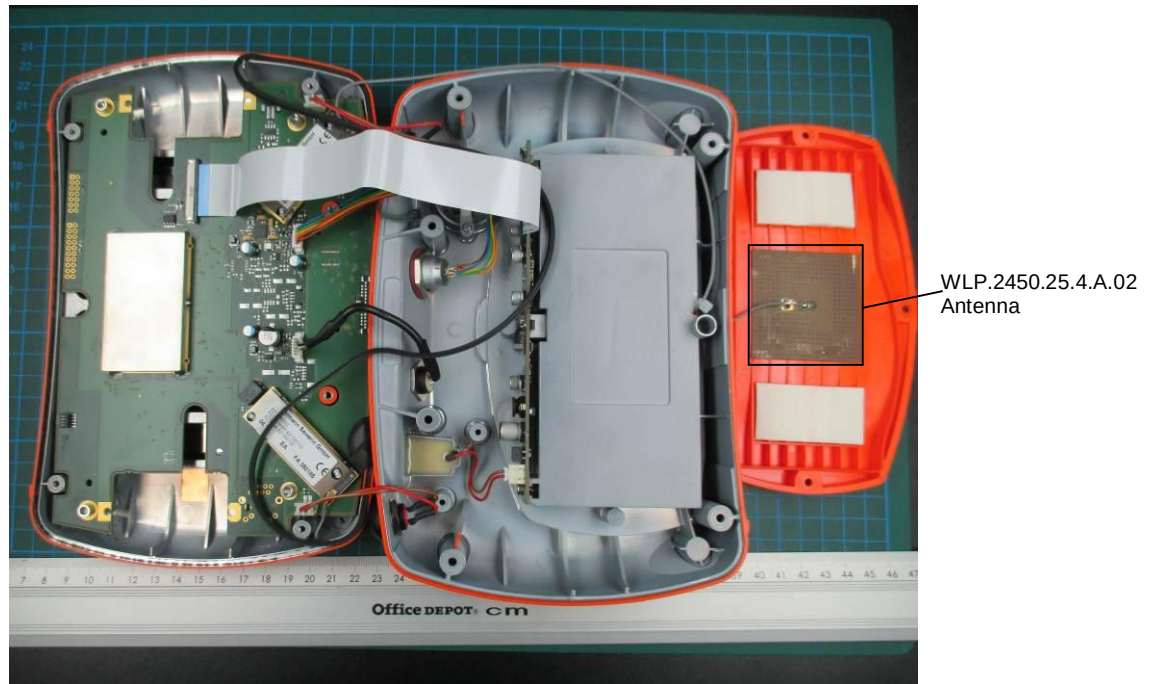
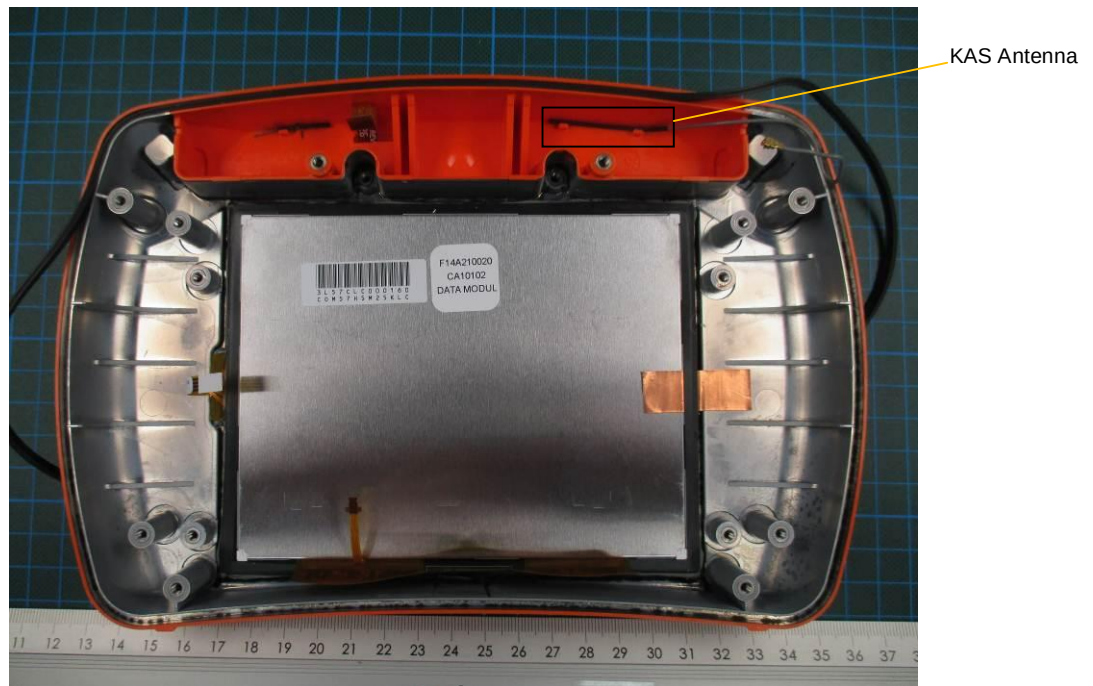


ANNEX C: INTERNAL PHOTOGRAPHS



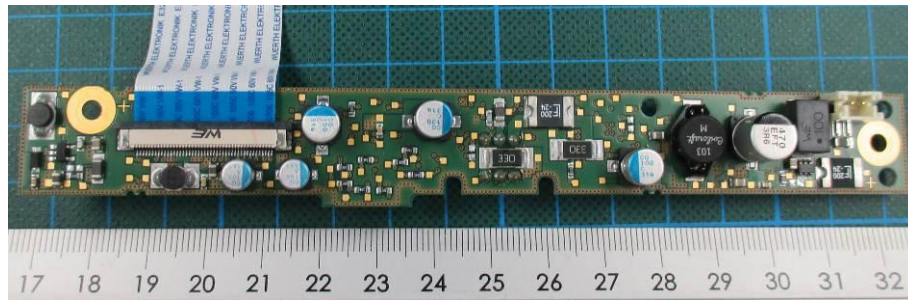
145725_34.JPG: Host Device – Housing open

ANNEX C: INTERNAL PHOTOGRAPHS



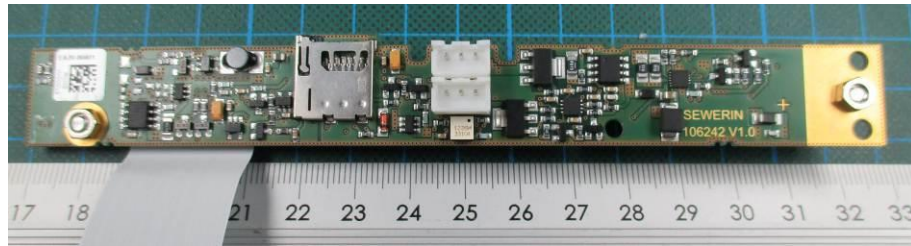
145725_34.JPG: Host Device – Housing open

ANNEX C: INTERNAL PHOTOGRAPHS



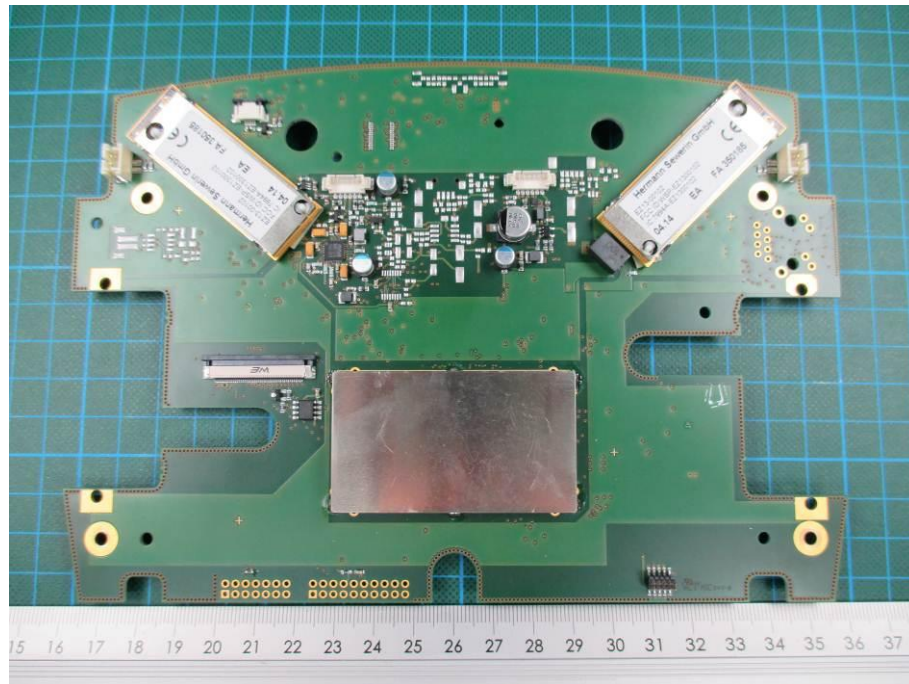
145725_35.JPG: Ancillary PCB – Top View

ANNEX C: INTERNAL PHOTOGRAPHS



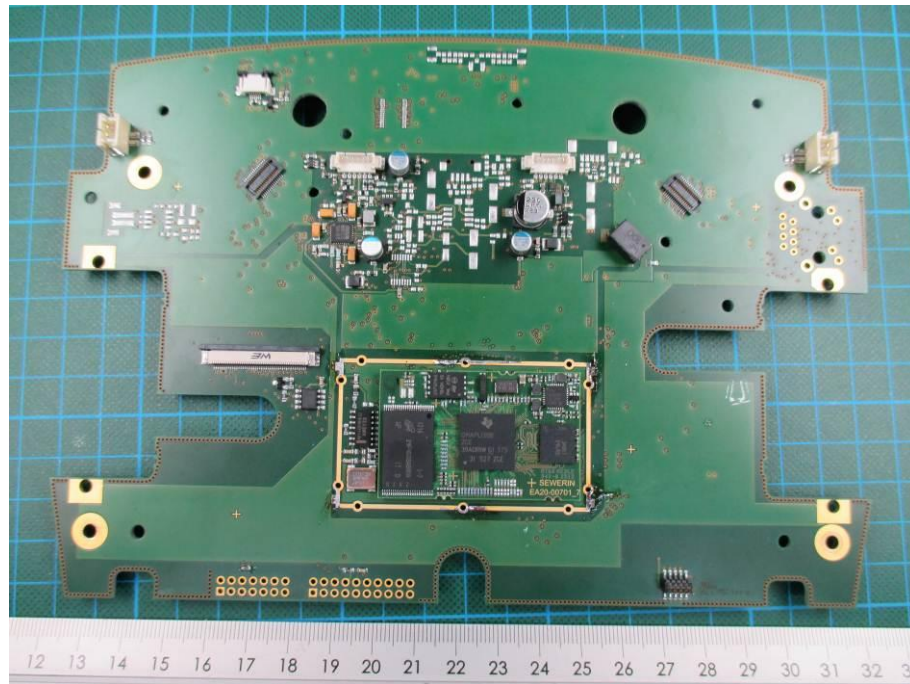
145725_36.JPG: Ancillary PCB – Bottom View

ANNEX C: INTERNAL PHOTOGRAPHS



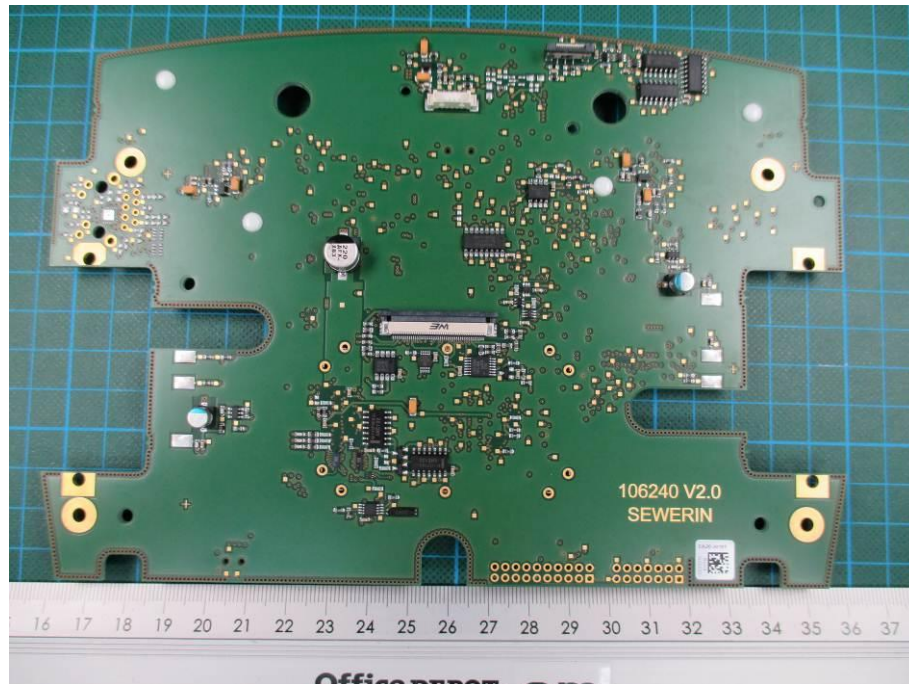
145725_37.JPG: Main PCB with RF modules & Shielding– Top View

ANNEX C: INTERNAL PHOTOGRAPHS



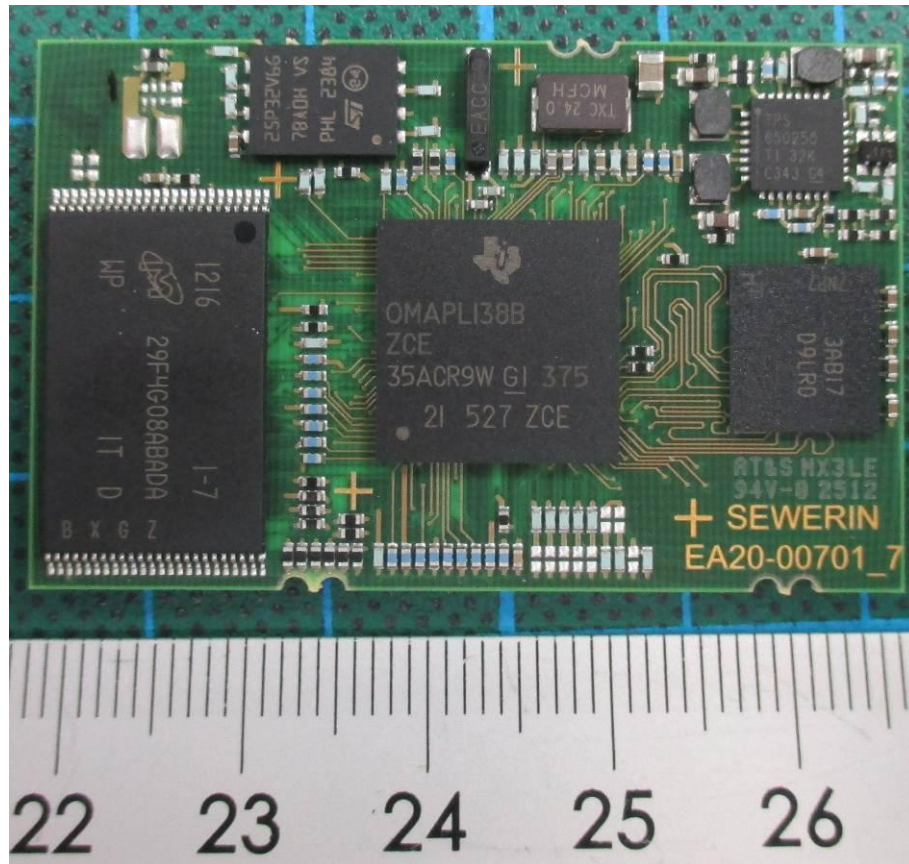
145725_38.JPG: Main PCB without RF modules & Shielding– Top View

ANNEX C: INTERNAL PHOTOGRAPHS



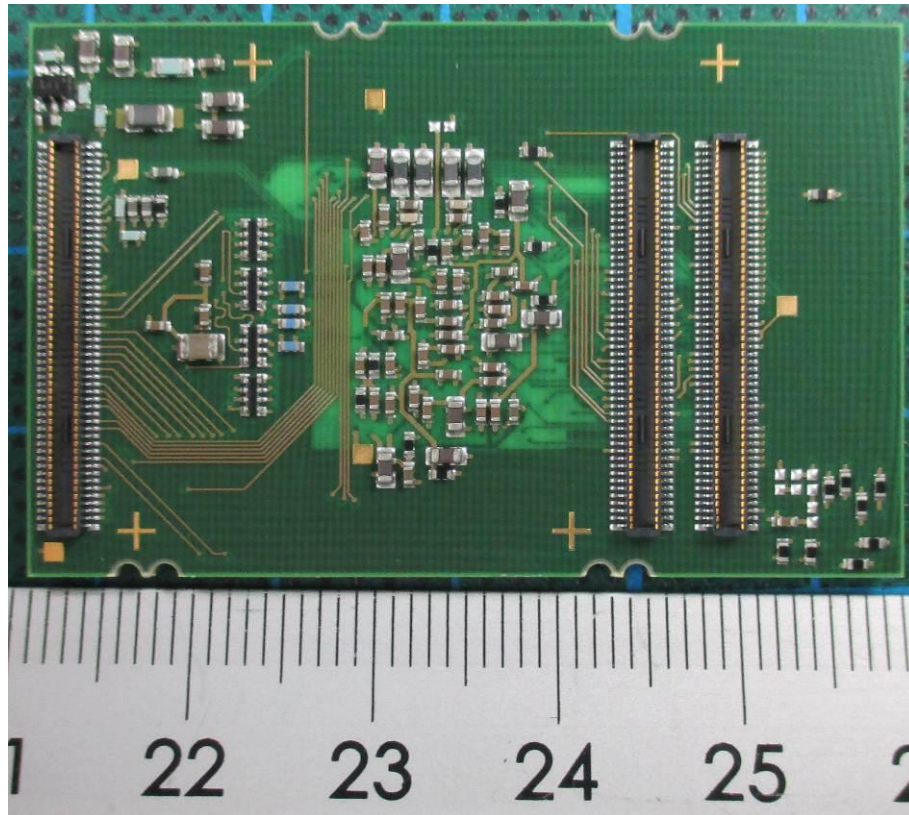
145725_39.JPG: Main PCB – Bottom View

ANNEX C: INTERNAL PHOTOGRAPHS



145725_40.JPG: Logic PCB – Top View

ANNEX C: INTERNAL PHOTOGRAPHS



145725_41.JPG: Logic PCB – Bottom View

ANNEX C: INTERNAL PHOTOGRAPHS



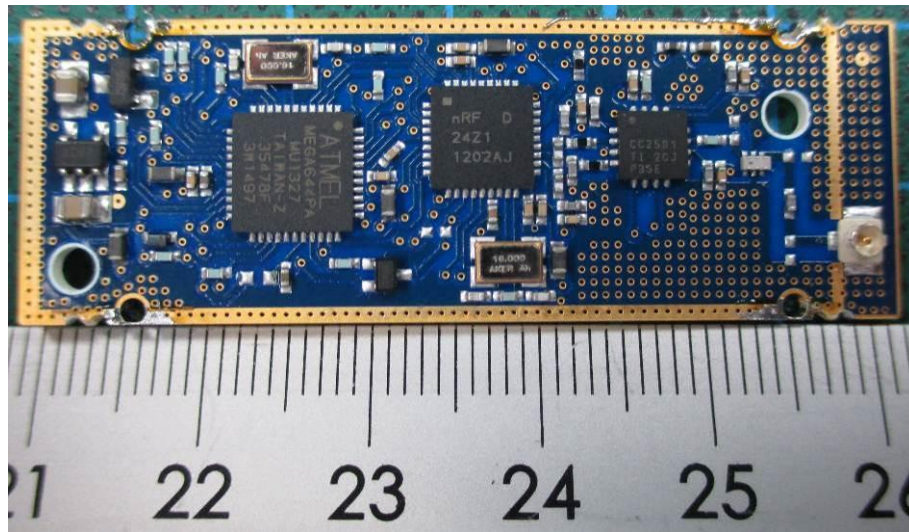
145725_42.JPG: RF Module 1 – Top View with shielding

ANNEX C: INTERNAL PHOTOGRAPHS



145725_44.JPG: RF Module 1 – Bottom View

ANNEX C: INTERNAL PHOTOGRAPHS



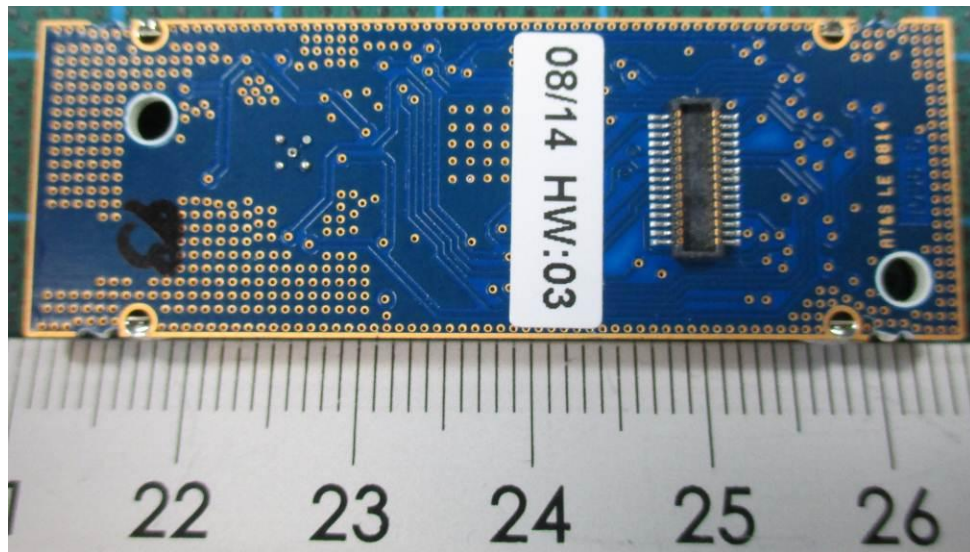
145725_43.JPG: RF Module 1 – Top View without shielding

ANNEX C: INTERNAL PHOTOGRAPHS



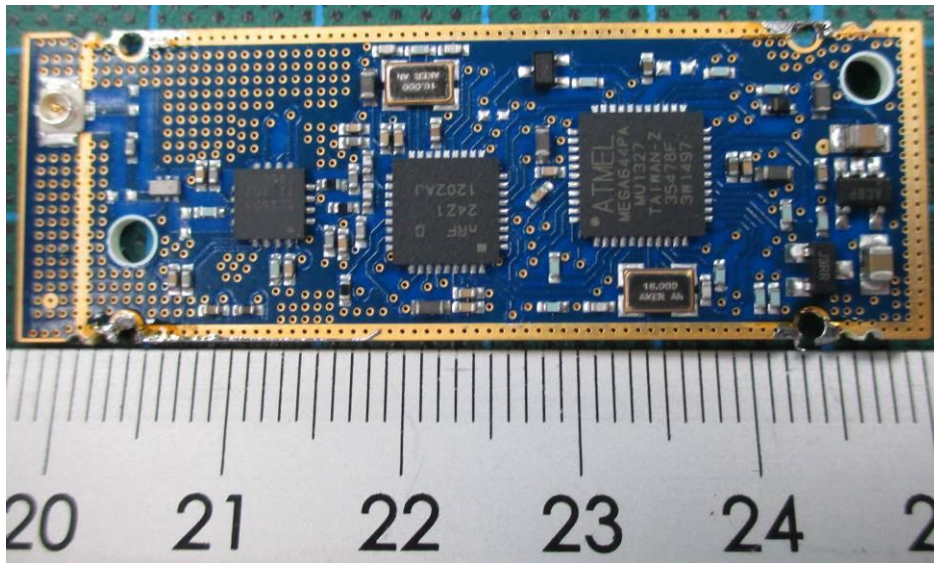
145725_45.JPG: RF Module 2 – Top View with shielding

ANNEX C: INTERNAL PHOTOGRAPHS



145725_46.JPG: RF Module 2 – Bottom View

ANNEX C: INTERNAL PHOTOGRAPHS



145725_47.JPG: RF Module 2 – Top View without shielding