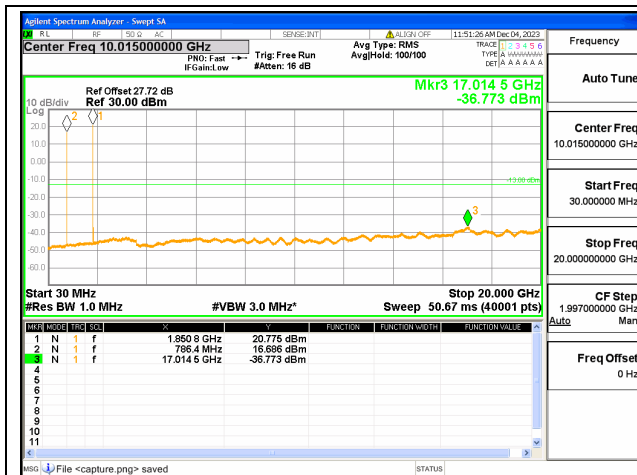
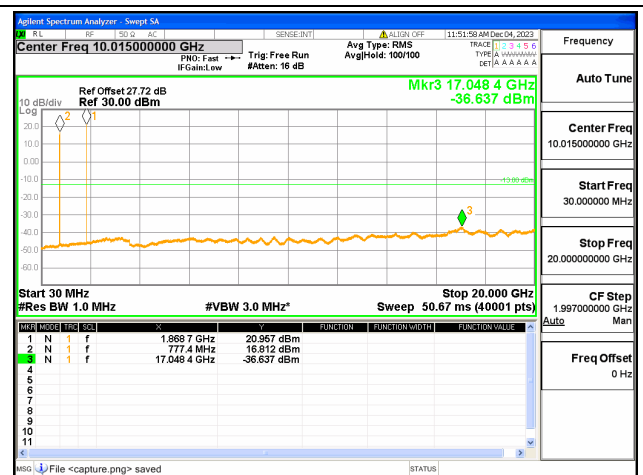
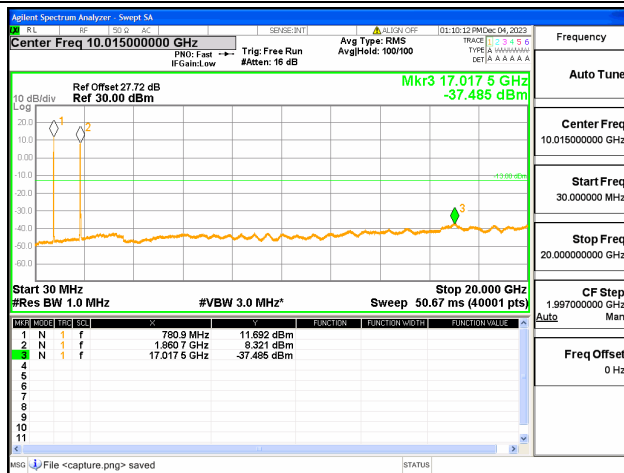
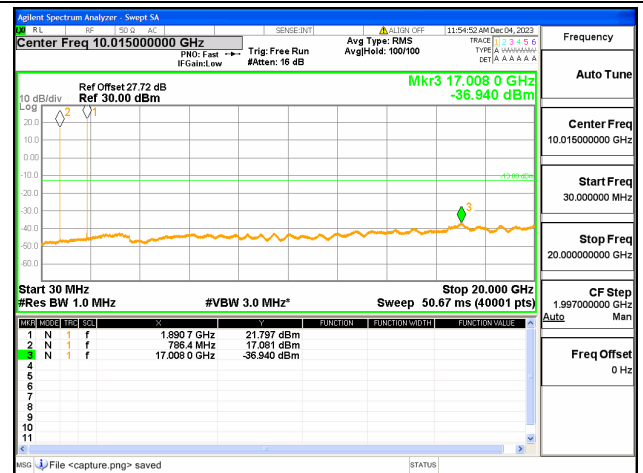
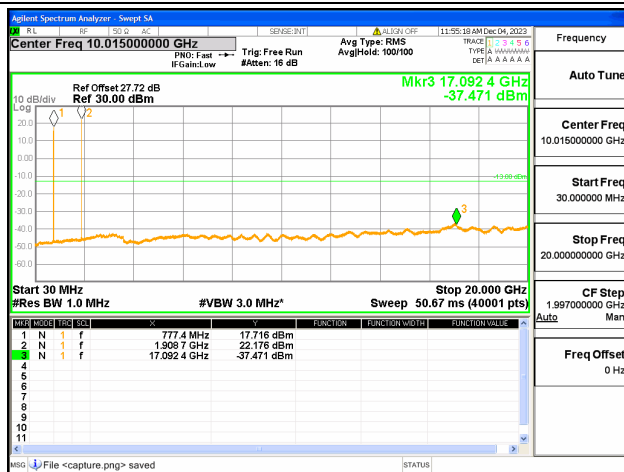
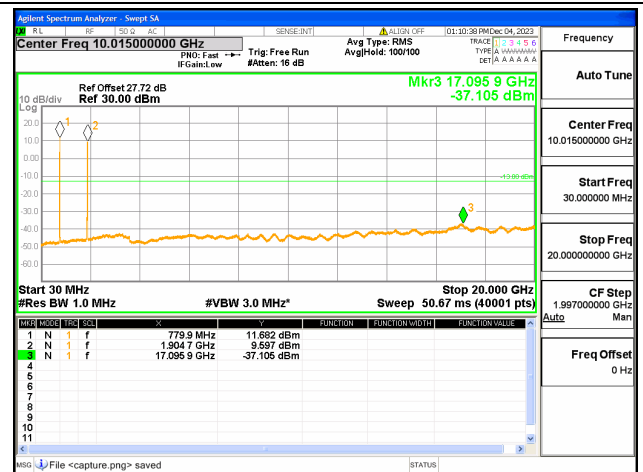
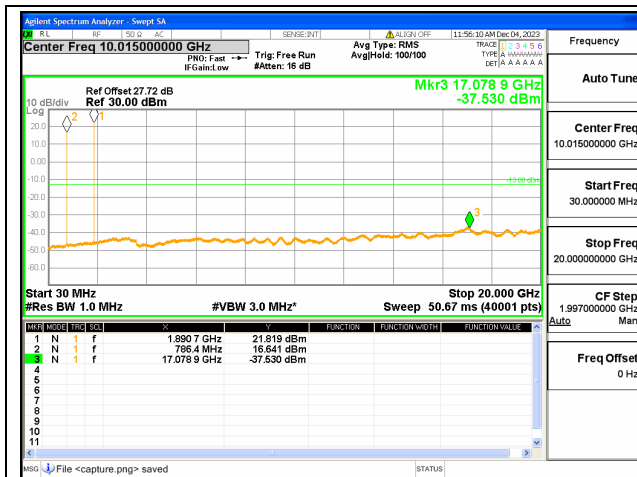
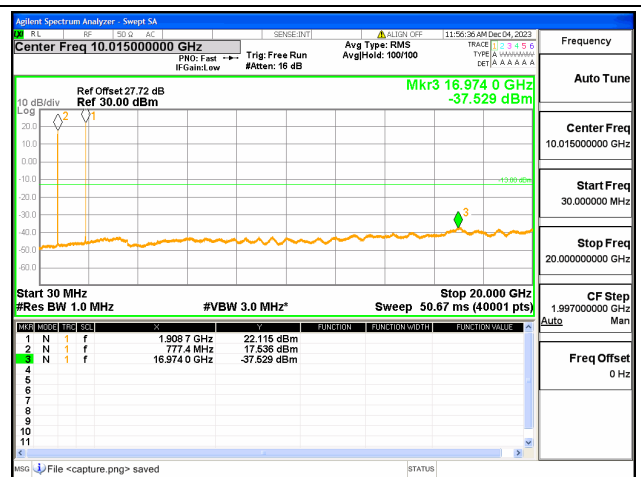


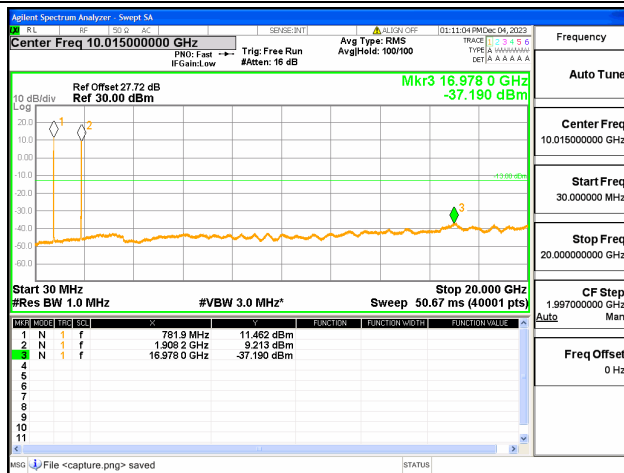
2A-13A / 20+10MHz / QPSK / Low+Mid CH /
1@0-1@492A-13A / 20+10MHz / QPSK / Low+Mid CH /
1@99-1@02A-13A / 20+10MHz / QPSK / Low+Mid CH /
100@0-50@02A-13A / 20+10MHz / QPSK / High+Low CH /
1@0-1@492A-13A / 20+10MHz / QPSK / High+Low CH /
1@99-1@02A-13A / 20+10MHz / QPSK / High+Low CH /
100@0-50@0



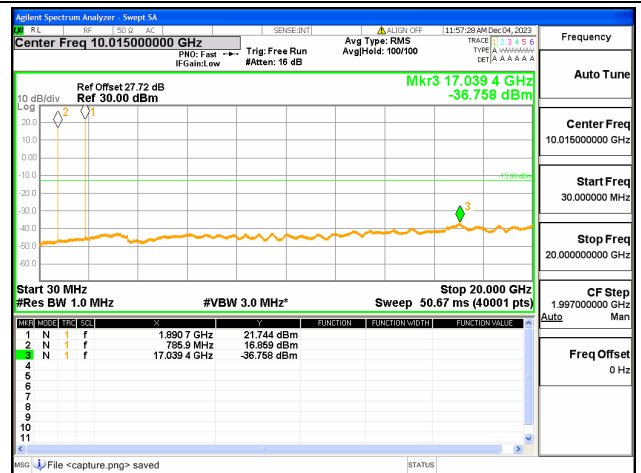
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1@0-1@49



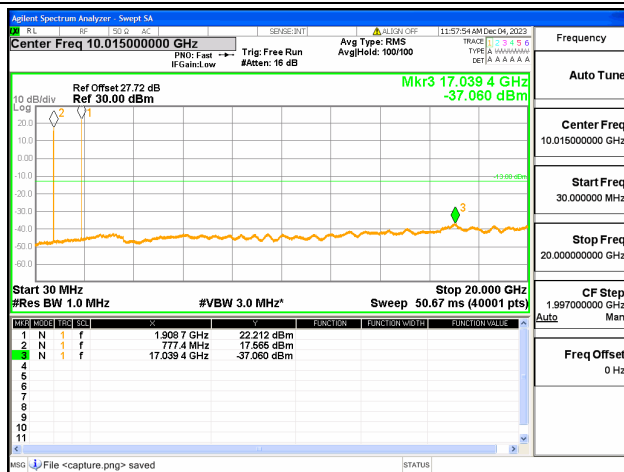
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1@99-1@0



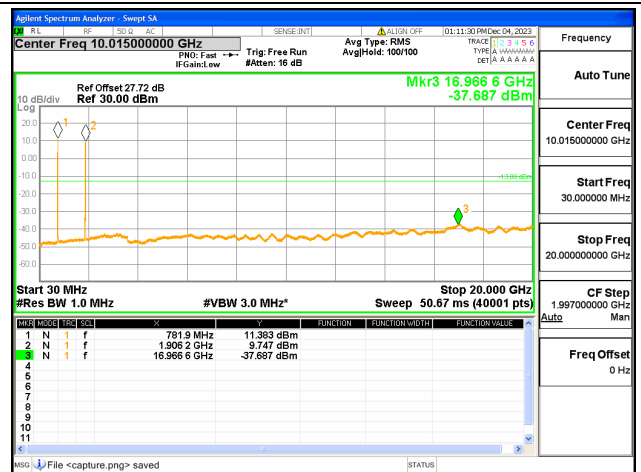
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100@0-50@0



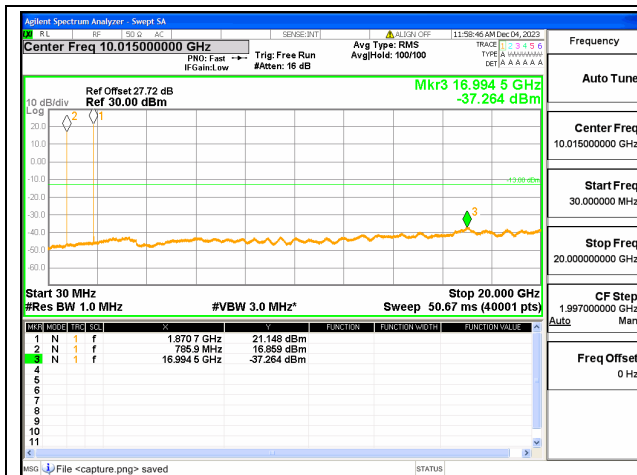
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1@0-1@49



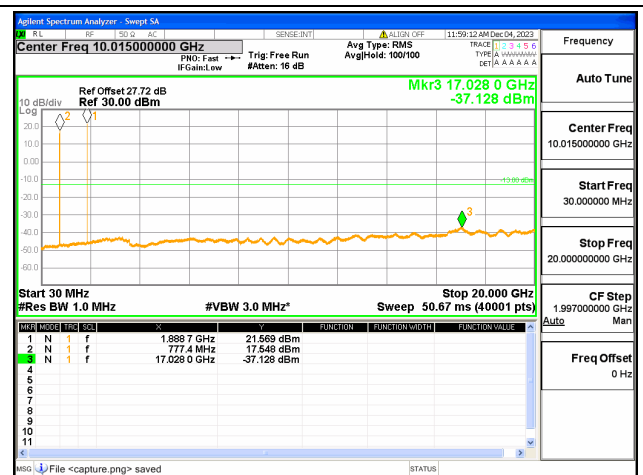
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1@99-1@0



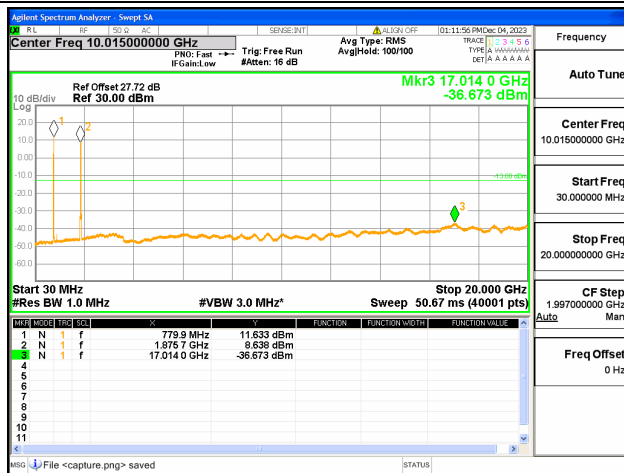
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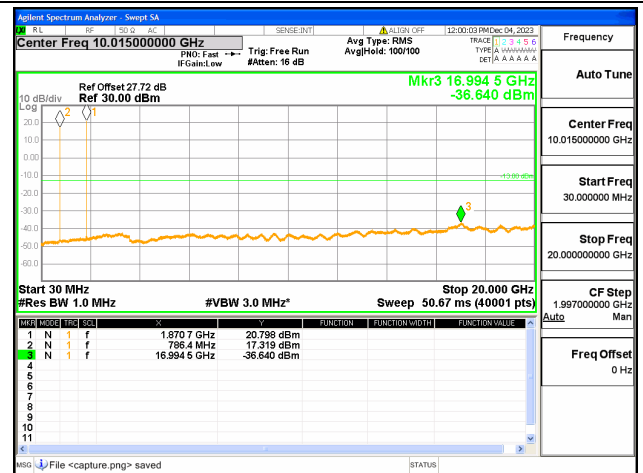
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1@0-1@49



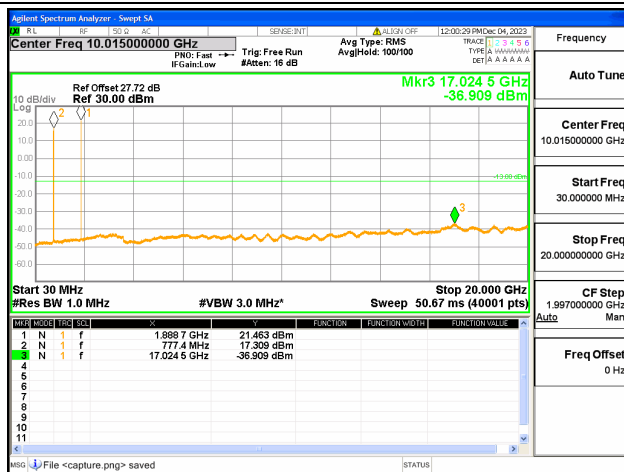
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1@99-1@0



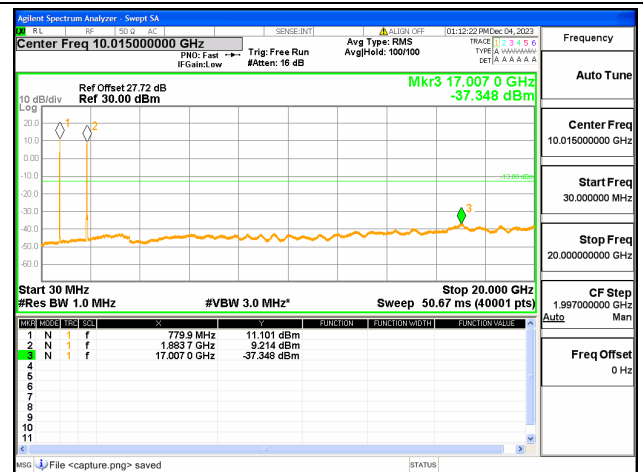
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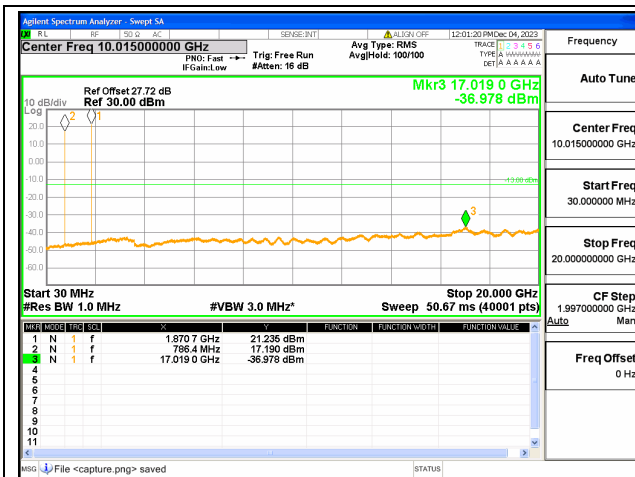
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1@0-1@49



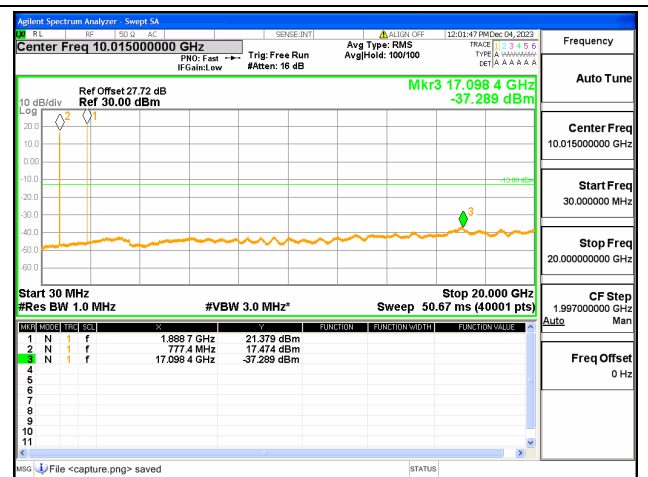
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1@99-1@0



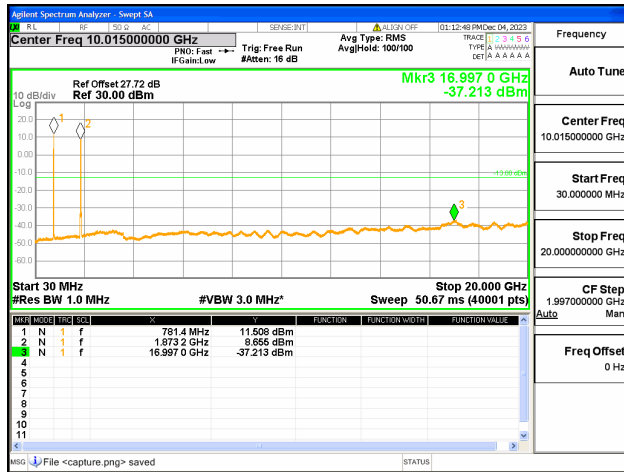
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100@0-50@0



2A-13A / 20+10MHz / QPSK / Mid+Mid CH /
1@0-1@49

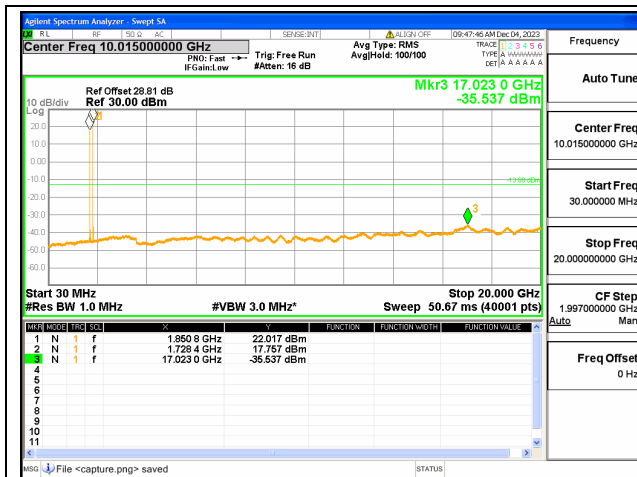
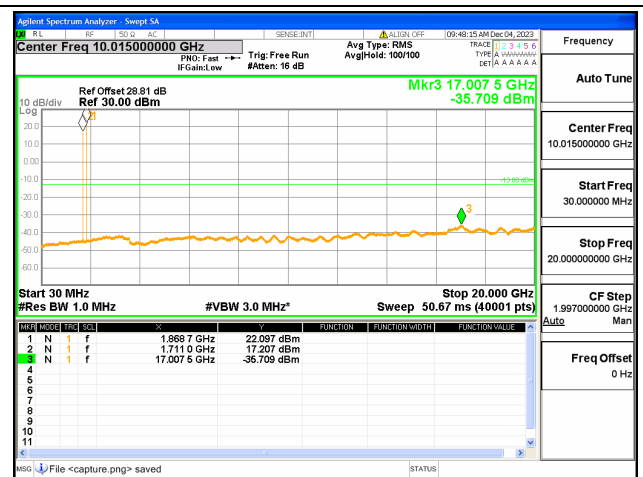
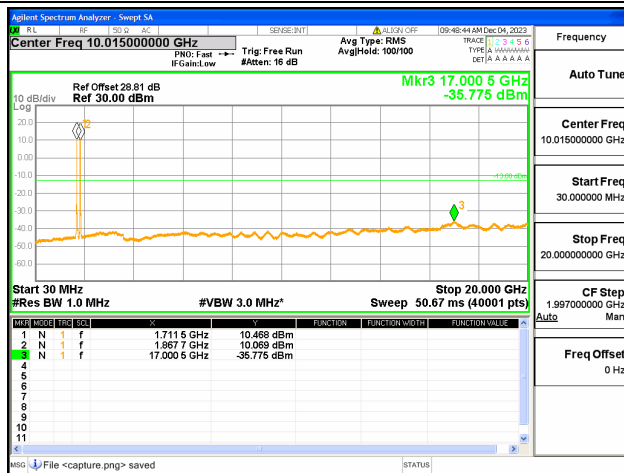
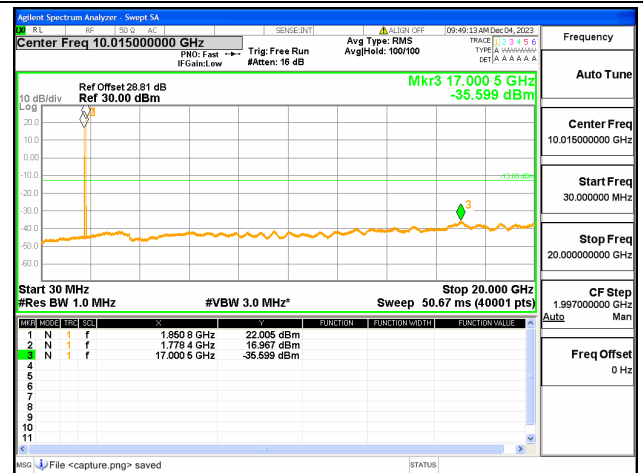
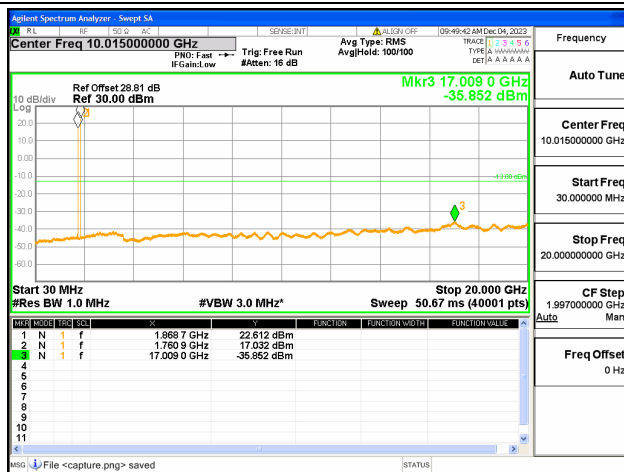
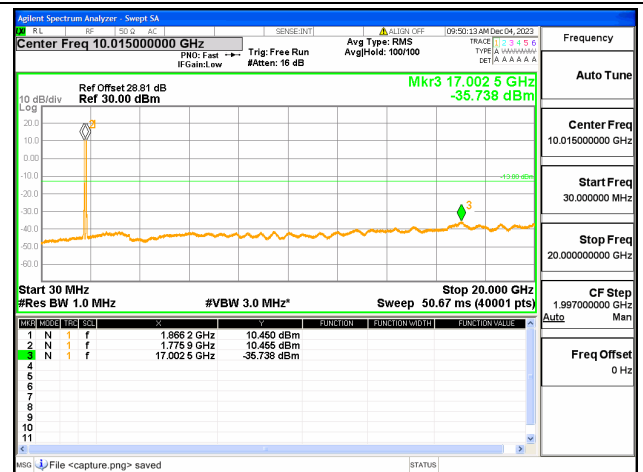


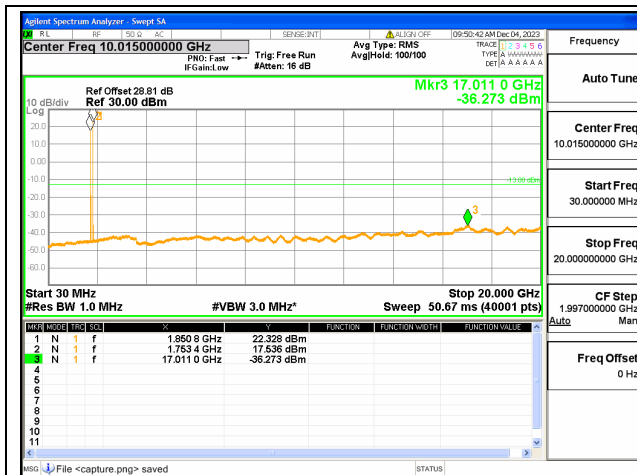
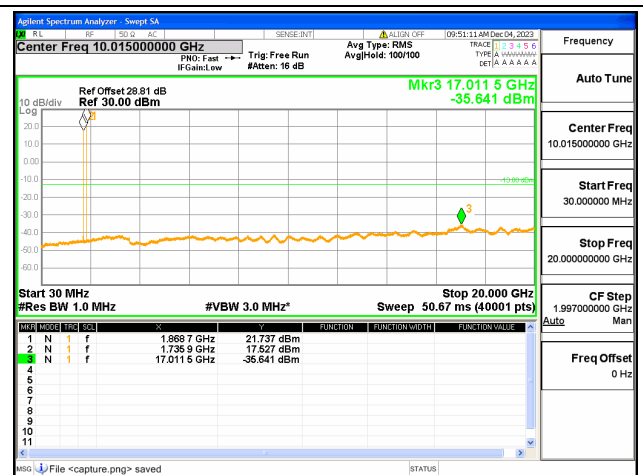
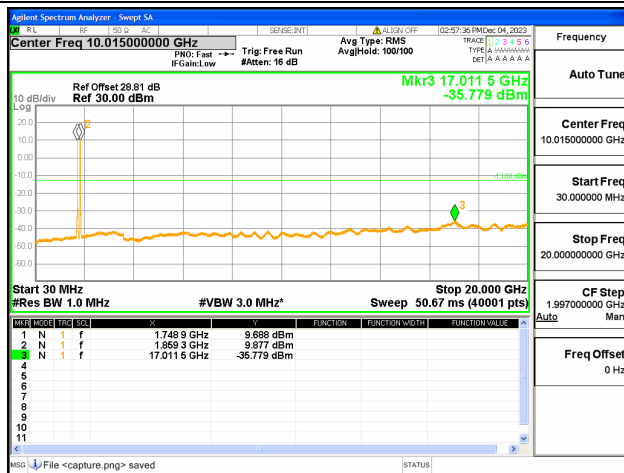
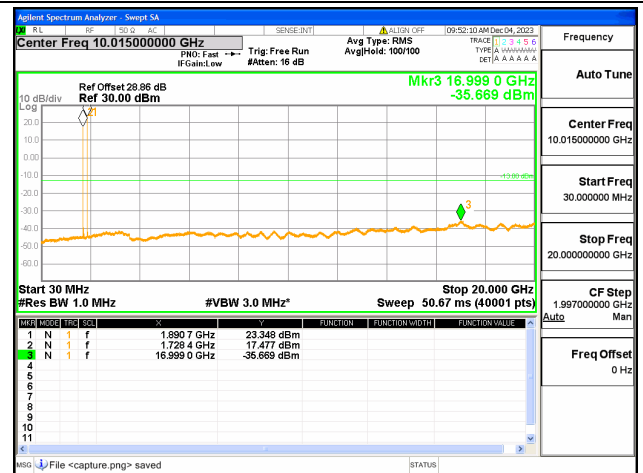
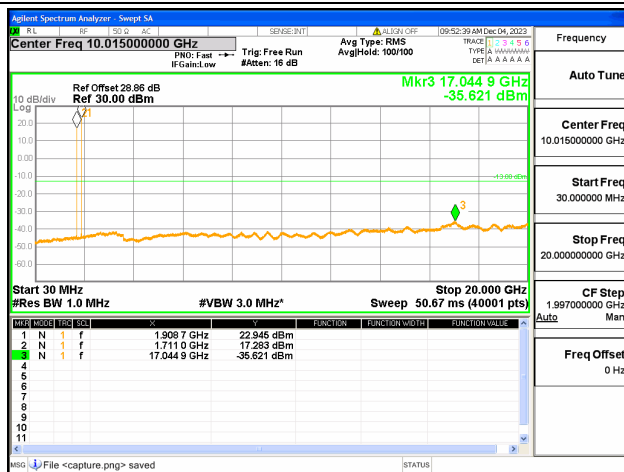
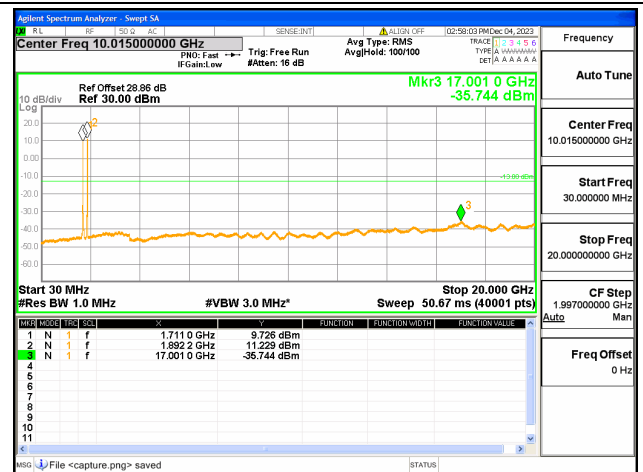
2A-13A / 20+10MHz / QPSK / Mid+Mid CH /
1@99-1@0

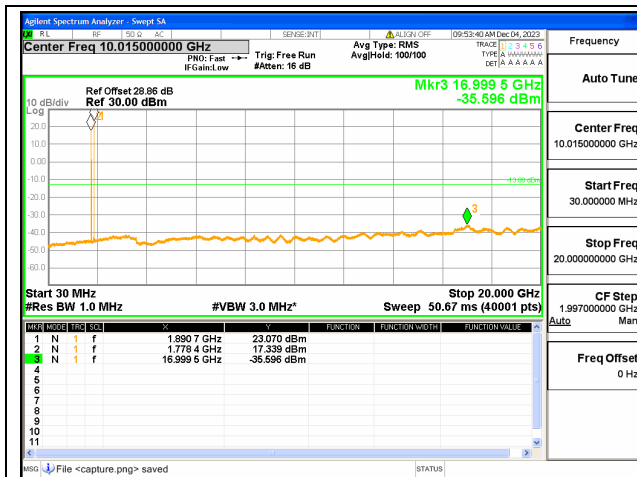


2A-13A / 20+10MHz / QPSK / Mid+Mid CH /
100@0-50@0

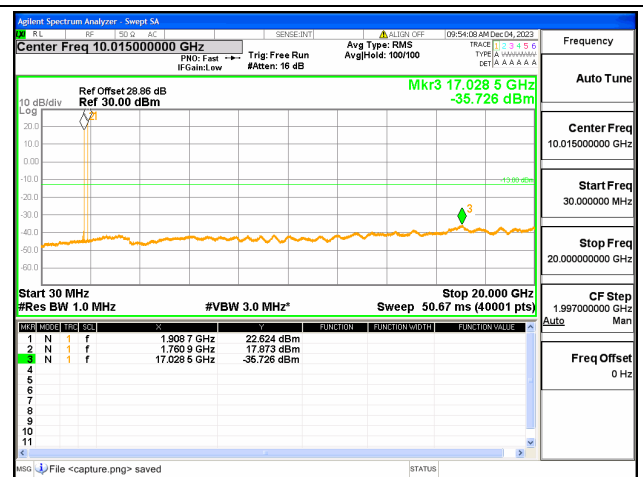
N/A

2A-66A / 20+20MHz / QPSK / Low+Low CH /
1@0-1@992A-66A / 20+20MHz / QPSK / Low+Low CH /
1@99-1@02A-66A / 20+20MHz / QPSK / Low+Low CH /
100@0-100@02A-66A / 20+20MHz / QPSK / Low+High CH /
1@0-1@992A-66A / 20+20MHz / QPSK / Low+High CH /
1@99-1@02A-66A / 20+20MHz / QPSK / Low+High CH /
100@0-100@0

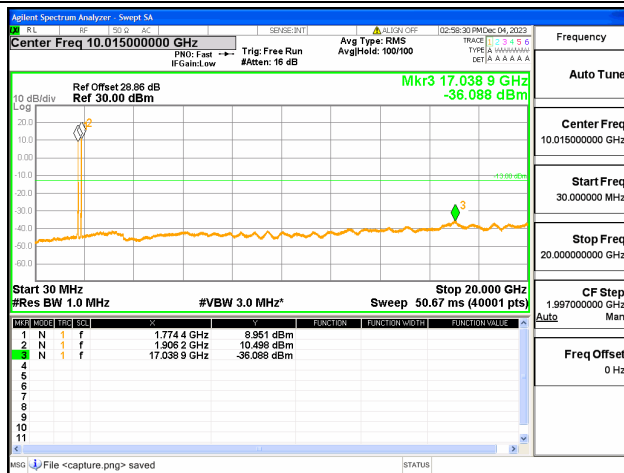
2A-66A / 20+20MHz / QPSK / Low+Mid CH /
1@0-1@992A-66A / 20+20MHz / QPSK / Low+Mid CH /
1@99-1@02A-66A / 20+20MHz / QPSK / Low+Mid CH /
100@0-100@02A-66A / 20+20MHz / QPSK / High+Low CH /
1@0-1@992A-66A / 20+20MHz / QPSK / High+Low CH /
1@99-1@02A-66A / 20+20MHz / QPSK / High+Low CH /
100@0-100@0



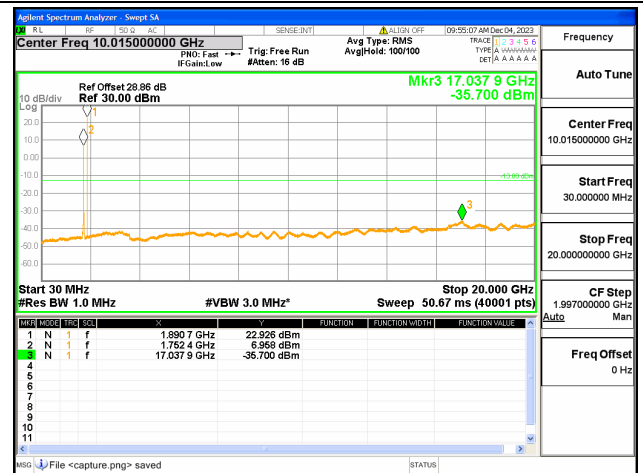
2A-66A / 20+20MHz / QPSK / High+High CH /
1@0-1@99



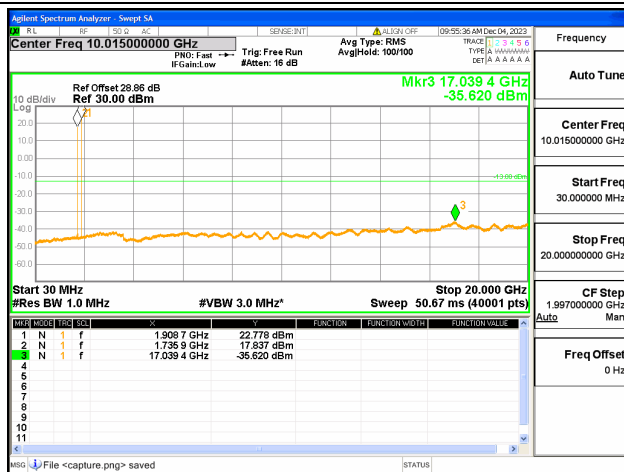
2A-66A / 20+20MHz / QPSK / High+High CH /
1@99-1@0



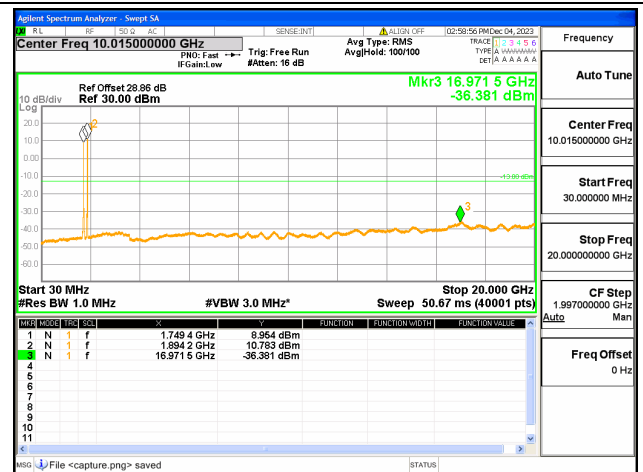
2A-66A / 20+20MHz / QPSK / High+High CH /
100@0-100@0



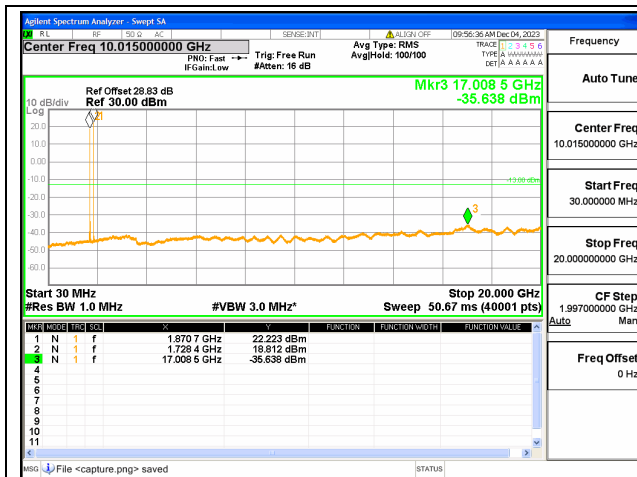
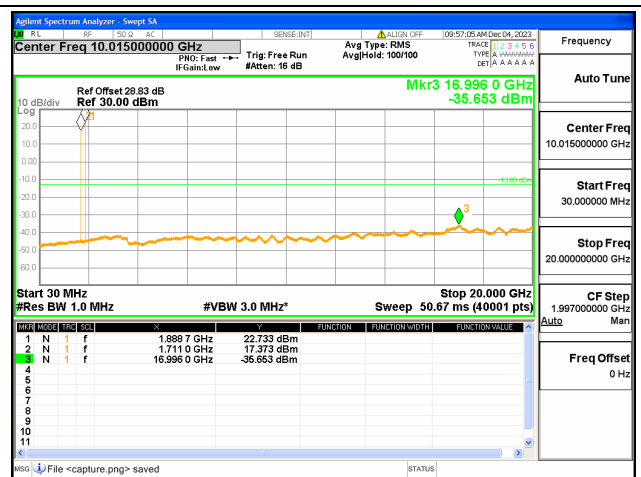
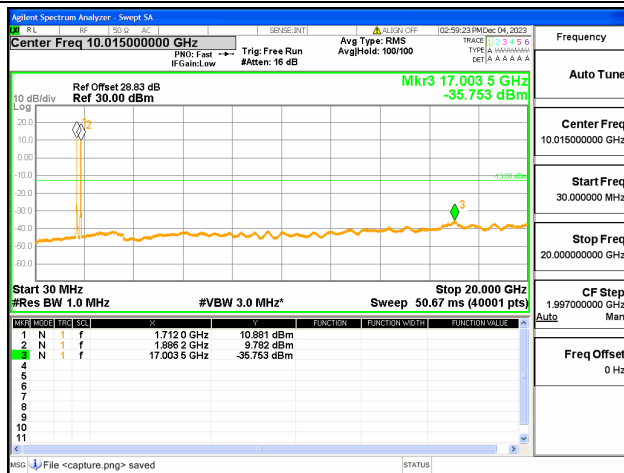
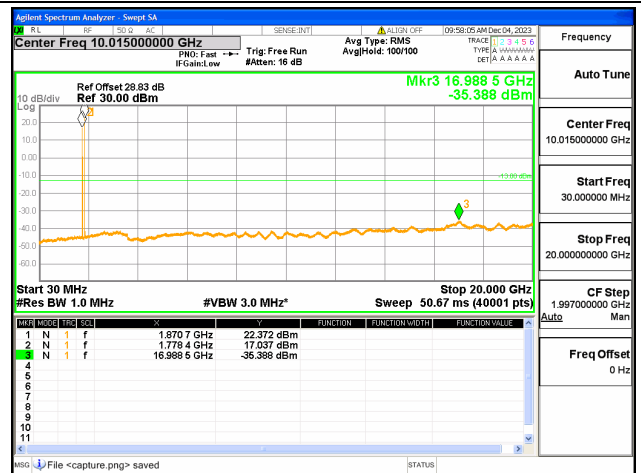
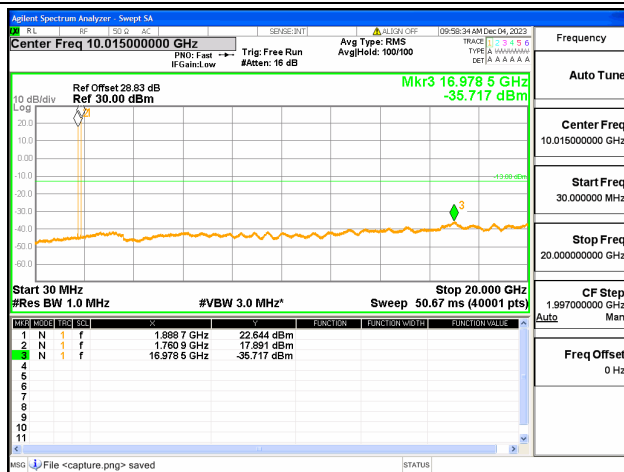
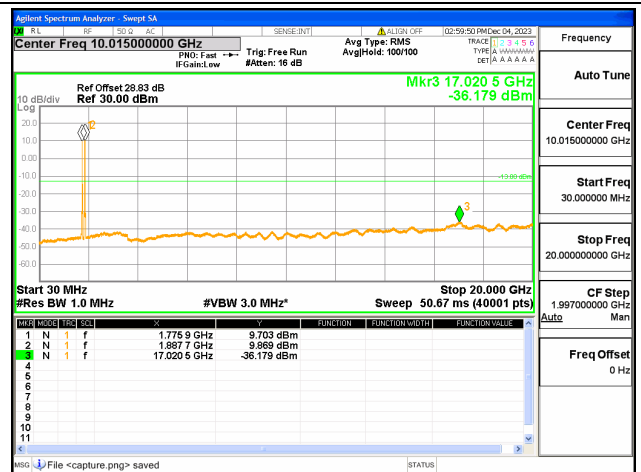
2A-66A / 20+20MHz / QPSK / High+Mid CH /
1@0-1@99

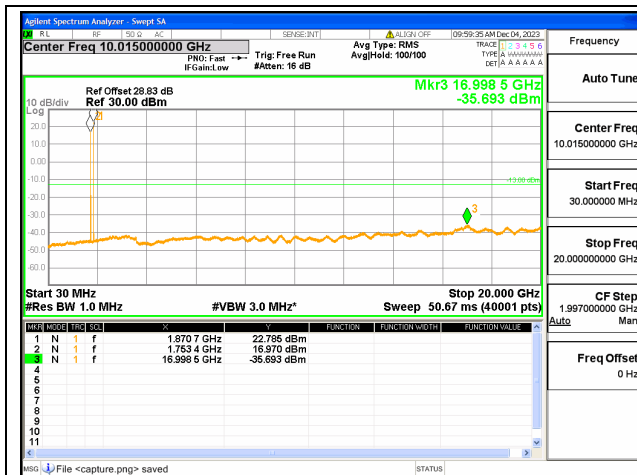


2A-66A / 20+20MHz / QPSK / High+Mid CH /
1@99-1@0

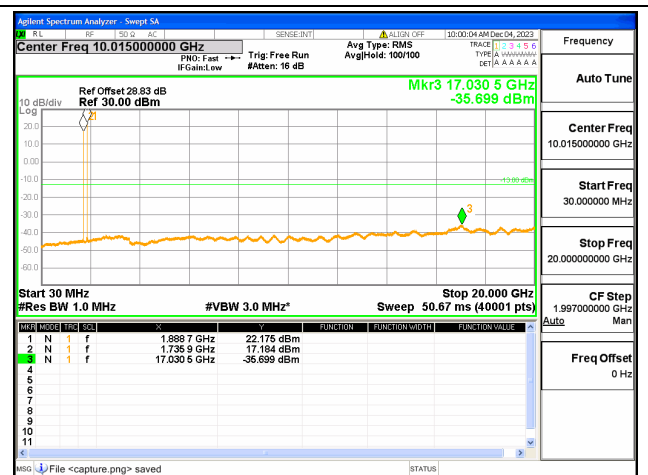


2A-66A / 20+20MHz / QPSK / High+Mid CH /
100@0-100@0

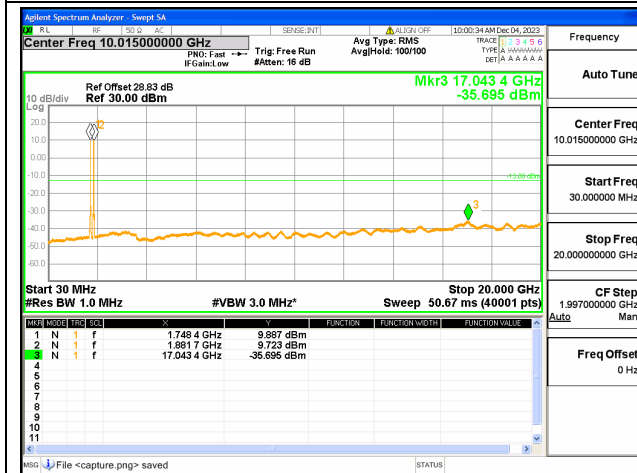
2A-66A / 20+20MHz / QPSK / Mid+Low CH /
1@0-1@992A-66A / 20+20MHz / QPSK / Mid+Low CH /
1@99-1@02A-66A / 20+20MHz / QPSK / Mid+Low CH /
100@0-100@02A-66A / 20+20MHz / QPSK / Mid+High CH /
1@0-1@992A-66A / 20+20MHz / QPSK / Mid+High CH /
1@99-1@02A-66A / 20+20MHz / QPSK / Mid+High CH /
100@0-100@0



2A-66A / 20+20MHz / QPSK / Mid+Mid CH /
1@0-1@99

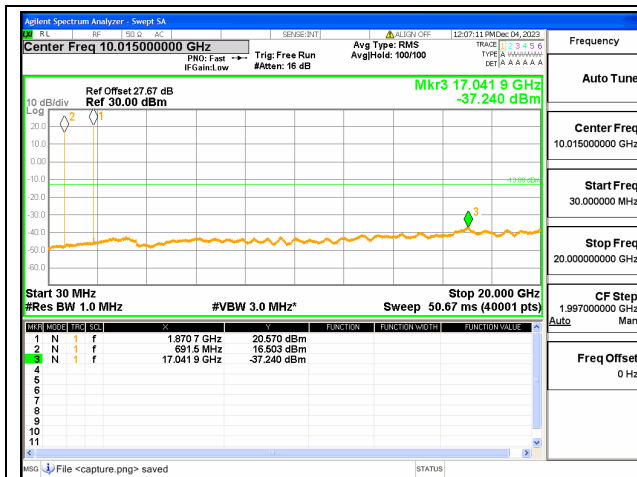


2A-66A / 20+20MHz / QPSK / Mid+Mid CH /
1@99-1@0

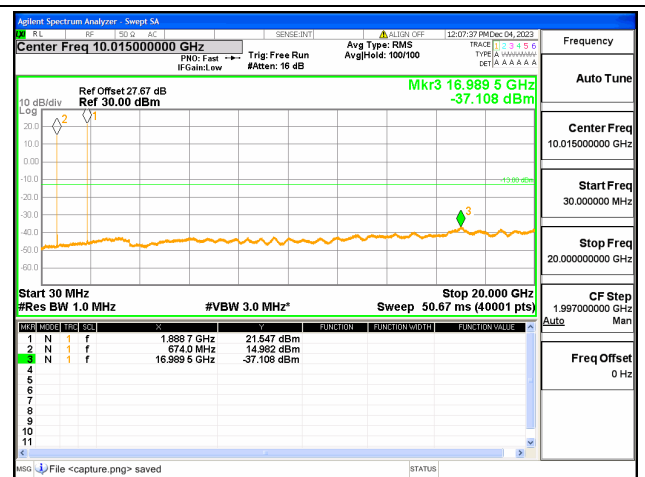


2A-66A / 20+20MHz / QPSK / Mid+Mid CH /
100@0-100@0

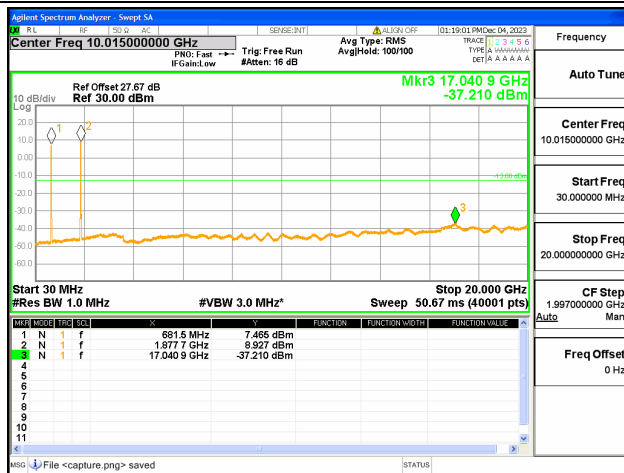
N/A



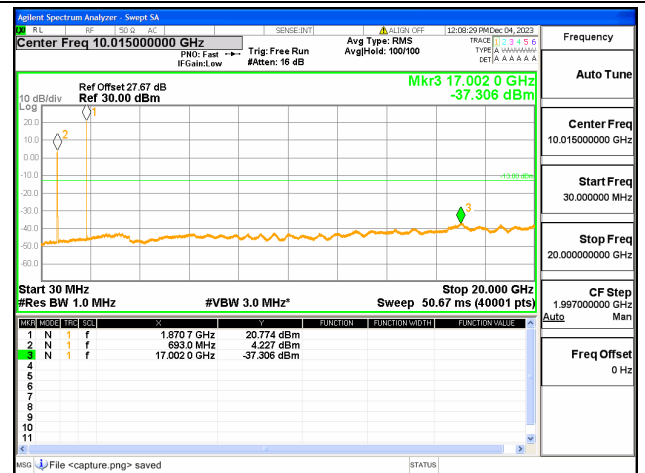
2A-71A / 20+20MHz / QPSK / Mid+Mid CH /
1@0-1@99



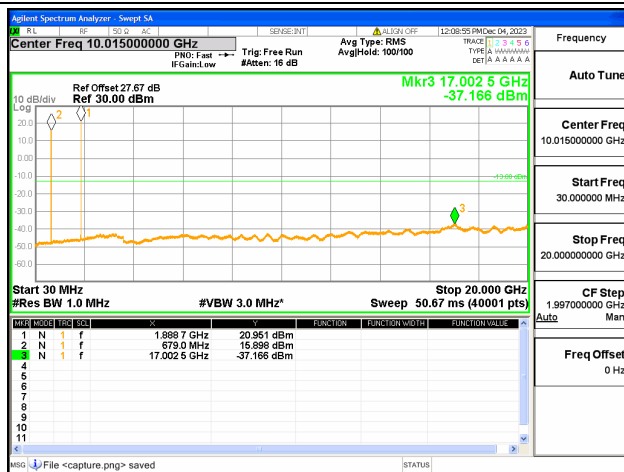
2A-71A / 20+20MHz / QPSK / Mid+Mid CH /
1@99-1@0



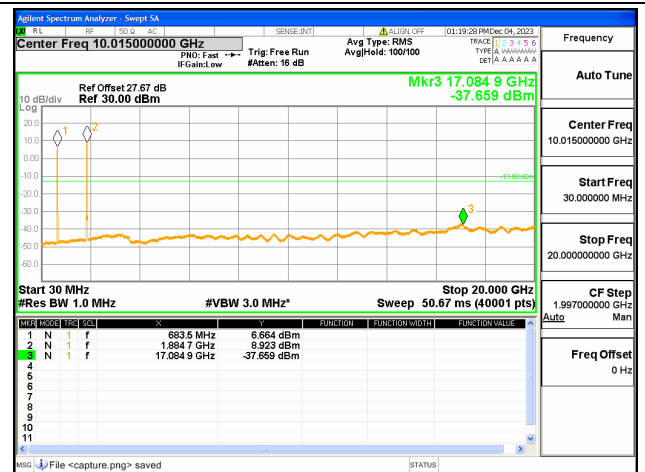
2A-71A / 20+20MHz / QPSK / Mid+Mid CH /
100@0-100@0



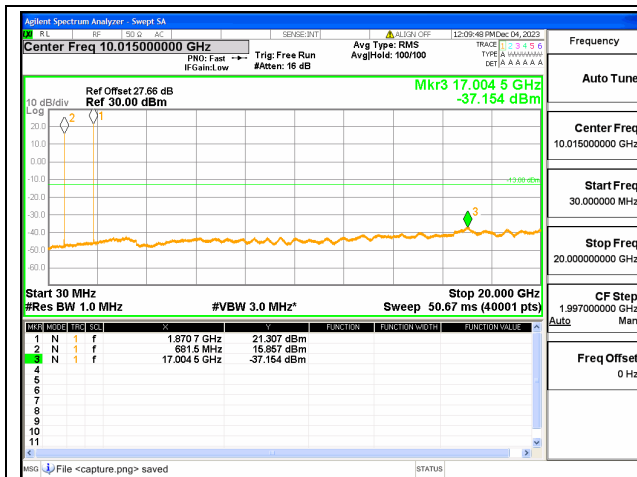
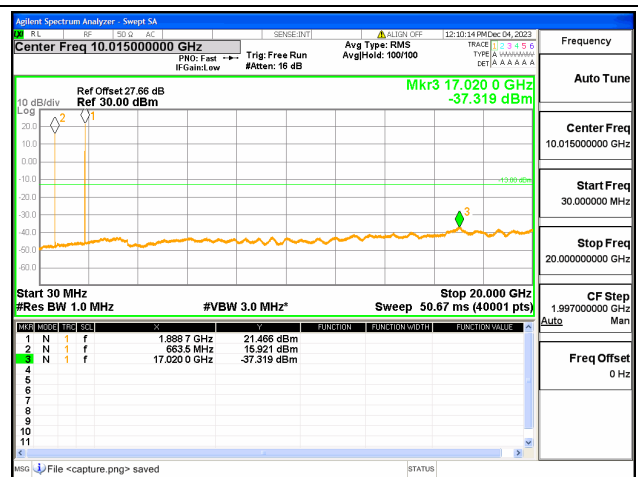
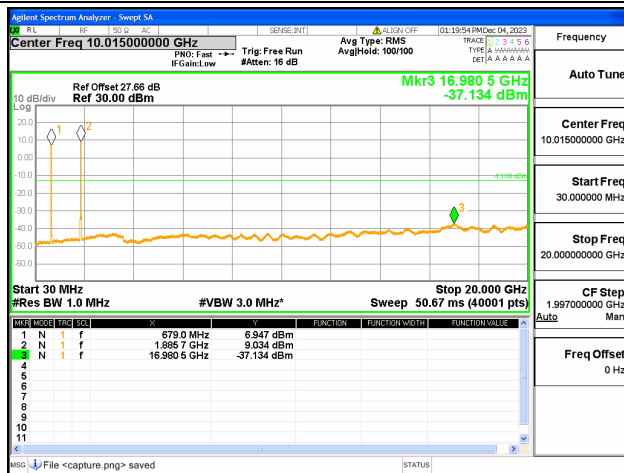
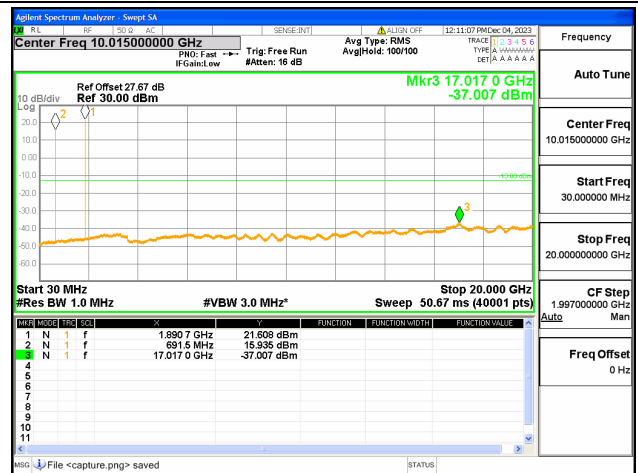
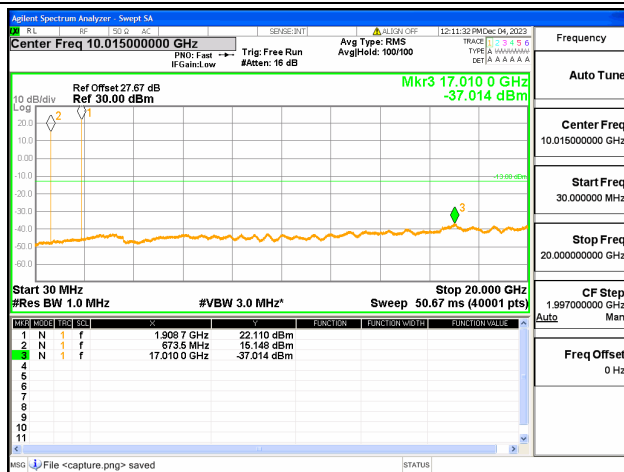
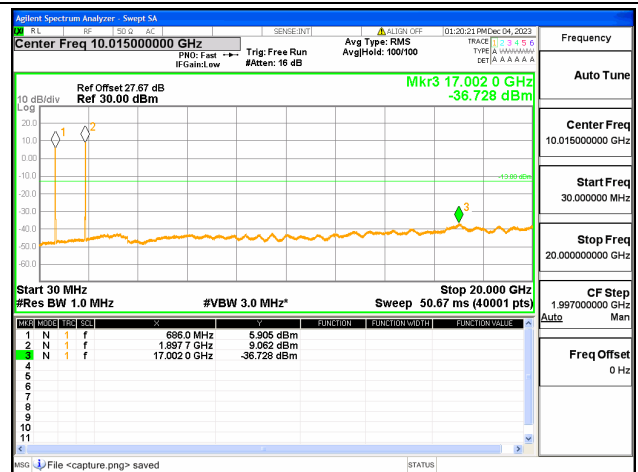
2A-71A / 20+20MHz / QPSK / Mid+High CH /
1@0-1@99

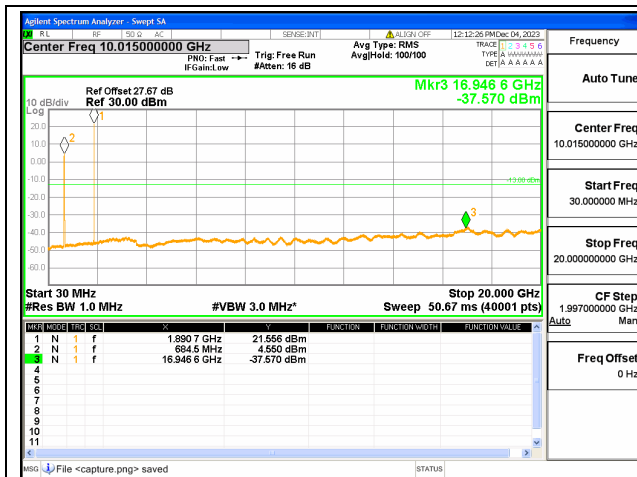


2A-71A / 20+20MHz / QPSK / Mid+High CH /
1@99-1@0

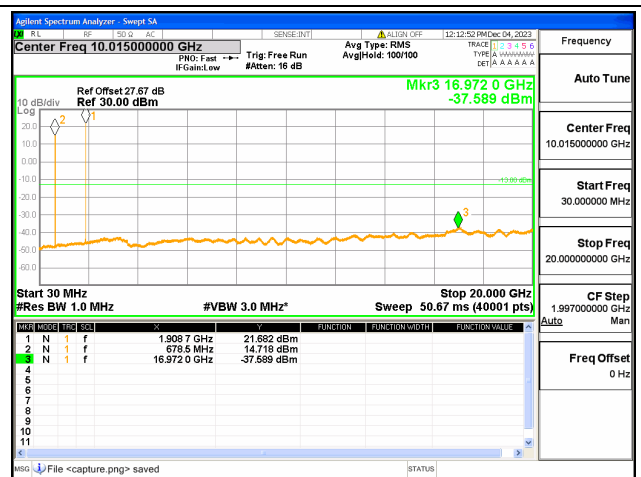


2A-71A / 20+20MHz / QPSK / Mid+High CH /
100@0-100@0

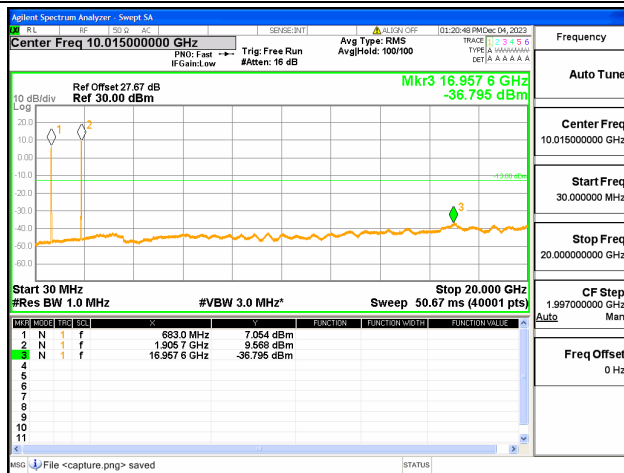
2A-71A / 20+20MHz / QPSK / Mid+Low CH /
1@0-1@992A-71A / 20+20MHz / QPSK / Mid+Low CH /
1@99-1@02A-71A / 20+20MHz / QPSK / Mid+Low CH /
100@0-100@02A-71A / 20+20MHz / QPSK / High+Mid CH /
1@0-1@992A-71A / 20+20MHz / QPSK / High+Mid CH /
1@99-1@02A-71A / 20+20MHz / QPSK / High+Mid CH /
100@0-100@0



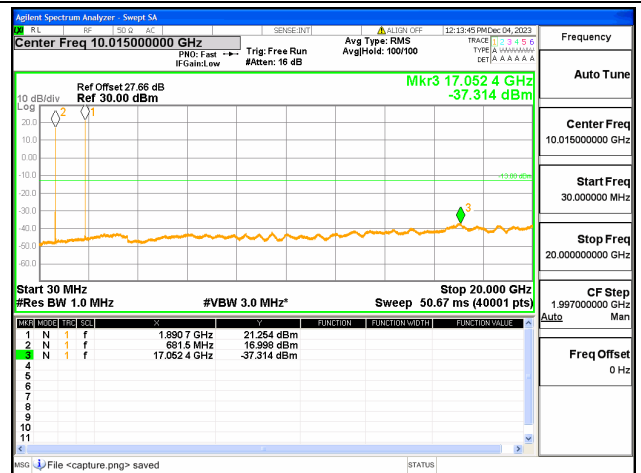
2A-71A / 20+20MHz / QPSK / High+High CH /
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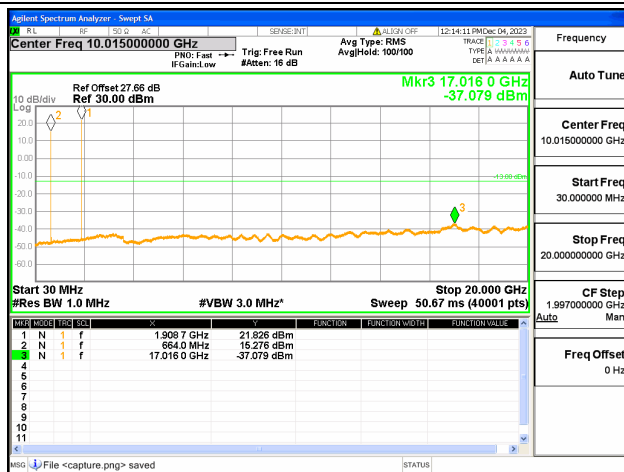
2A-71A / 20+20MHz / QPSK / High+High CH /
1@99-1@0



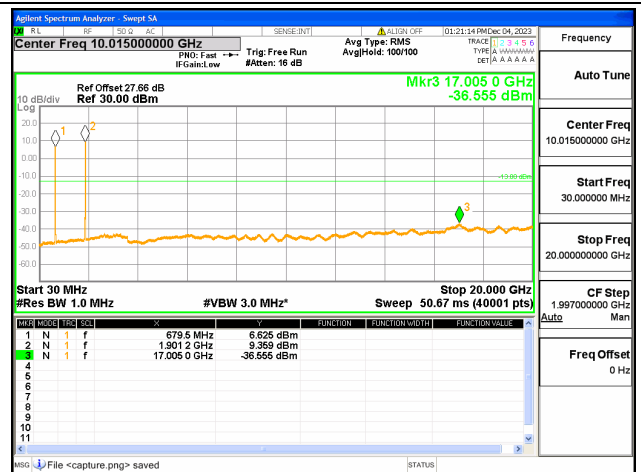
2A-71A / 20+20MHz / QPSK / High+High CH /
100@0-100@0



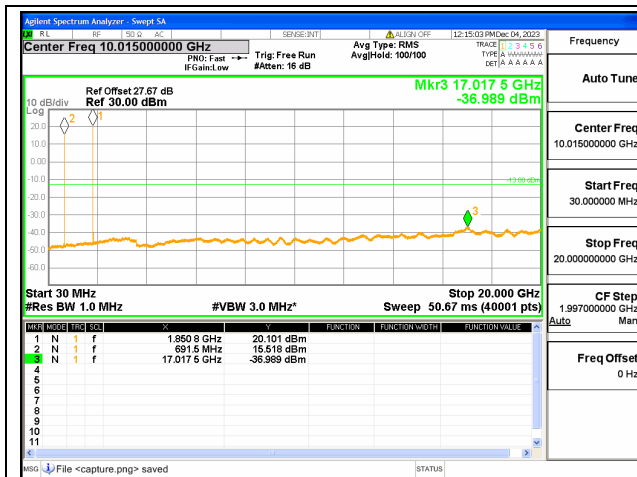
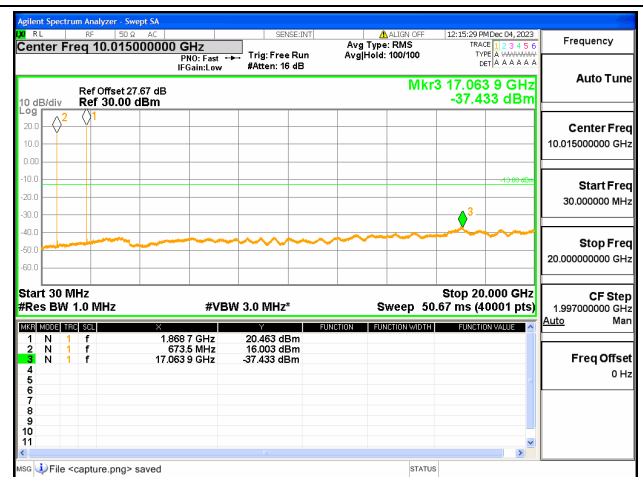
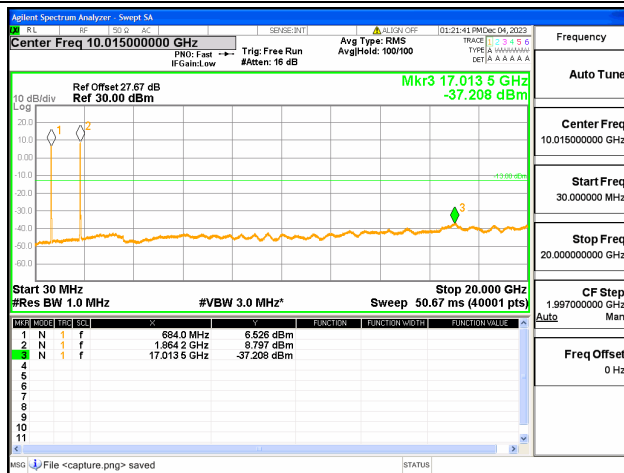
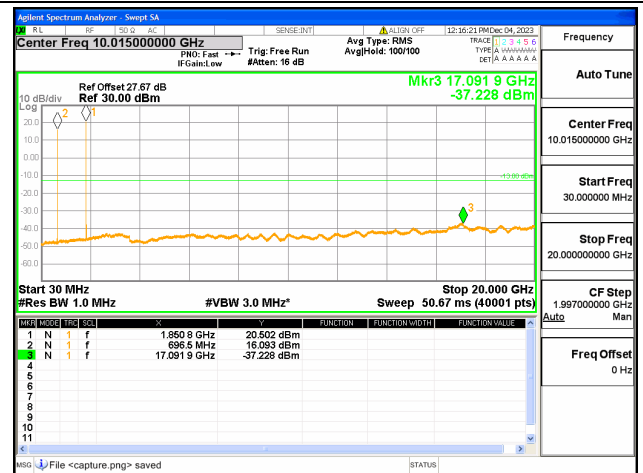
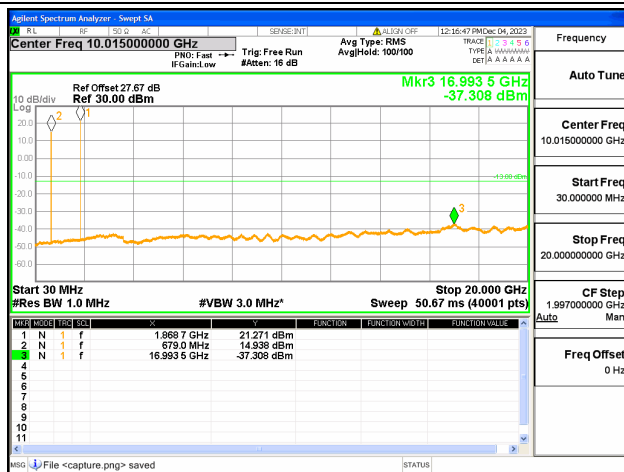
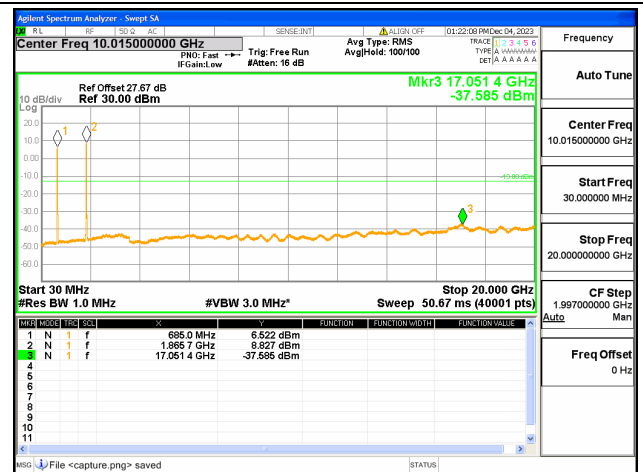
2A-71A / 20+20MHz / QPSK / High+Low CH /
1@0-1@99

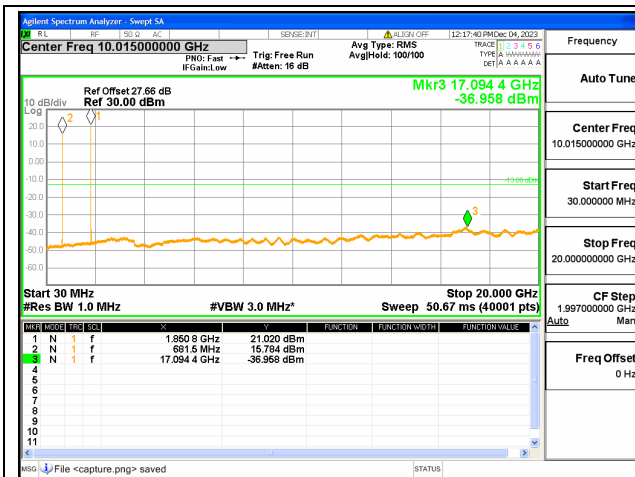


2A-71A / 20+20MHz / QPSK / High+Low CH /
1@99-1@0

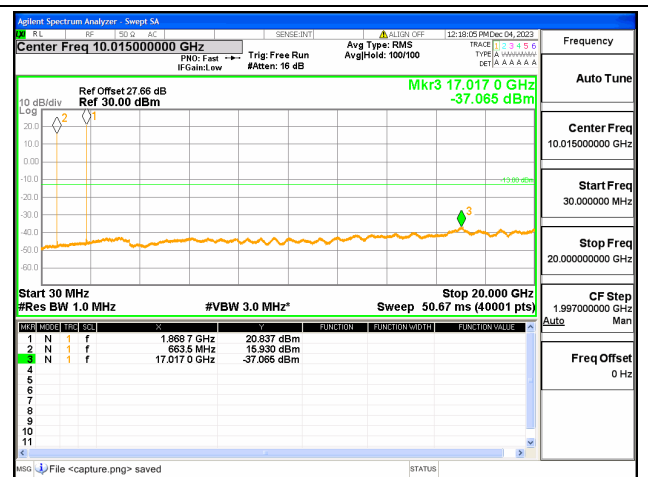


2A-71A / 20+20MHz / QPSK / High+Low CH /
100@0-100@0

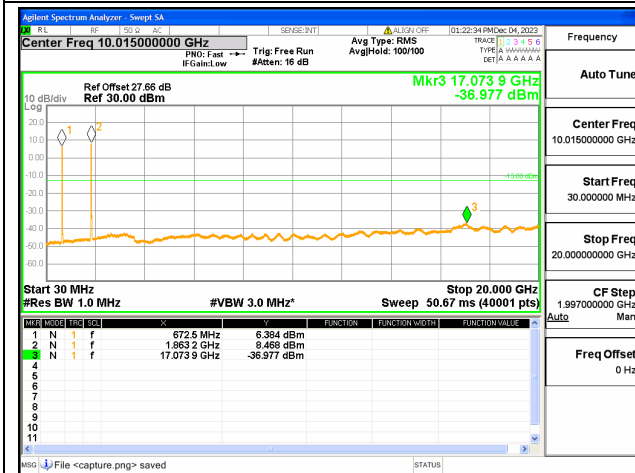
2A-71A / 20+20MHz / QPSK / Low+Mid CH /
1@0-1@992A-71A / 20+20MHz / QPSK / Low+Mid CH /
1@99-1@02A-71A / 20+20MHz / QPSK / Low+Mid CH /
100@0-100@02A-71A / 20+20MHz / QPSK / Low+High CH /
1@0-1@992A-71A / 20+20MHz / QPSK / Low+High CH /
1@99-1@02A-71A / 20+20MHz / QPSK / Low+High CH /
100@0-100@0



2A-71A / 20+20MHz / QPSK / Low+Low CH /
1@0-1@99

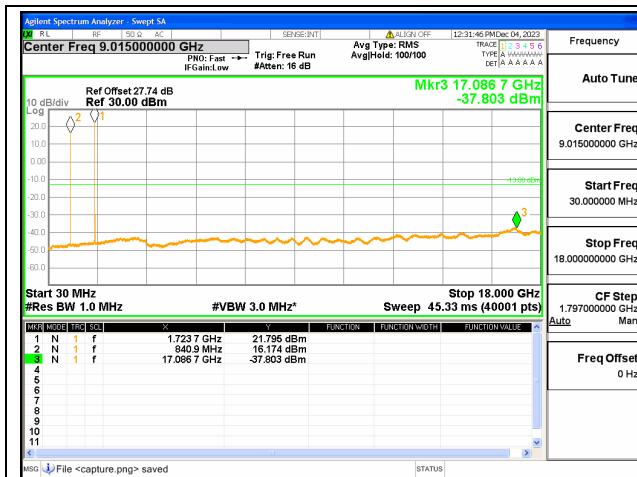
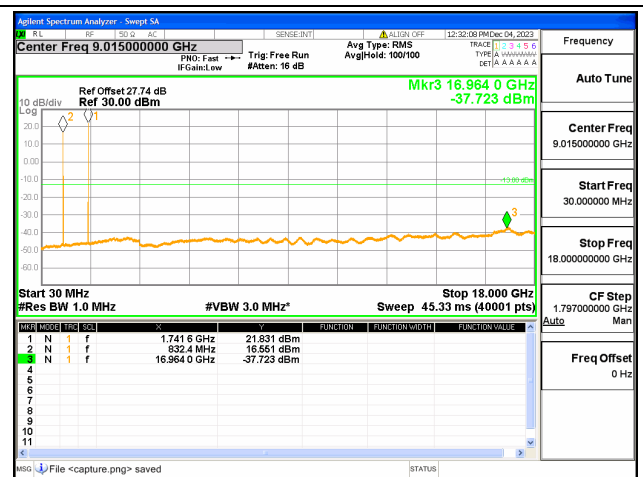
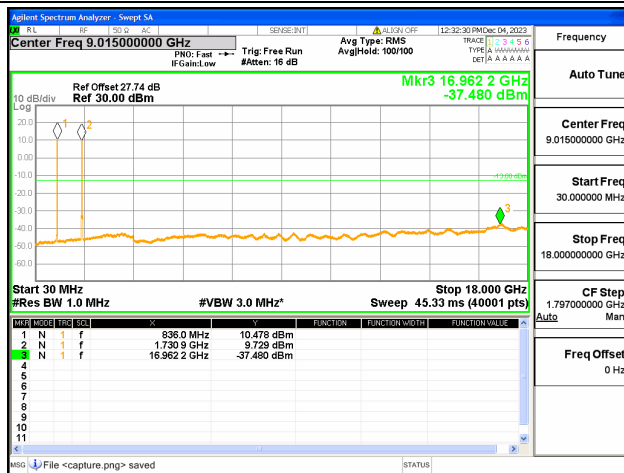
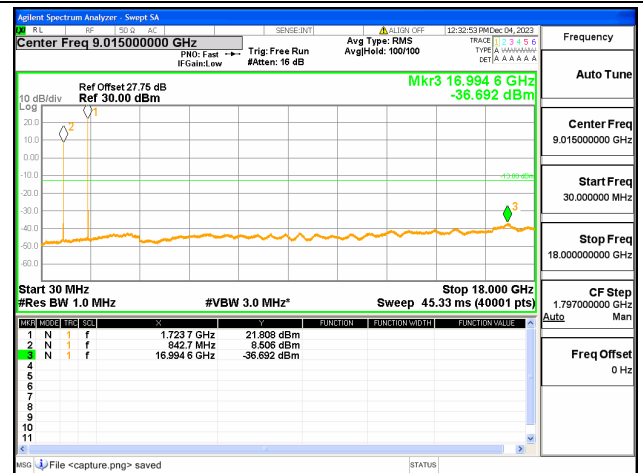
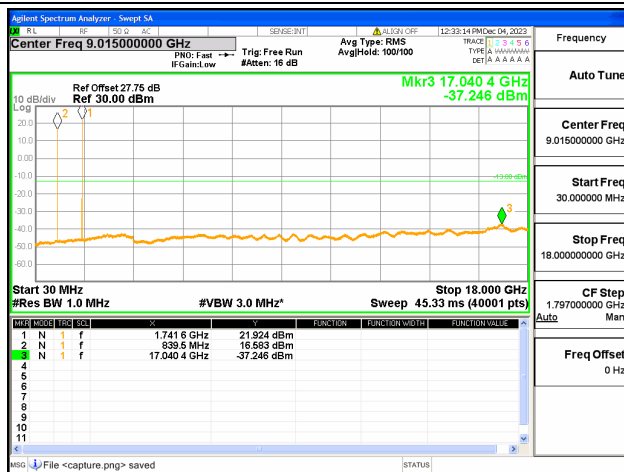
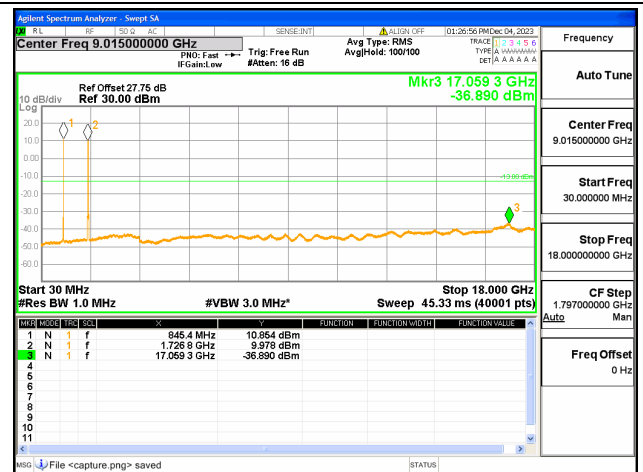


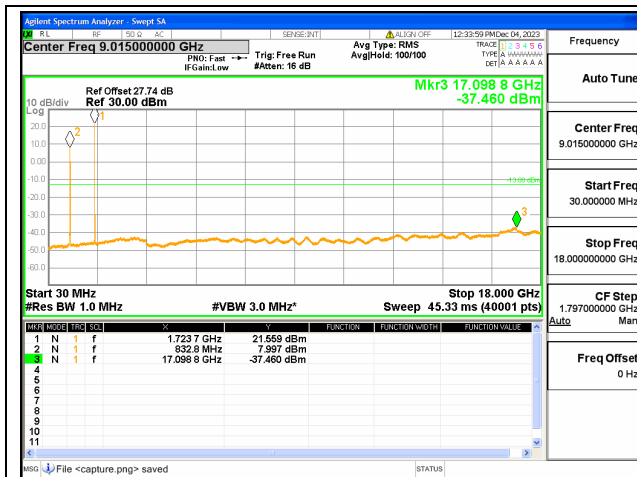
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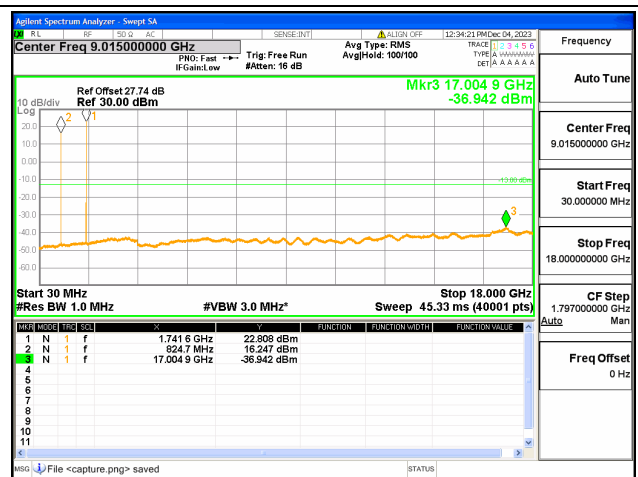
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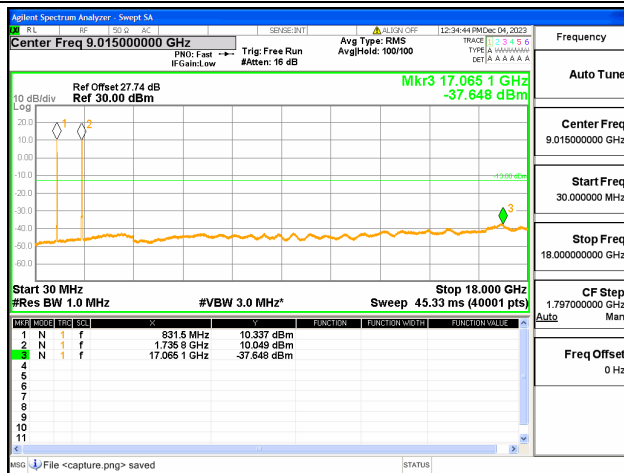
4A-5A / 20+10MHz / QPSK / Mid+Mid CH /
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1@99-1@04A-5A / 20+10MHz / QPSK / Mid+Mid CH /
100@0-50@04A-5A / 20+10MHz / QPSK / Mid+High CH /
1@0-1@494A-5A / 20+10MHz / QPSK / Mid+High CH /
1@99-1@04A-5A / 20+10MHz / QPSK / Mid+High CH /
100@0-50@0



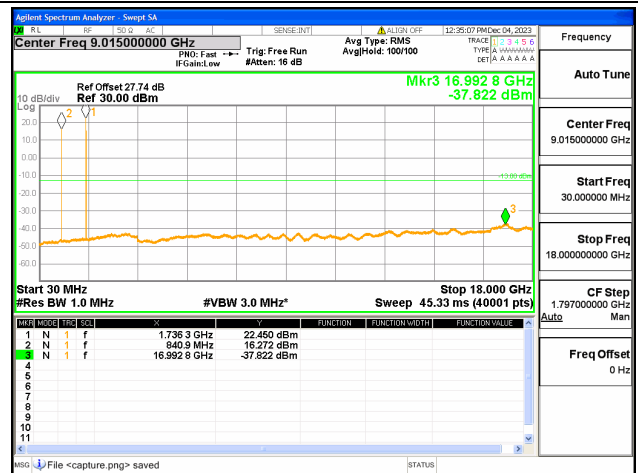
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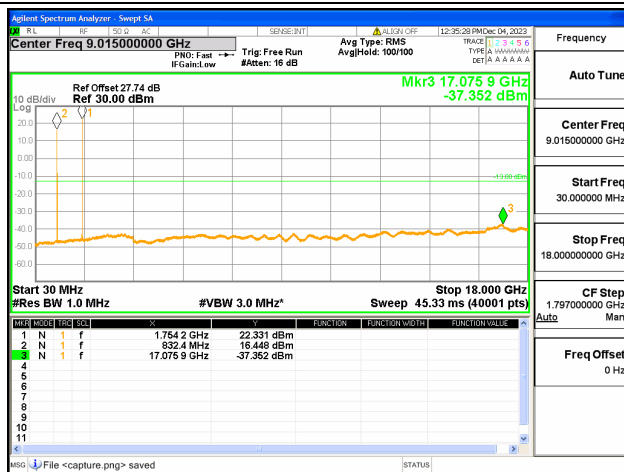
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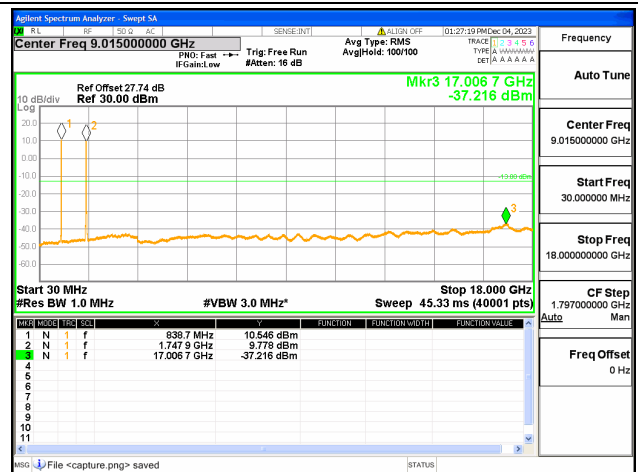
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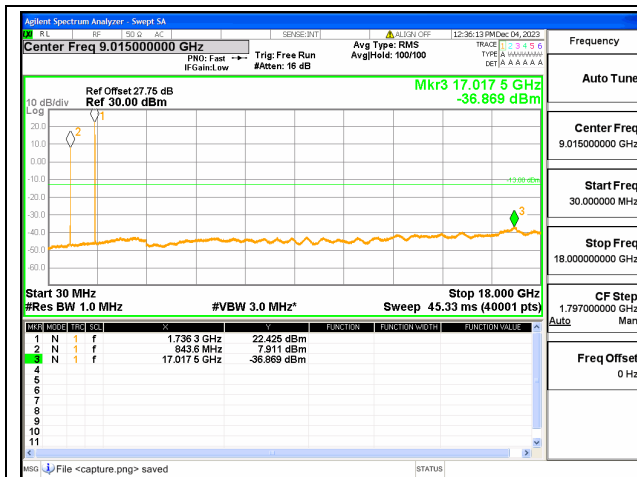
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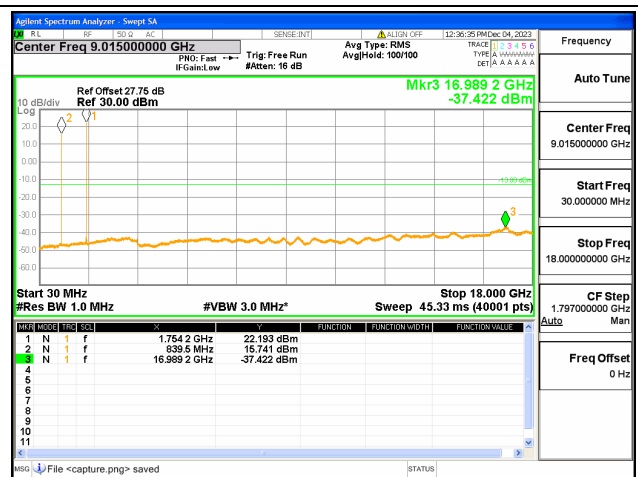
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1@99-1@0



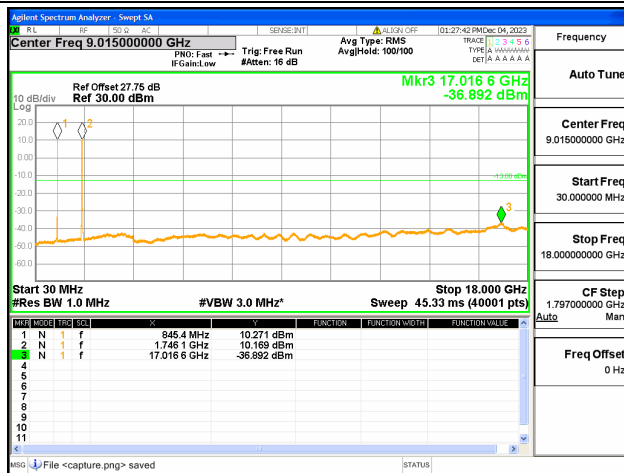
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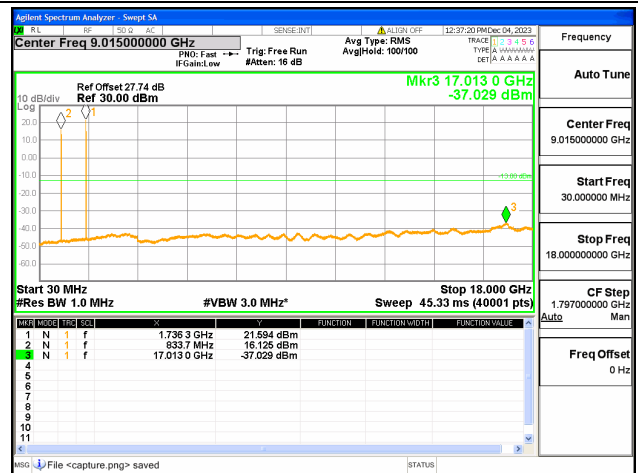
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1@0-1@49



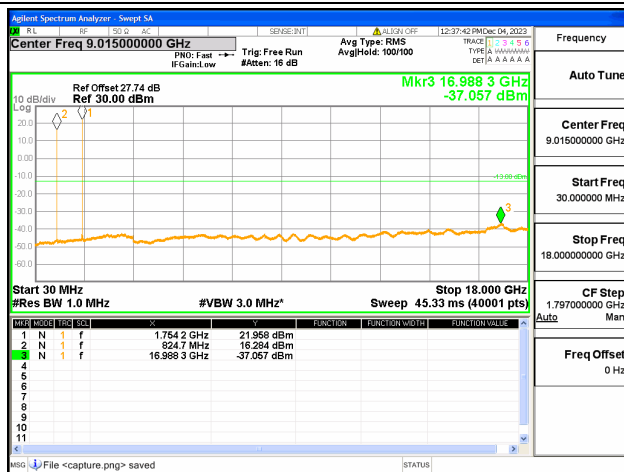
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1@99-1@0



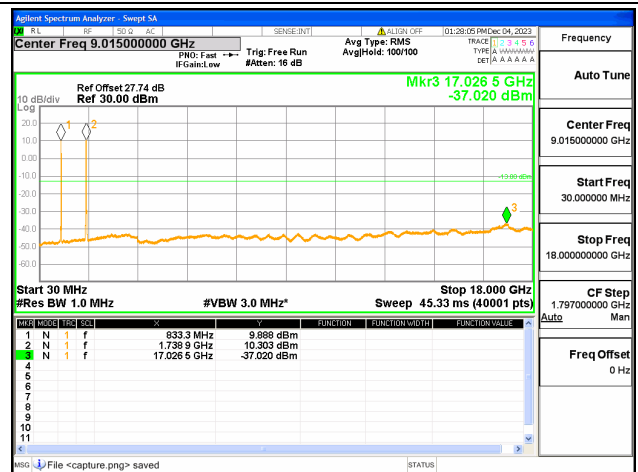
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100@0-50@0



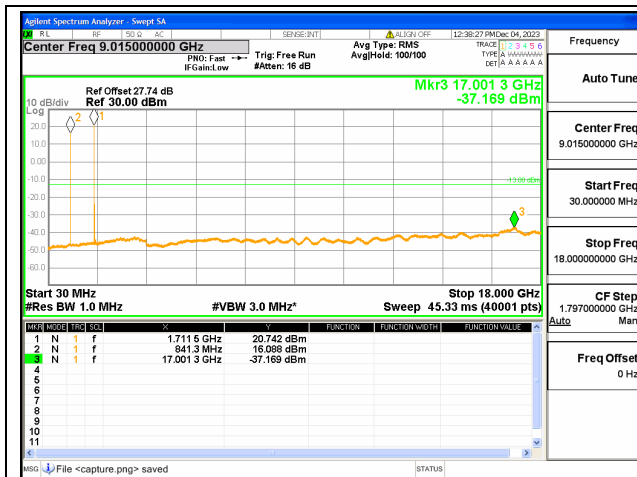
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1@0-1@49



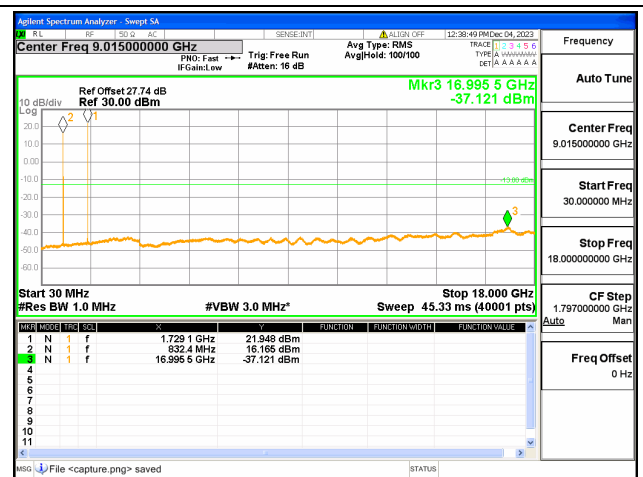
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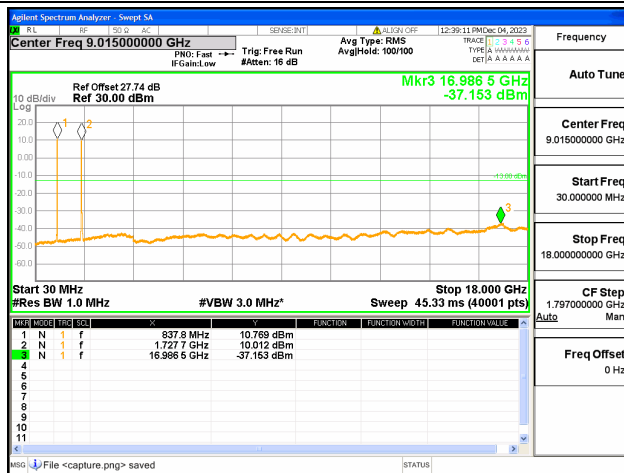
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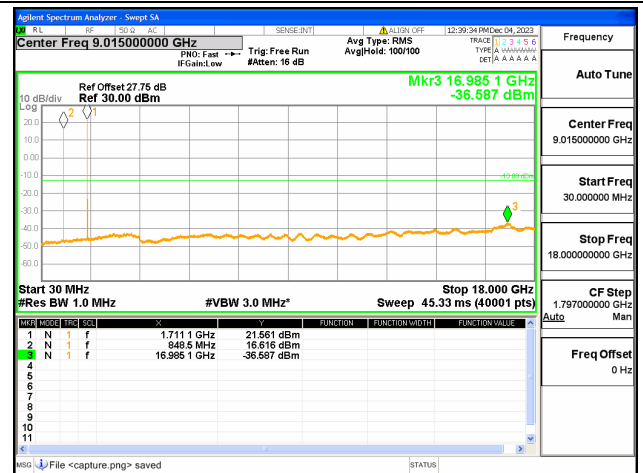
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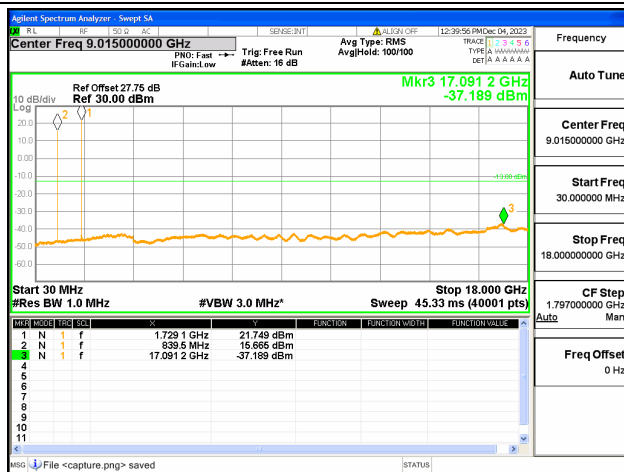
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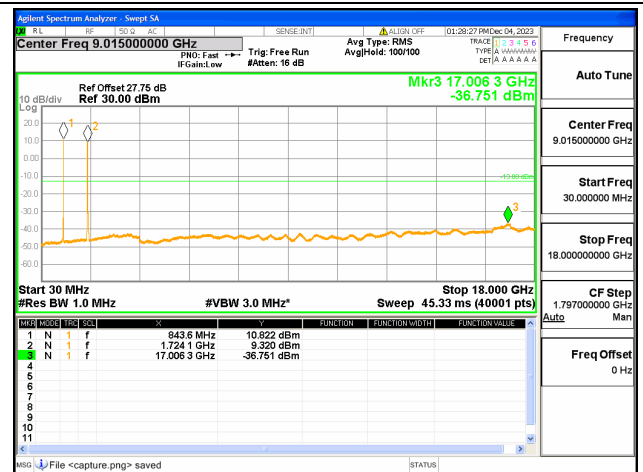
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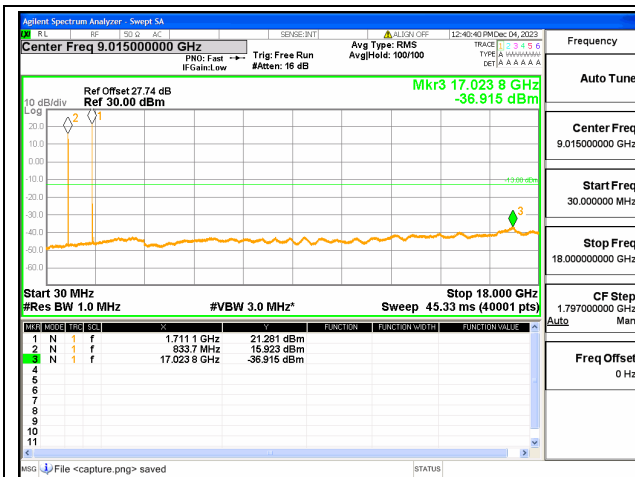
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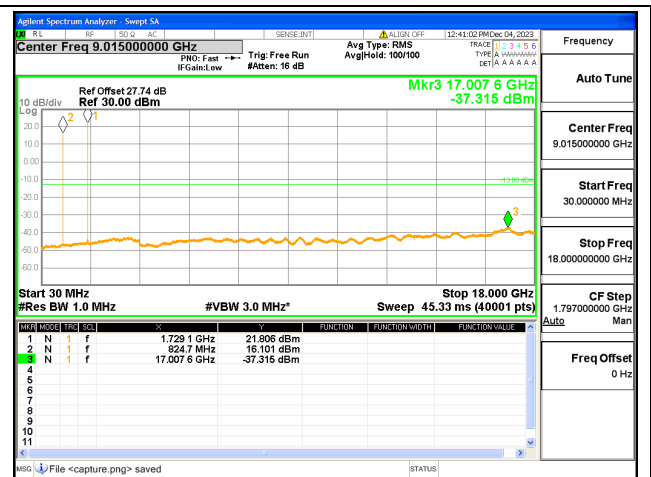
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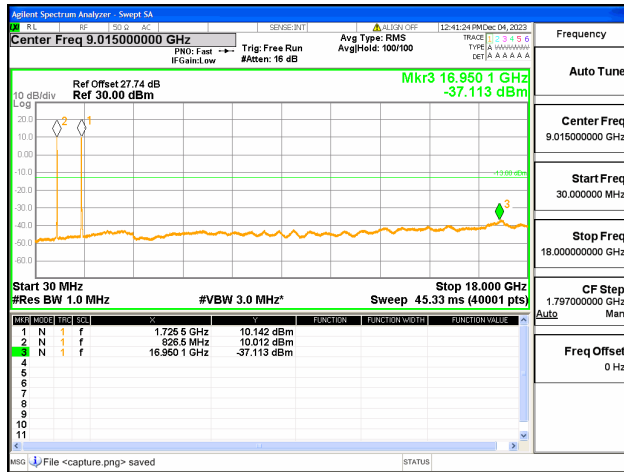
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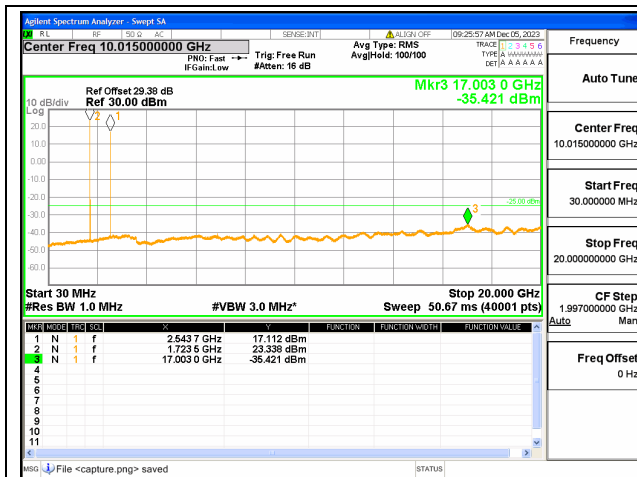
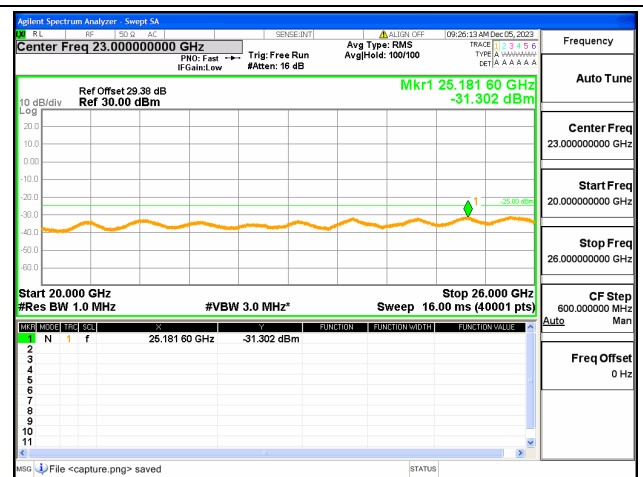
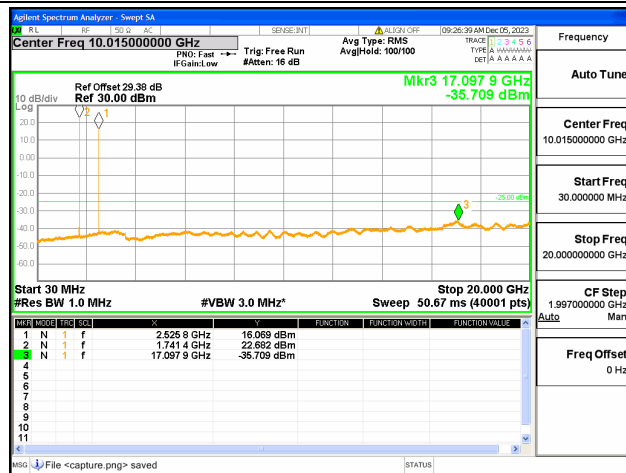
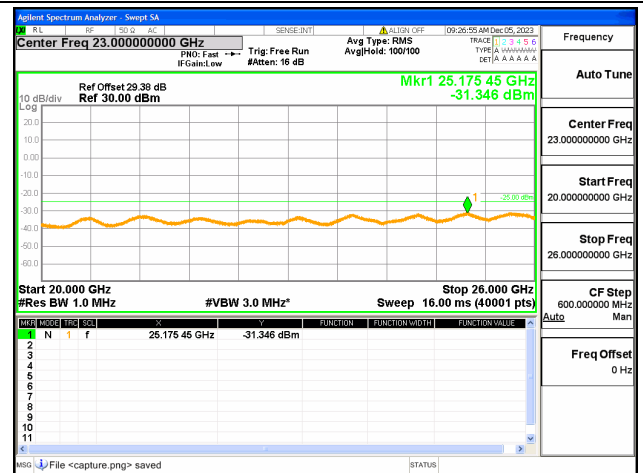
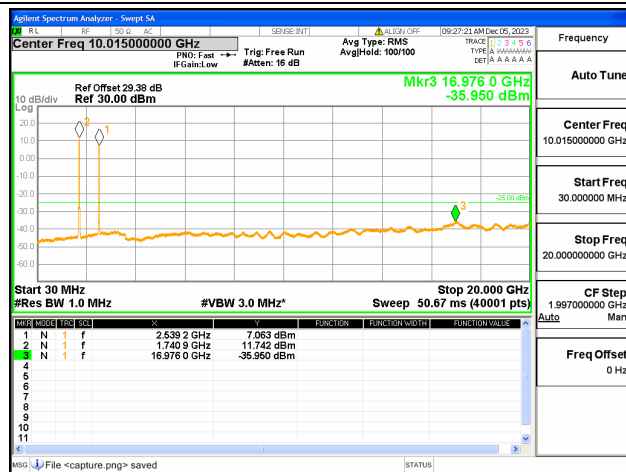
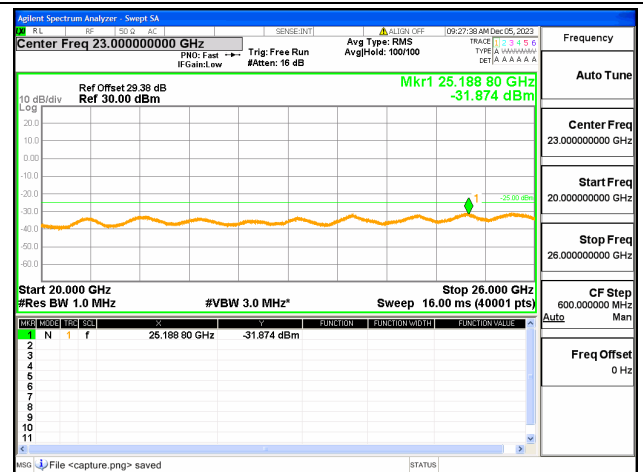


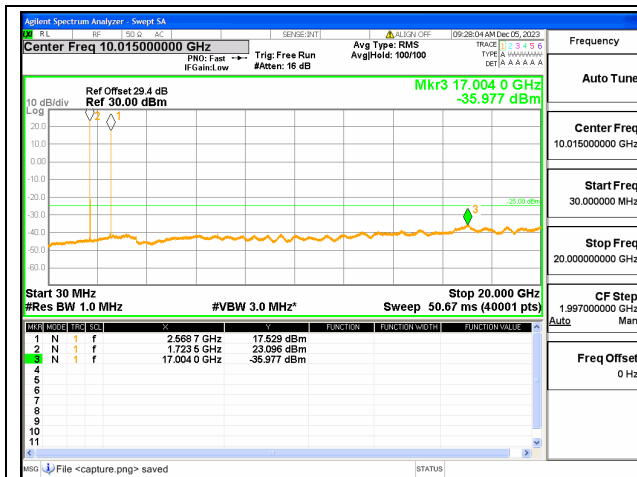
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1@99-1@0



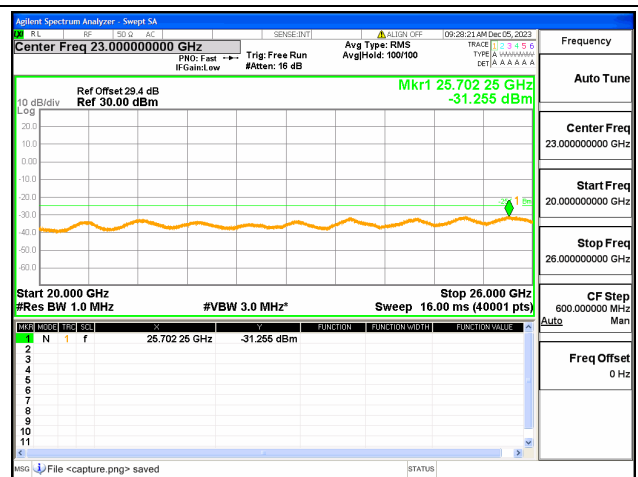
4A-5A / 20+10MHz / QPSK / Low+Low CH /
100@0-50@0

N/A

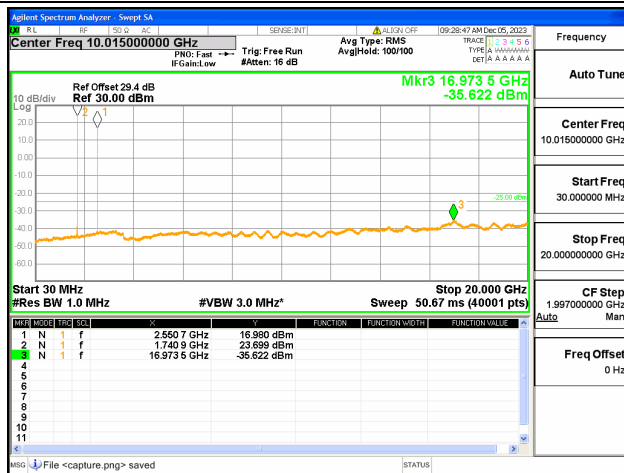
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CH / 1@0-1@994A-7A-20G-26G / 20+20MHz / QPSK / Mid+Mid
CH / 1@0-1@994A-7A-30M-20G / 20+20MHz / QPSK / Mid+Mid
CH / 1@99-1@04A-7A-20G-26G / 20+20MHz / QPSK / Mid+Mid
CH / 1@99-1@04A-7A-30M-20G / 20+20MHz / QPSK / Mid+Mid
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CH / 100@0-100@0



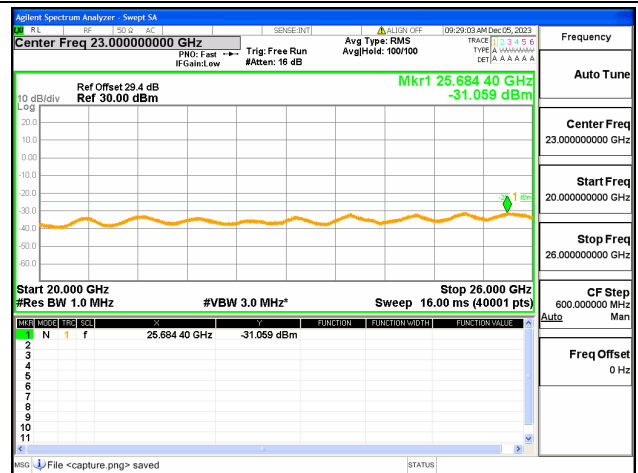
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Mid+High CH / 1@0-1@99



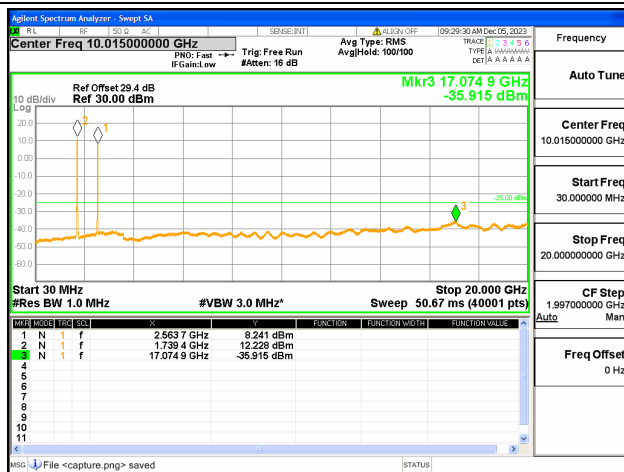
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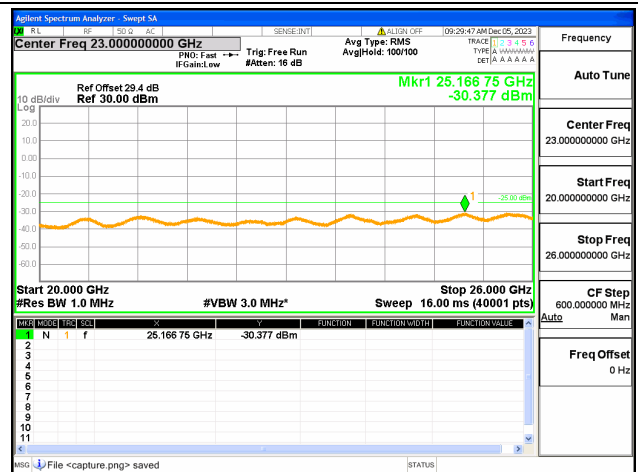
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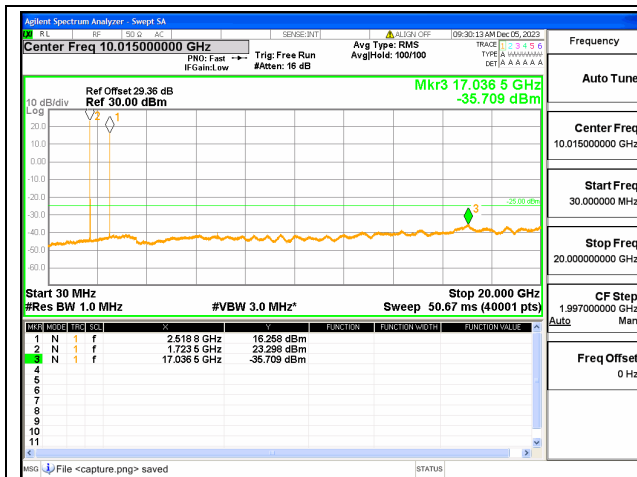
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Mid+High CH / 1@99-1@0



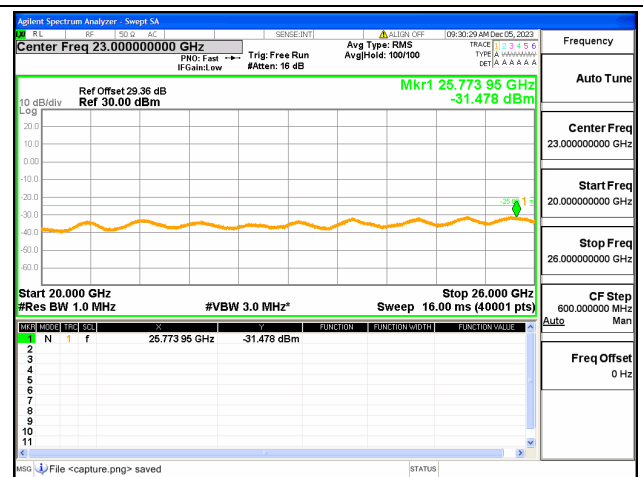
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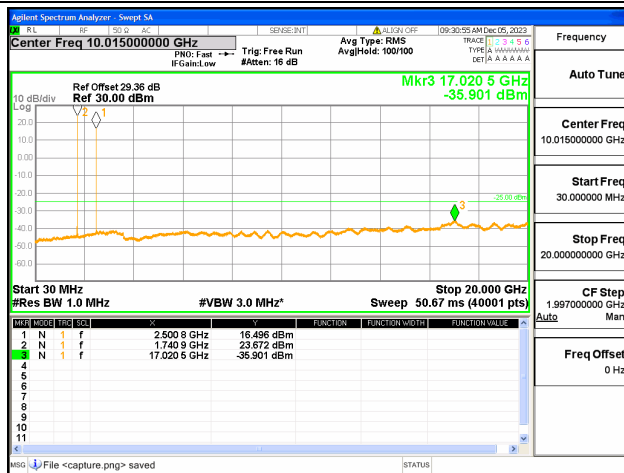
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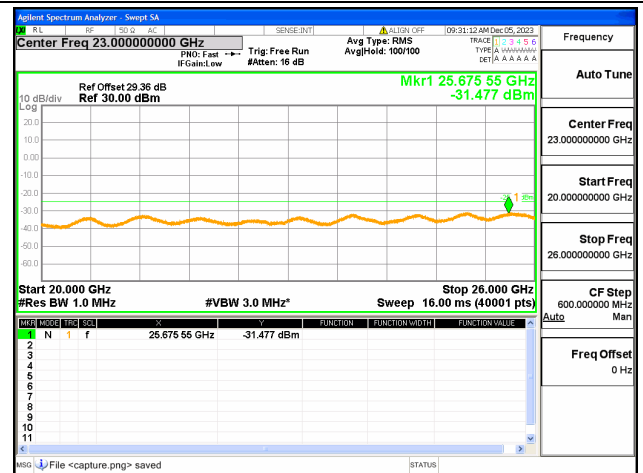
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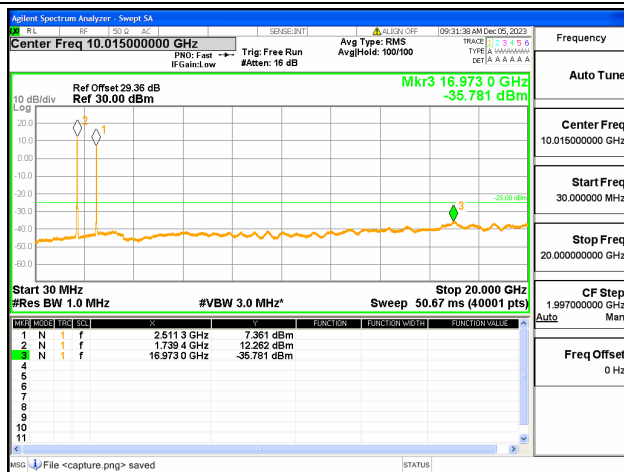
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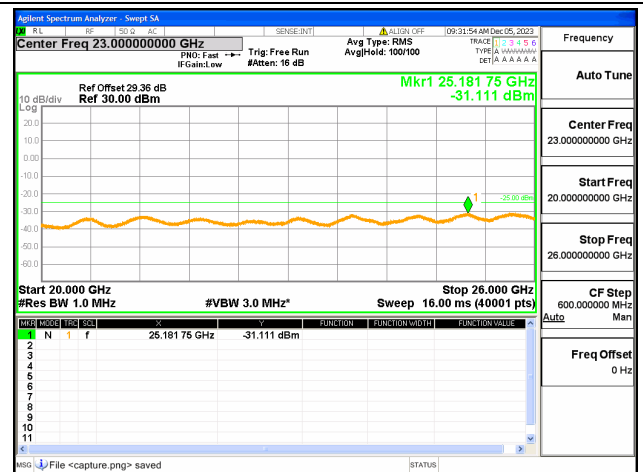
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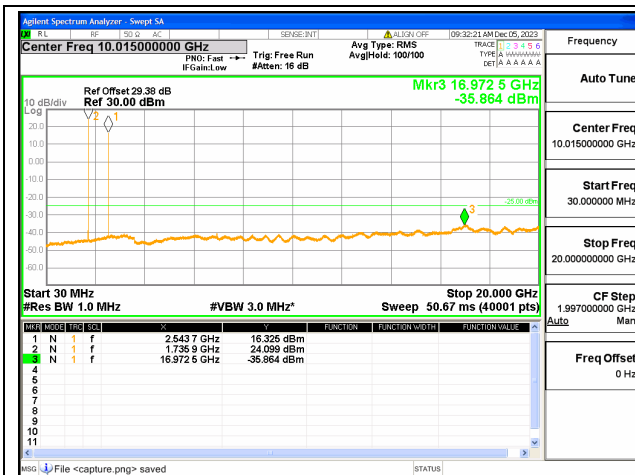
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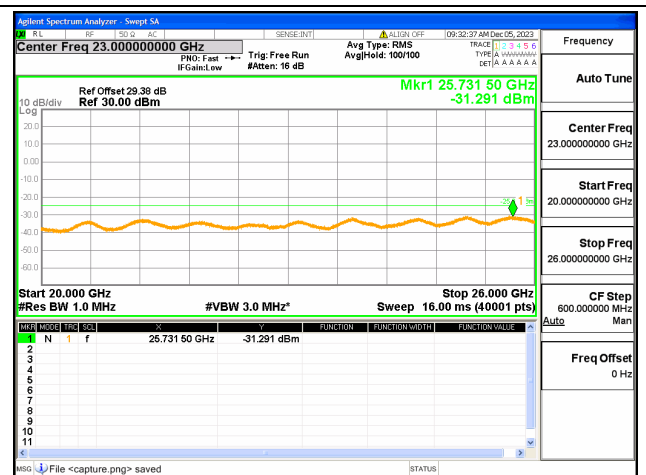
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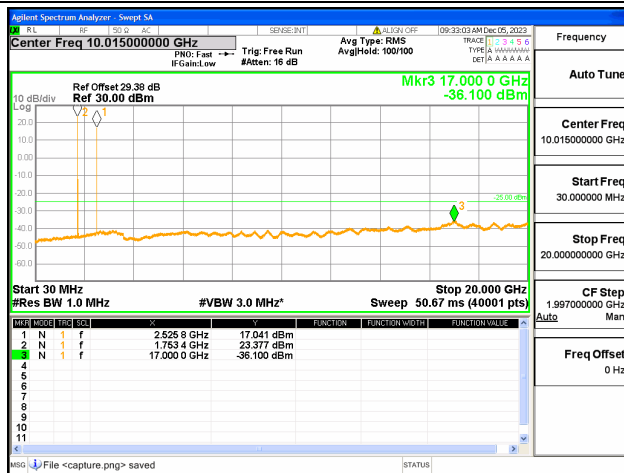
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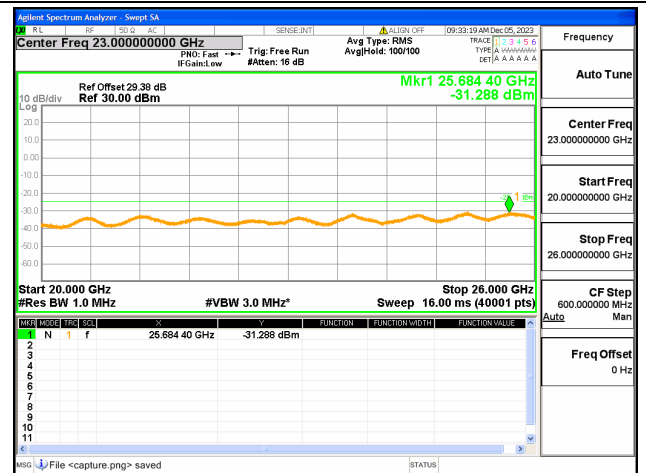
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High+Mid CH / 1@0-1@99



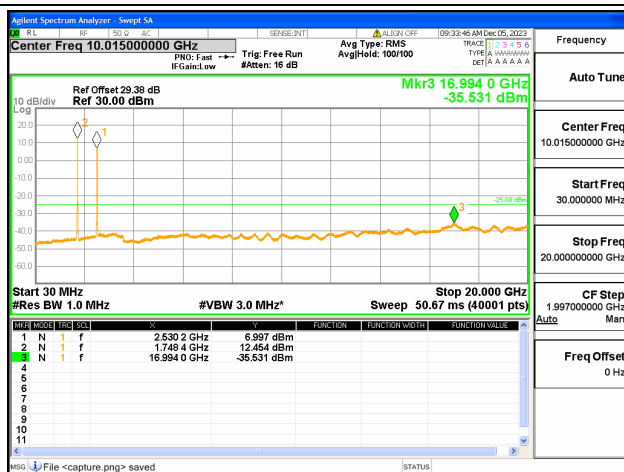
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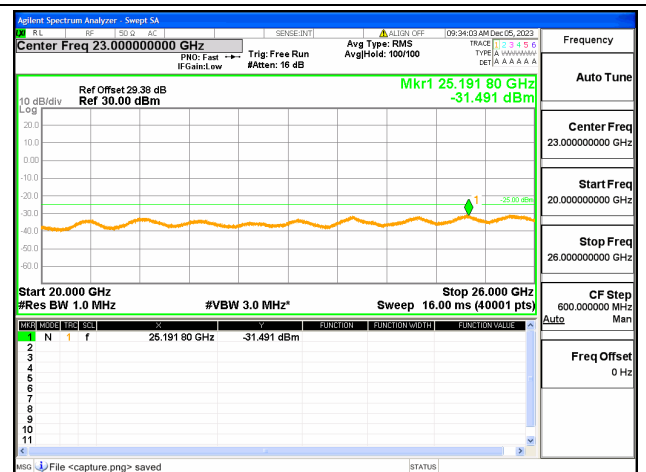
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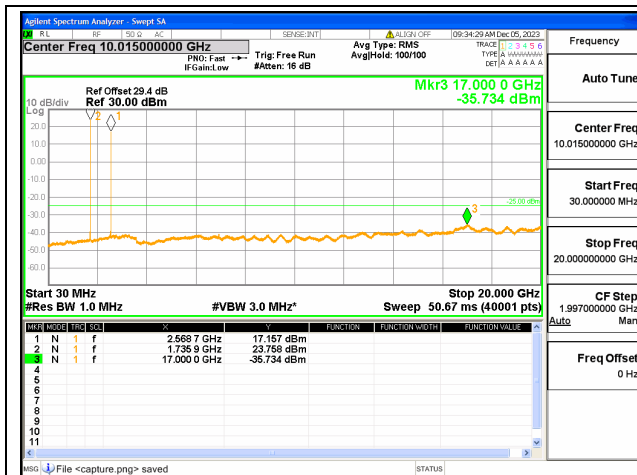
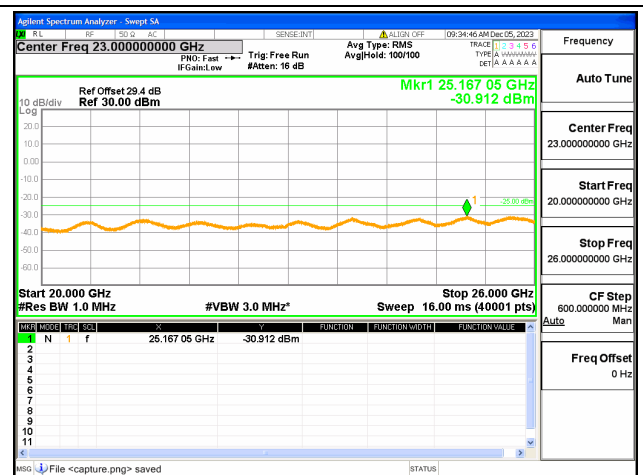
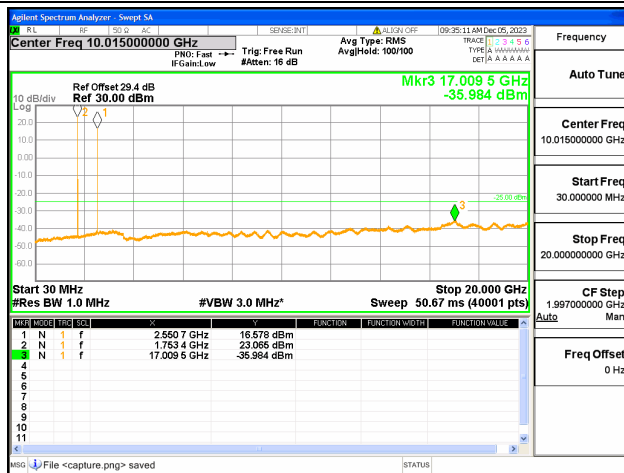
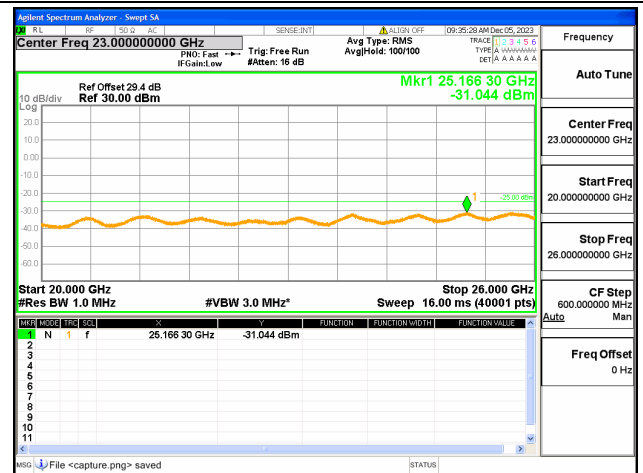
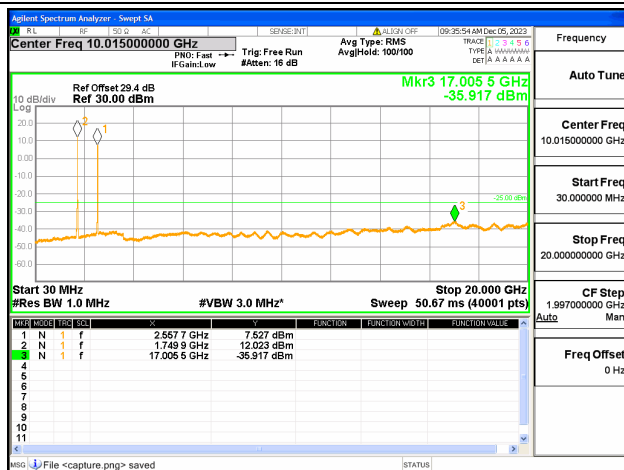
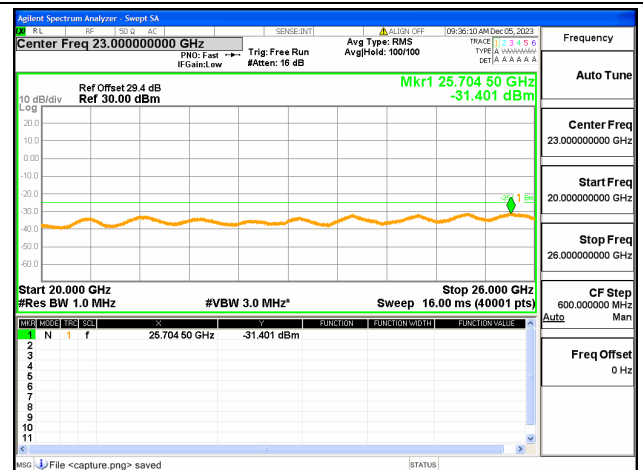
4A-7A-20G-26G / 20+20MHz / QPSK /
High+Mid CH / 1@99-1@0



4A-7A-30M-20G / 20+20MHz / QPSK /
High+Mid CH / 100@0-100@0

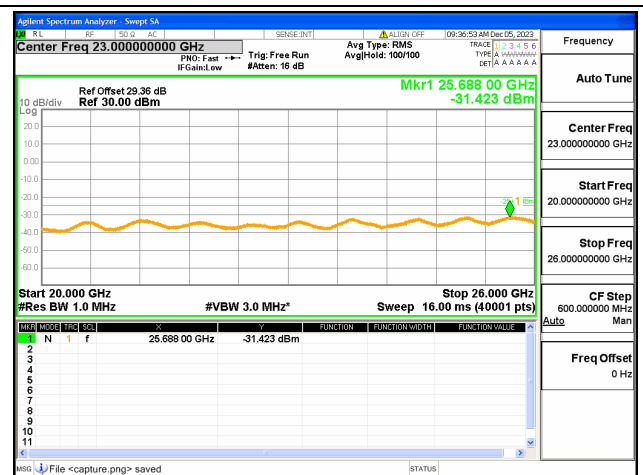


4A-7A-20G-26G / 20+20MHz / QPSK /
High+Mid CH / 100@0-100@0

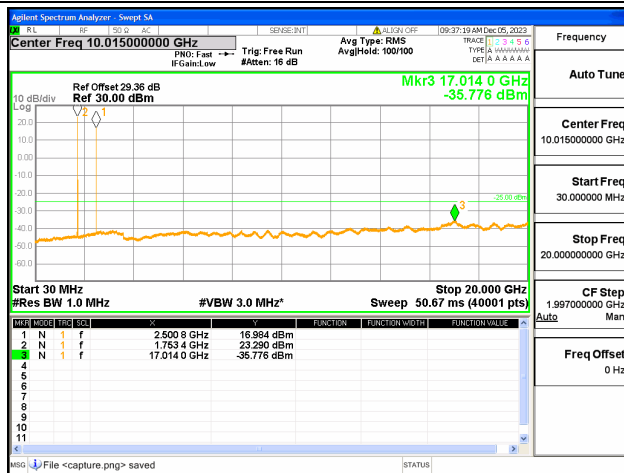
4A-7A-30M-20G / 20+20MHz / QPSK /
High+High CH / 1@0-1@994A-7A-20G-26G / 20+20MHz / QPSK /
High+High CH / 1@0-1@994A-7A-30M-20G / 20+20MHz / QPSK /
High+High CH / 1@99-1@04A-7A-20G-26G / 20+20MHz / QPSK /
High+High CH / 1@99-1@04A-7A-30M-20G / 20+20MHz / QPSK /
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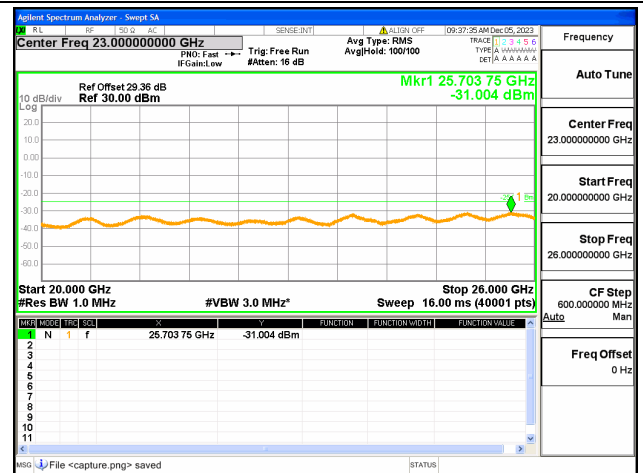
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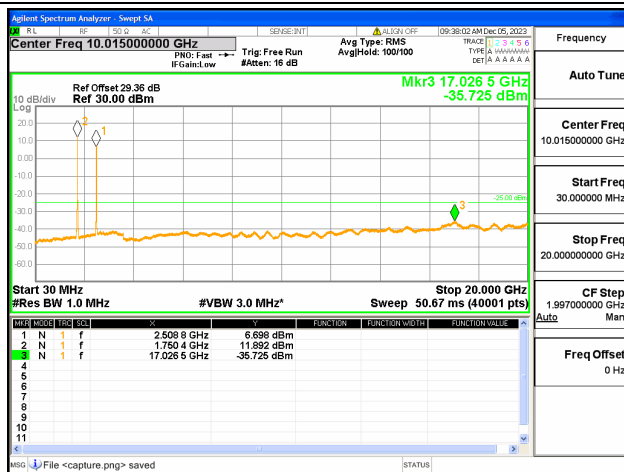
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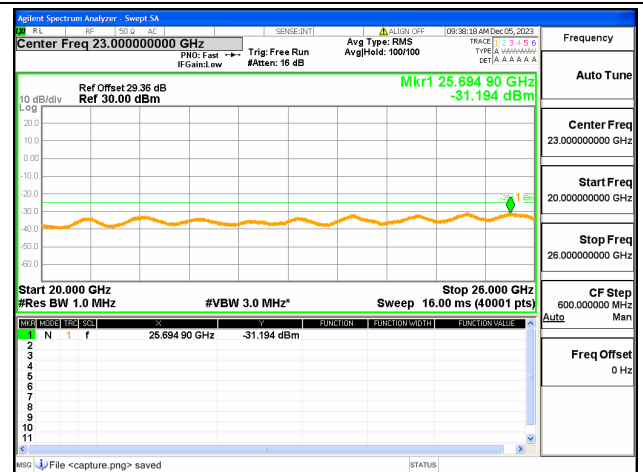
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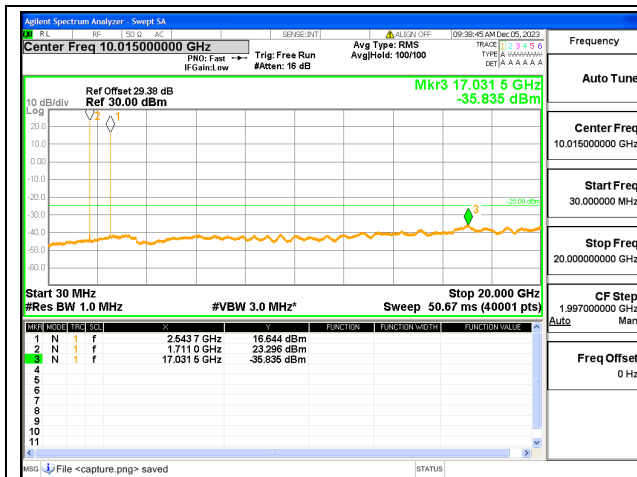
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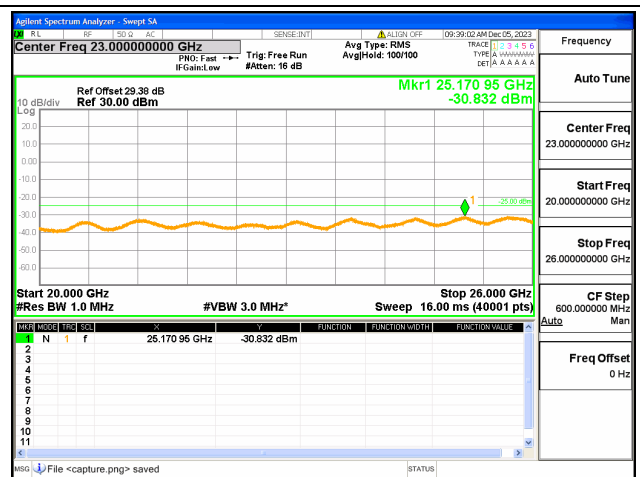
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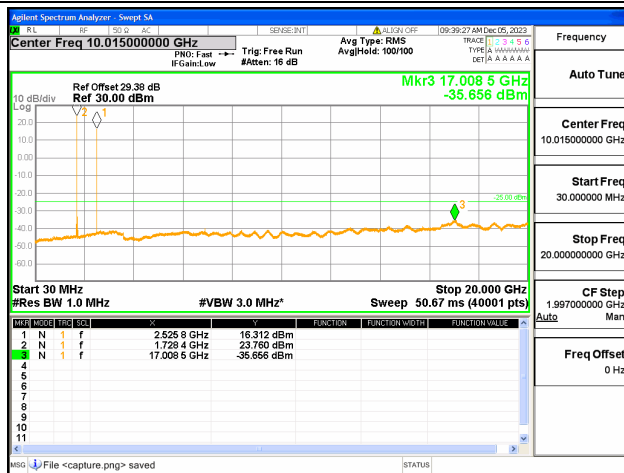
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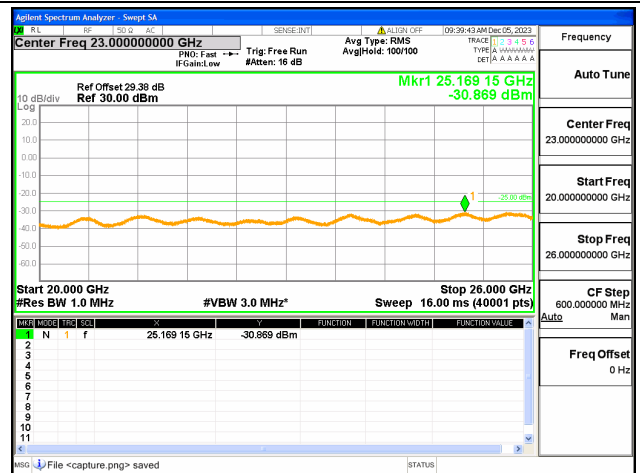
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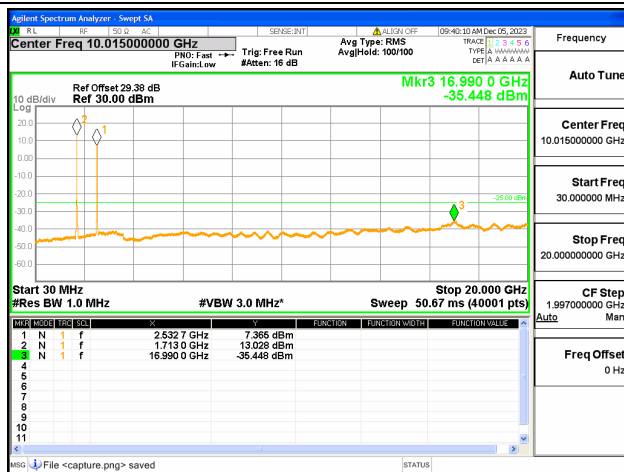
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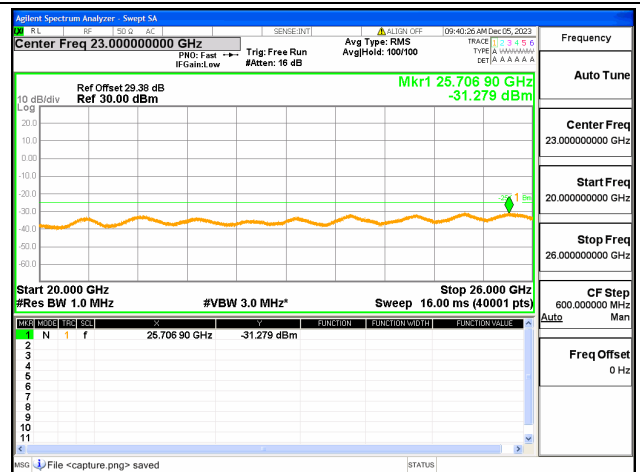
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Low+Mid CH / 1@99-1@0



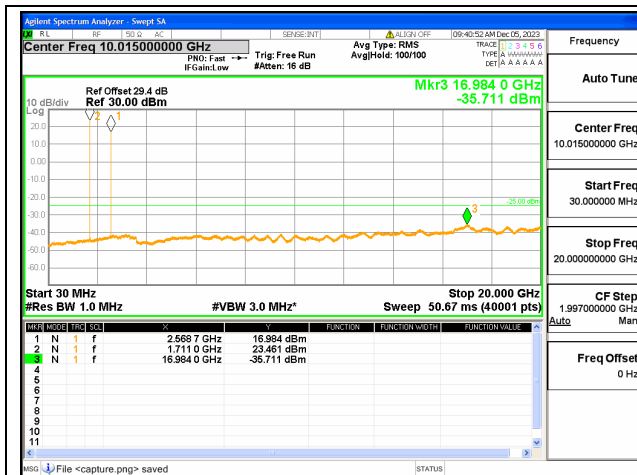
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Low+Mid CH / 1@99-1@0



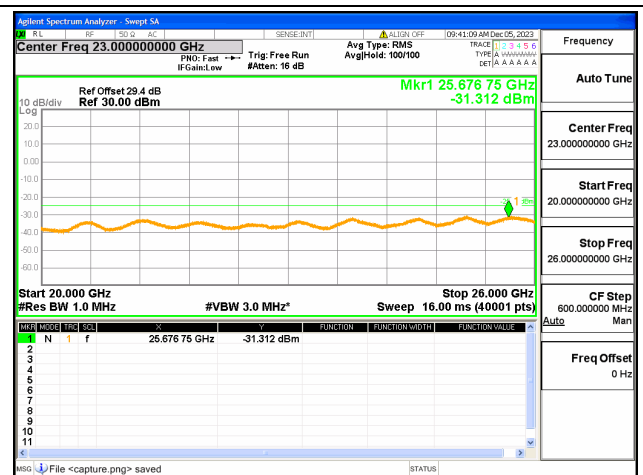
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Low+Mid CH / 100@0-100@0



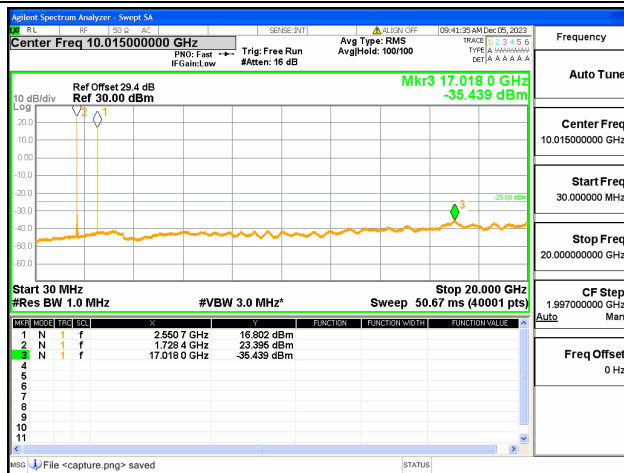
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Low+Mid CH / 100@0-100@0



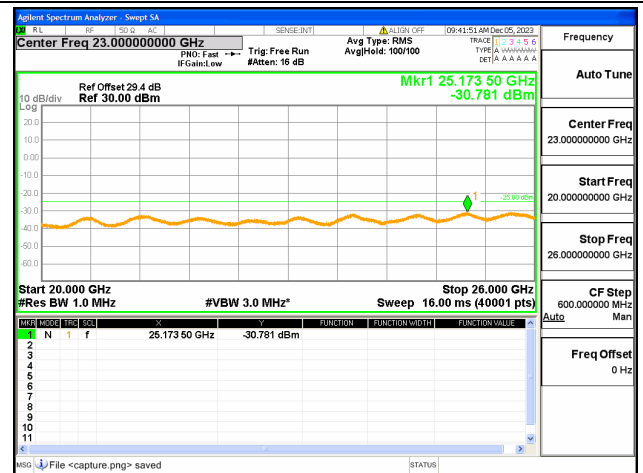
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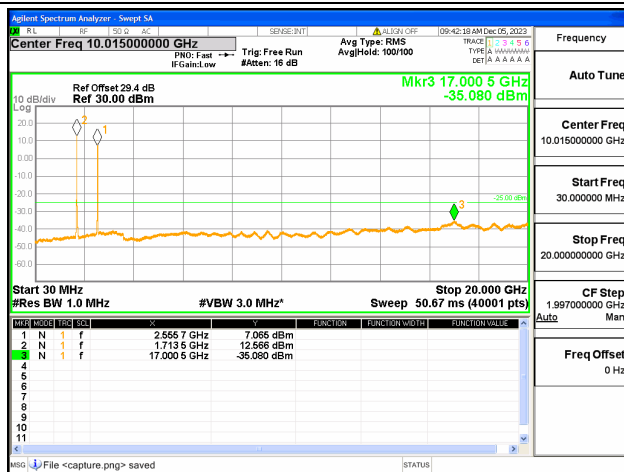
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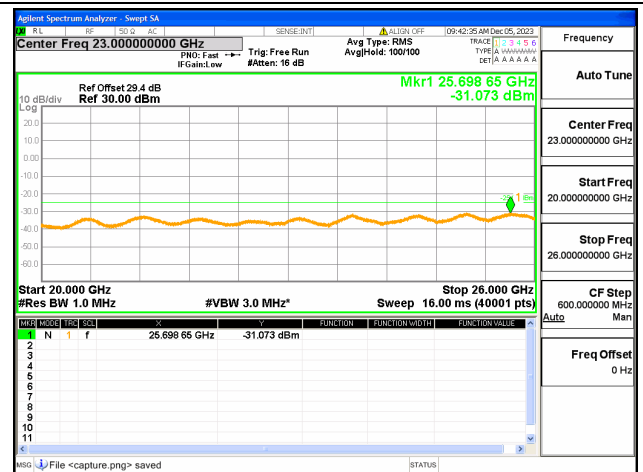
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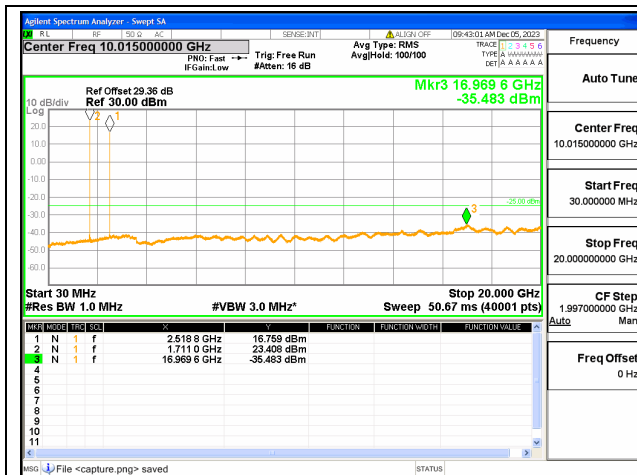
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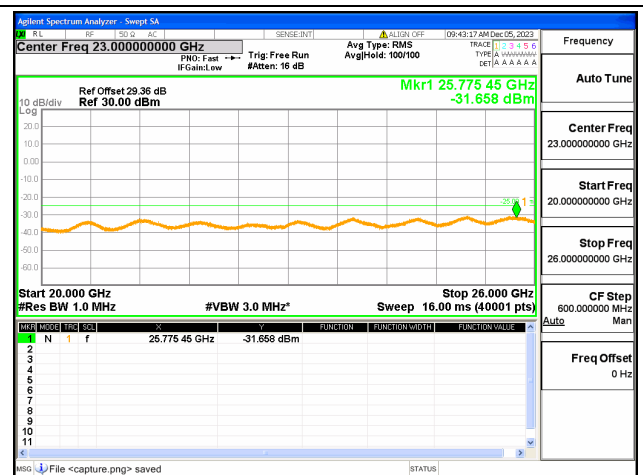
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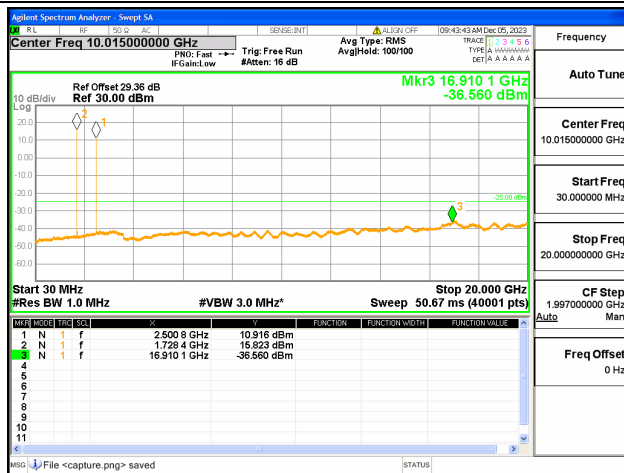
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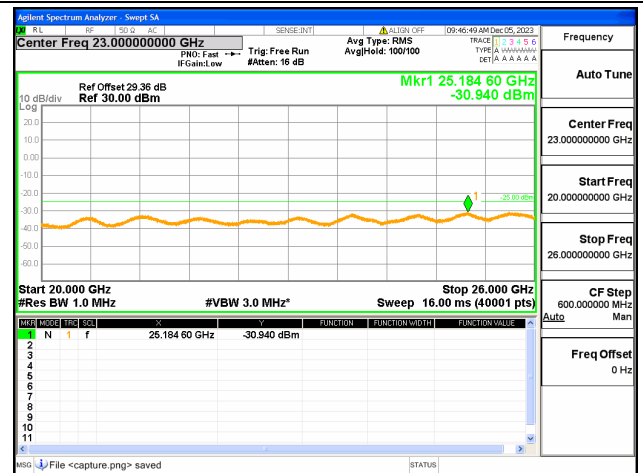
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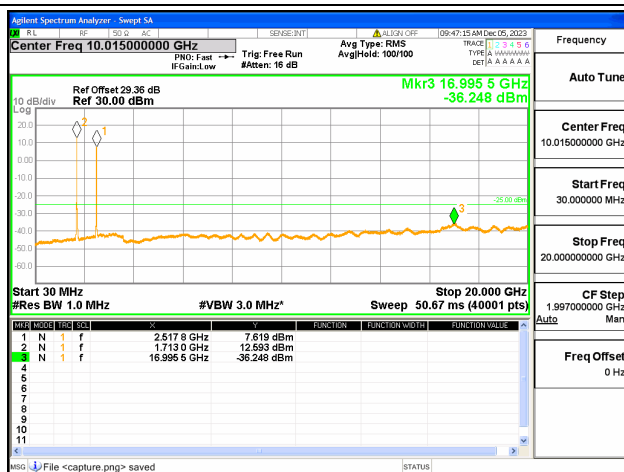
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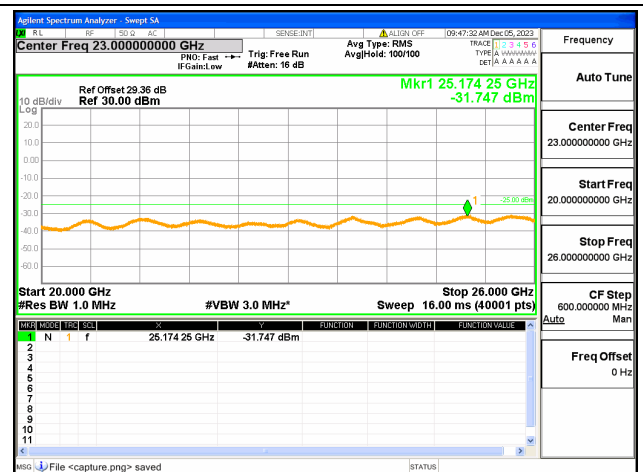
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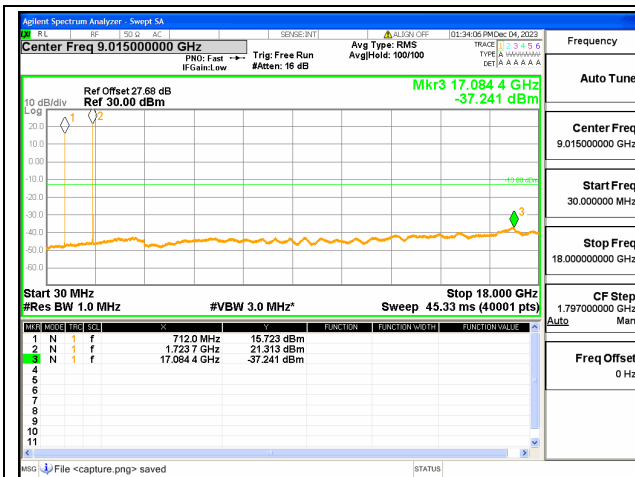
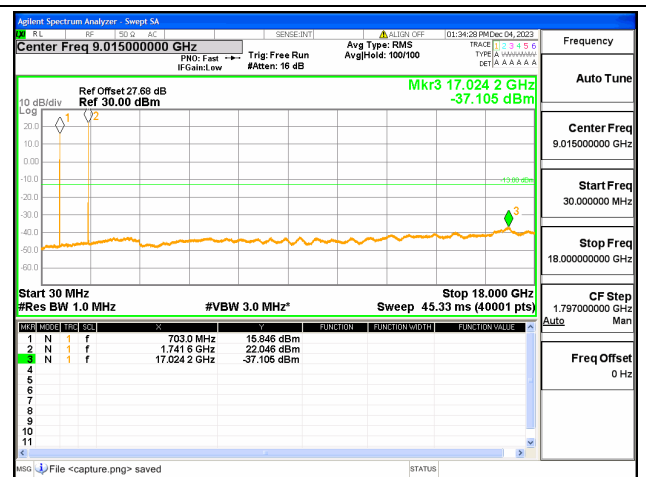
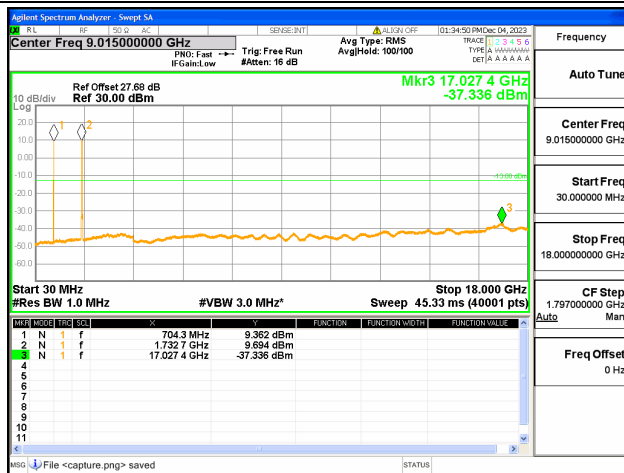
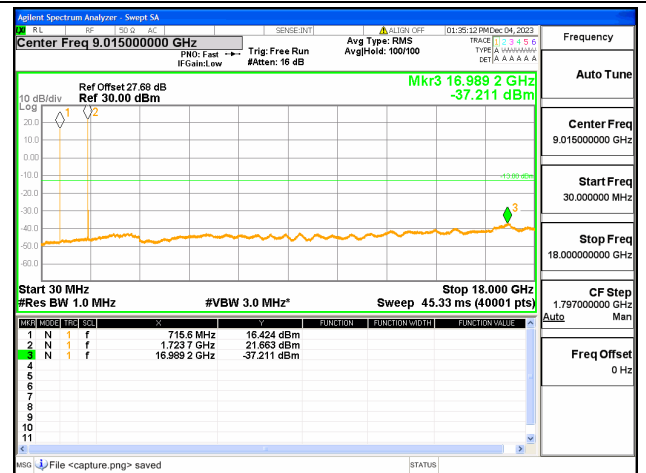
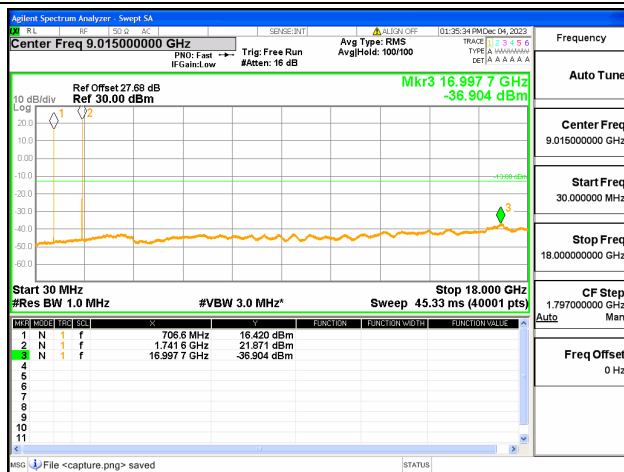
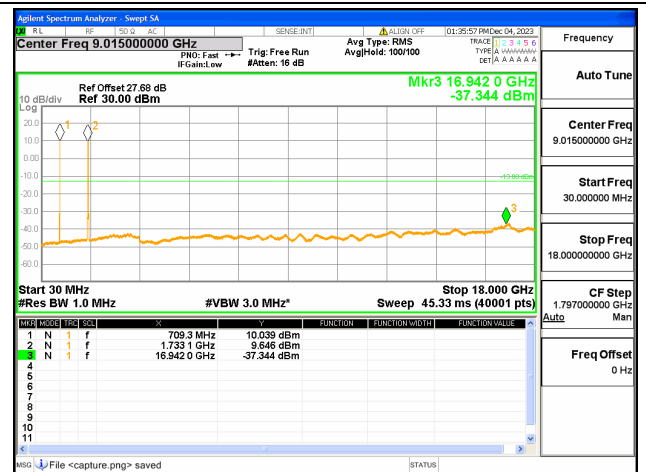
4A-7A-20G-26G / 20+20MHz / QPSK /
Low+Low CH / 1@99-1@0



4A-7A-30M-20G / 20+20MHz / QPSK /
Low+Low CH / 100@0-100@0



4A-7A-20G-26G / 20+20MHz / QPSK /
Low+Low CH / 100@0-100@0

4A-12A / 20+10MHz / QPSK / Mid+Mid CH /
1@0-1@494A-12A / 20+10MHz / QPSK / Mid+Mid CH /
1@99-1@04A-12A / 20+10MHz / QPSK / Mid+Mid CH /
100@0-50@04A-12A / 20+10MHz / QPSK / Mid+High CH /
1@0-1@494A-12A / 20+10MHz / QPSK / Mid+High CH /
1@99-1@04A-12A / 20+10MHz / QPSK / Mid+High CH /
100@0-50@0