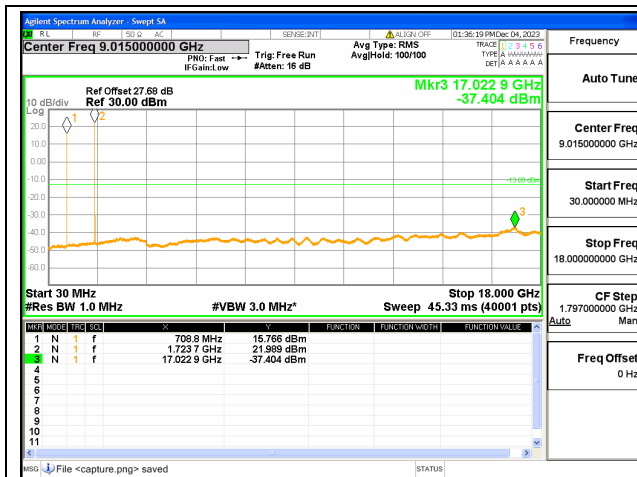
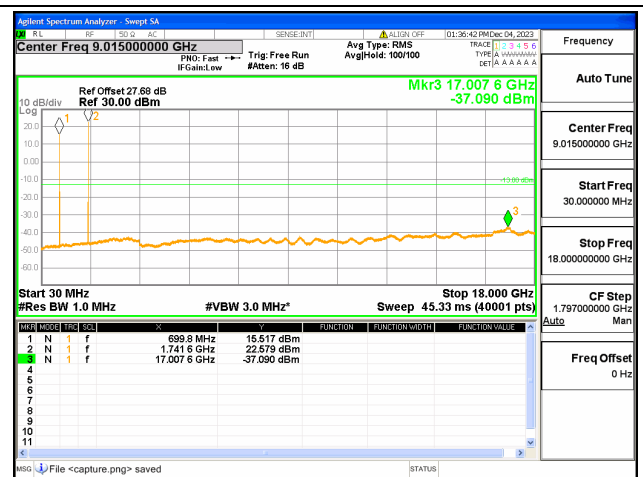
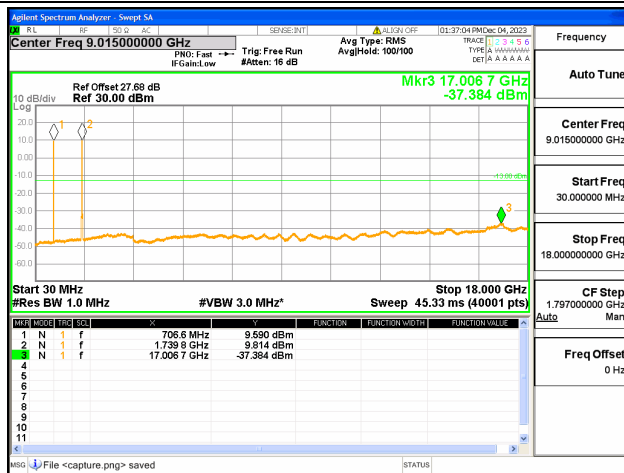




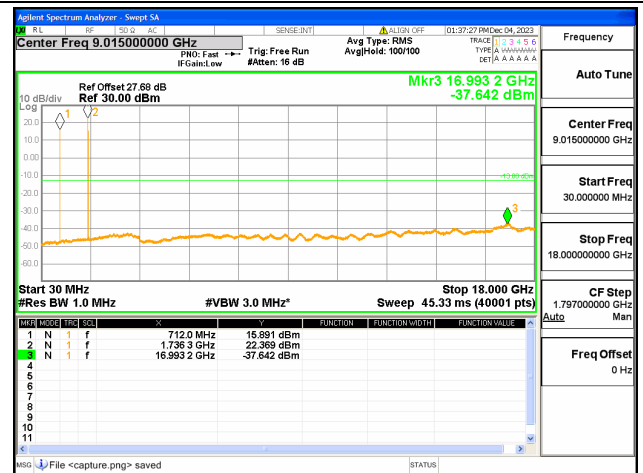
4A-12A / 20+10MHz / QPSK / Mid+Low CH /
1@0-1@49



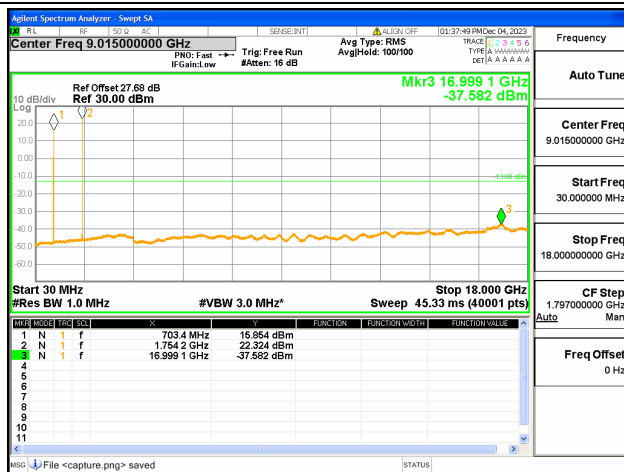
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1@99-1@0



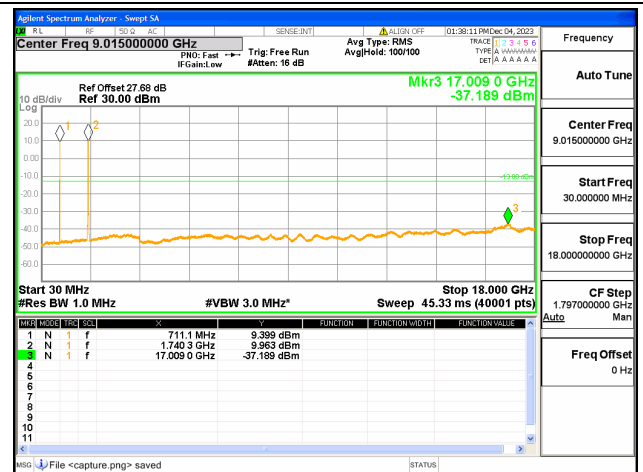
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100@0-50@0



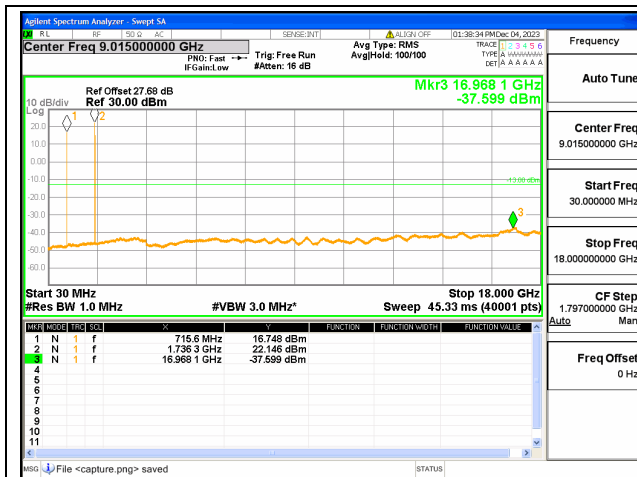
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1@0-1@49



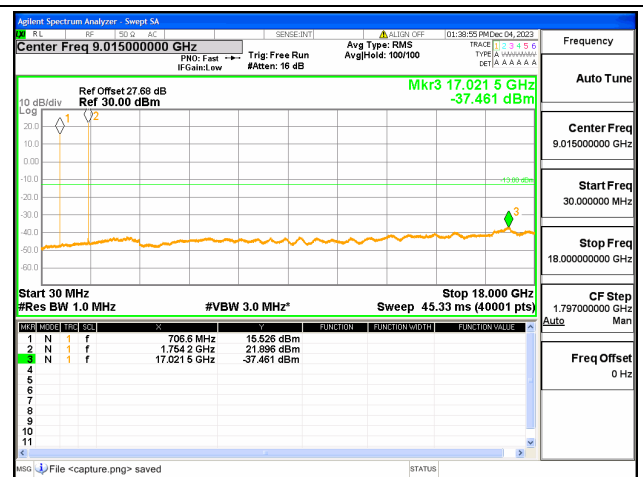
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1@99-1@0



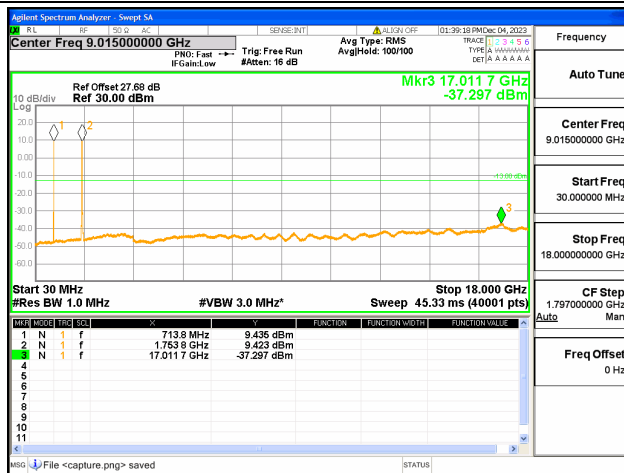
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100@0-50@0



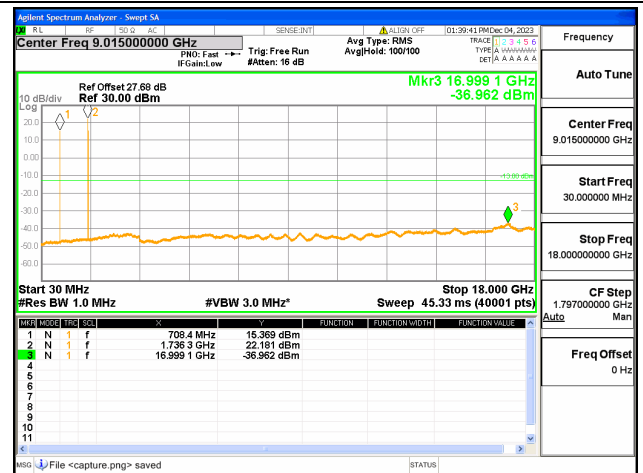
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1@0-1@49



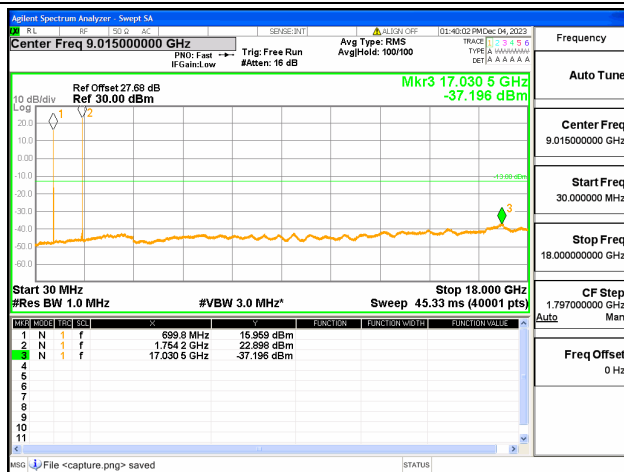
4A-12A / 20+10MHz / QPSK / High+High CH /
1@99-1@0



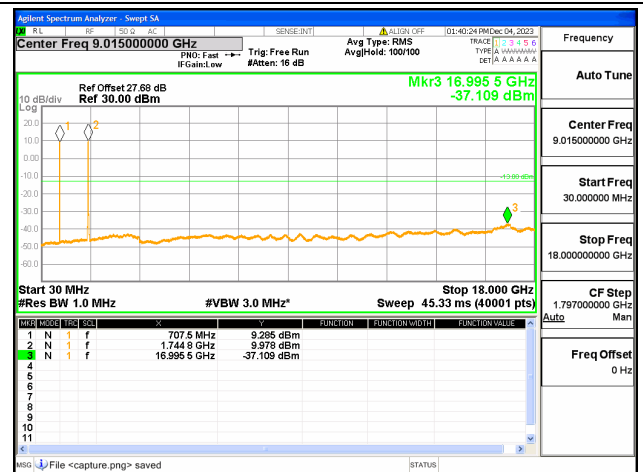
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100@0-50@0



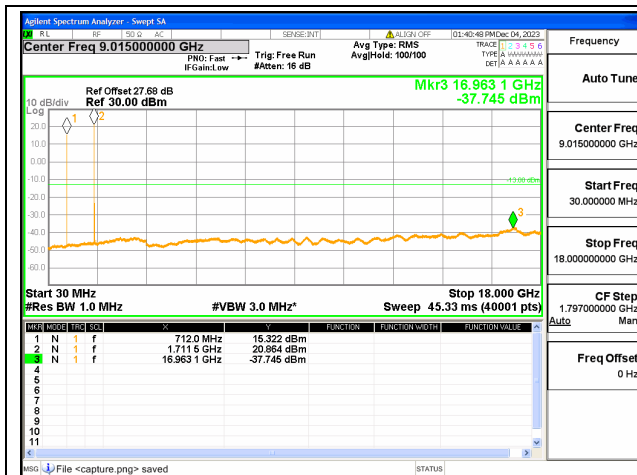
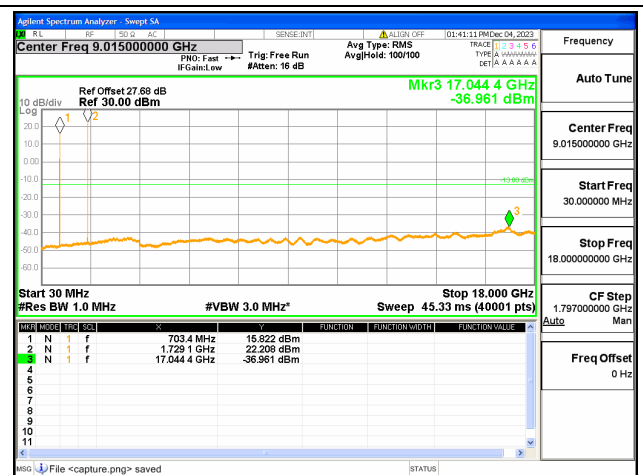
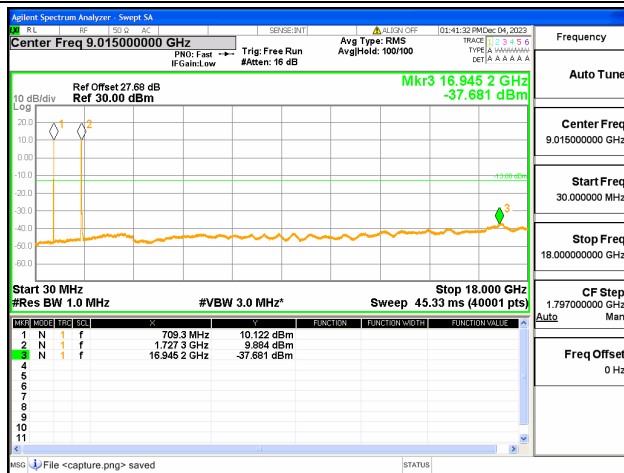
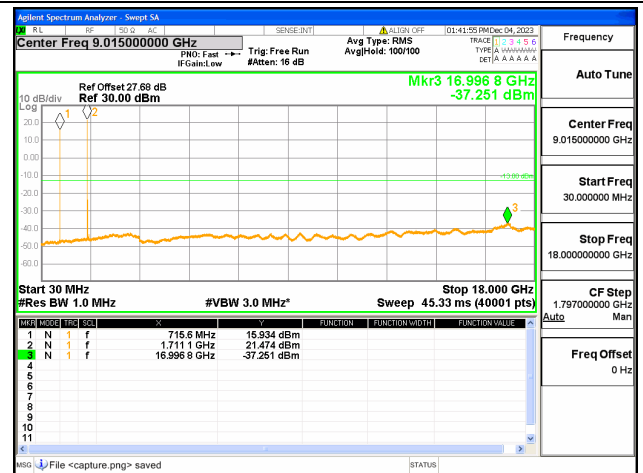
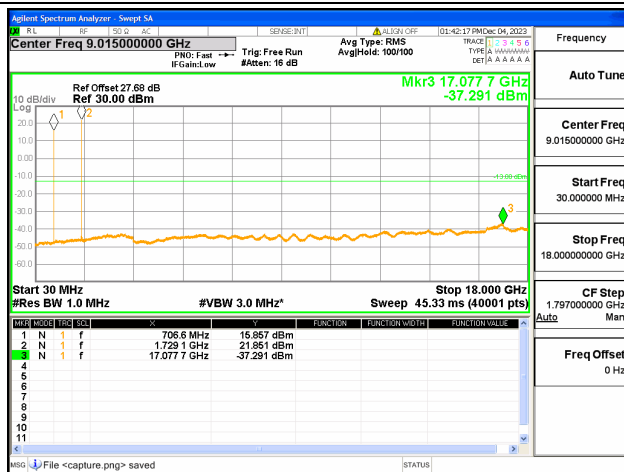
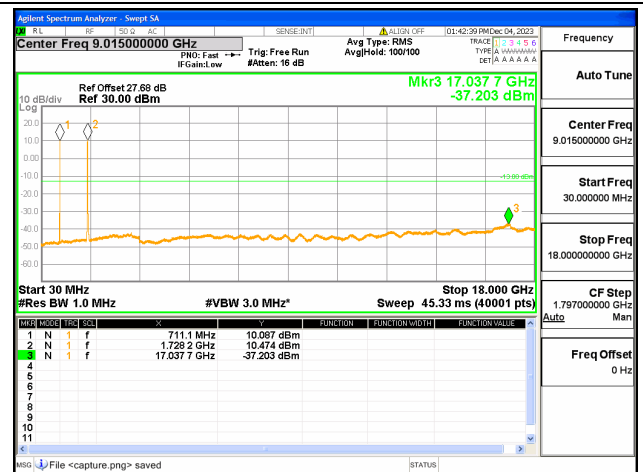
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1@0-1@49

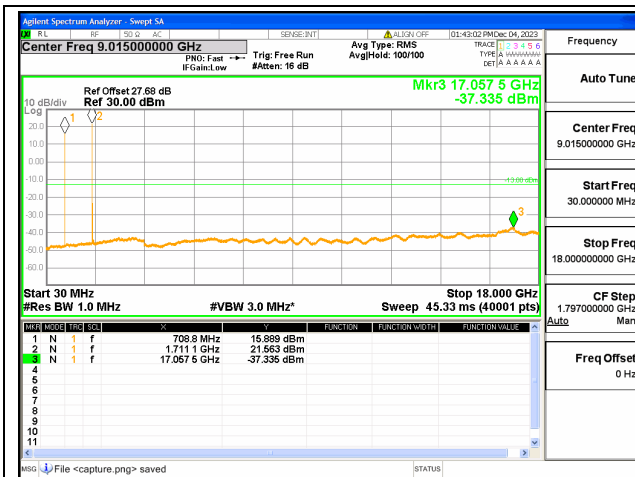


4A-12A / 20+10MHz / QPSK / High+Low CH /
1@99-1@0

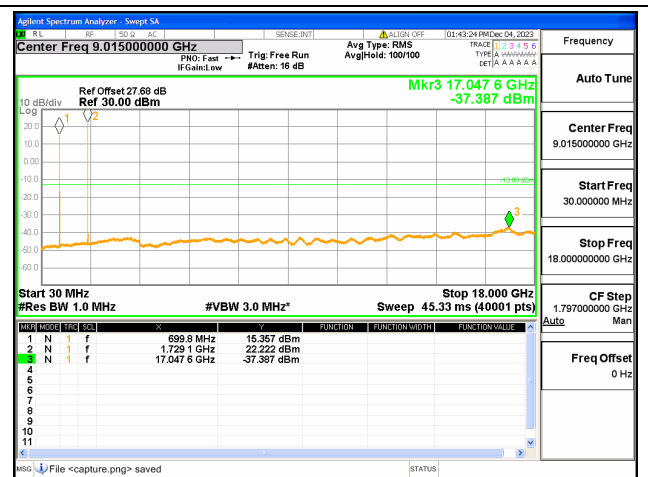


4A-12A / 20+10MHz / QPSK / High+Low CH /
100@0-50@0

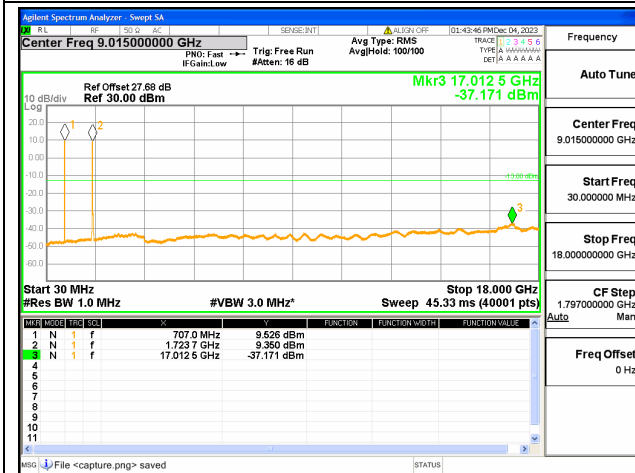
4A-12A / 20+10MHz / QPSK / Low+Mid CH /
1@0-1@494A-12A / 20+10MHz / QPSK / Low+Mid CH /
1@99-1@04A-12A / 20+10MHz / QPSK / Low+Mid CH /
100@0-50@04A-12A / 20+10MHz / QPSK / Low+High CH /
1@0-1@494A-12A / 20+10MHz / QPSK / Low+High CH /
1@99-1@04A-12A / 20+10MHz / QPSK / Low+High CH /
100@0-50@0



4A-12A / 20+10MHz / QPSK / Low+Low CH /
1@0-1@49

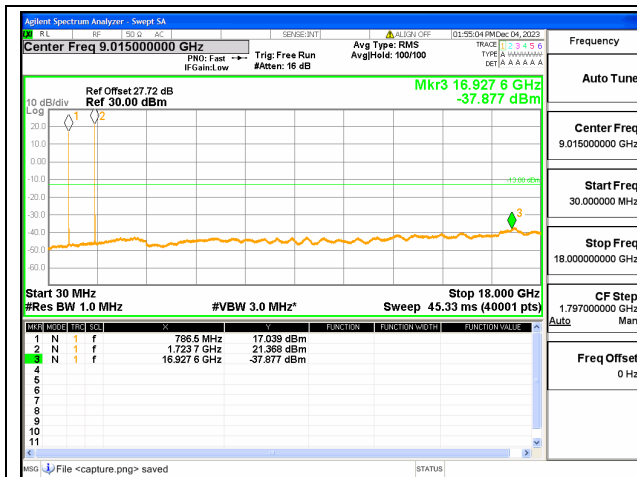


4A-12A / 20+10MHz / QPSK / Low+Low CH /
1@99-1@0

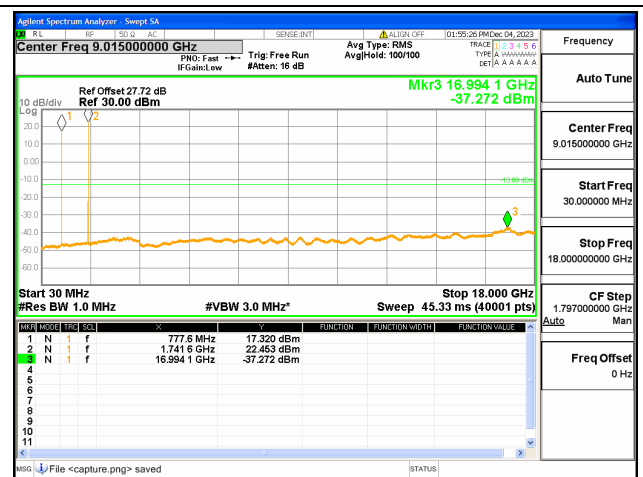


4A-12A / 20+10MHz / QPSK / Low+Low CH /
100@0-50@0

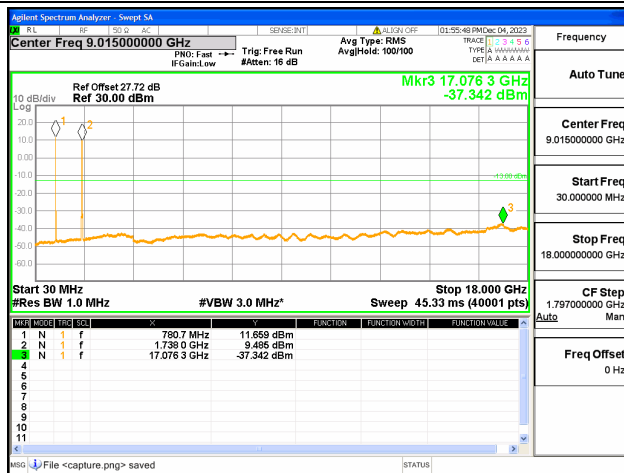
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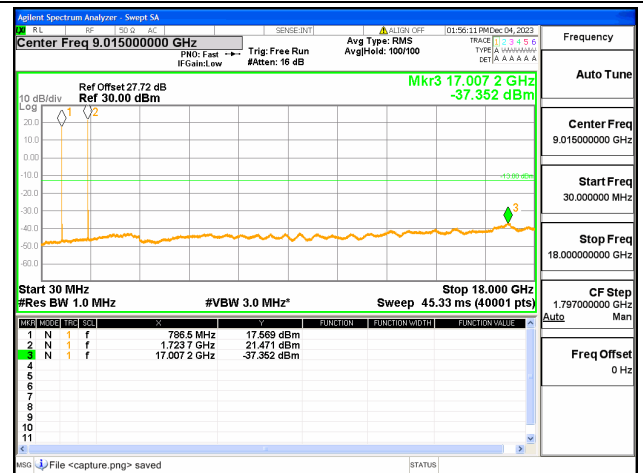
4A-13A / 20+10MHz / QPSK / Mid+Mid CH /
1@0-1@49



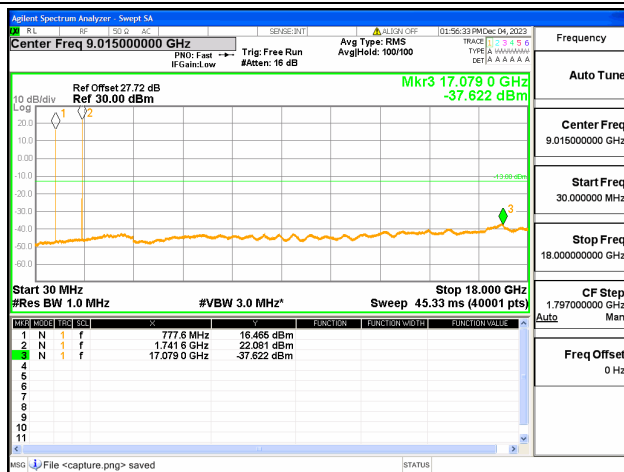
4A-13A / 20+10MHz / QPSK / Mid+Mid CH /
1@99-1@0



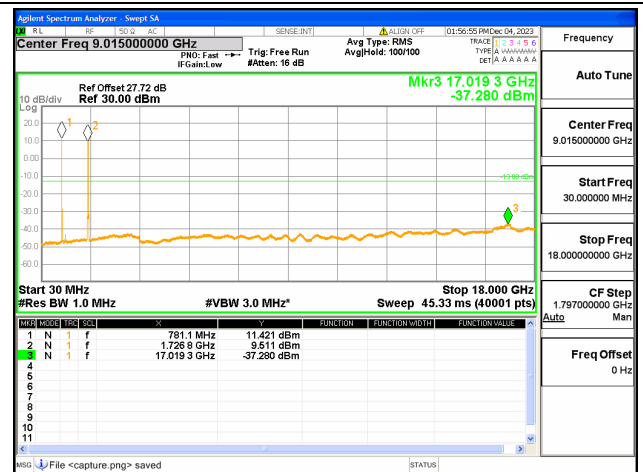
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100@0-50@0



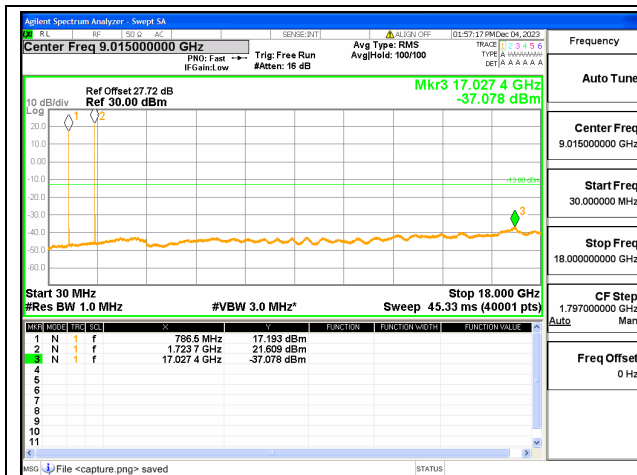
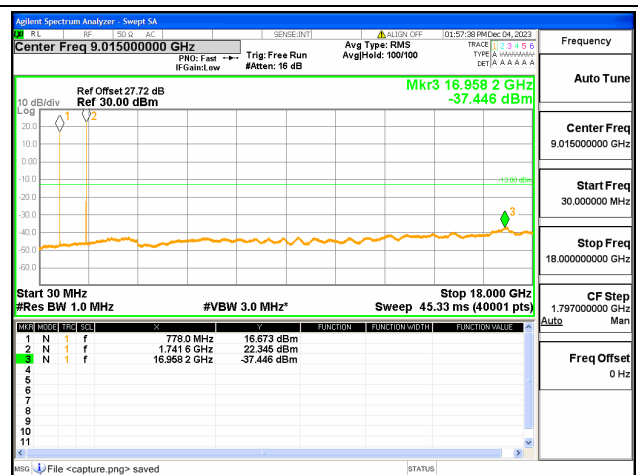
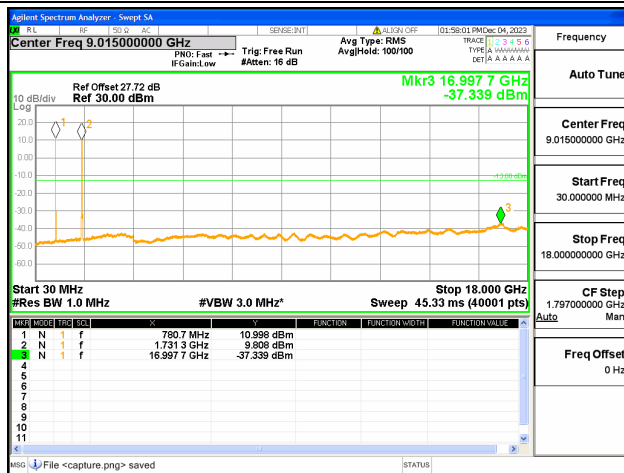
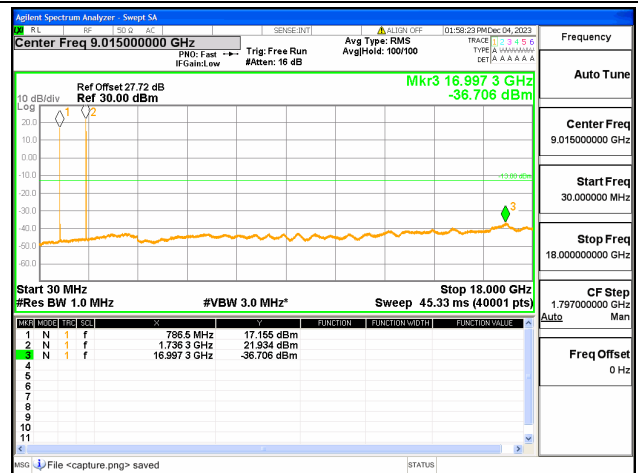
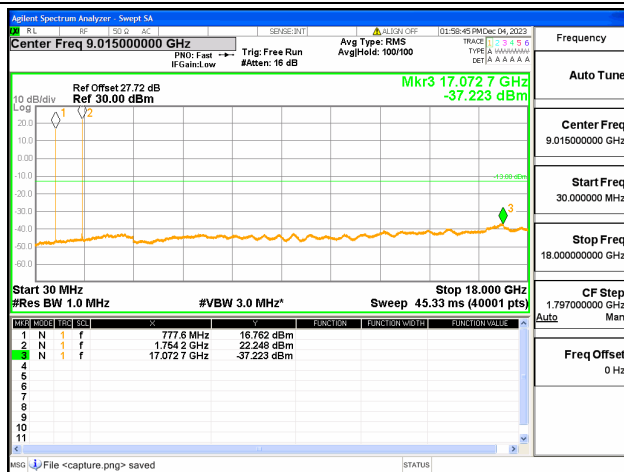
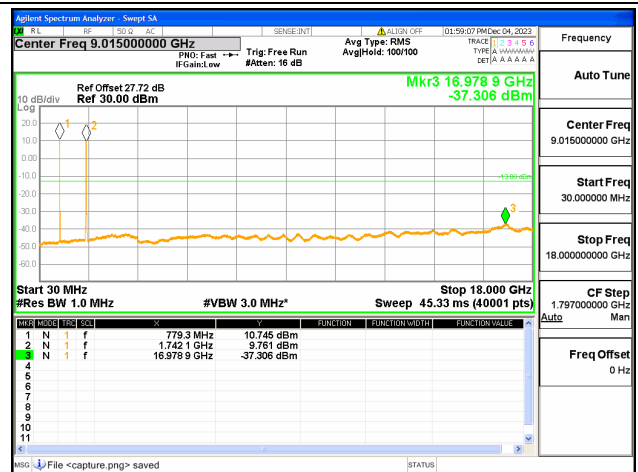
4A-13A / 20+10MHz / QPSK / Mid+High CH /
1@0-1@49

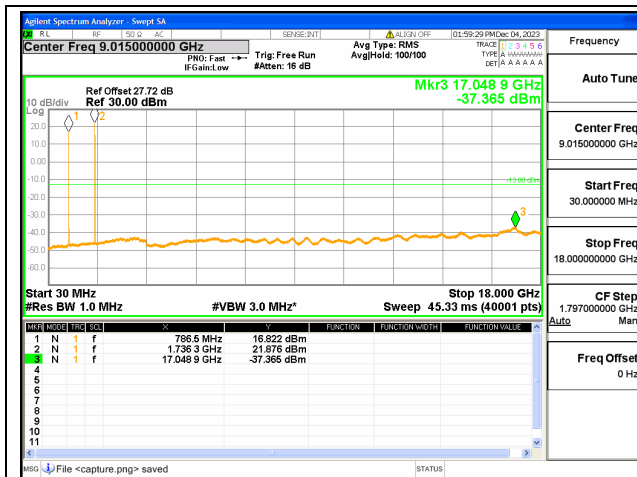


4A-13A / 20+10MHz / QPSK / Mid+High CH /
1@99-1@0

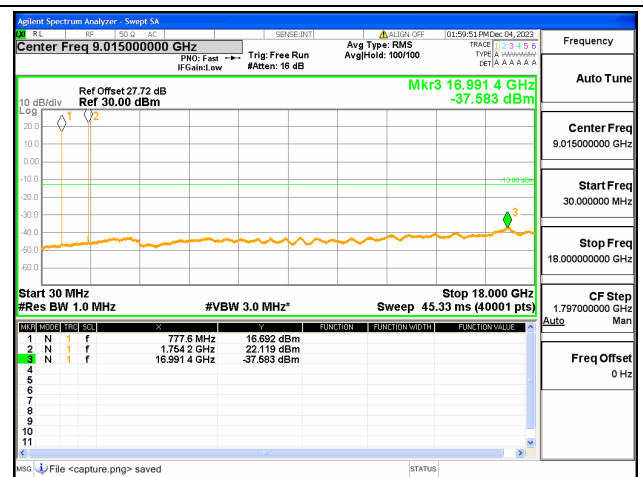


4A-13A / 20+10MHz / QPSK / Mid+High CH /
100@0-50@0

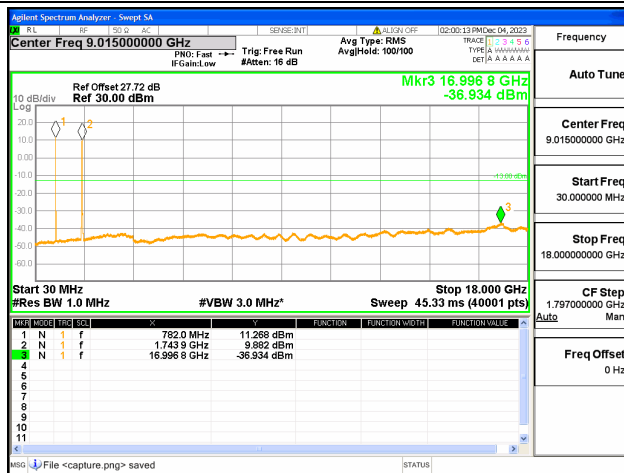
4A-13A / 20+10MHz / QPSK / Mid+Low CH /
1@0-1@494A-13A / 20+10MHz / QPSK / Mid+Low CH /
1@99-1@04A-13A / 20+10MHz / QPSK / Mid+Low CH /
100@0-50@04A-13A / 20+10MHz / QPSK / High+Mid CH /
1@0-1@494A-13A / 20+10MHz / QPSK / High+Mid CH /
1@99-1@04A-13A / 20+10MHz / QPSK / High+Mid CH /
100@0-50@0



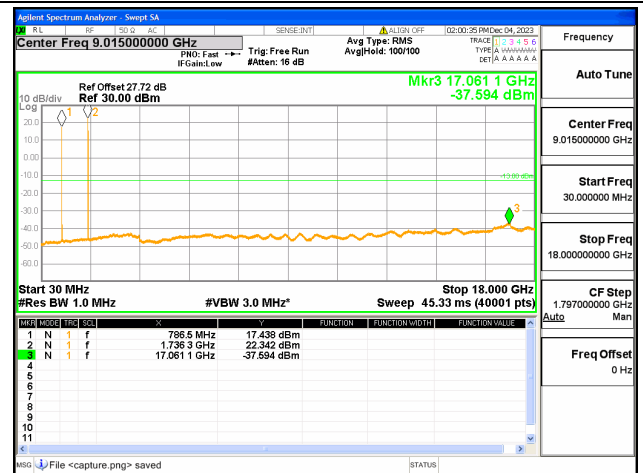
4A-13A / 20+10MHz / QPSK / High+High CH /
1@0-1@49



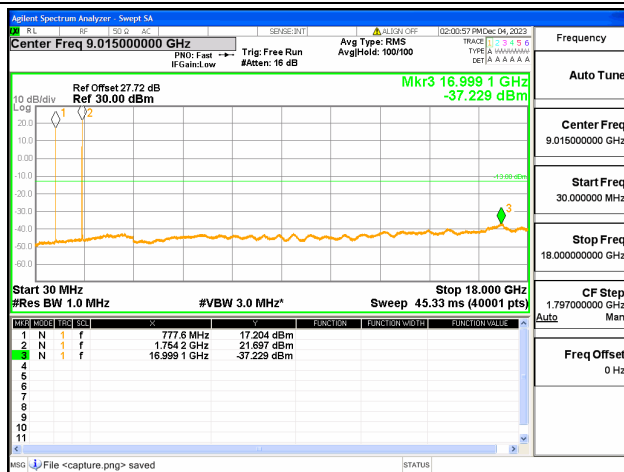
4A-13A / 20+10MHz / QPSK / High+High CH /
1@99-1@0



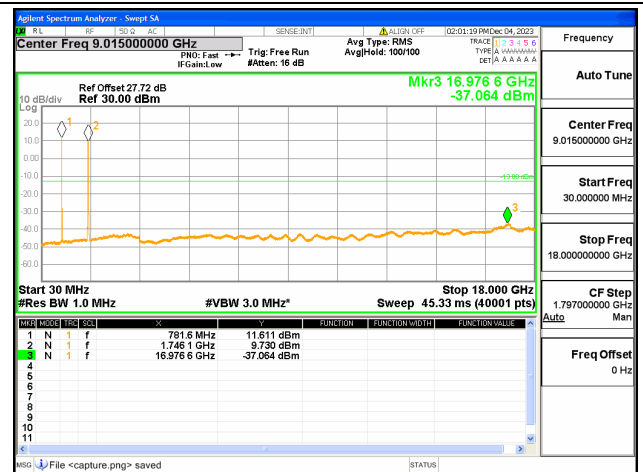
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100@0-50@0



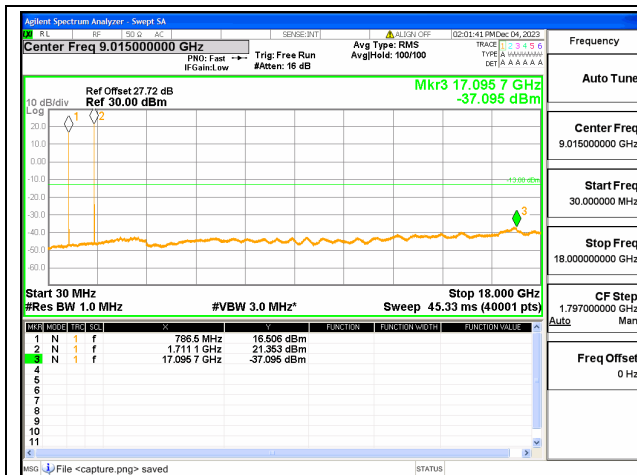
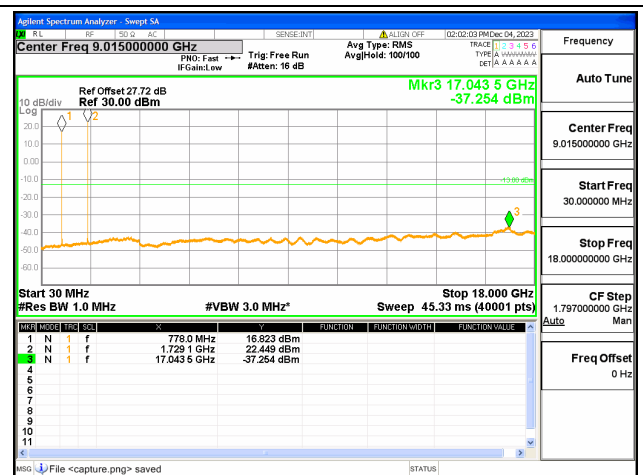
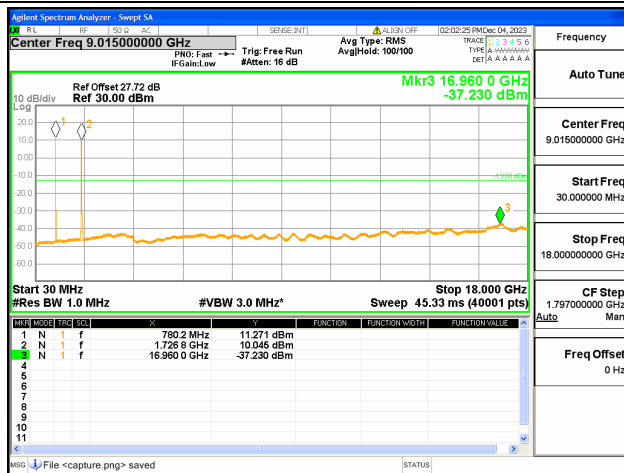
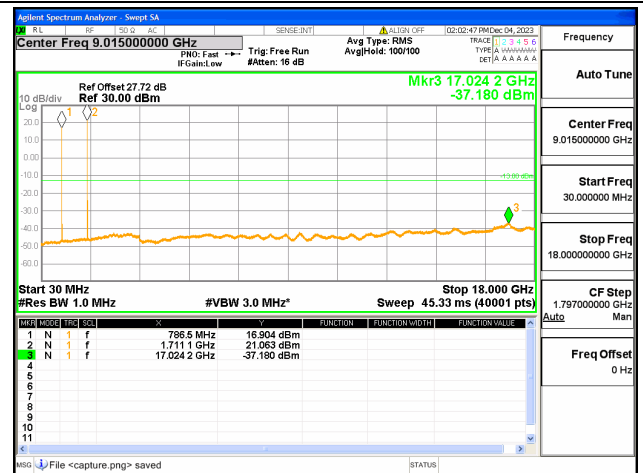
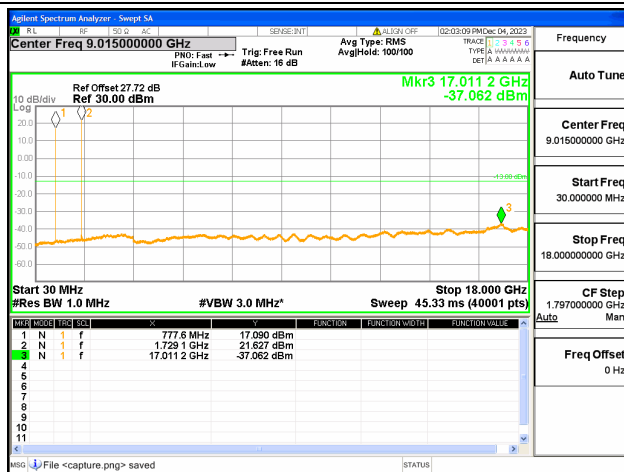
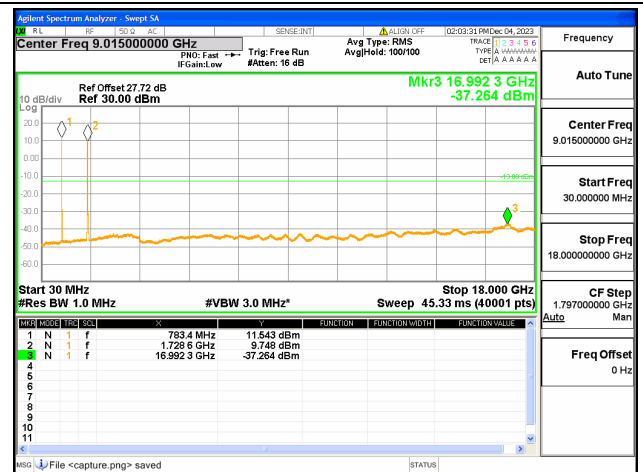
4A-13A / 20+10MHz / QPSK / High+Low CH /
1@0-1@49

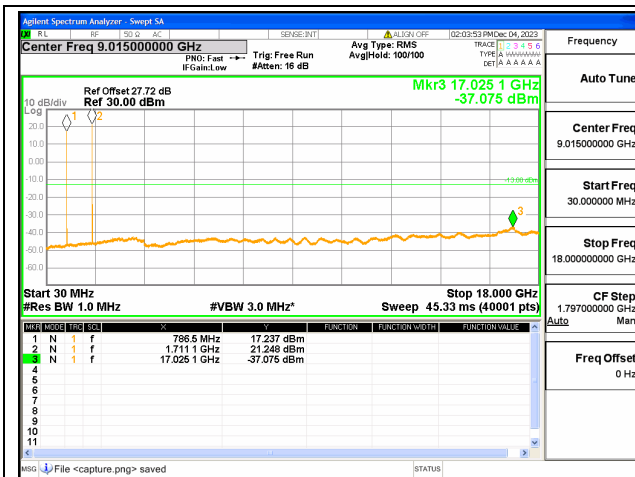


4A-13A / 20+10MHz / QPSK / High+Low CH /
1@99-1@0

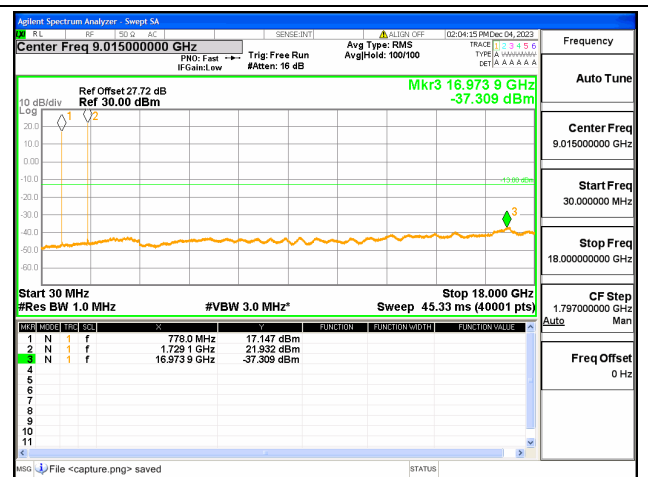


4A-13A / 20+10MHz / QPSK / High+Low CH /
100@0-50@0

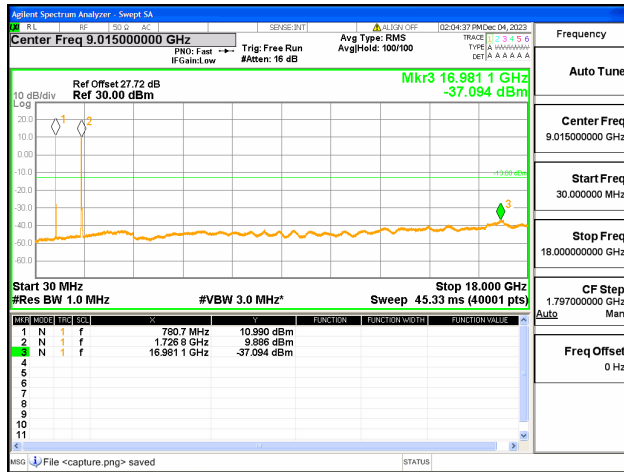
4A-13A / 20+10MHz / QPSK / Low+Mid CH /
1@0-1@494A-13A / 20+10MHz / QPSK / Low+Mid CH /
1@99-1@04A-13A / 20+10MHz / QPSK / Low+Mid CH /
100@0-50@04A-13A / 20+10MHz / QPSK / Low+High CH /
1@0-1@494A-13A / 20+10MHz / QPSK / Low+High CH /
1@99-1@04A-13A / 20+10MHz / QPSK / Low+High CH /
100@0-50@0



4A-13A / 20+10MHz / QPSK / Low+Low CH /
1@0-1@49

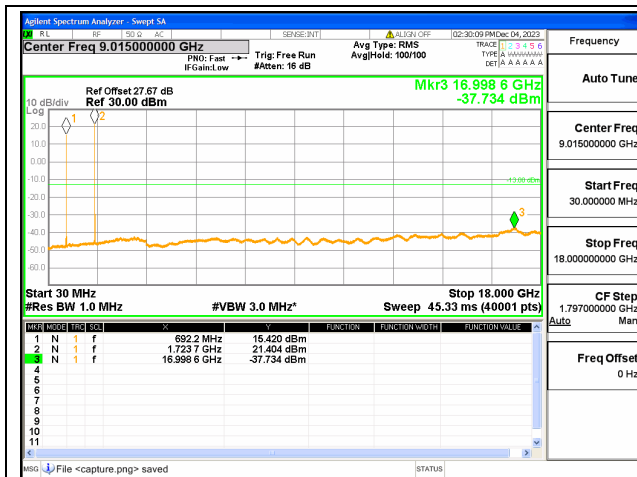
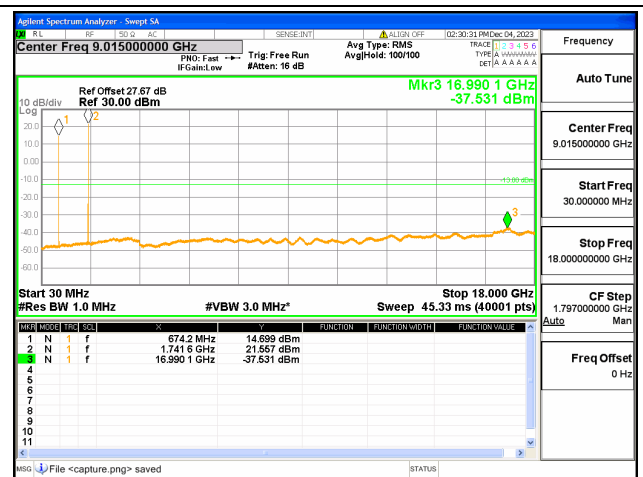
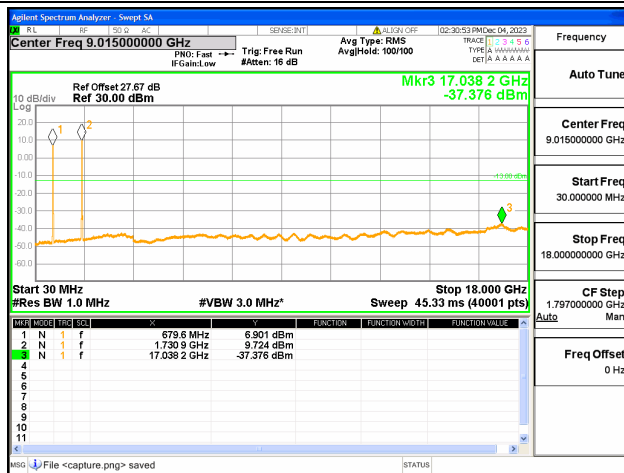
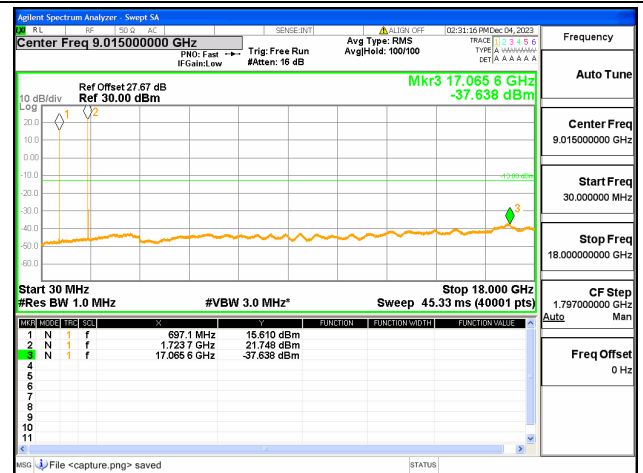
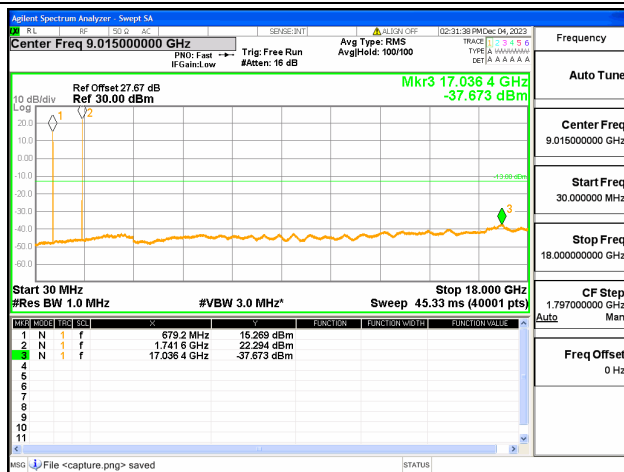
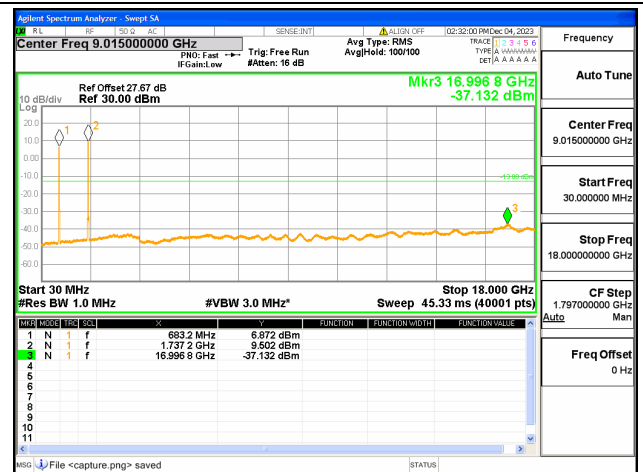


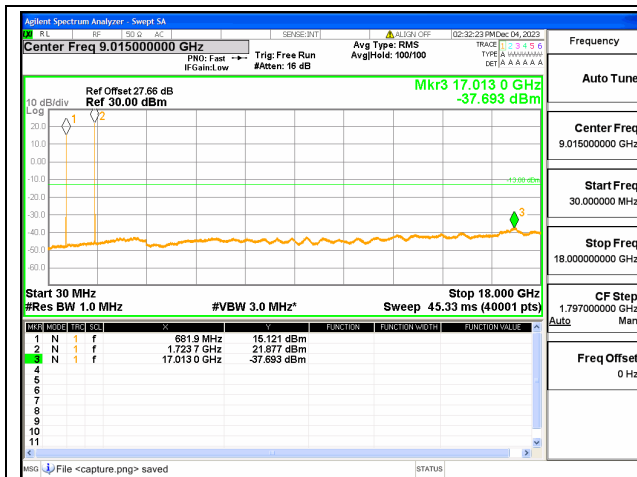
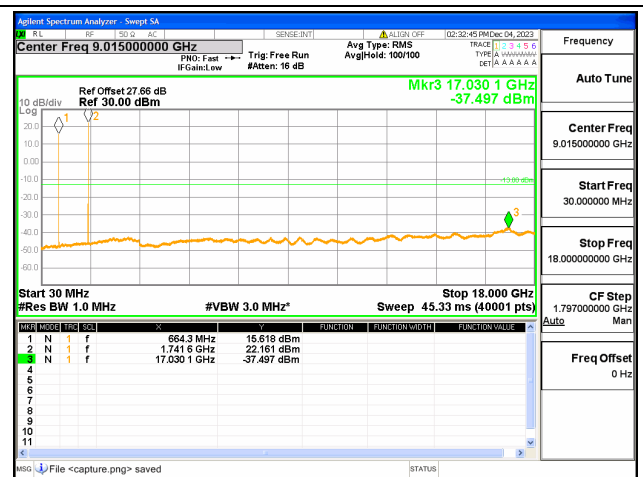
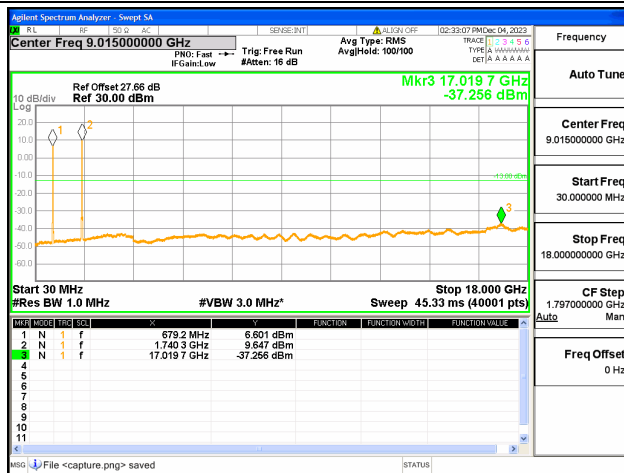
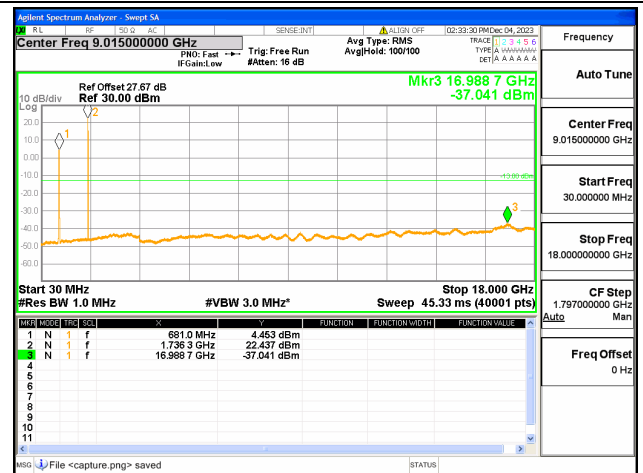
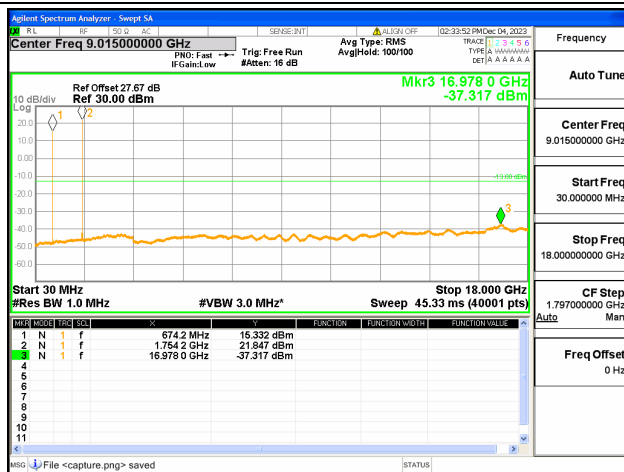
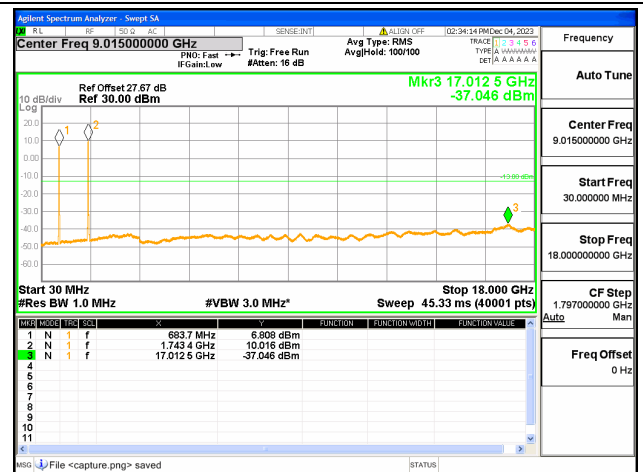
4A-13A / 20+10MHz / QPSK / Low+Low CH /
1@99-1@0

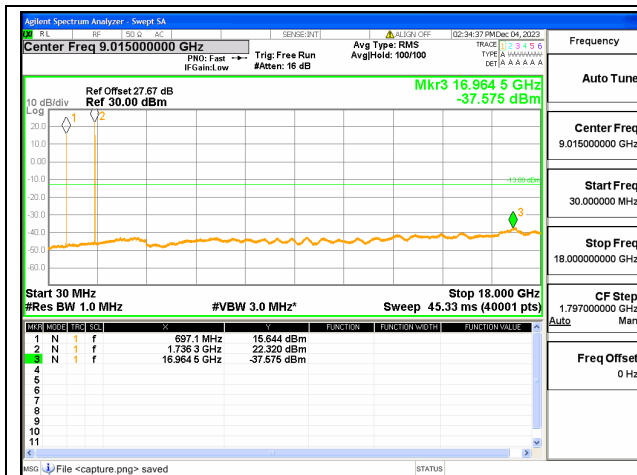


4A-13A / 20+10MHz / QPSK / Low+Low CH /
100@0-50@0

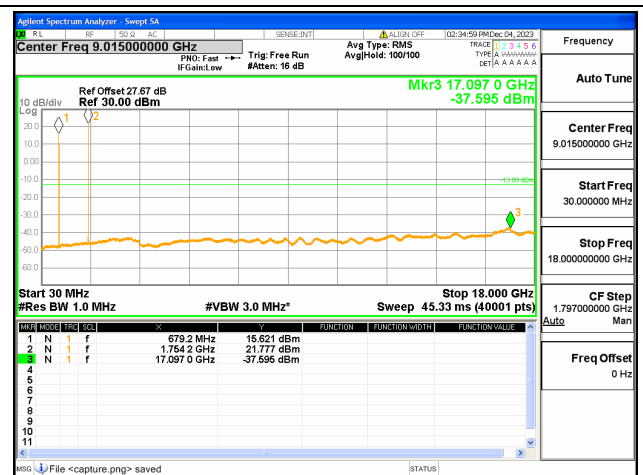
N/A

4A-71A / 20+20MHz / QPSK / Mid+Mid CH /
1@0-1@994A-71A / 20+20MHz / QPSK / Mid+Mid CH /
1@99-1@04A-71A / 20+20MHz / QPSK / Mid+Mid CH /
100@0-100@04A-71A / 20+20MHz / QPSK / Mid+High CH /
1@0-1@994A-71A / 20+20MHz / QPSK / Mid+High CH /
1@99-1@04A-71A / 20+20MHz / QPSK / Mid+High CH /
100@0-100@0

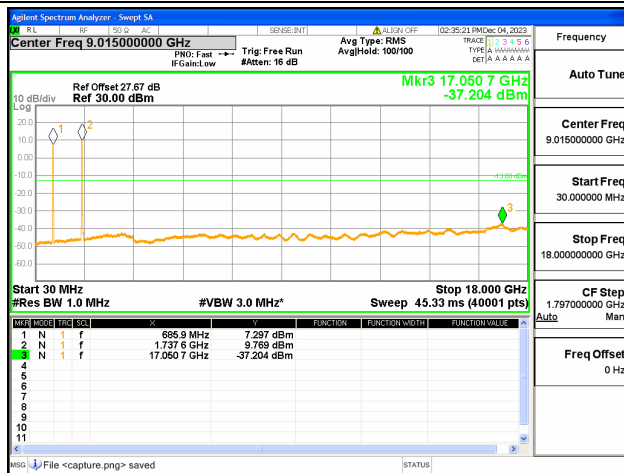
4A-71A / 20+20MHz / QPSK / Mid+Low CH /
1@0-1@994A-71A / 20+20MHz / QPSK / Mid+Low CH /
1@99-1@04A-71A / 20+20MHz / QPSK / Mid+Low CH /
100@0-100@04A-71A / 20+20MHz / QPSK / High+Mid CH /
1@0-1@994A-71A / 20+20MHz / QPSK / High+Mid CH /
1@99-1@04A-71A / 20+20MHz / QPSK / High+Mid CH /
100@0-100@0



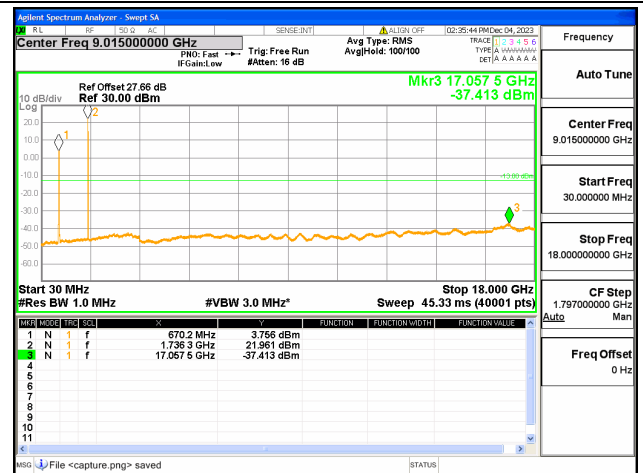
4A-71A / 20+20MHz / QPSK / High+High CH /
1@0-1@99



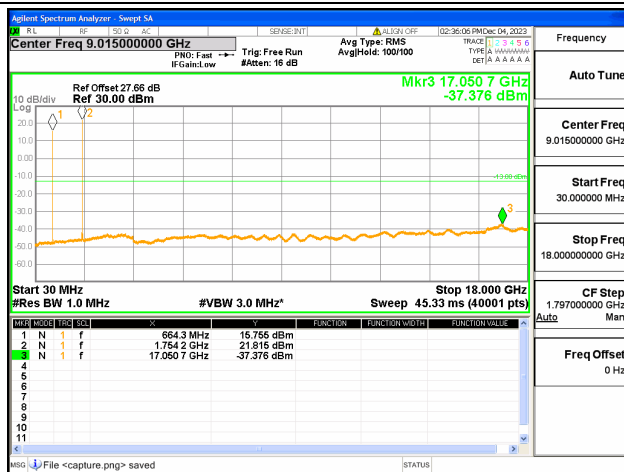
4A-71A / 20+20MHz / QPSK / High+High CH /
1@99-1@0



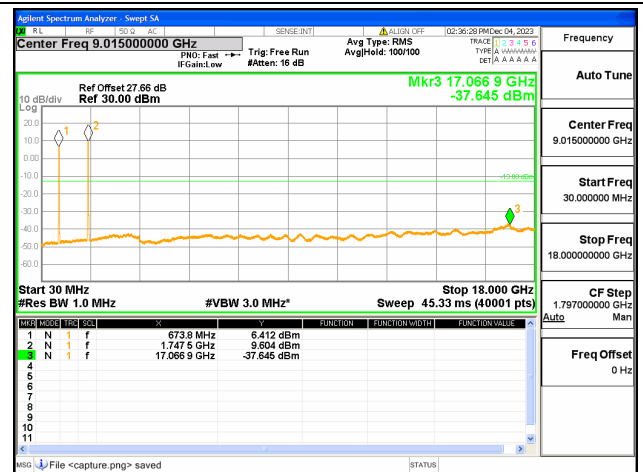
4A-71A / 20+20MHz / QPSK / High+High CH /
100@0-100@0



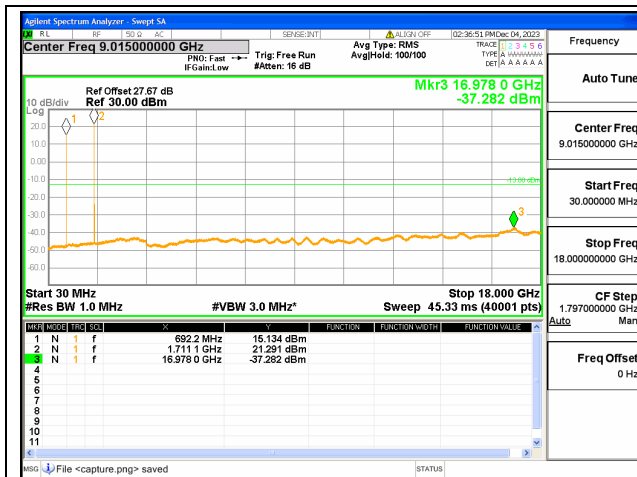
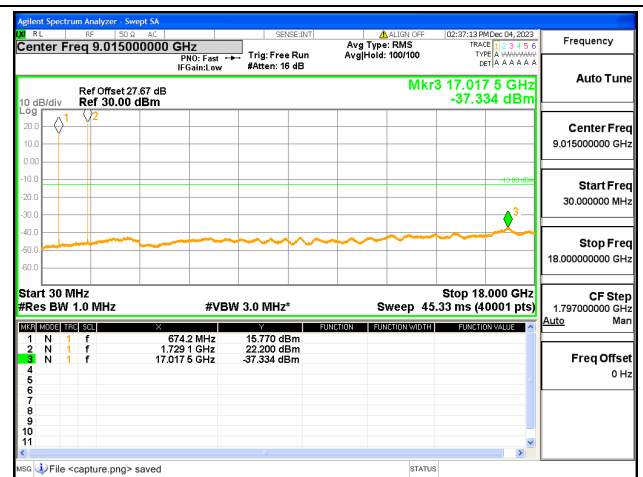
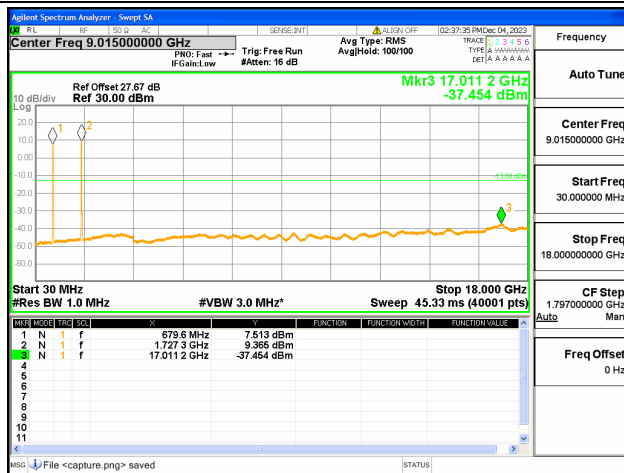
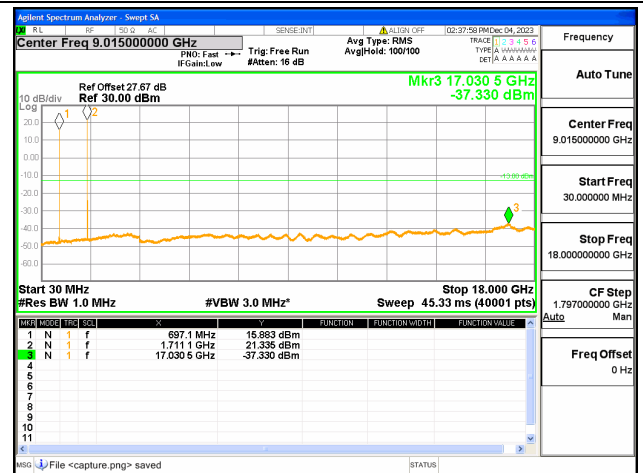
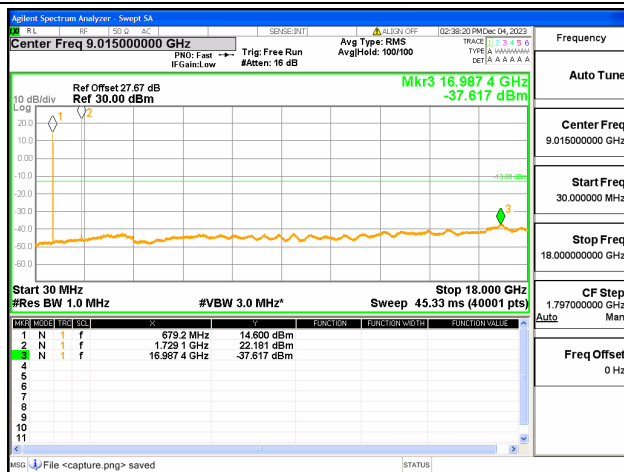
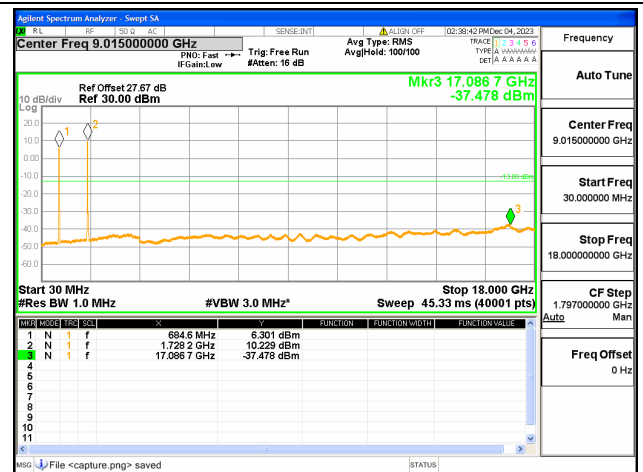
4A-71A / 20+20MHz / QPSK / High+Low CH /
1@0-1@99

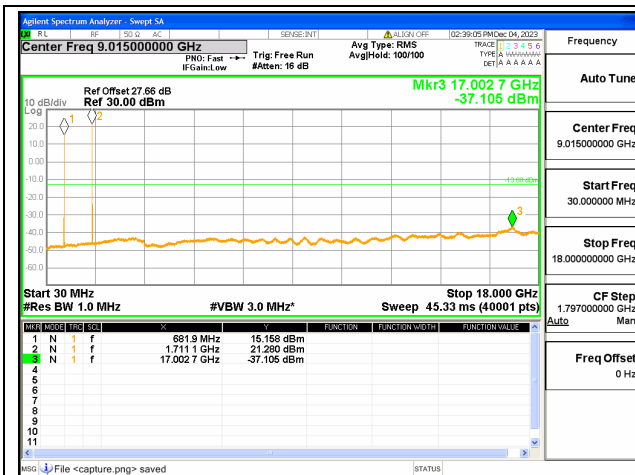


4A-71A / 20+20MHz / QPSK / High+Low CH /
1@99-1@0



4A-71A / 20+20MHz / QPSK / High+Low CH /
100@0-100@0

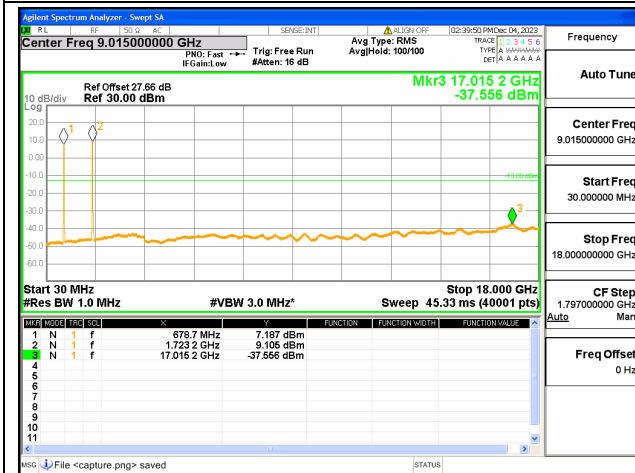
4A-71A / 20+20MHz / QPSK / Low+Mid CH /
1@0-1@994A-71A / 20+20MHz / QPSK / Low+Mid CH /
1@99-1@04A-71A / 20+20MHz / QPSK / Low+Mid CH /
100@0-100@04A-71A / 20+20MHz / QPSK / Low+High CH /
1@0-1@994A-71A / 20+20MHz / QPSK / Low+High CH /
1@99-1@04A-71A / 20+20MHz / QPSK / Low+High CH /
100@0-100@0



4A-71A / 20+20MHz / QPSK / Low+Low CH /
1@0-1@99

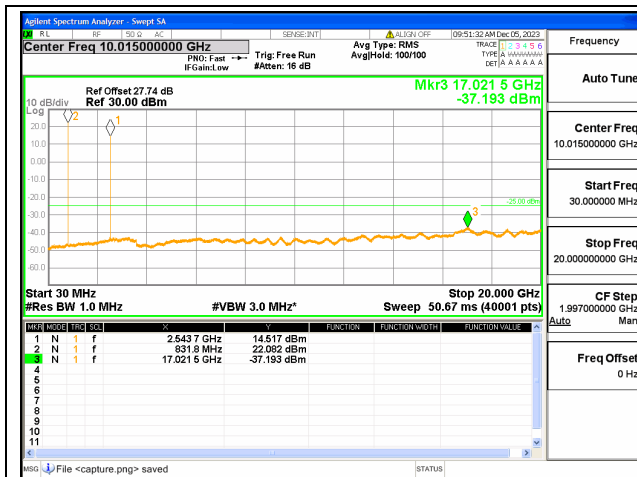
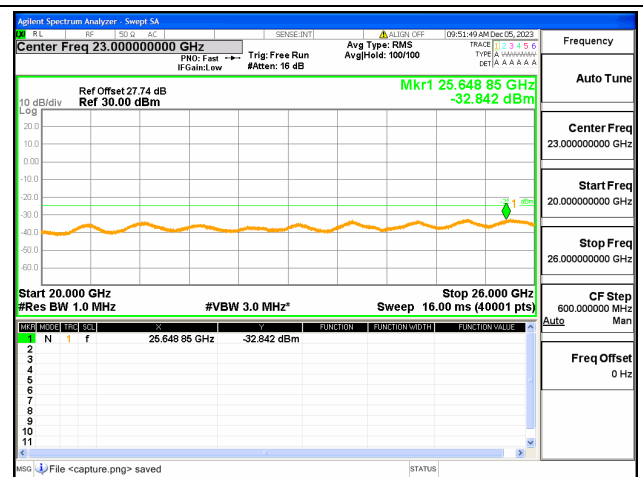
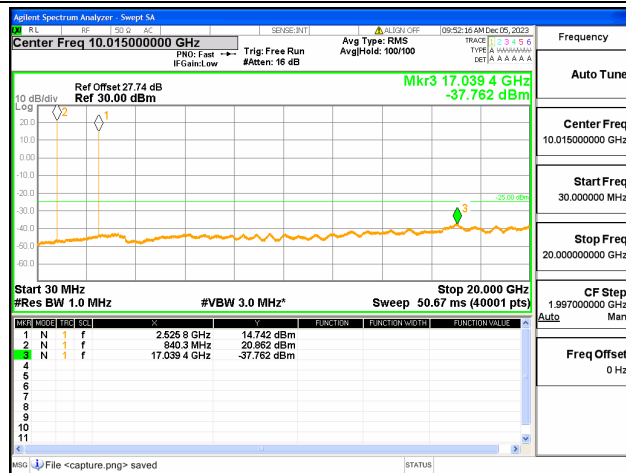
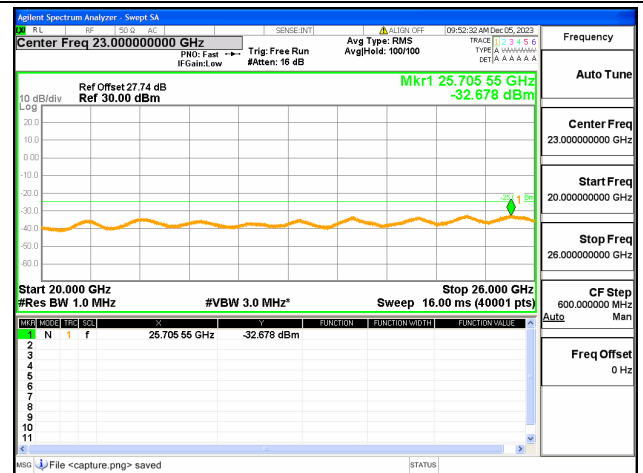
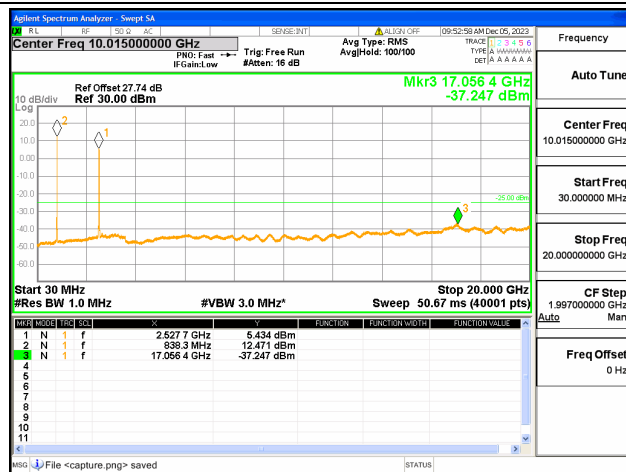
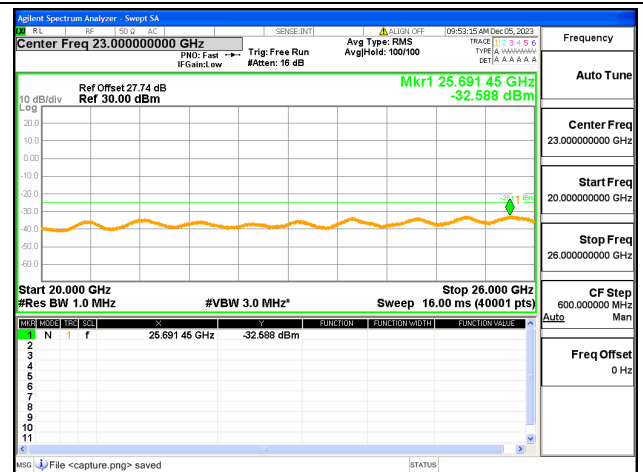


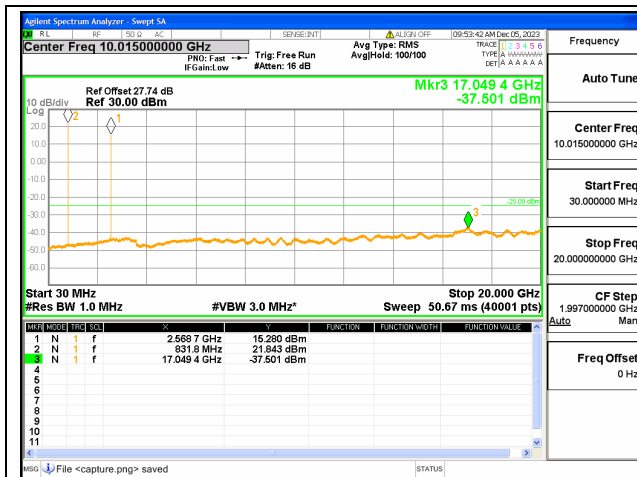
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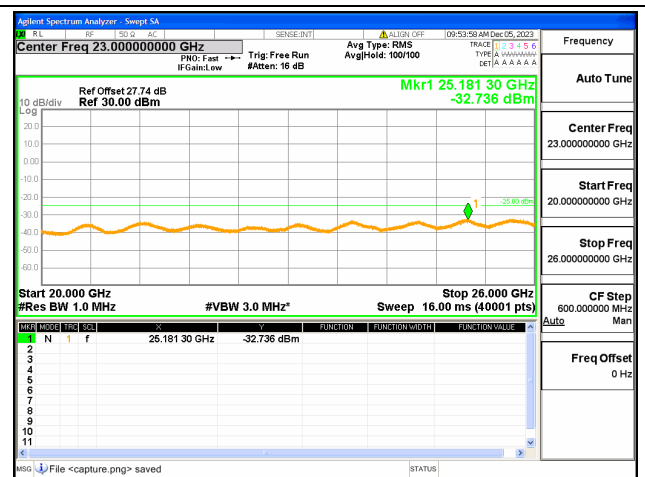
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100@0-100@0

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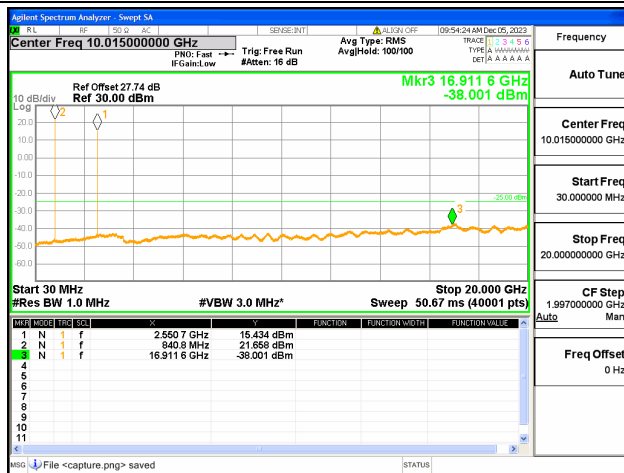
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CH / 1@0-1@995A-7A-30M-20G / 10+20MHz / QPSK / Mid+Mid
CH / 1@49-1@05A-7A-20G-26G / 10+20MHz / QPSK / Mid+Mid
CH / 1@49-1@05A-7A-30M-20G / 10+20MHz / QPSK / Mid+Mid
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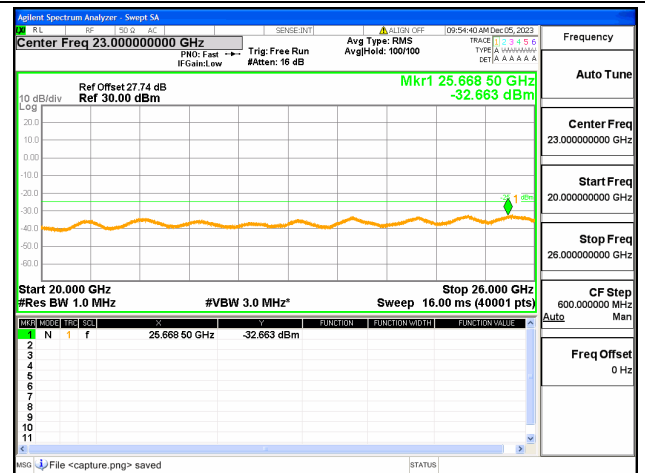
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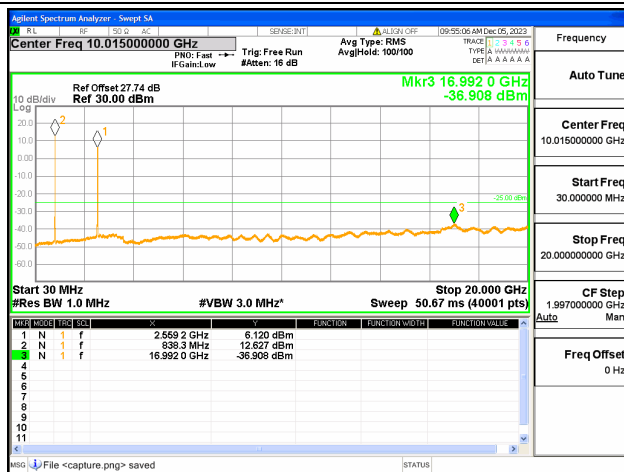
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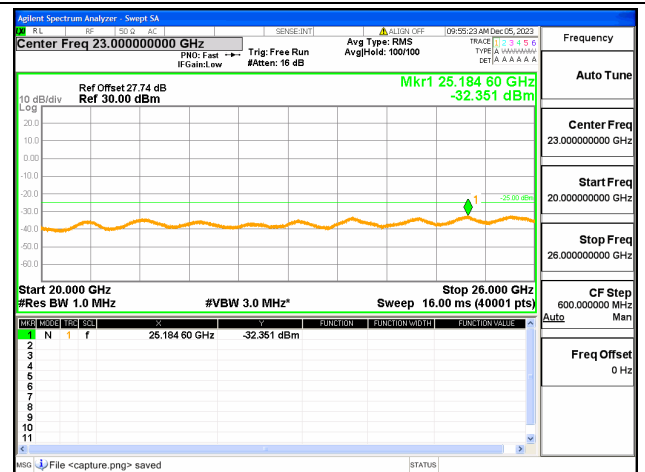
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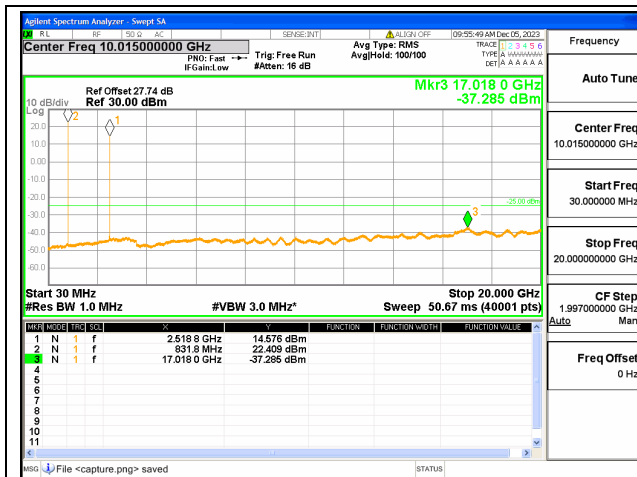
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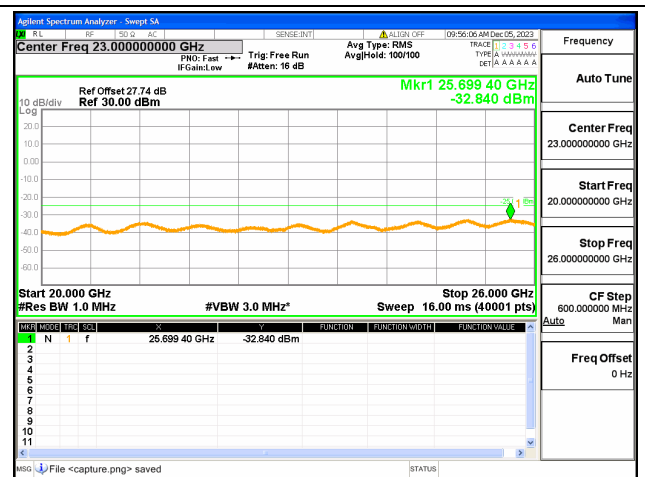
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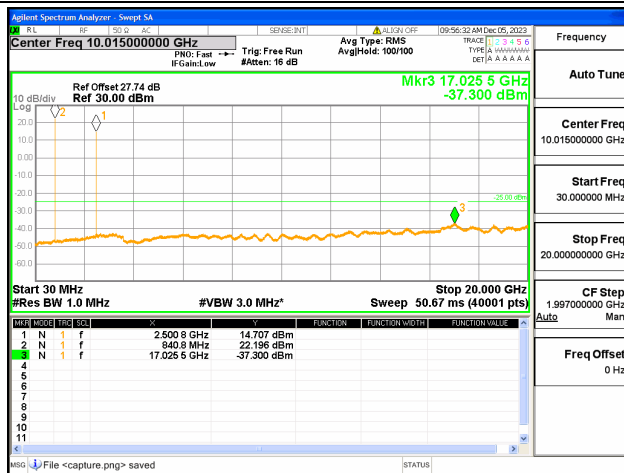
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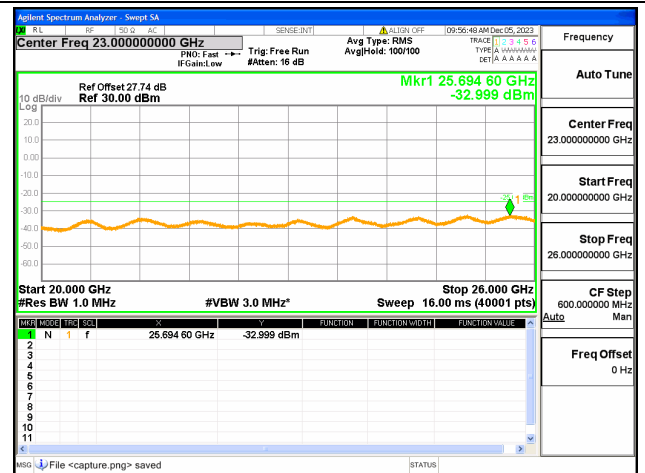
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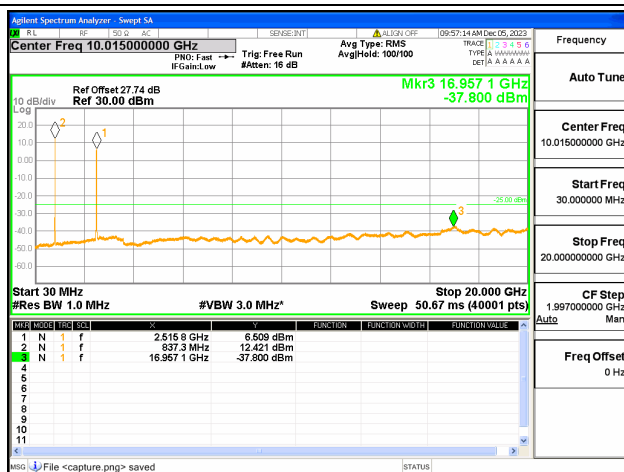
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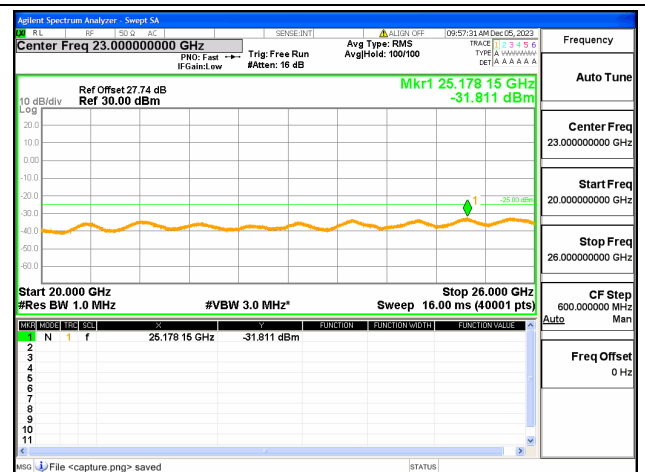
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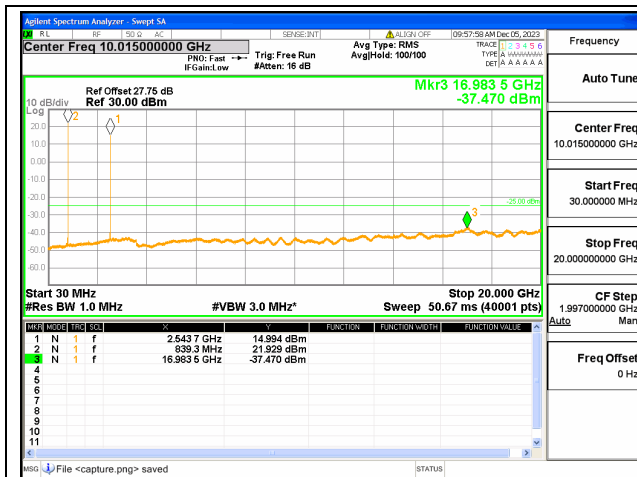
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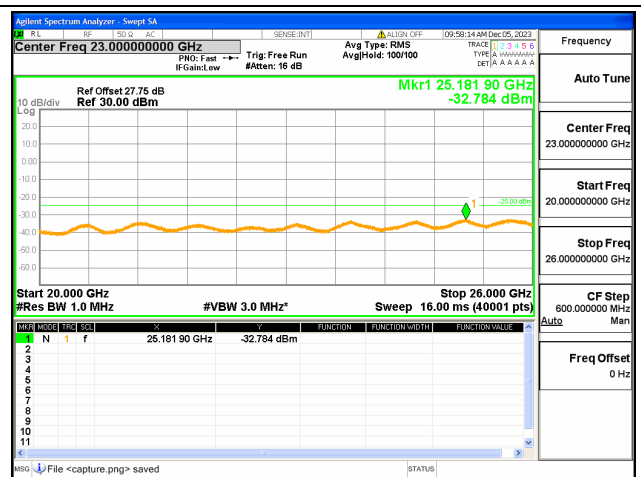
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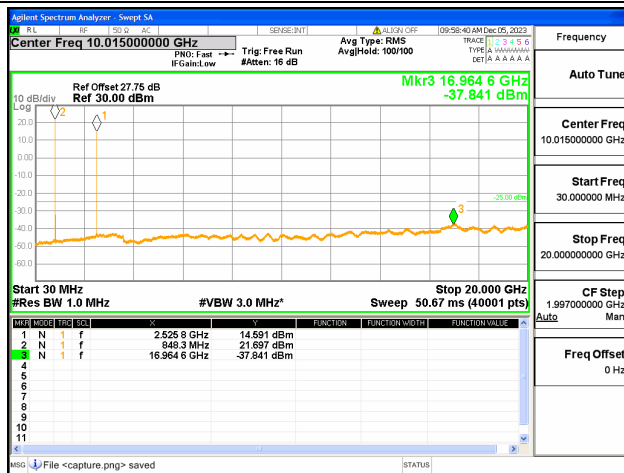
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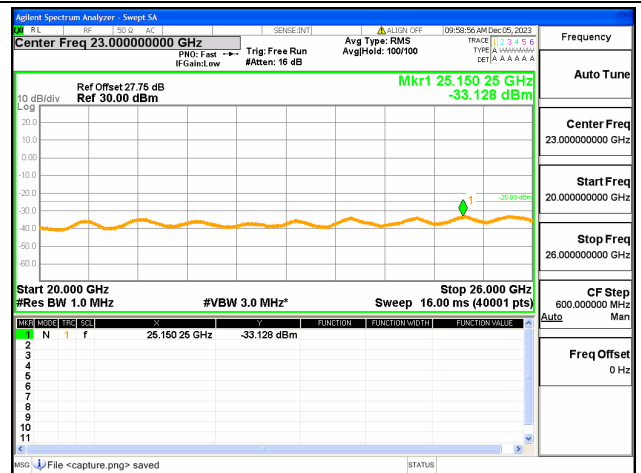
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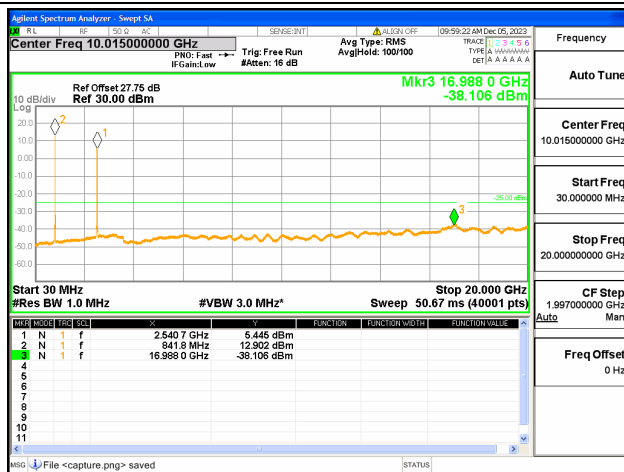
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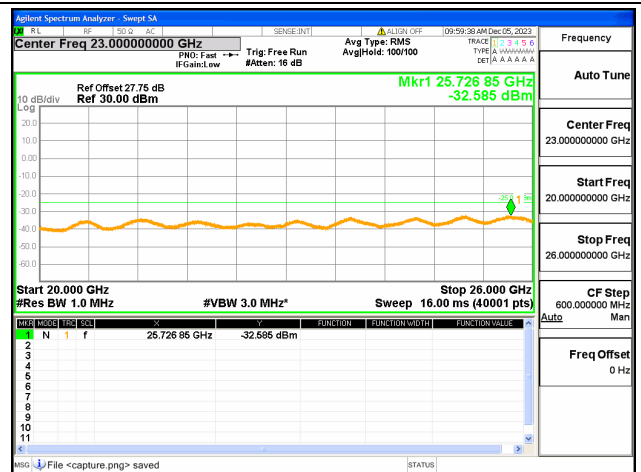
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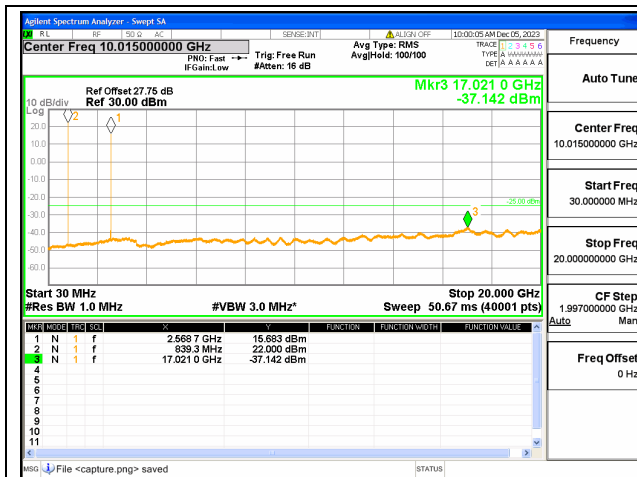
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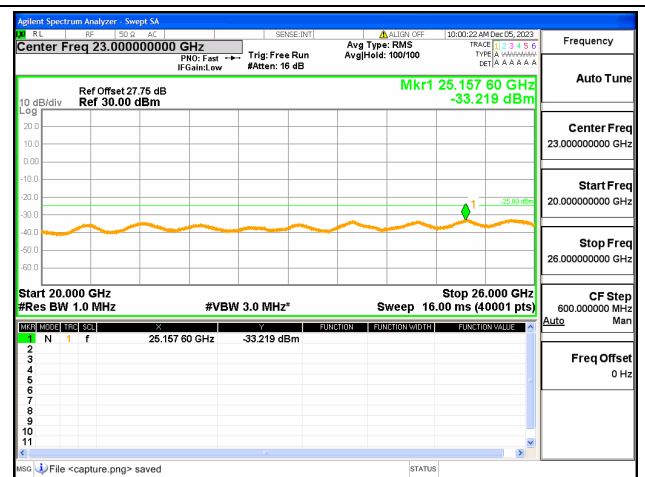
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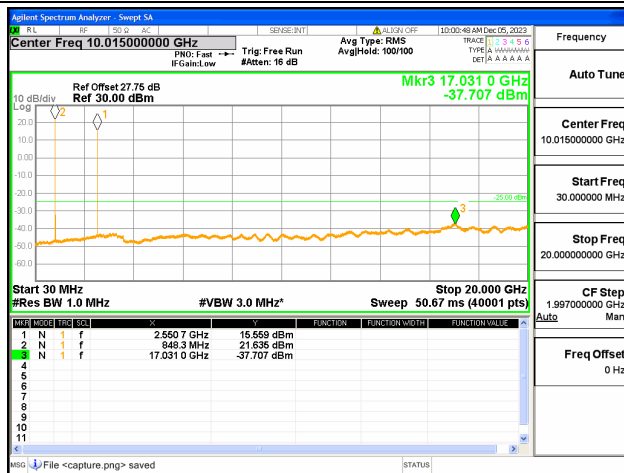
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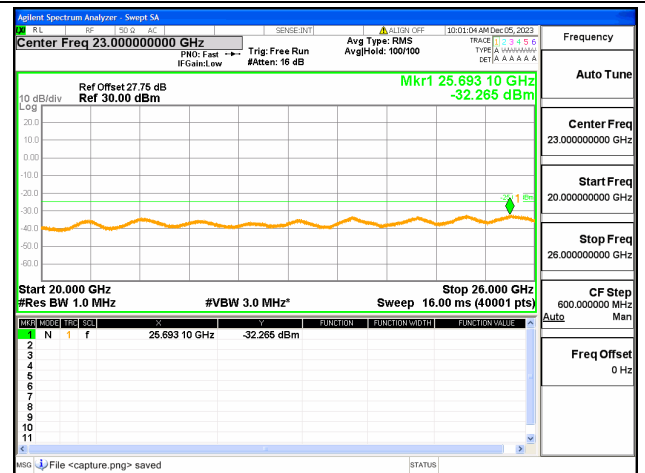
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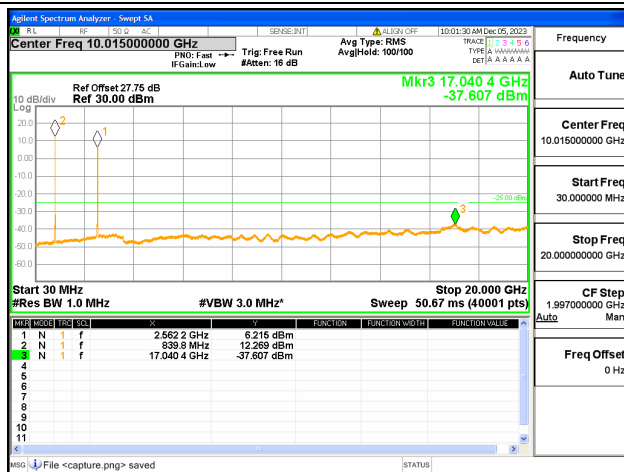
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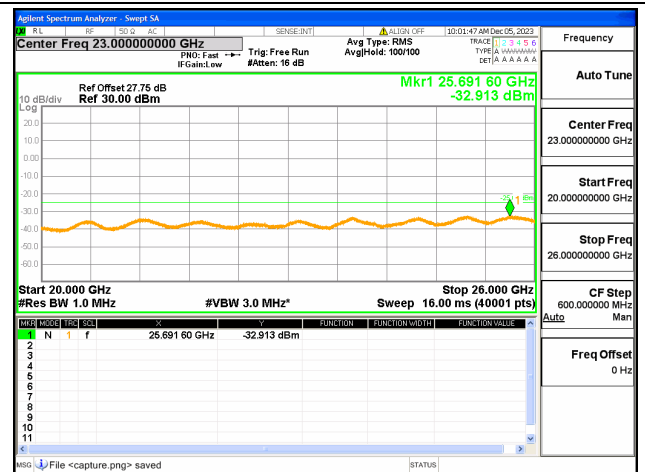
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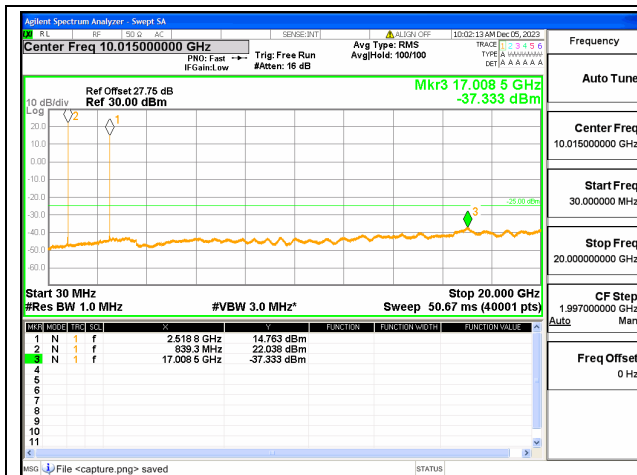
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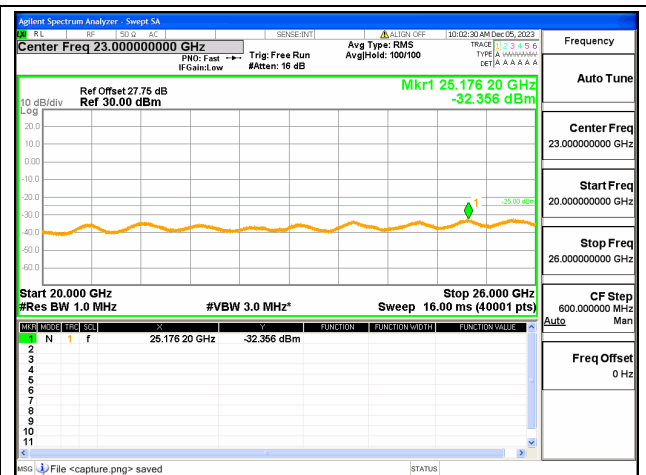
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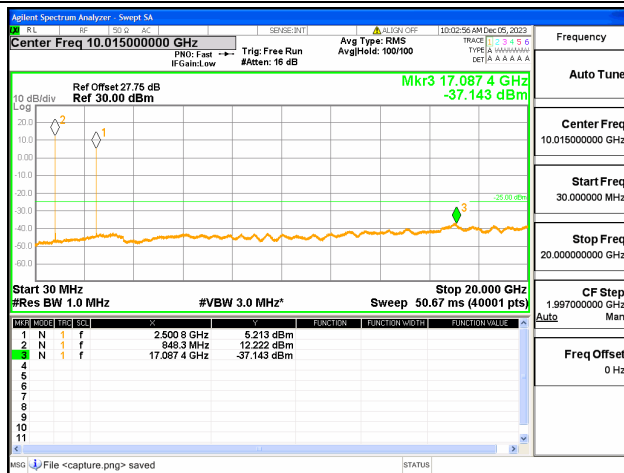
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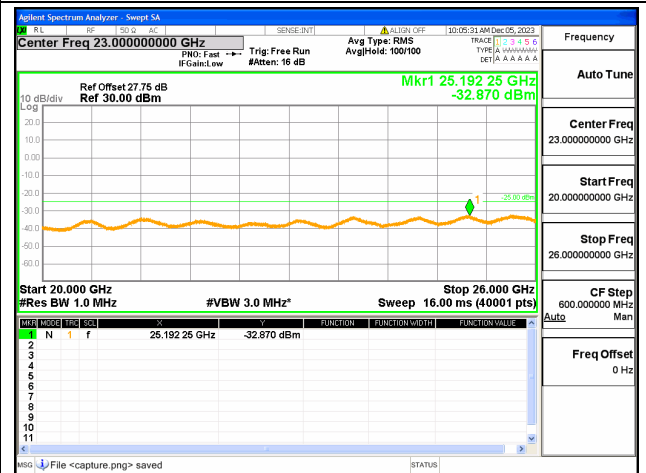
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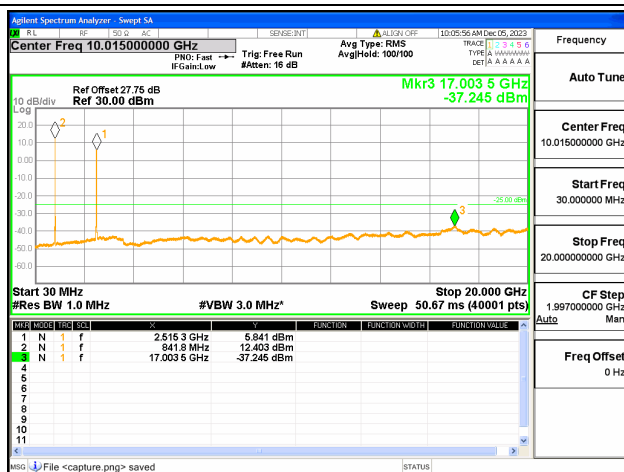
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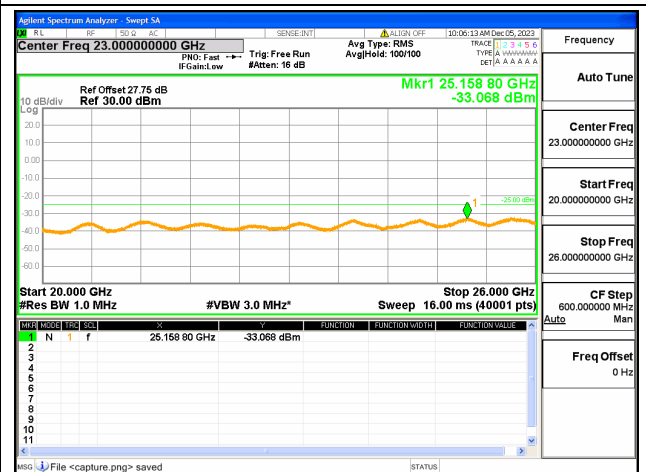
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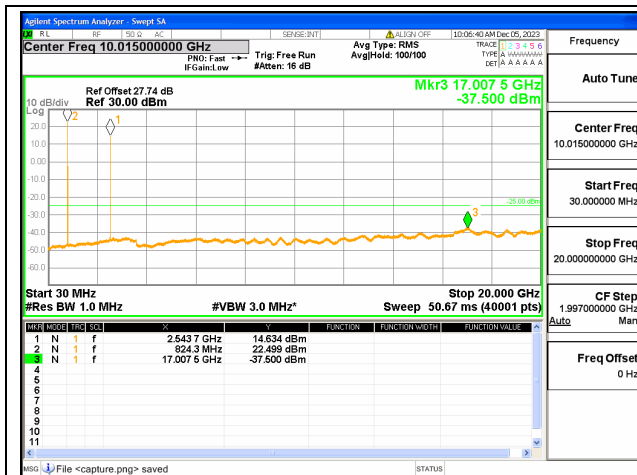
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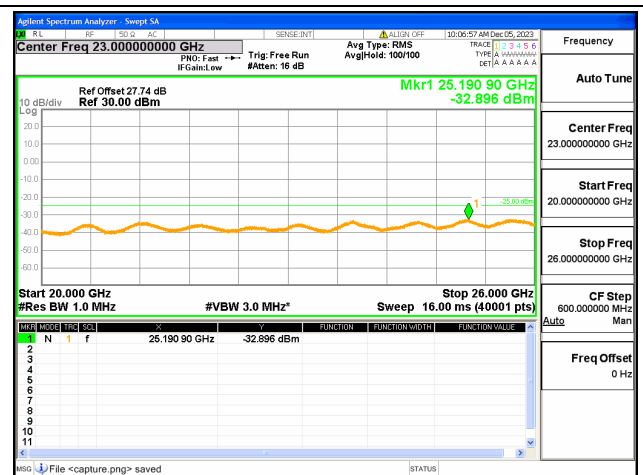
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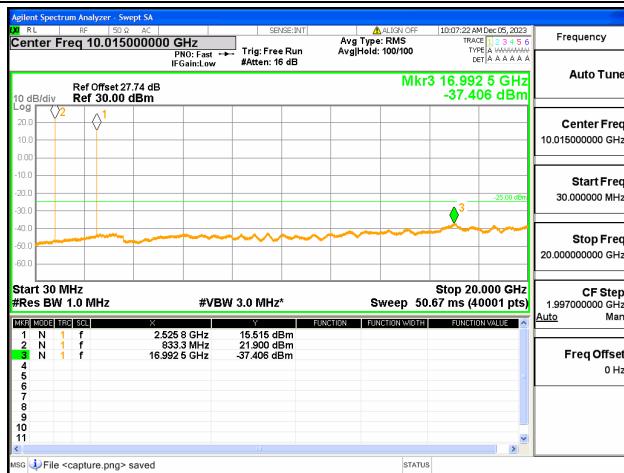
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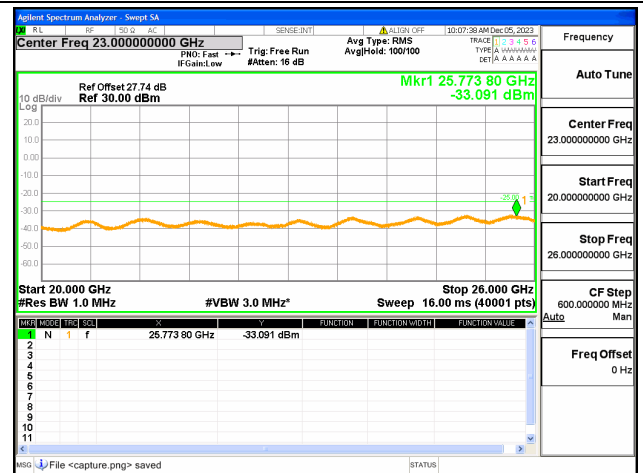
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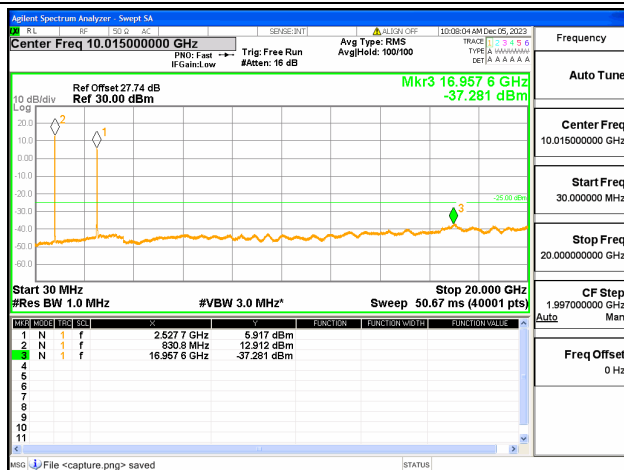
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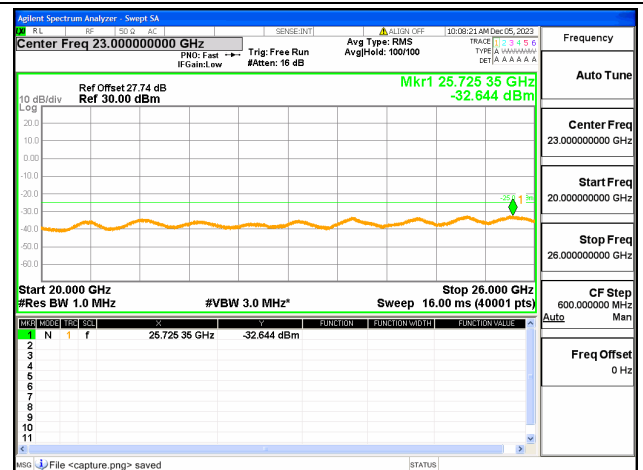
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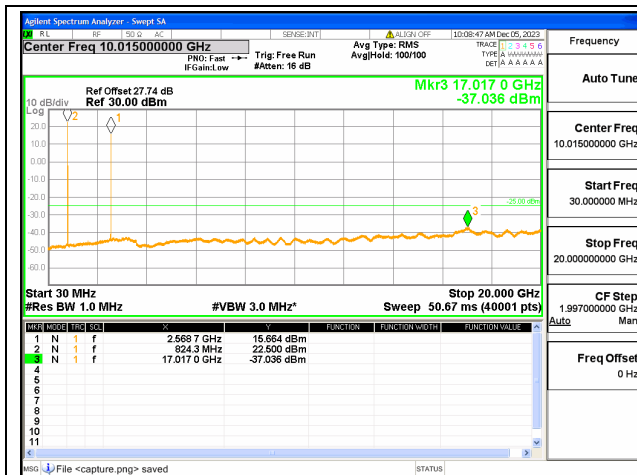
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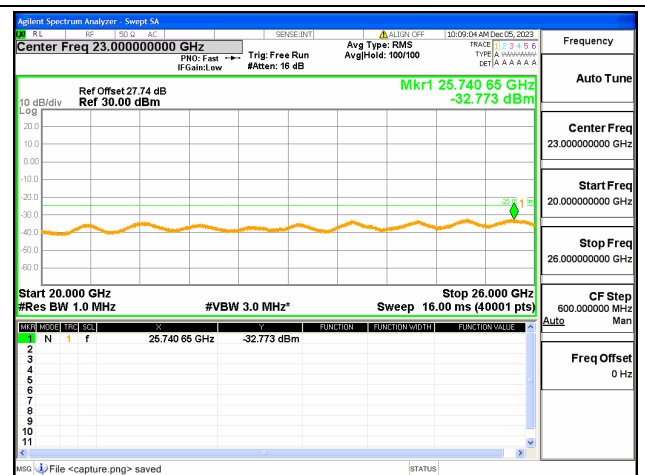
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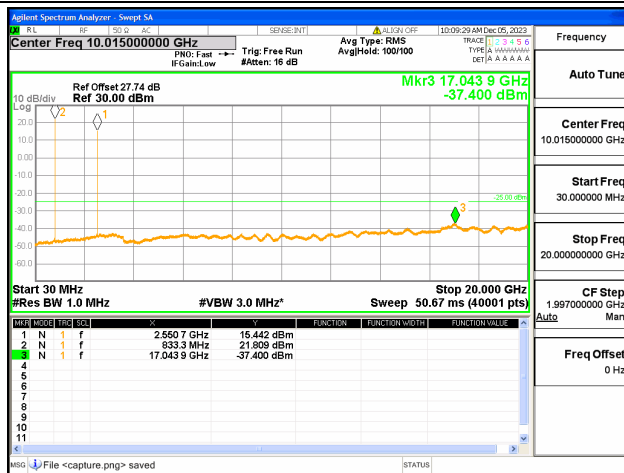
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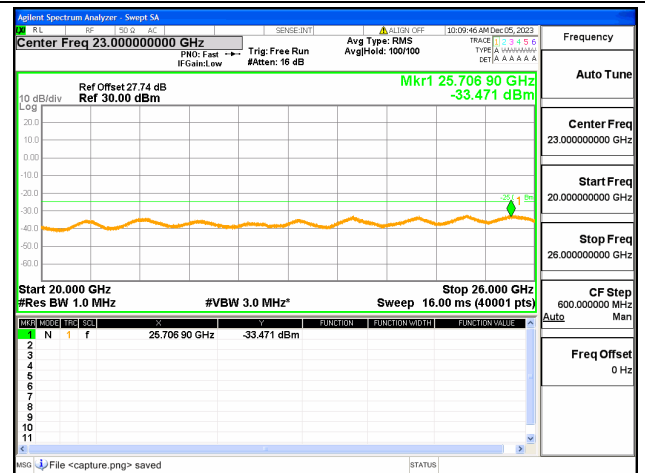
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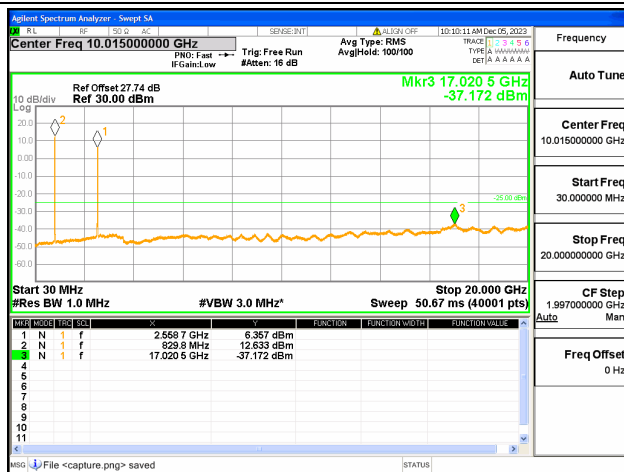
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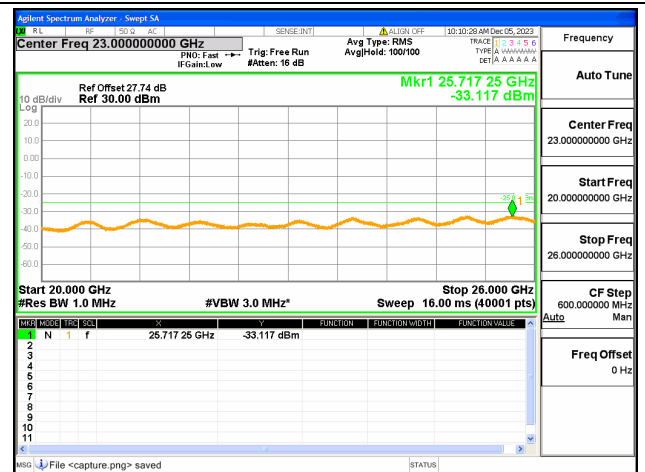
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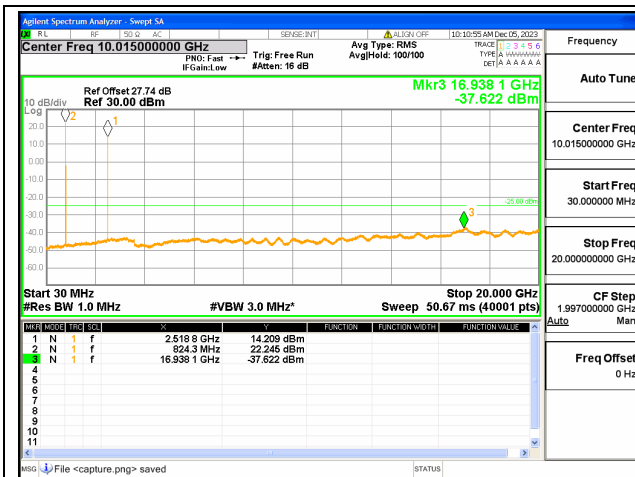
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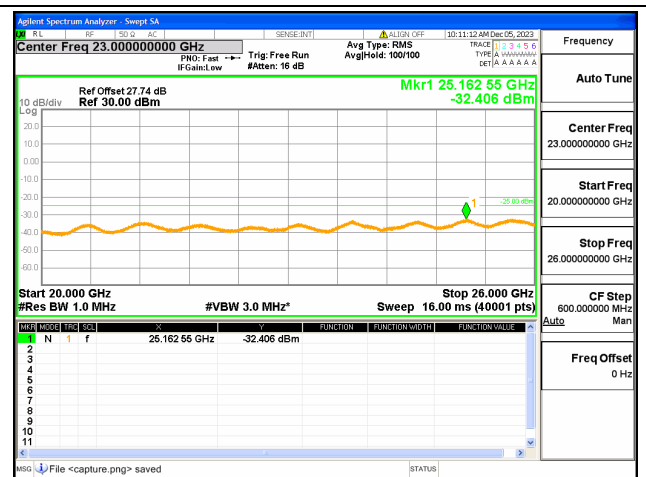
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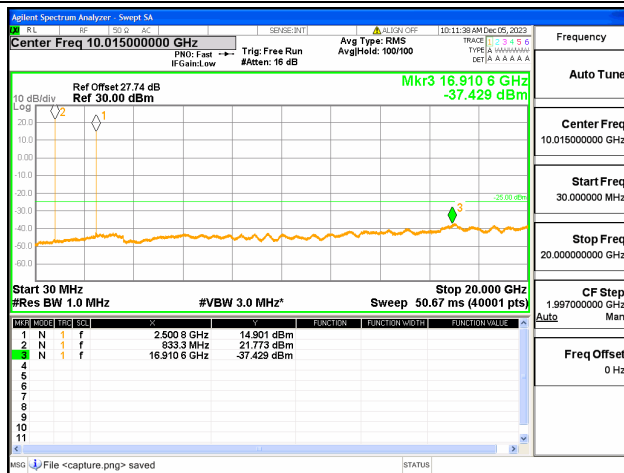
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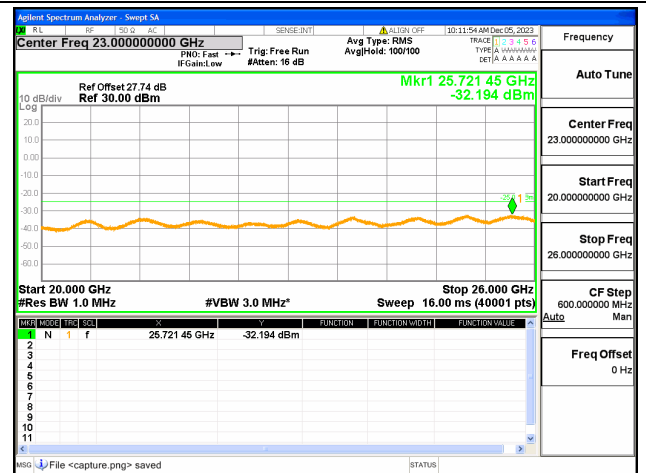
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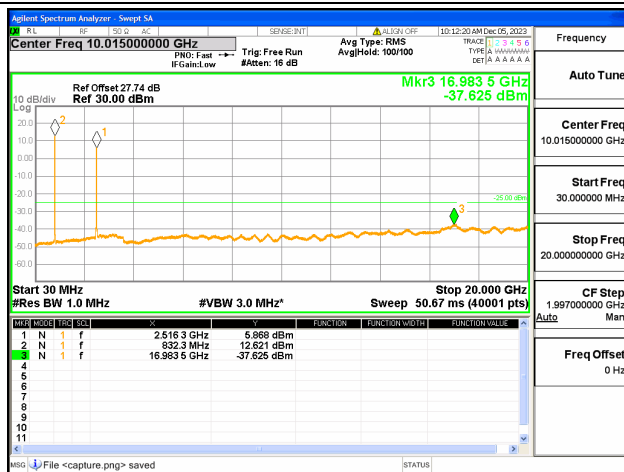
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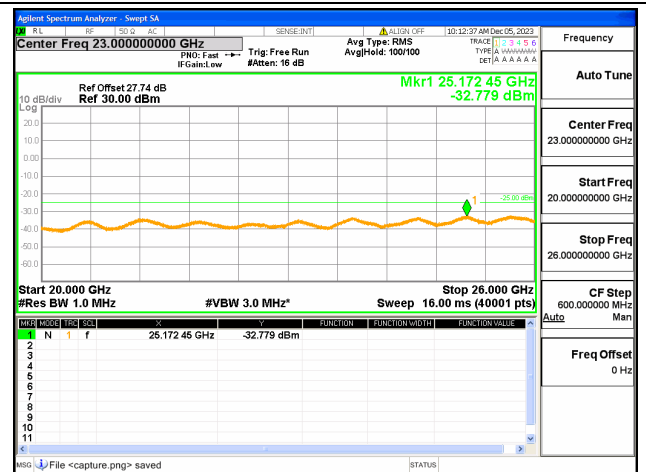
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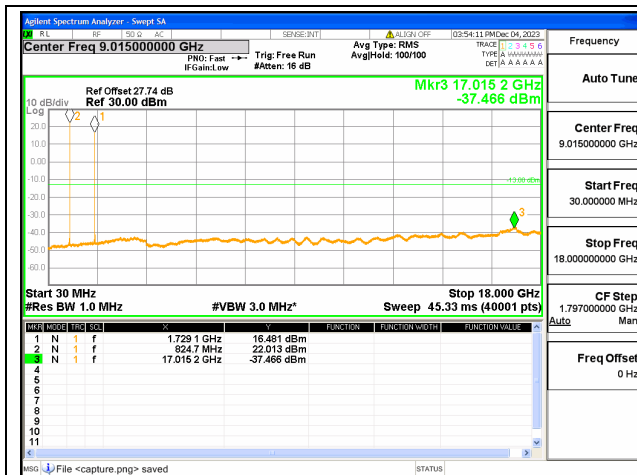
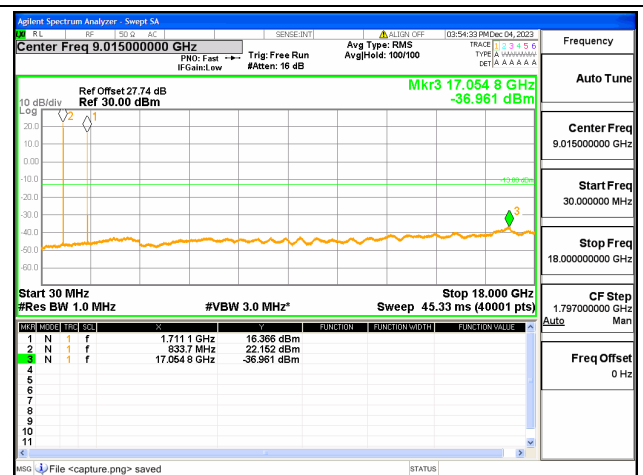
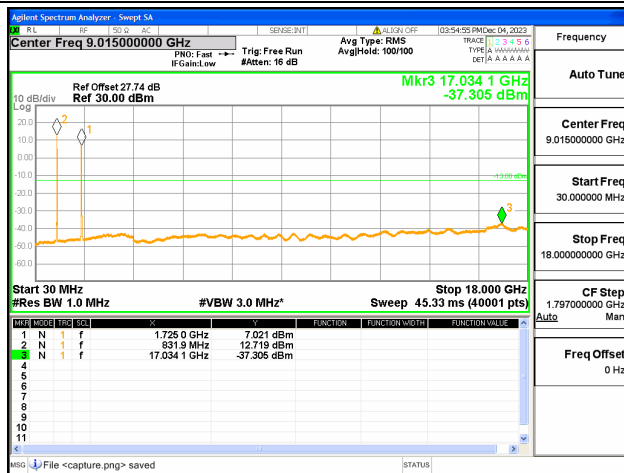
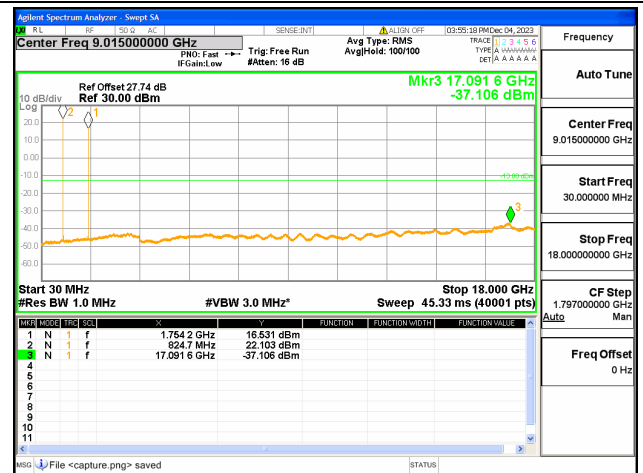
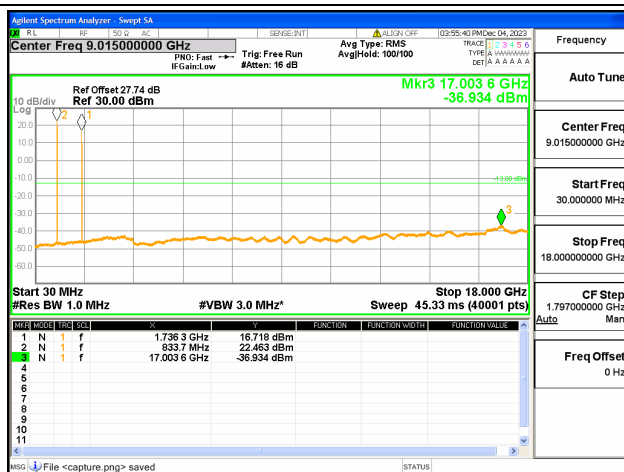
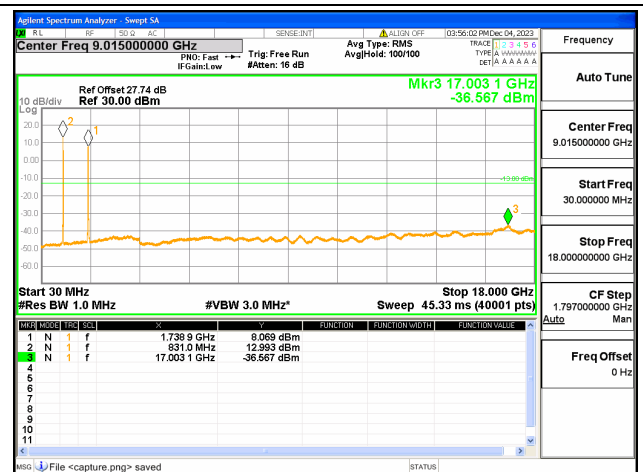
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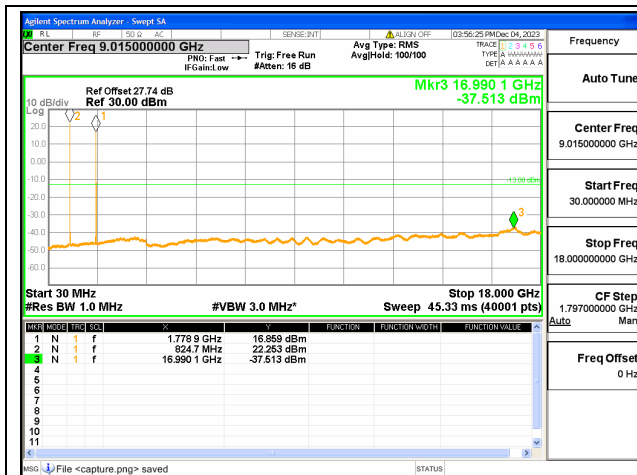


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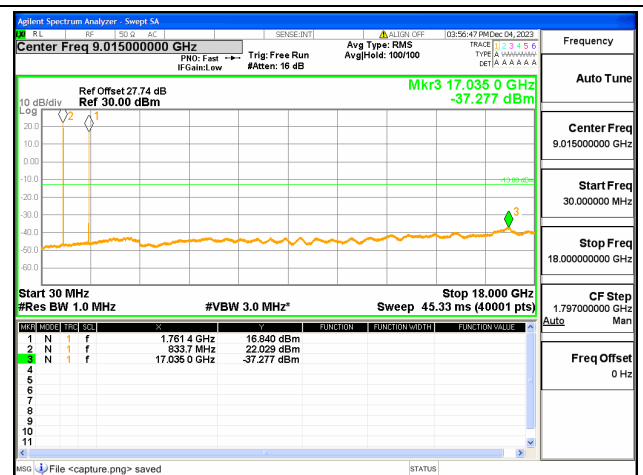


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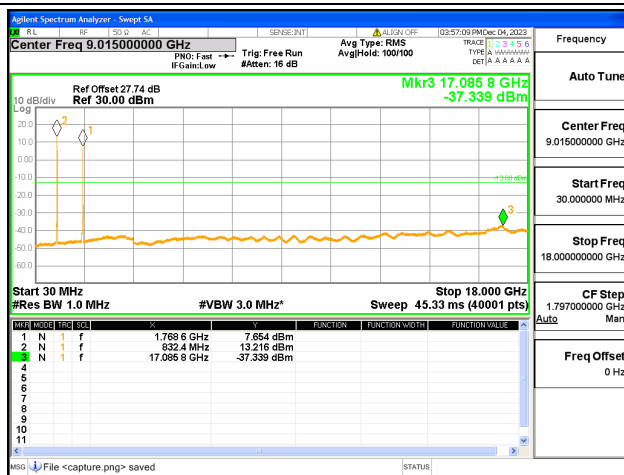
5A-66A / 10+20MHz / QPSK / Low+Low CH /
1@0-1@995A-66A / 10+20MHz / QPSK / Low+Low CH /
1@49-1@05A-66A / 10+20MHz / QPSK / Low+Low CH /
50@0-100@05A-66A / 10+20MHz / QPSK / Low+Mid CH /
1@0-1@995A-66A / 10+20MHz / QPSK / Low+Mid CH /
1@49-1@05A-66A / 10+20MHz / QPSK / Low+Mid CH /
50@0-100@0



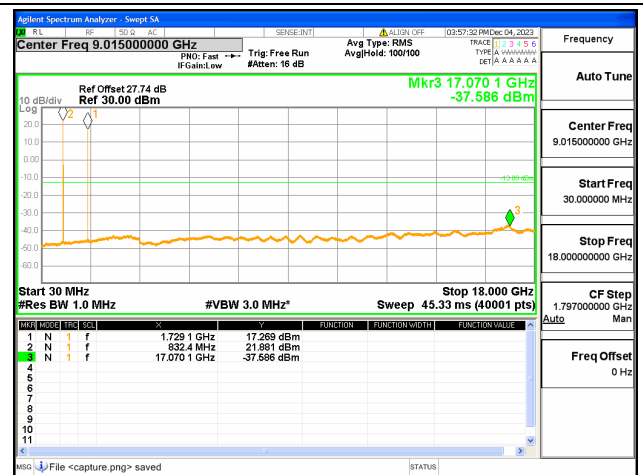
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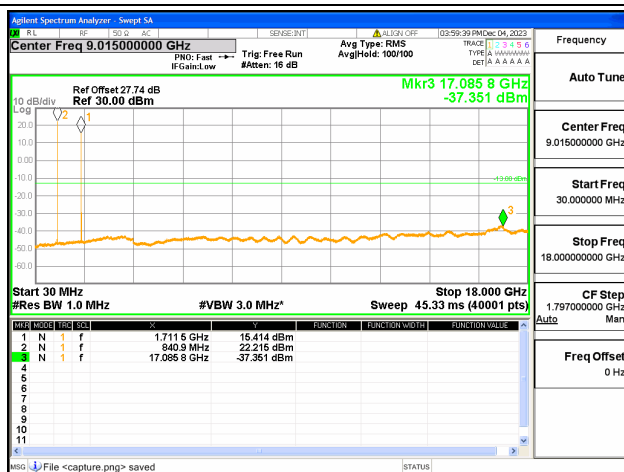
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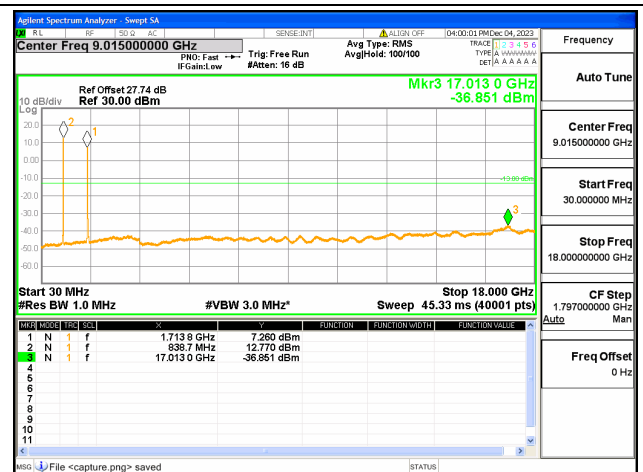
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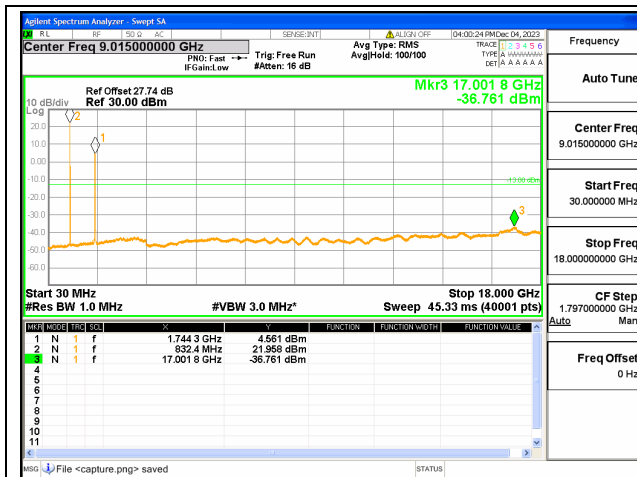
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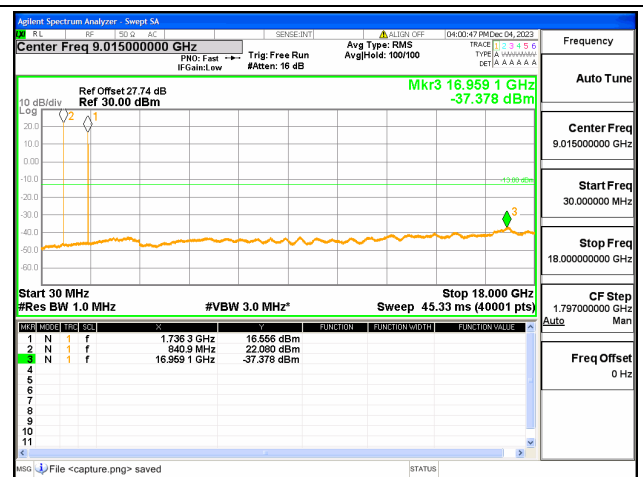
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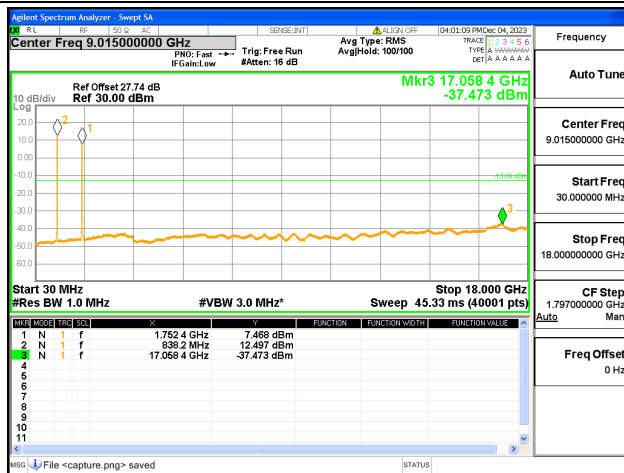
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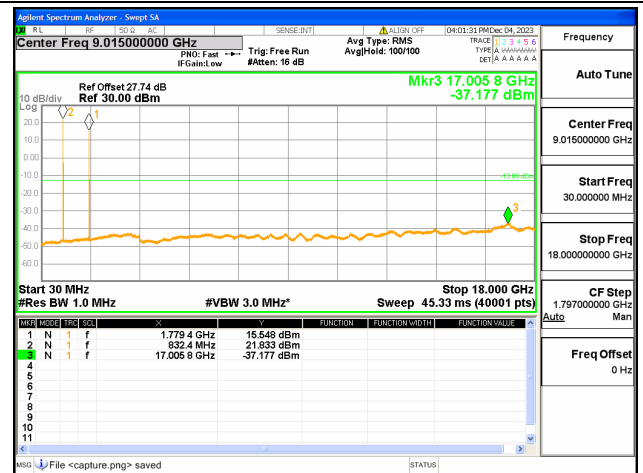
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1@0-1@99



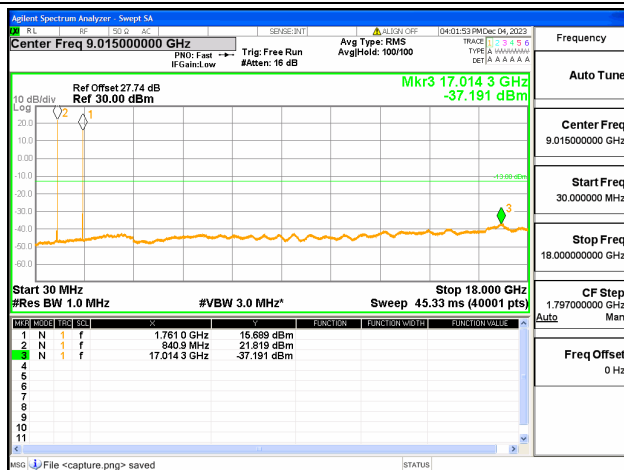
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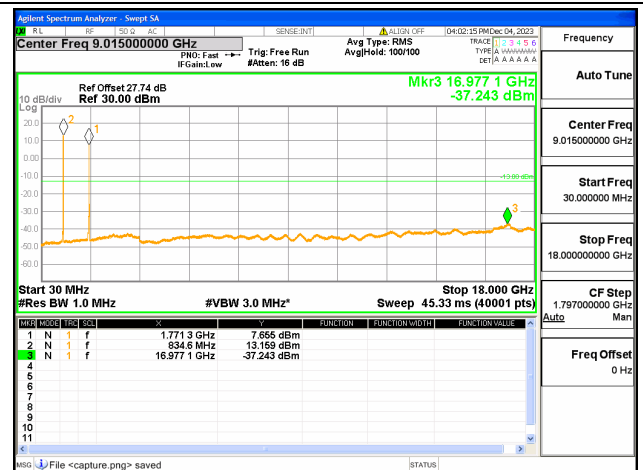
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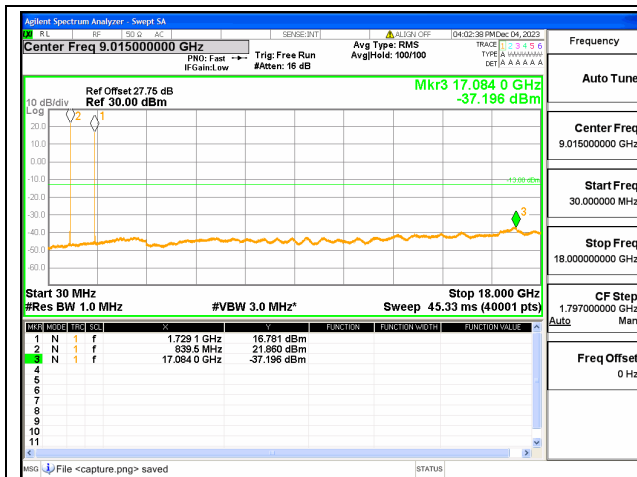
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1@0-1@99



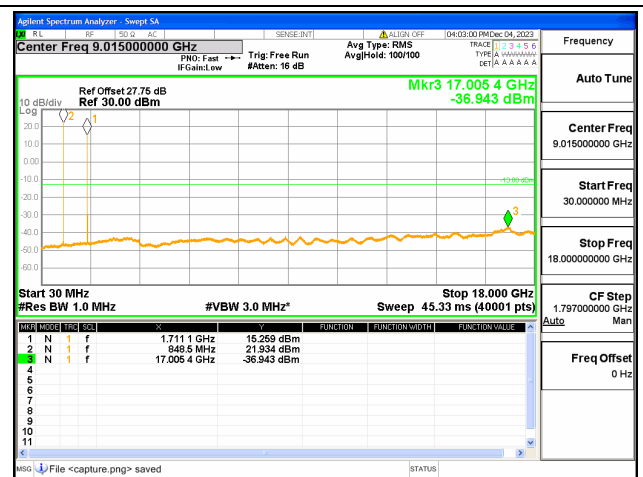
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1@49-1@0



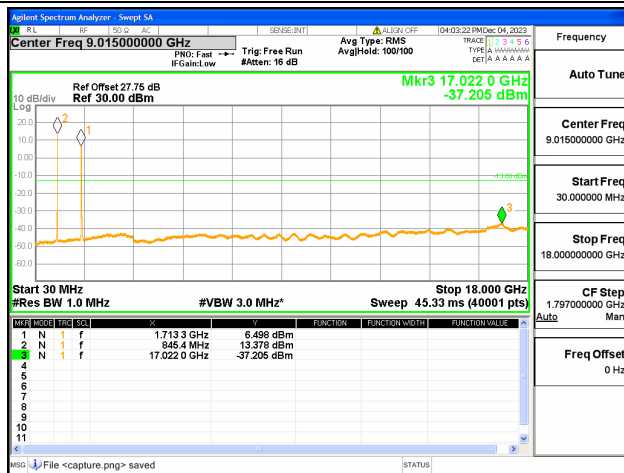
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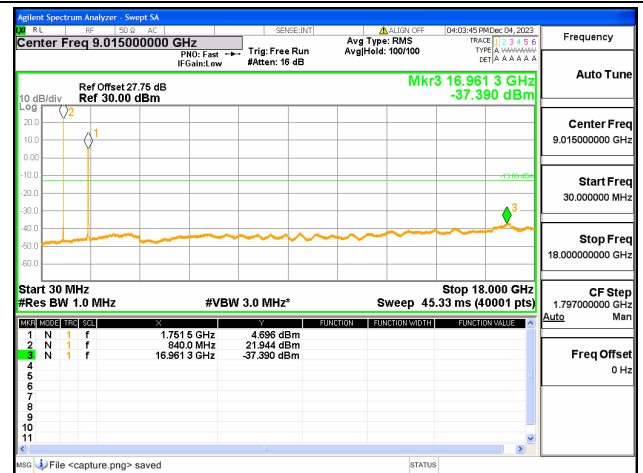
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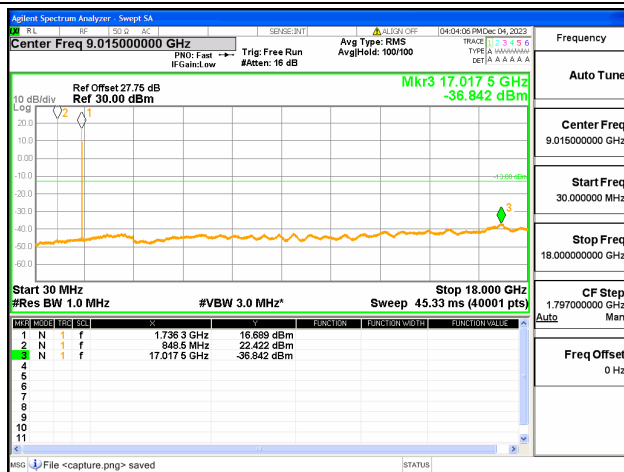
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1@49-1@0



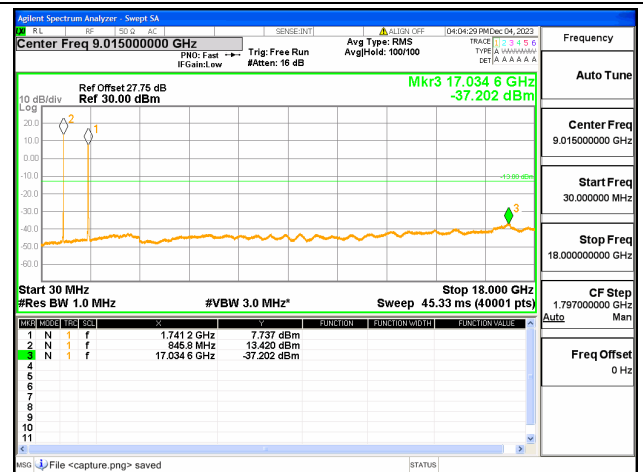
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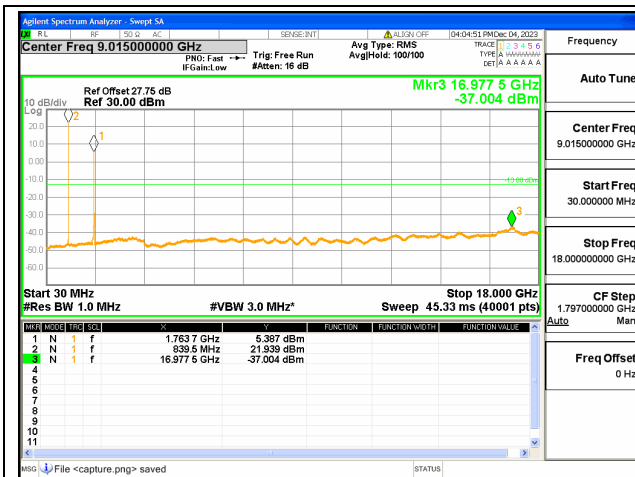
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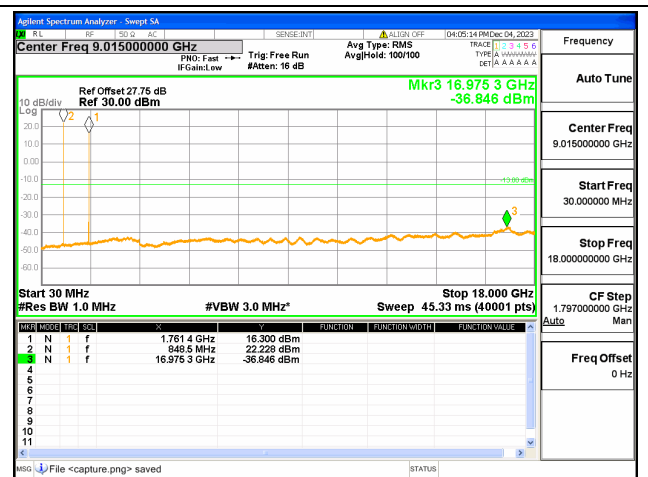
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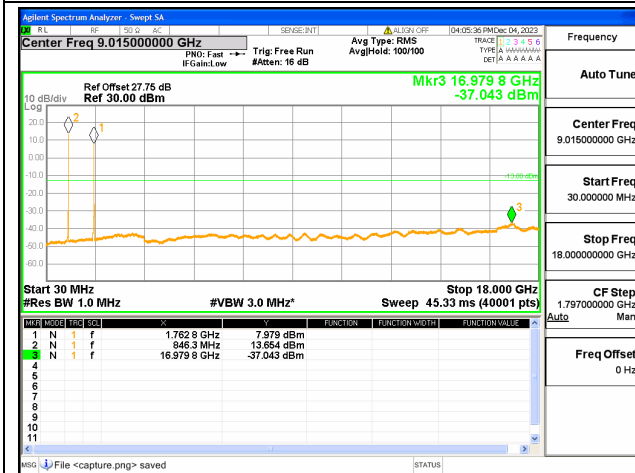
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5A-66A / 10+20MHz / QPSK / High+High CH /
1@0-1@99

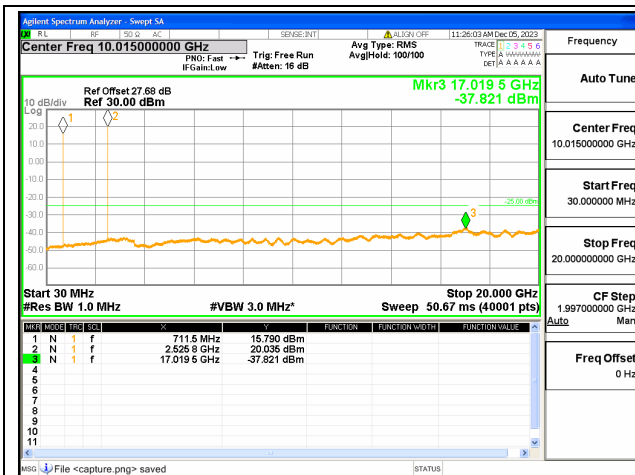


5A-66A / 10+20MHz / QPSK / High+High CH /
1@49-1@0

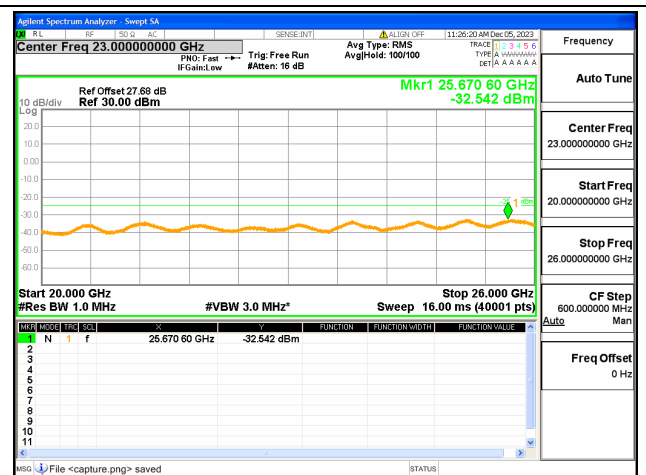


5A-66A / 10+20MHz / QPSK / High+High CH /
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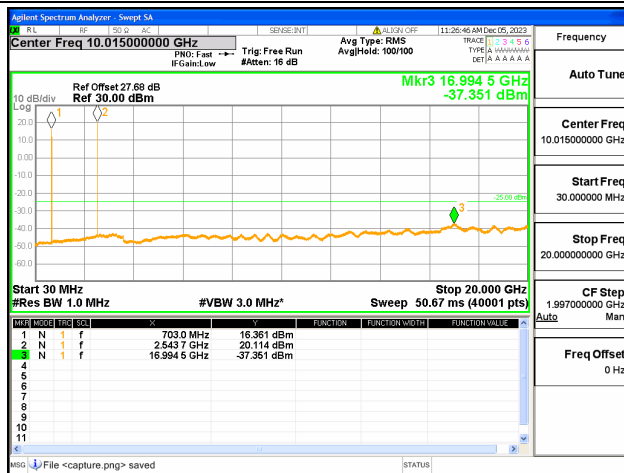
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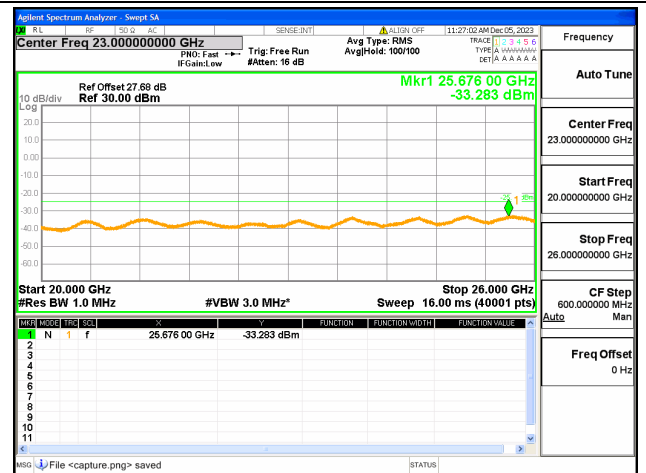
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Mid+Mid CH / 1@0-1@49



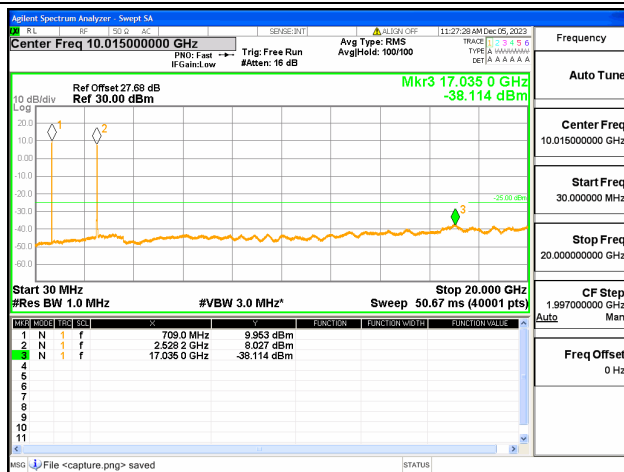
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Mid+Mid CH / 1@0-1@49



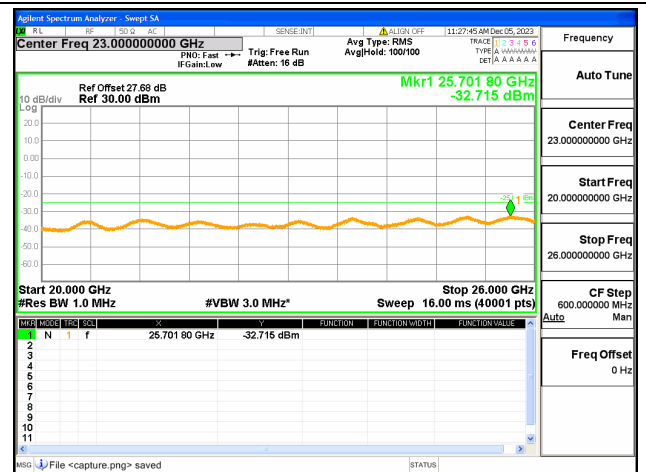
7A-12A-30M-20G / 20+10MHz / QPSK /
Mid+Mid CH / 1@99-1@0



7A-12A-20G-26G / 20+10MHz / QPSK /
Mid+Mid CH / 1@99-1@0



7A-12A-30M-20G / 20+10MHz / QPSK /
Mid+Mid CH / 100@0-50@0



7A-12A-20G-26G / 20+10MHz / QPSK /
Mid+Mid CH / 100@0-50@0