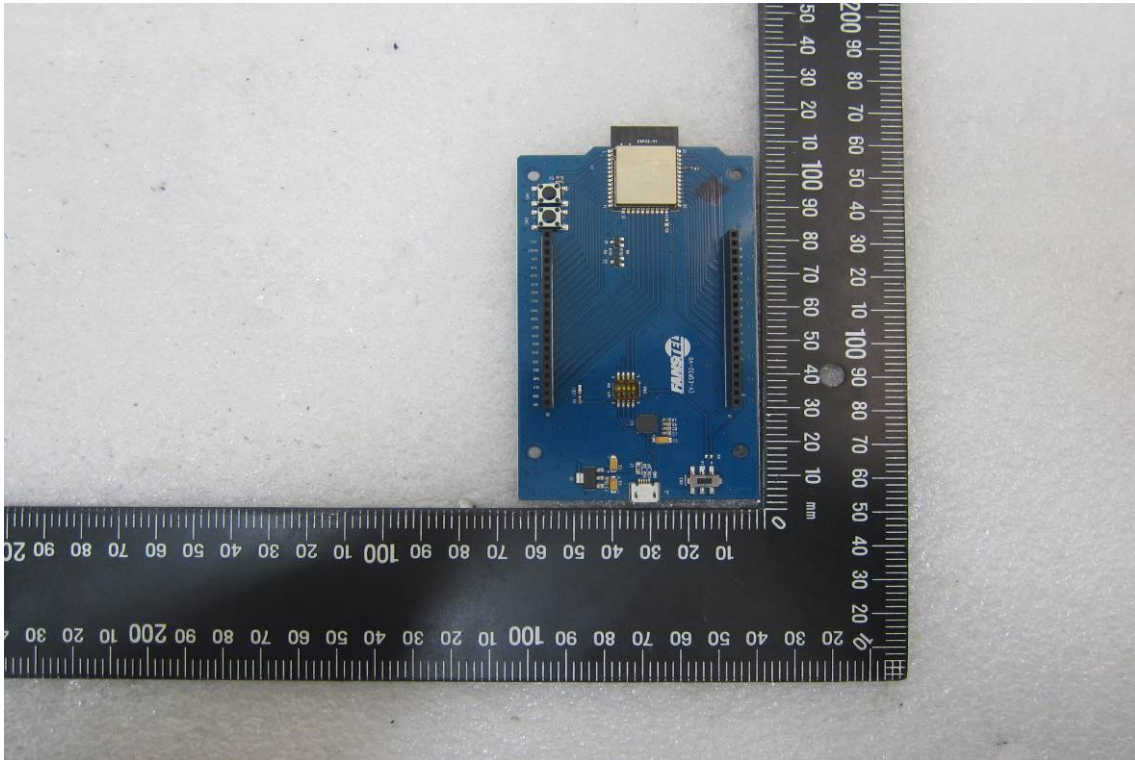


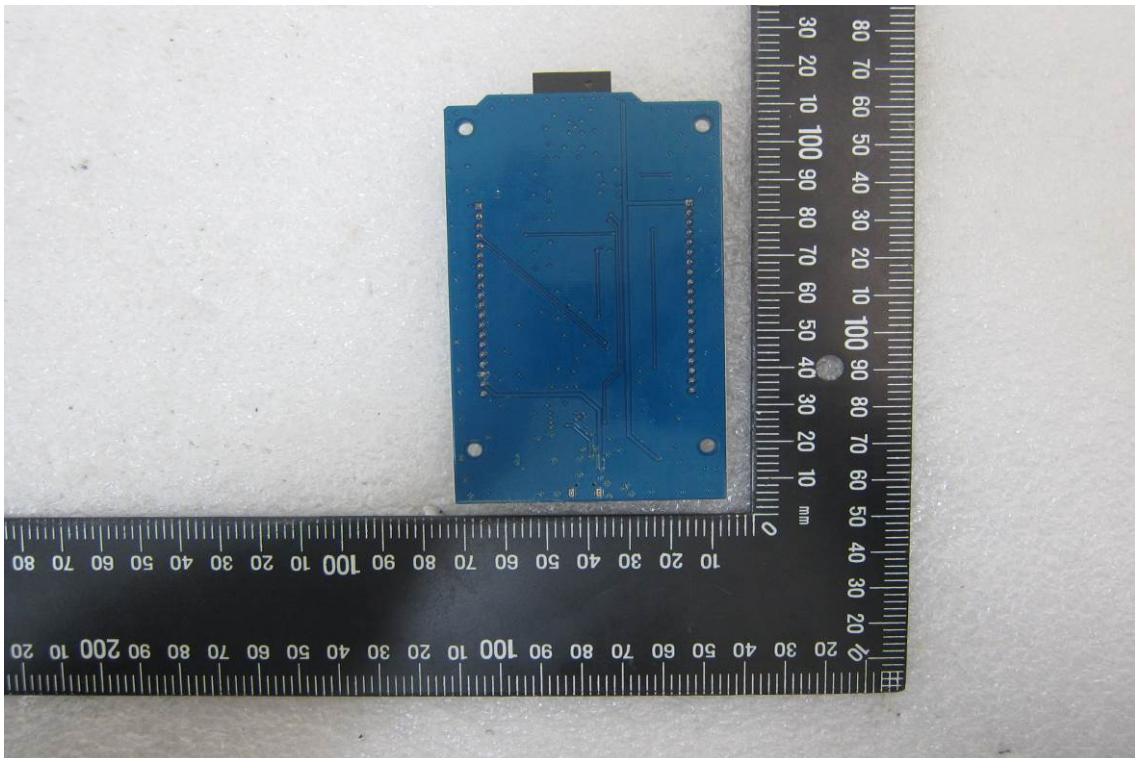
## **APPENDIX 2**

# **PHOTOGRAPHS OF EUT**

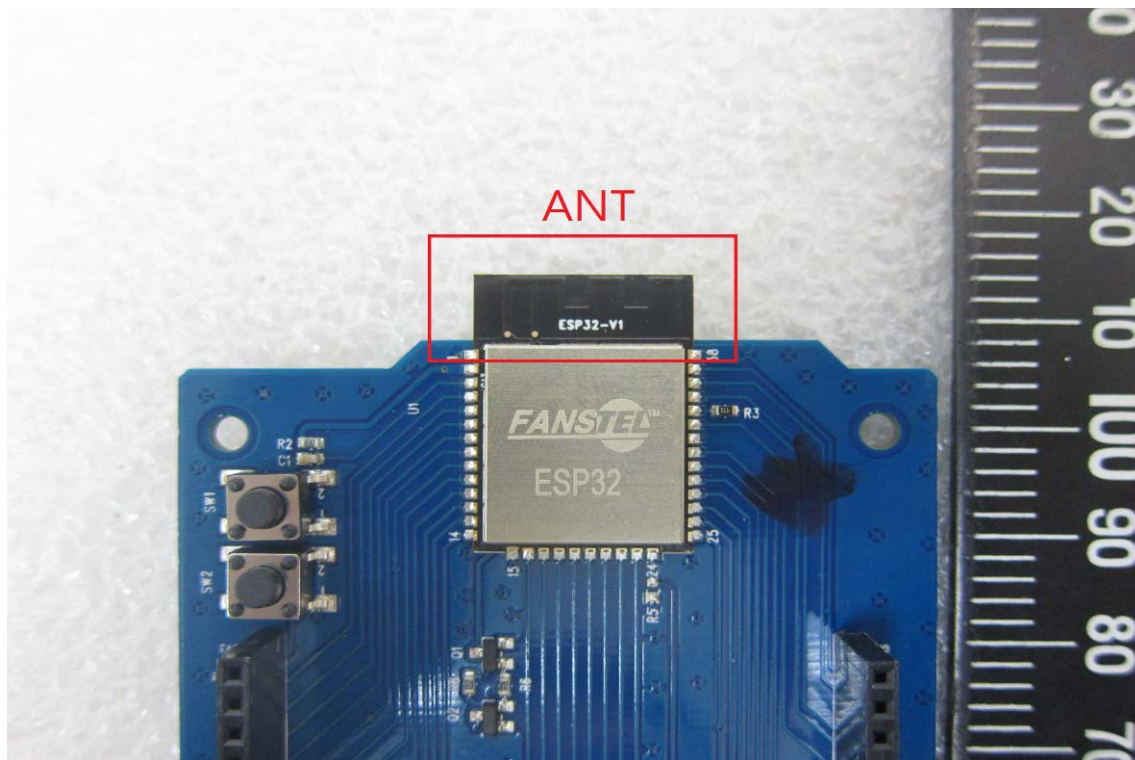
**EUT 1 ESP32**



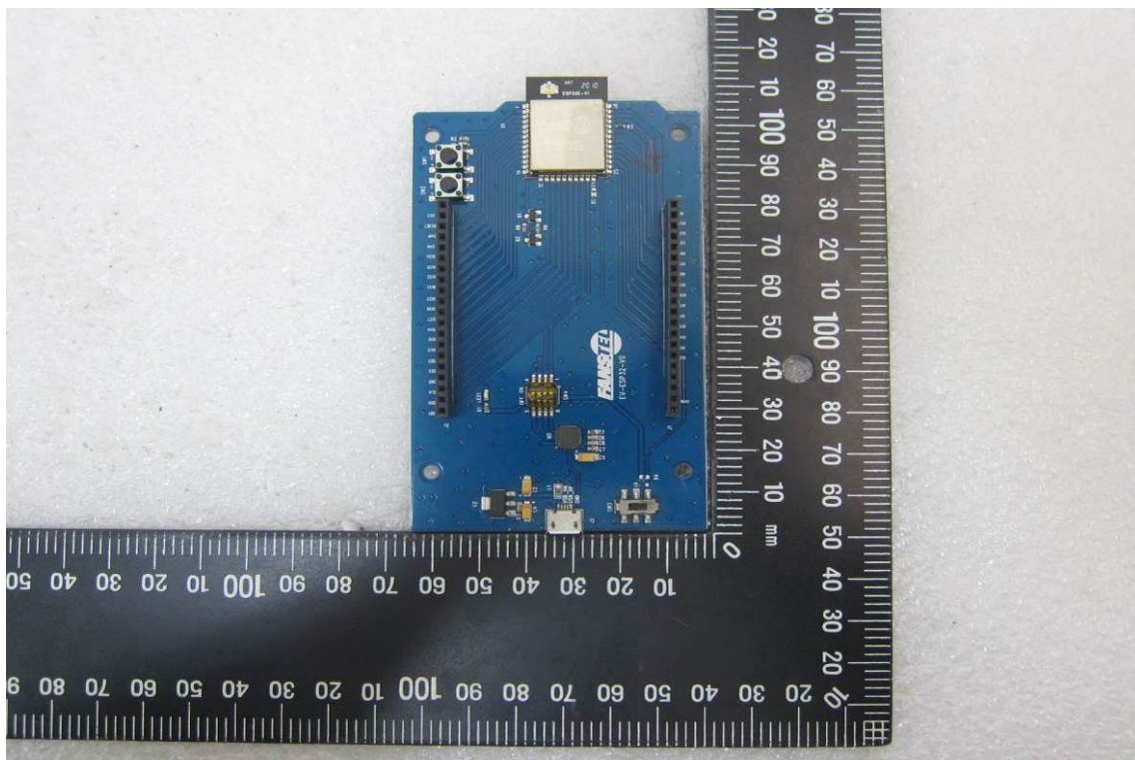
**EUT 2 ESP32**



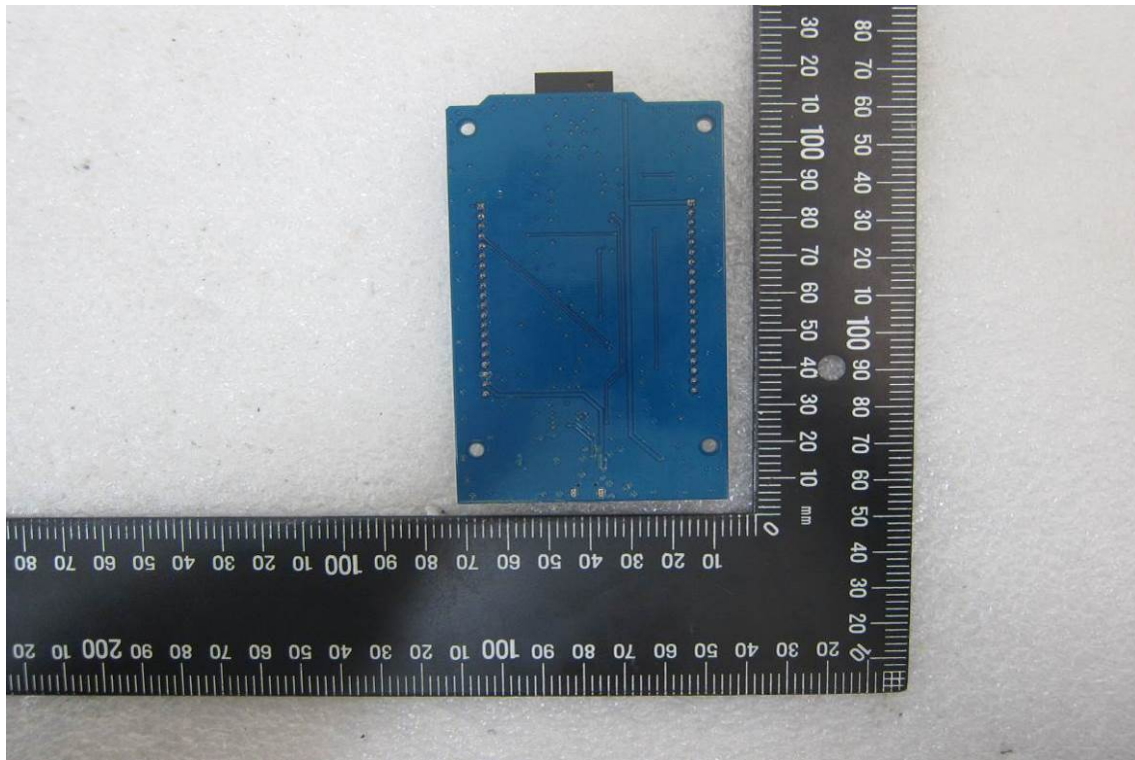
**EUT 3 ESP32**



**EUT 4 ESP32E**



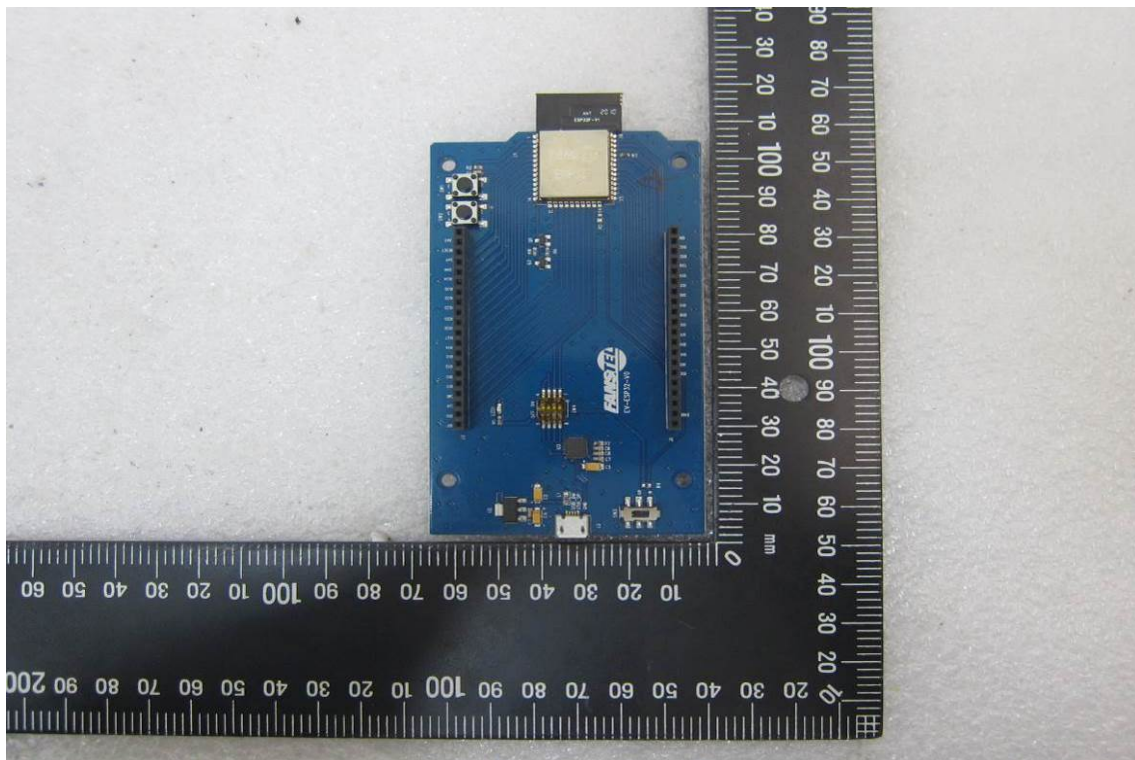
***EUT 5 ESP32E***



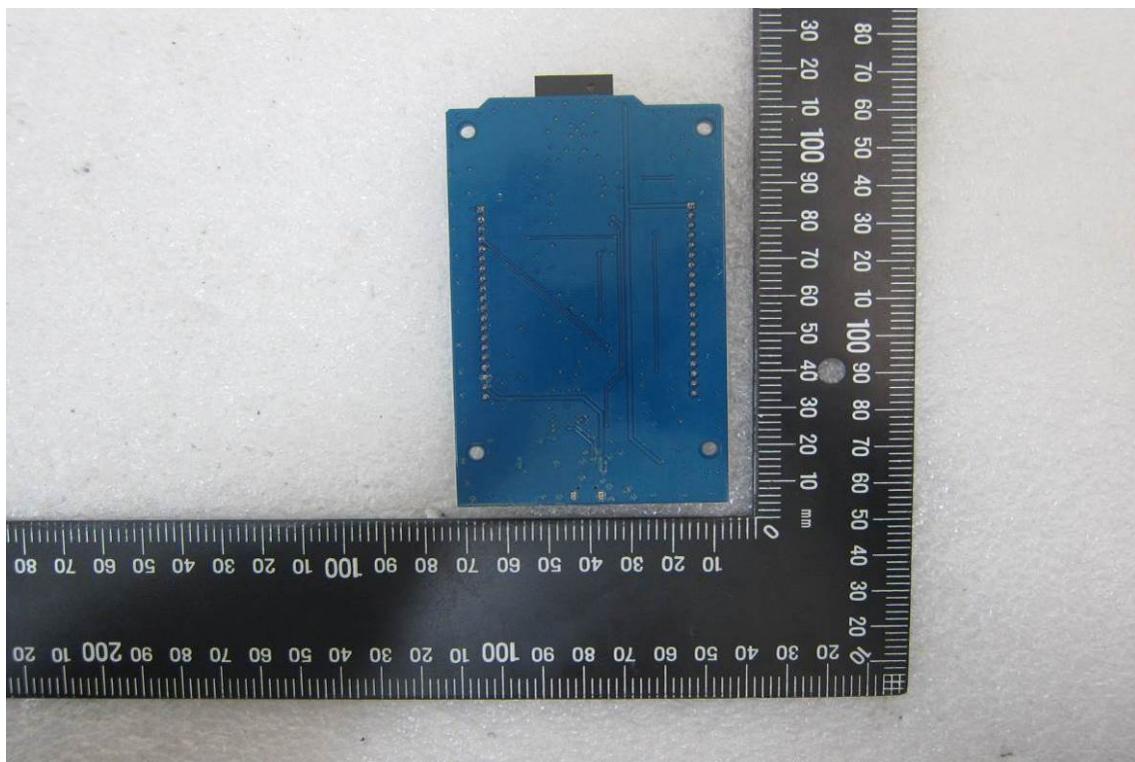
***EUT 6 ESP32E***



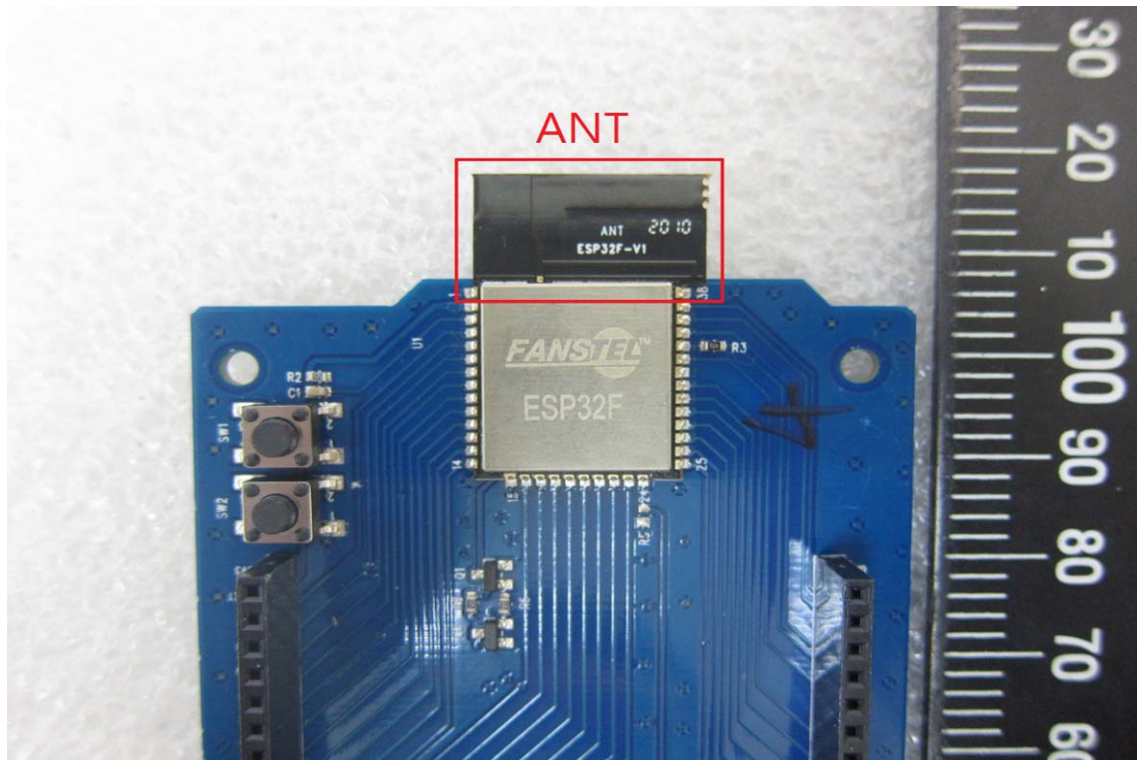
**EUT 7    ESP32F**



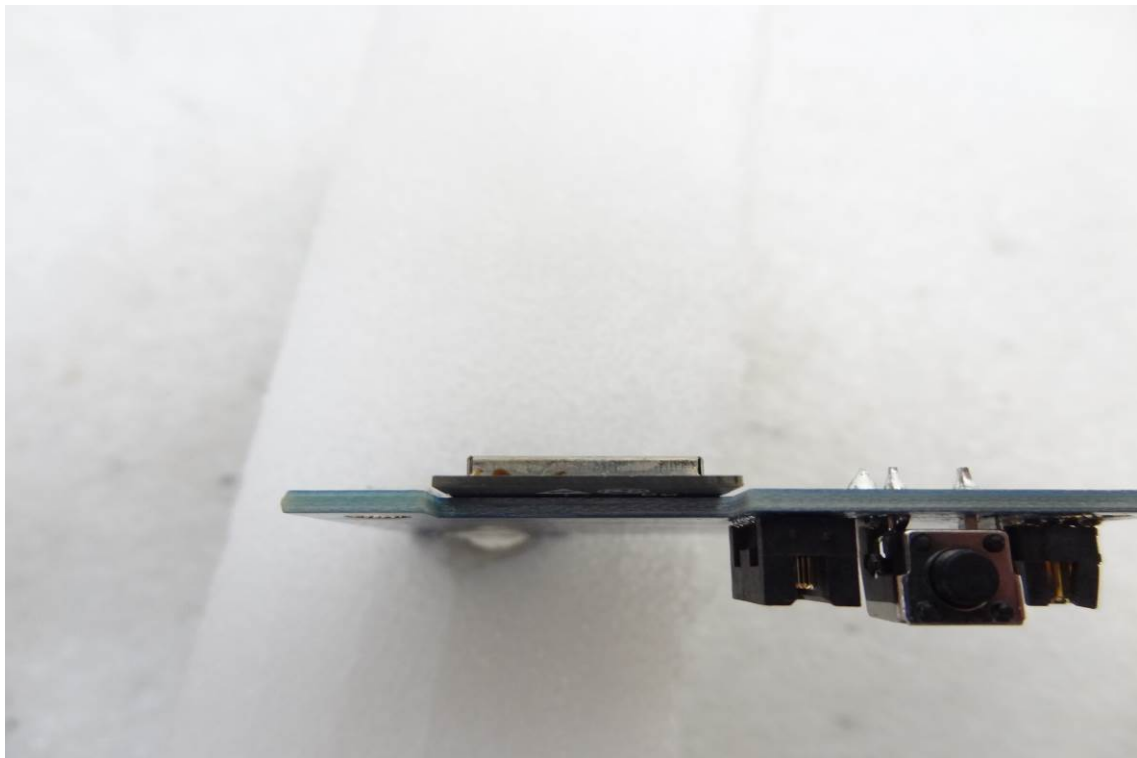
**EUT 8    ESP32F**



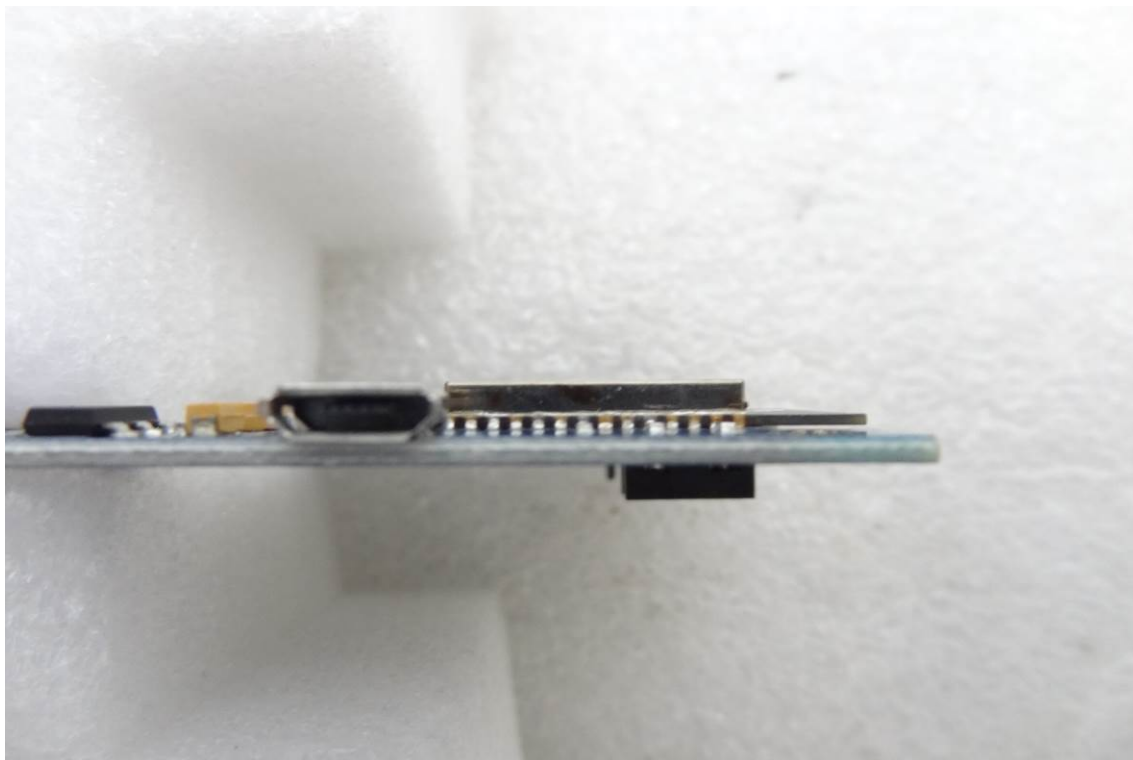
**EUT 9 ESP32F**



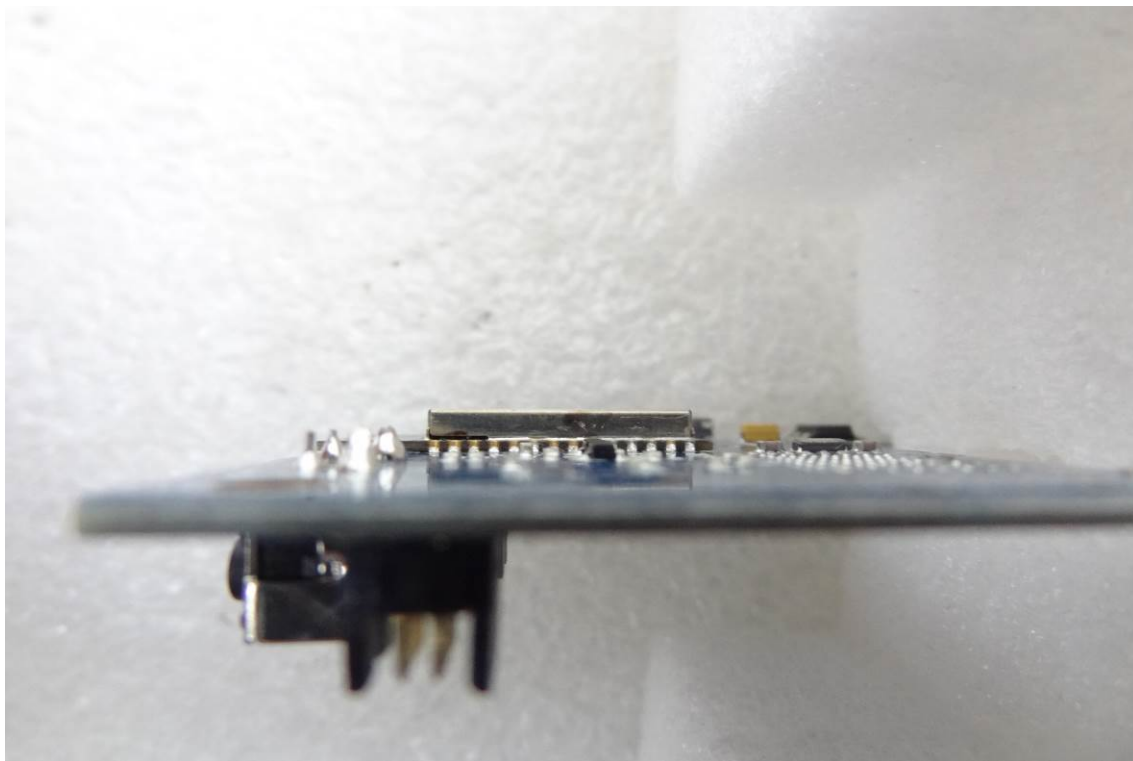
**EUT 10**



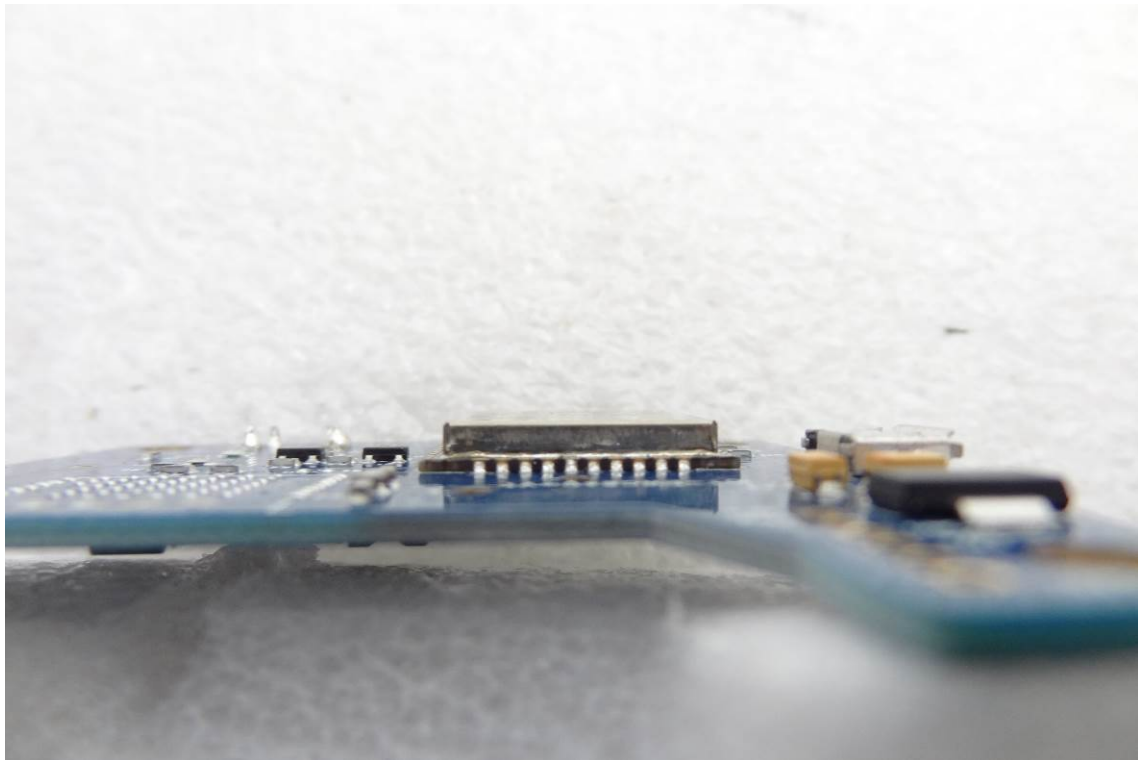
***EUT 11***



***EUT 12***



***EUT 13***



***EUT 14***

