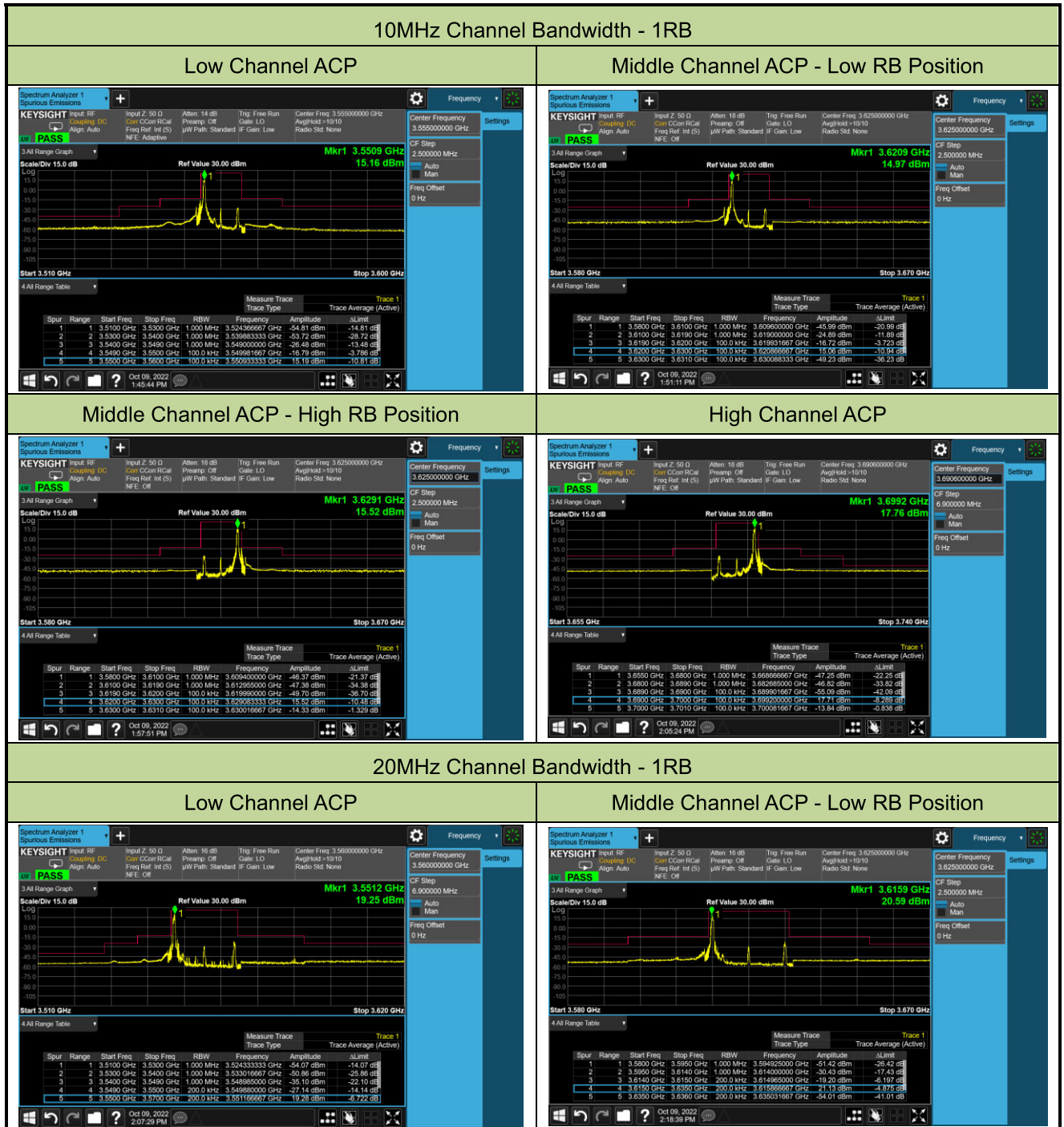
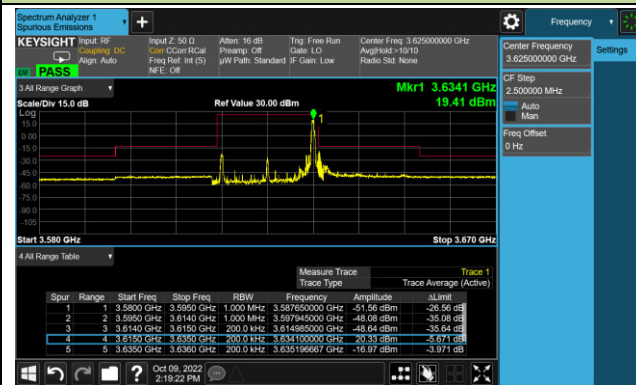


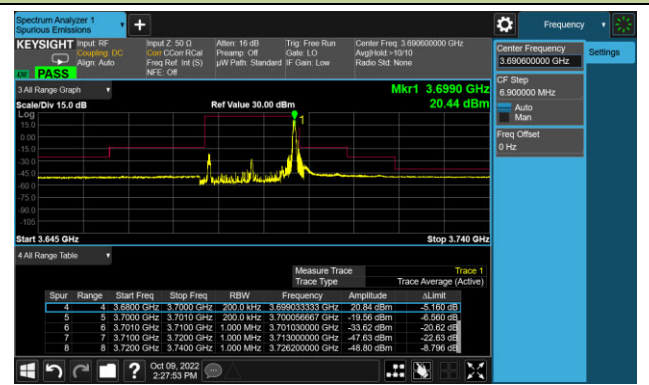
Test Site	SIP-SR1	Test Engineer	Candy Luo
Test Date	2022-10-09 ~ 2022-10-09	Test Band	n48_ENDC



Middle Channel ACP - High RB Position

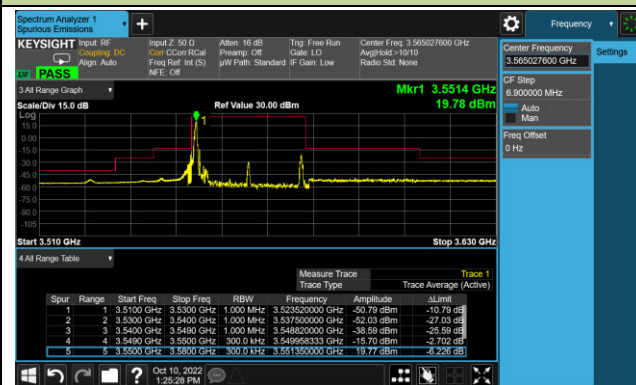


High Channel ACP

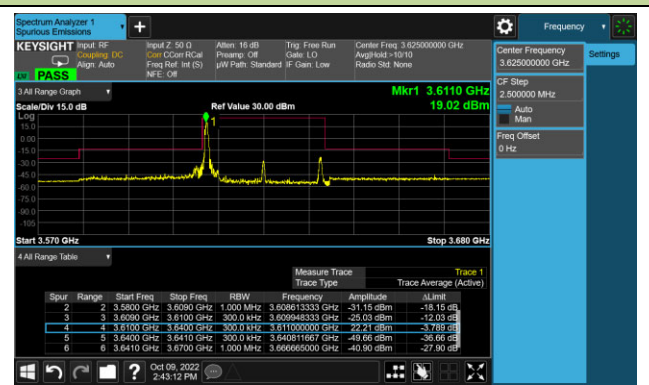


30MHz Channel Bandwidth - 1RB

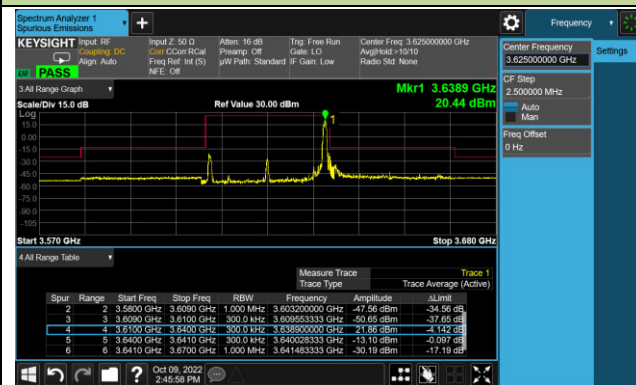
Low Channel ACP



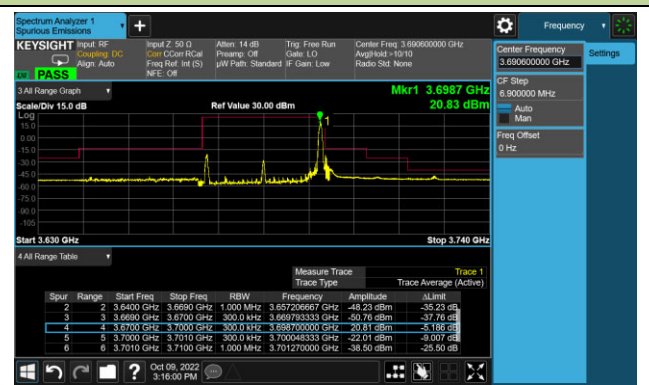
Middle Channel ACP - Low RB Position



Middle Channel ACP - High RB Position

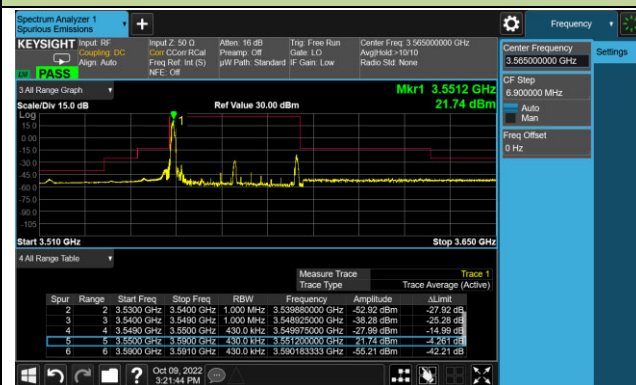


High Channel ACP

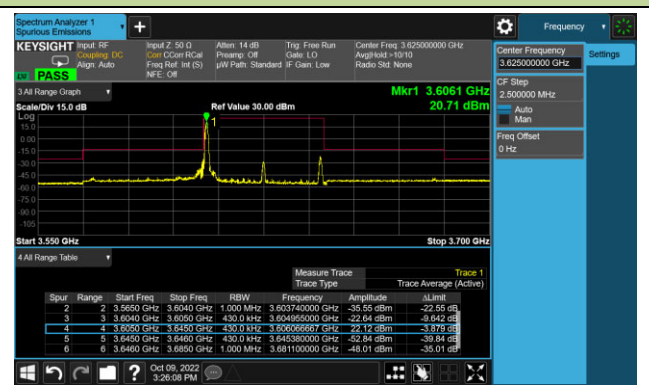


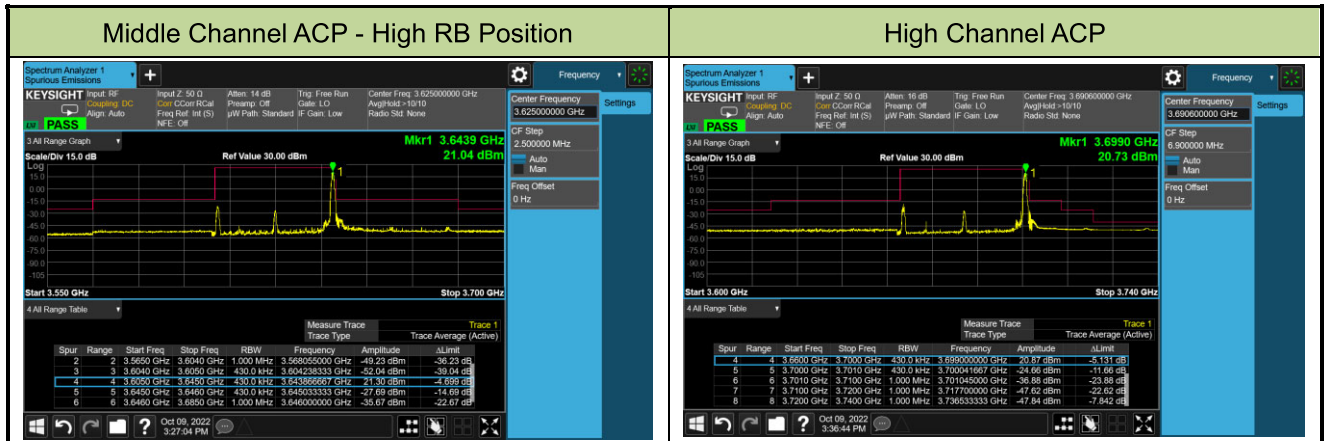
40MHz Channel Bandwidth - 1RB

Low Channel ACP



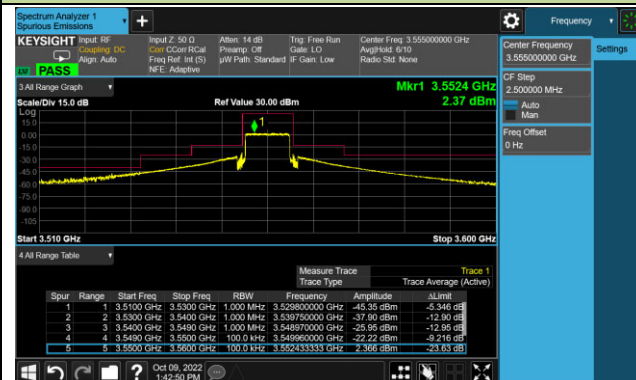
Middle Channel ACP - Low RB Position



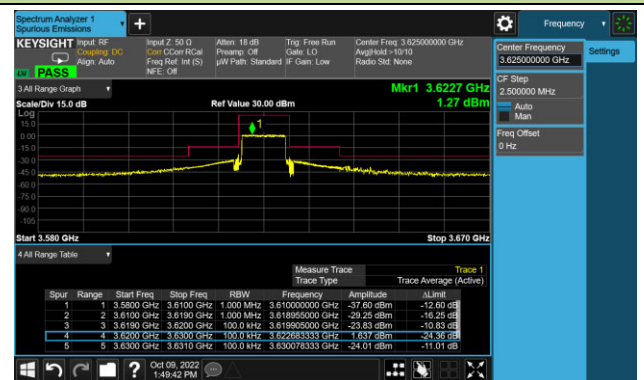


10MHz Channel Bandwidth - Full RB

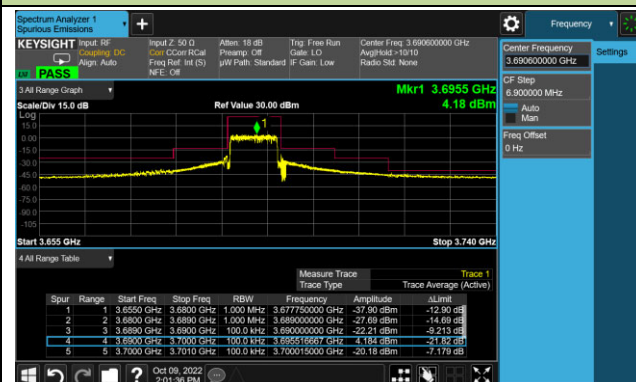
Low Channel ACP



Middle Channel ACP

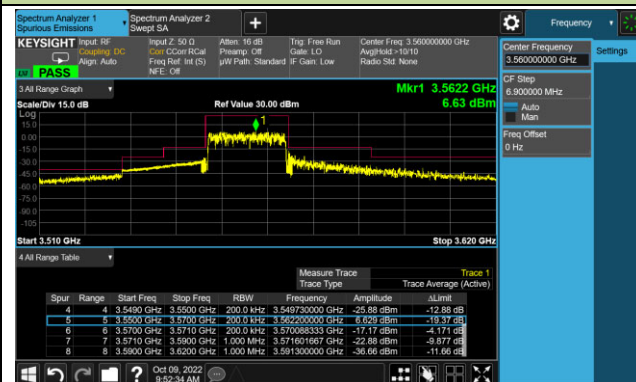


High Channel ACP

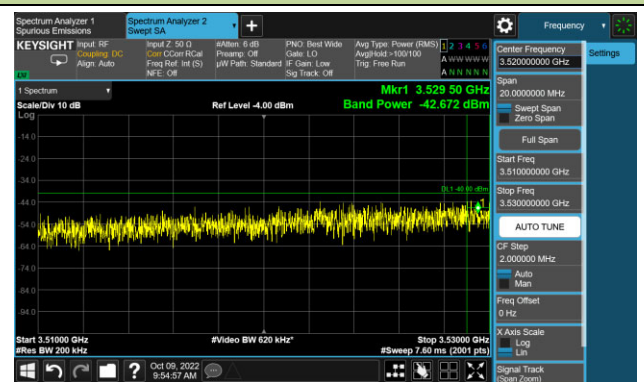


20MHz Channel Bandwidth - Full RB

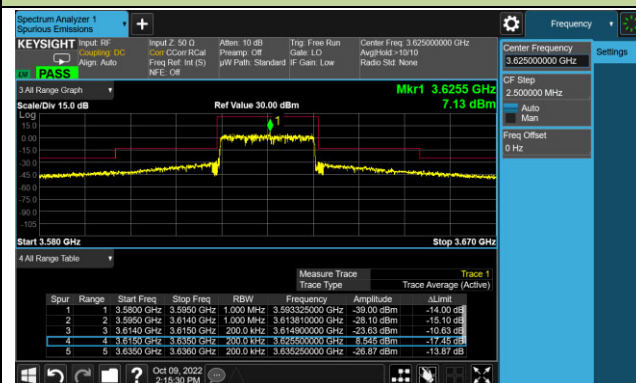
Low Channel ACP



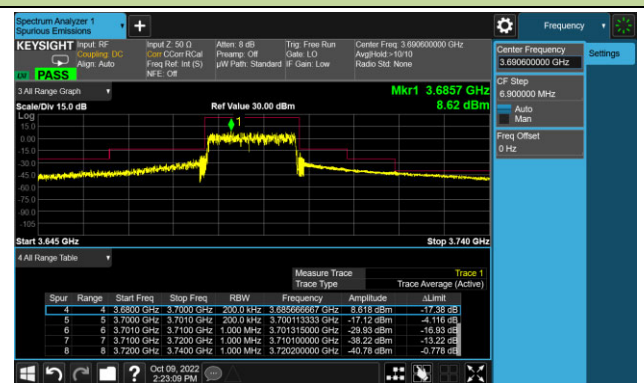
Lower Extended Band Edge



Middle Channel ACP

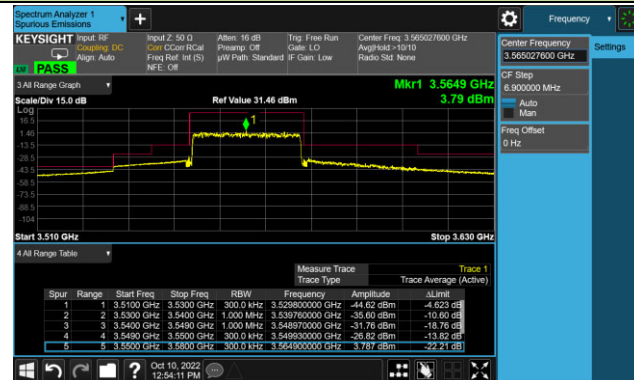


High Channel ACP

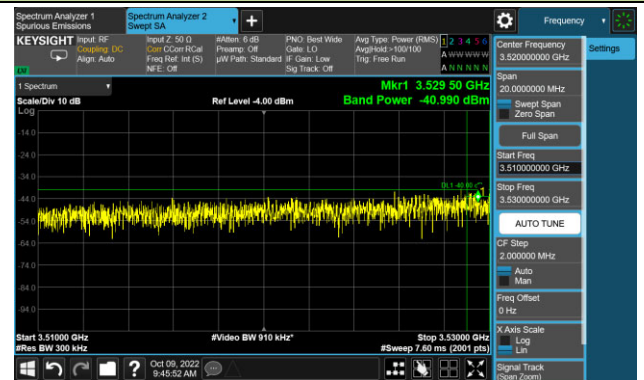


30MHz Channel Bandwidth - Full RB

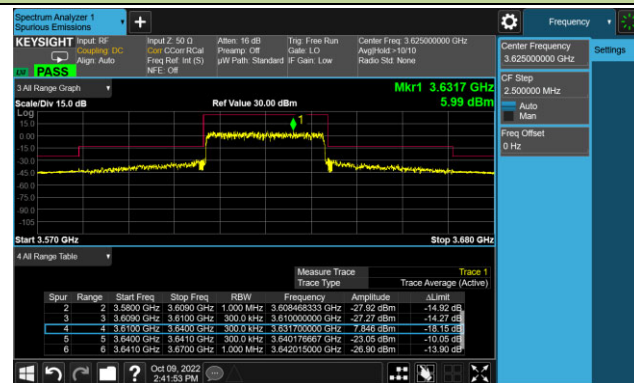
Low Channel ACP



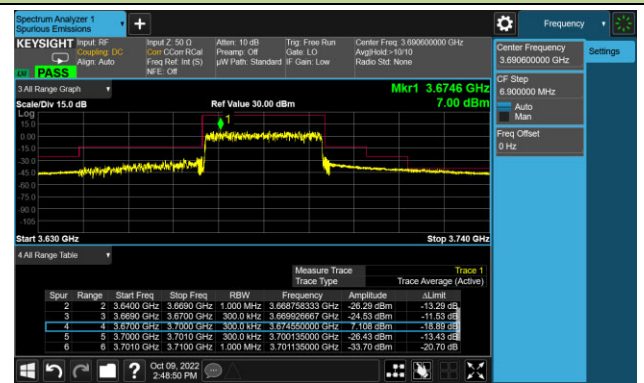
Lower Extended Band Edge



Middle Channel ACP

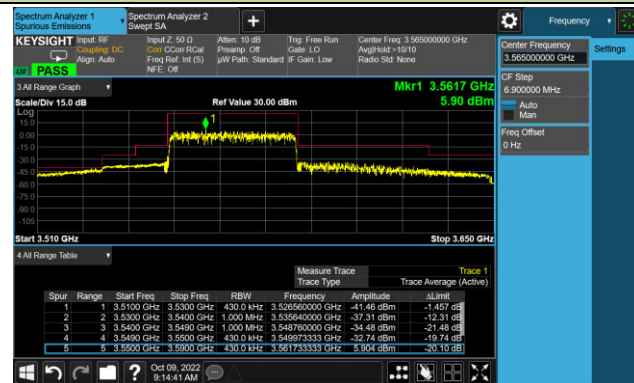


High Channel ACP

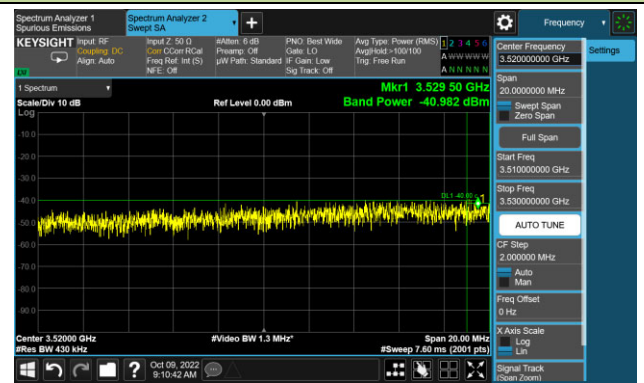


40MHz Channel Bandwidth - Full RB

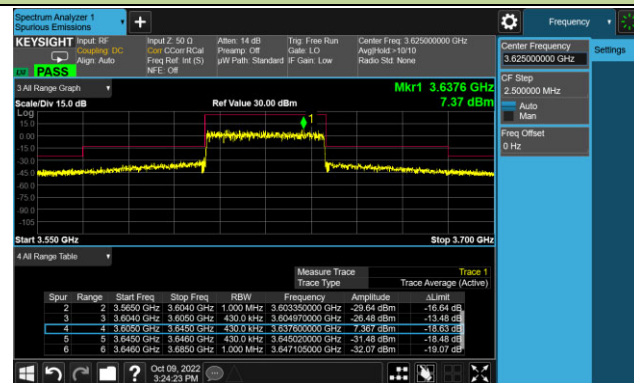
Low Channel ACP



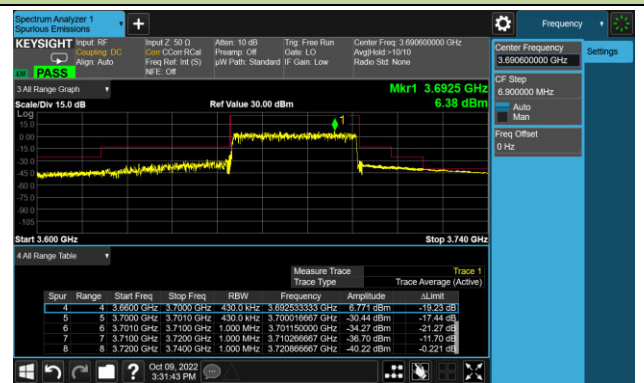
Lower Extended Band Edge



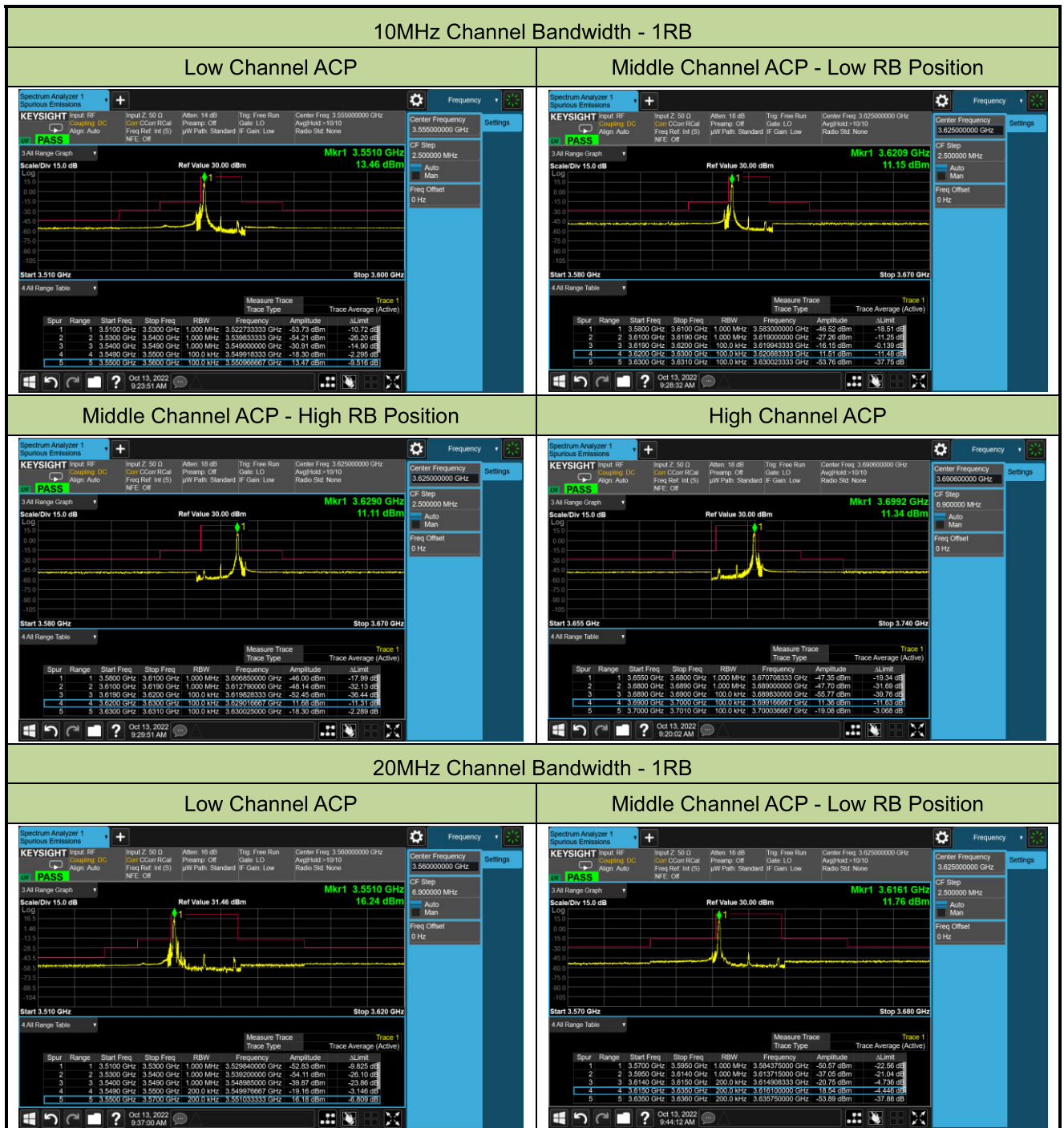
Middle Channel ACP



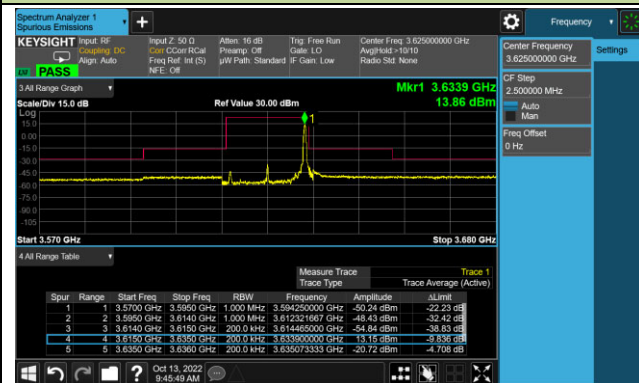
High Channel ACP



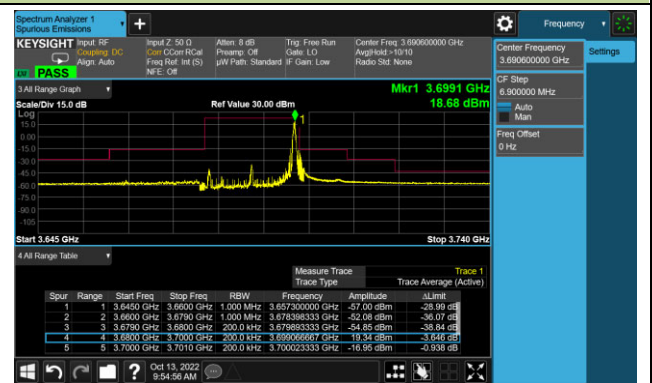
Test Site	SIP-SR1	Test Engineer	Candy Luo
Test Date	2022-10-09 ~ 2022-10-14	Test Band	n48_UL MIMO (Port 0)



Middle Channel ACP - High RB Position

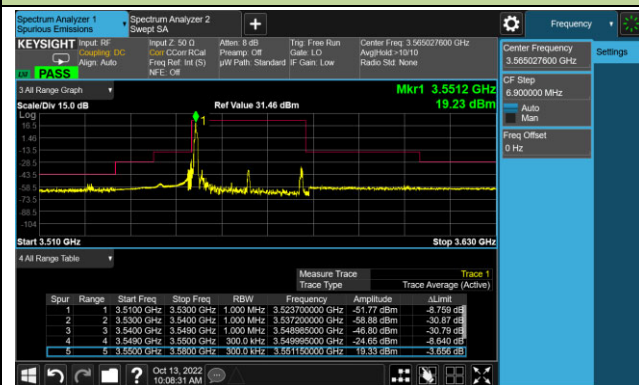


High Channel ACP

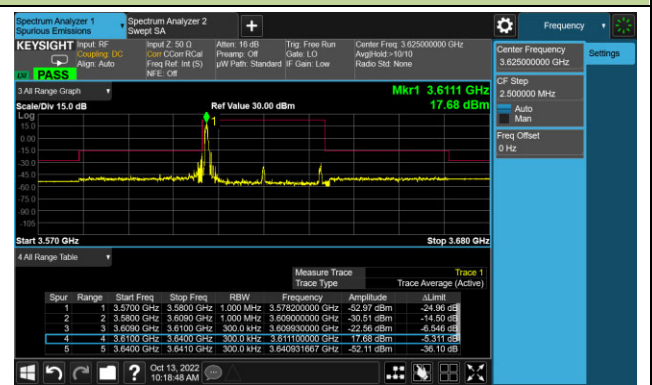


30MHz Channel Bandwidth - 1RB

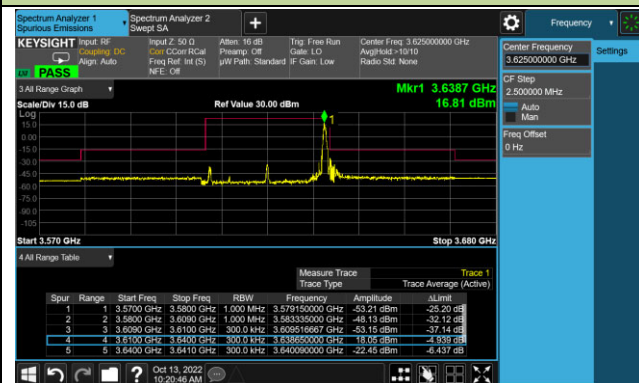
Low Channel ACP



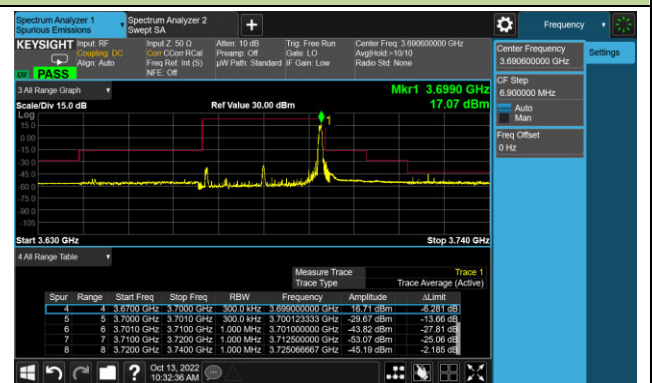
Middle Channel ACP - Low RB Position



Middle Channel ACP - High RB Position

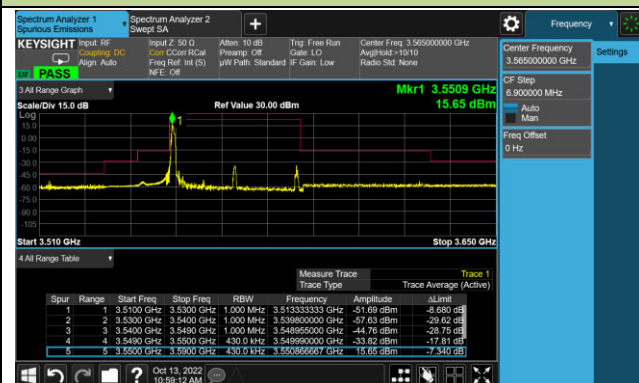


High Channel ACP

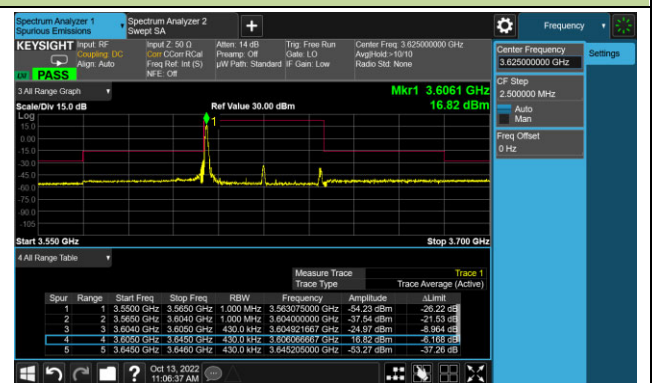


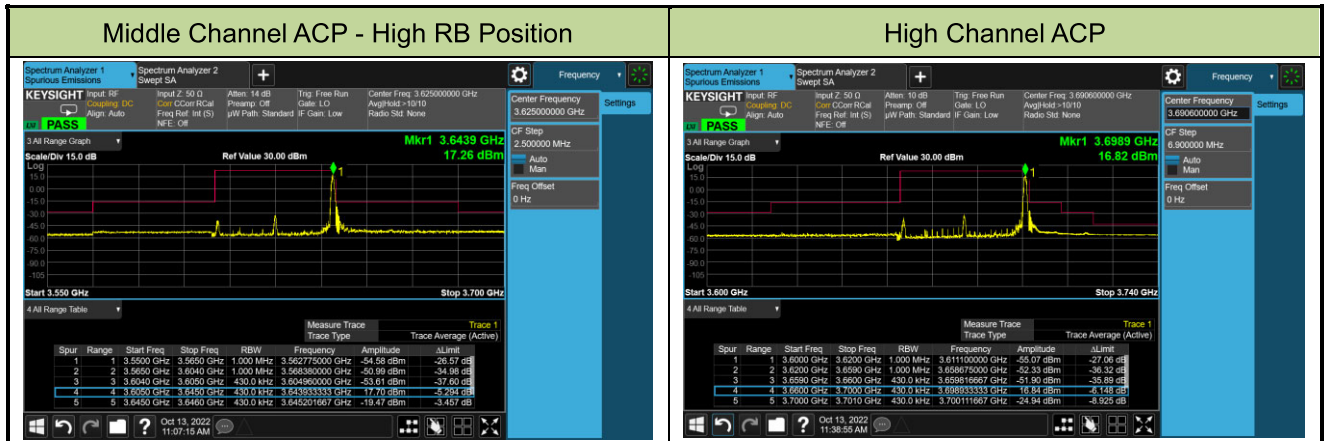
40MHz Channel Bandwidth - 1RB

Low Channel ACP



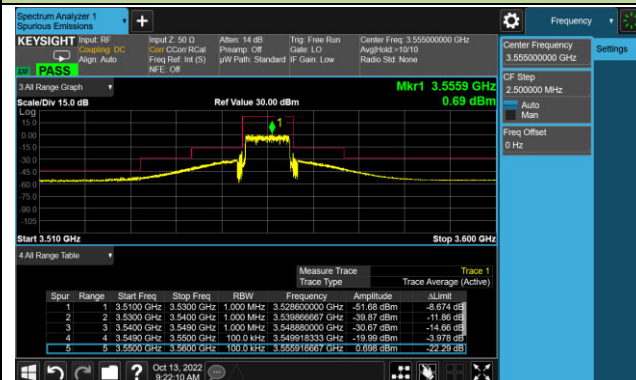
Middle Channel ACP - Low RB Position



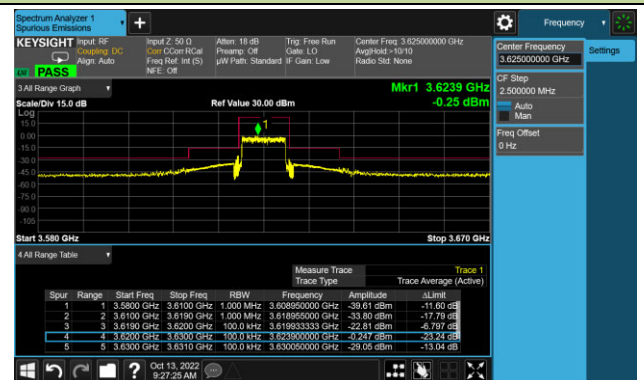


10MHz Channel Bandwidth - Full RB

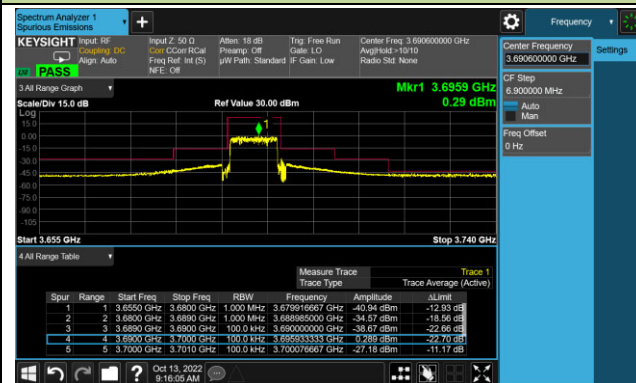
Low Channel ACP



Middle Channel ACP

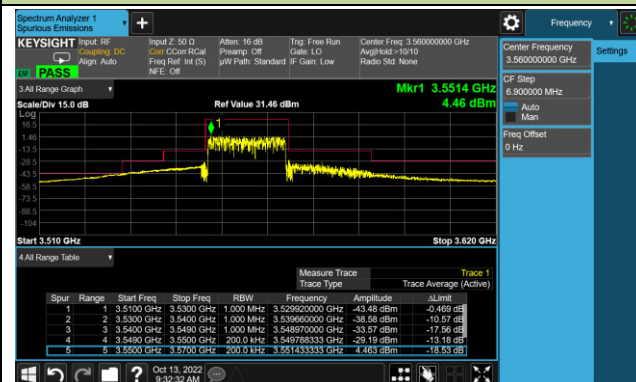


High Channel ACP

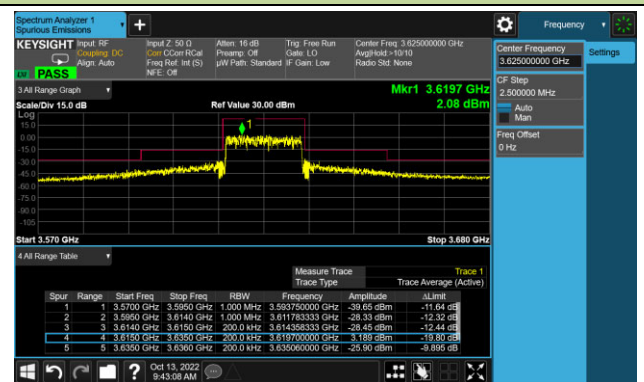


20MHz Channel Bandwidth - Full RB

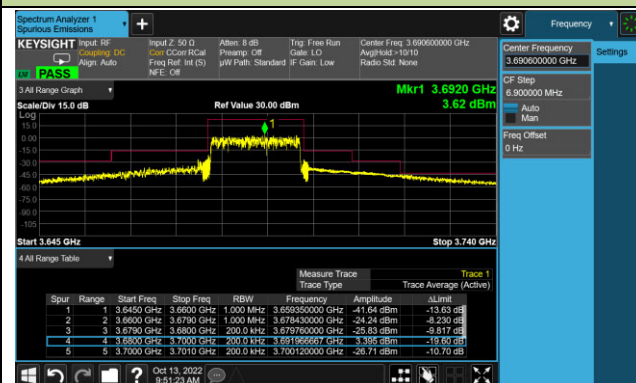
Low Channel ACP



Middle Channel ACP

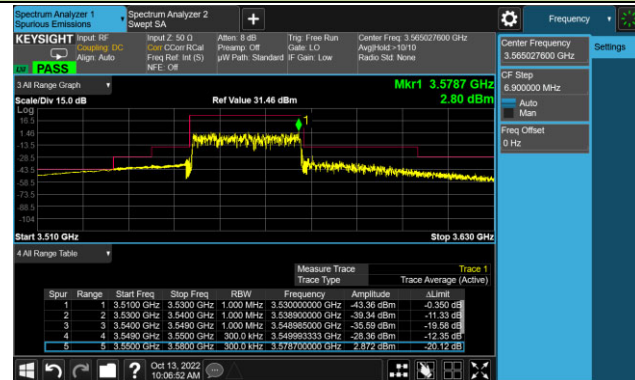


High Channel ACP

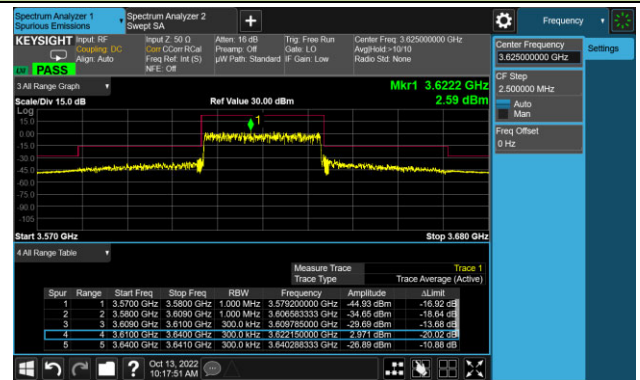


30MHz Channel Bandwidth - Full RB

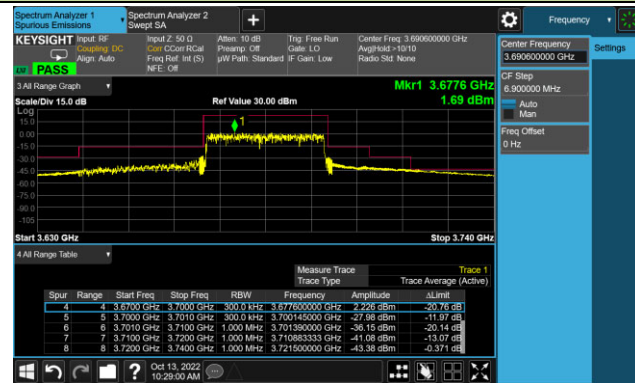
Low Channel ACP



Middle Channel ACP

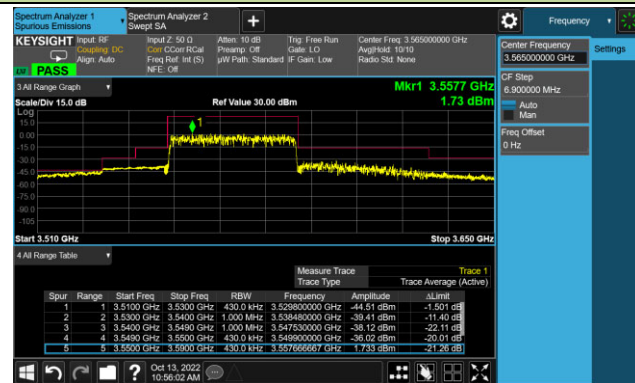


High Channel ACP



40MHz Channel Bandwidth - Full RB

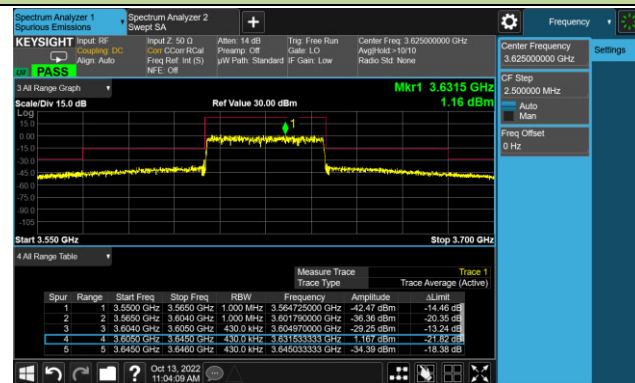
Low Channel ACP



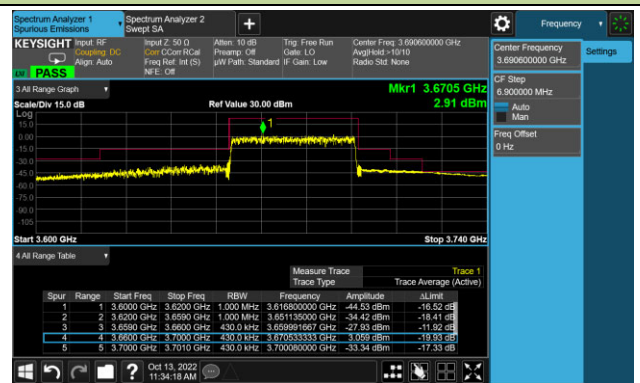
Lower Extended Band Edge



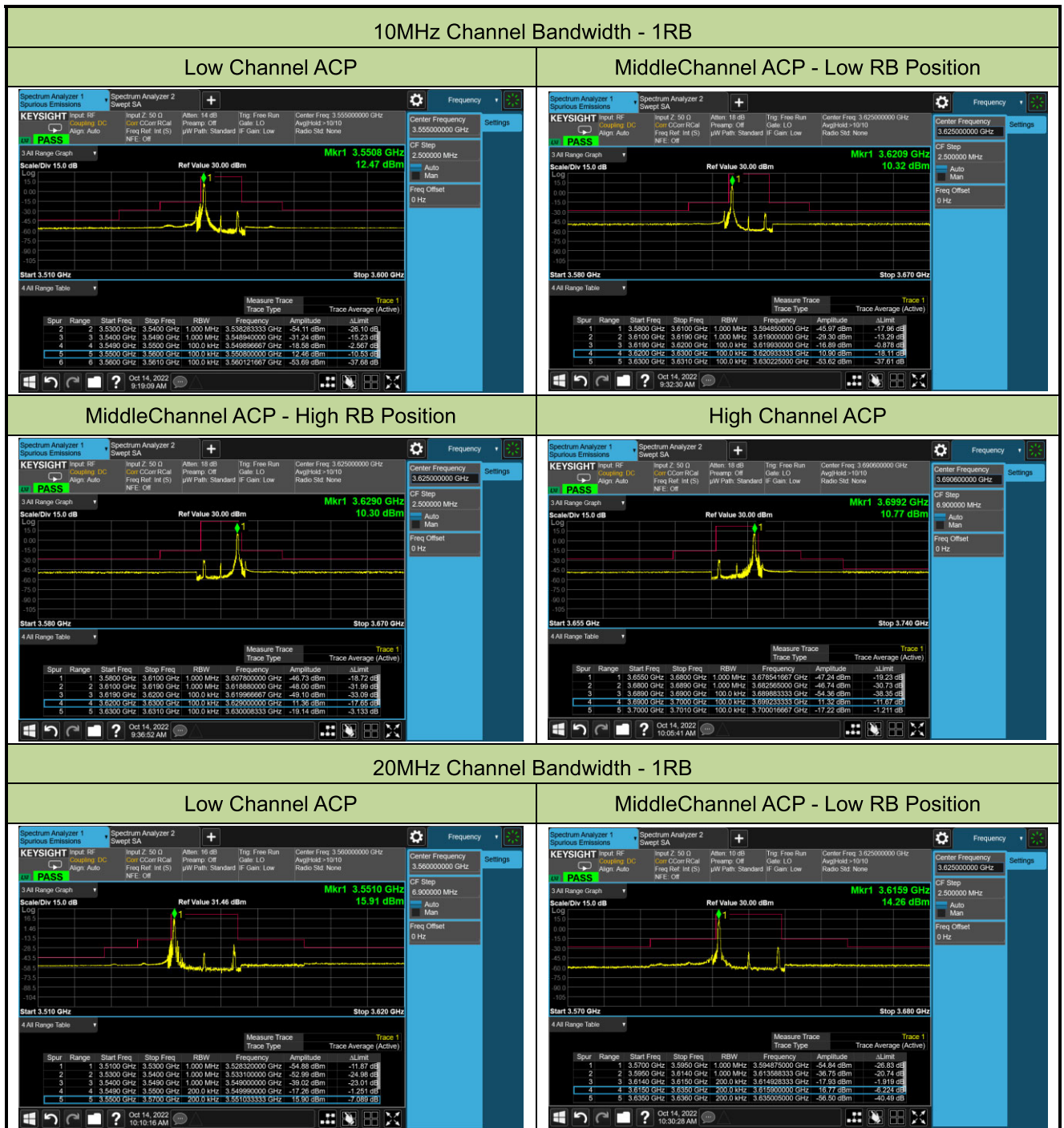
Middle Channel ACP



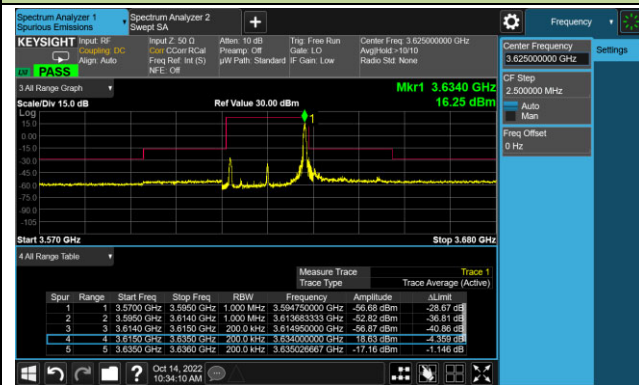
High Channel ACP



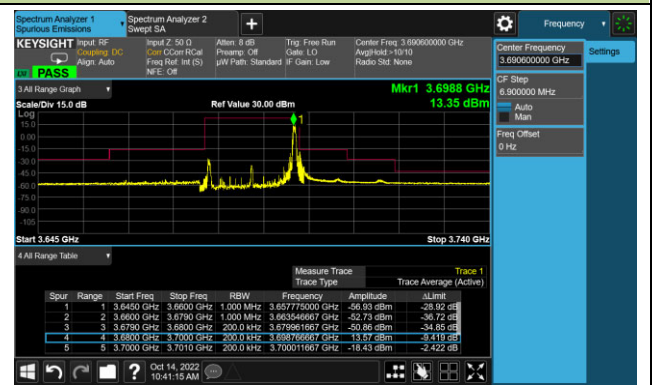
Test Site	SIP-SR1	Test Engineer	Candy Luo
Test Date	2022-10-09 ~ 2022-10-14	Test Band	n48_UL MIMO (Port 3)



MiddleChannel ACP - High RB Position

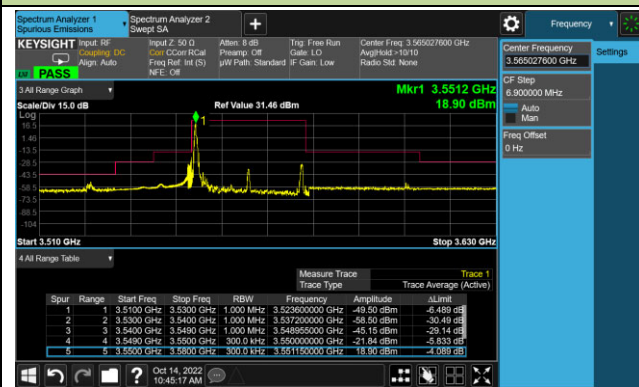


High Channel ACP

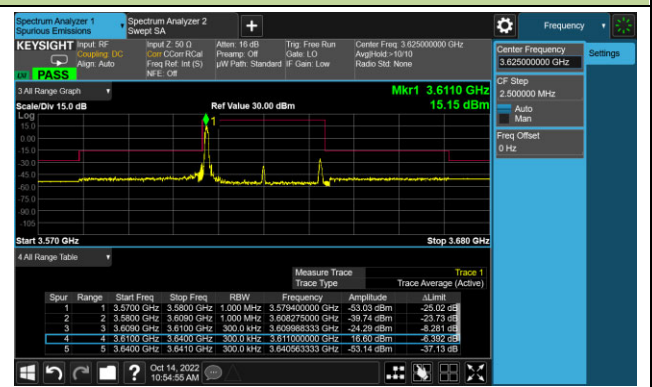


30MHz Channel Bandwidth - 1RB

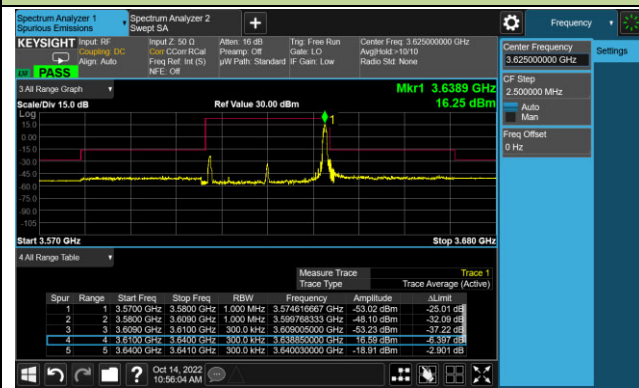
Low Channel ACP



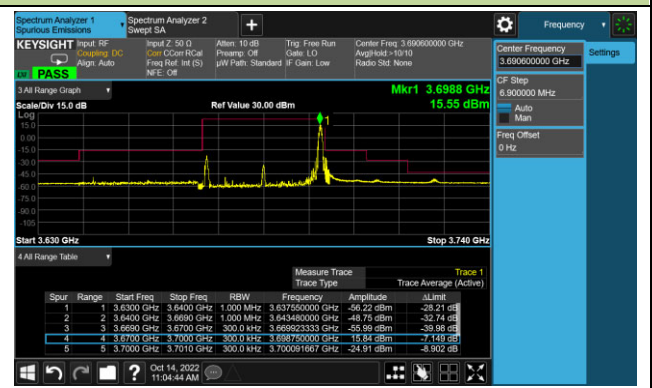
MiddleChannel ACP - Low RB Position



MiddleChannel ACP - High RB Position

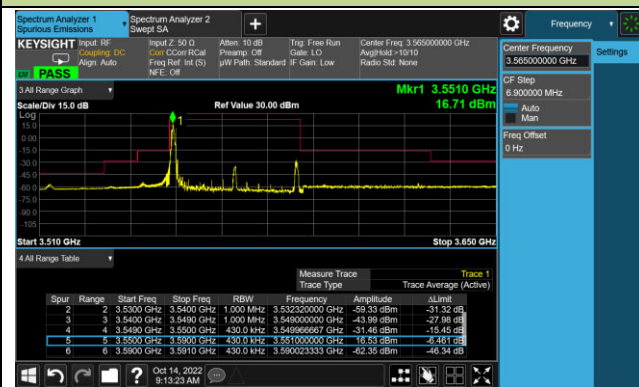


High Channel ACP



40MHz Channel Bandwidth - 1RB

Low Channel ACP



MiddleChannel ACP - Low RB Position

