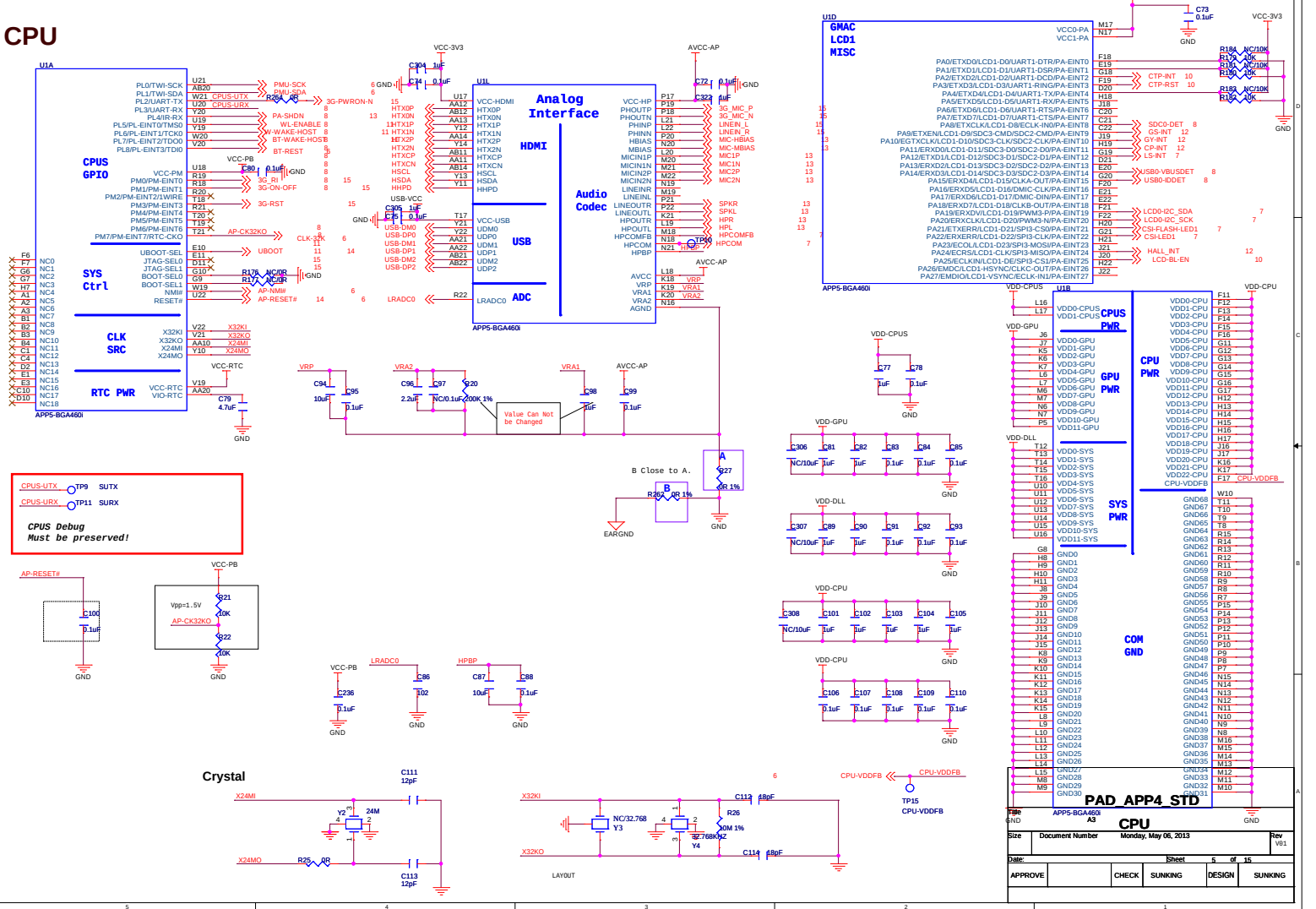




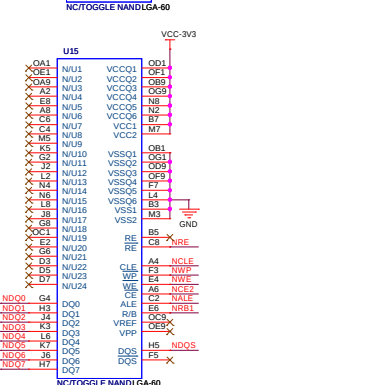
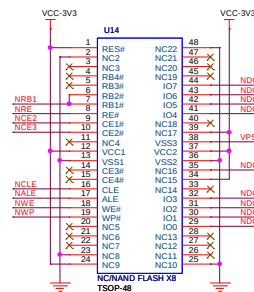
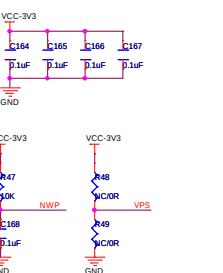
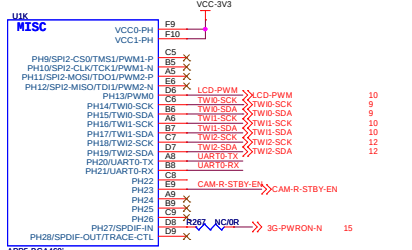
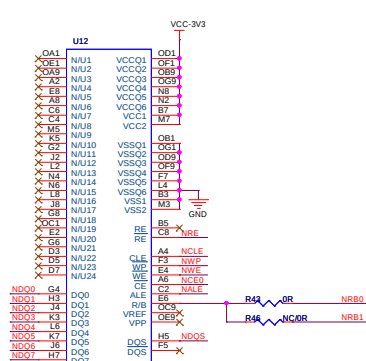
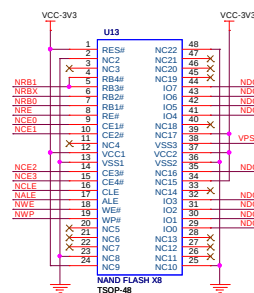
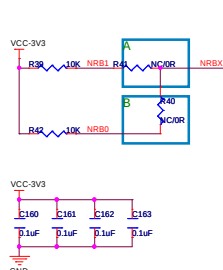
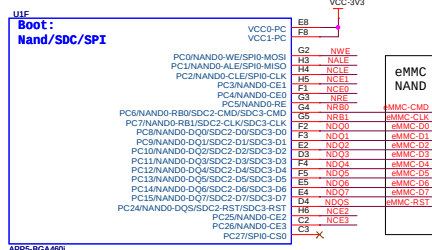
PAD APP4 STD				
File A2 DDR3 256*8*4				
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DDR3 8bit x 4ea

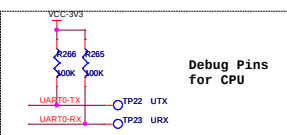
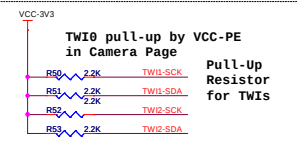
CPU



NAND



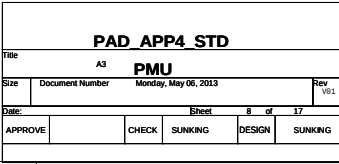
TSSOP CE Count(1)	Resistor A	Resistor B
1	Disconnect	Disconnect
2	Connect	Disconnect
4	Disconnect	Connect



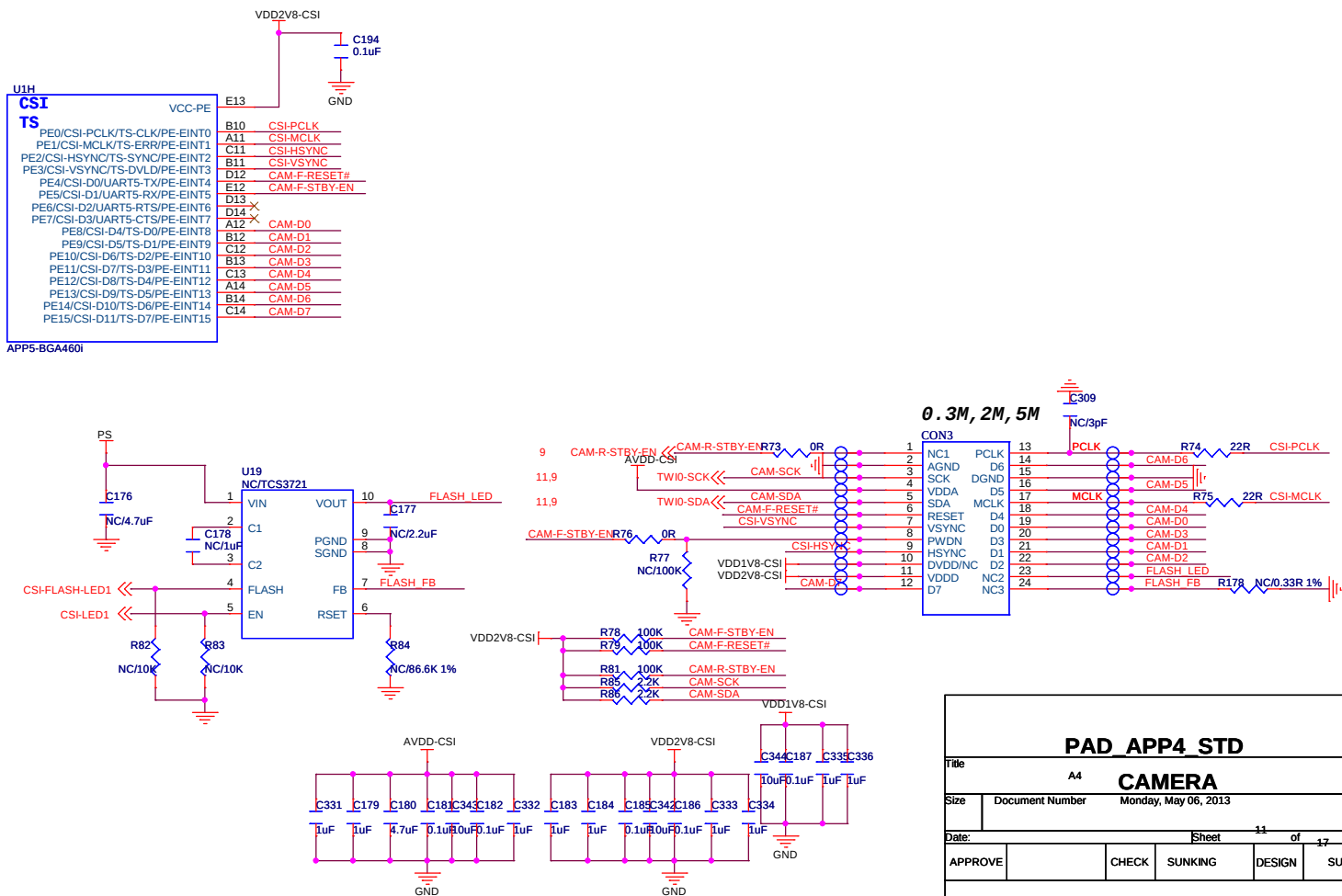
- (1) TSSOP CE Count: Chip Select Count of one TSSOP Nand Flash, can be 1
- (2) Refer to the Datasheet of Nand Flash to determine VPS should be
- (3) Both Toggle NAND and TSOP NAND can be layout together.

Date: 1/2/4.		PAD APP4 STD	
Full-up or -down.		A3	
		NAND	
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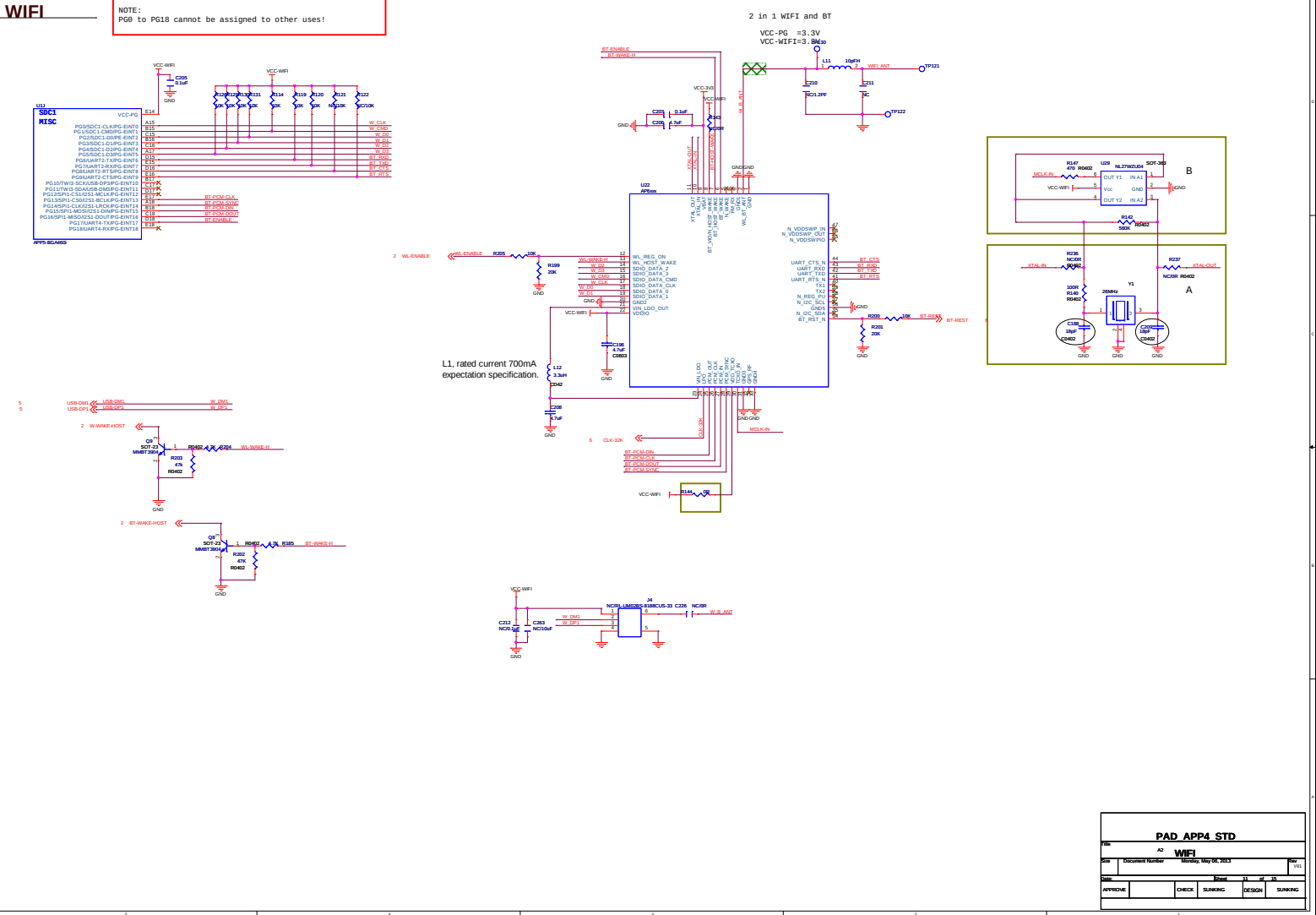
DC-IN-5V-2a TP16



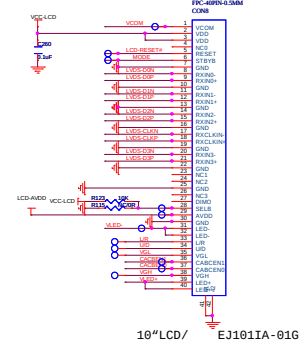
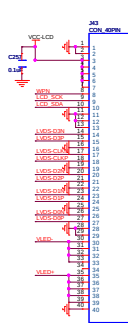
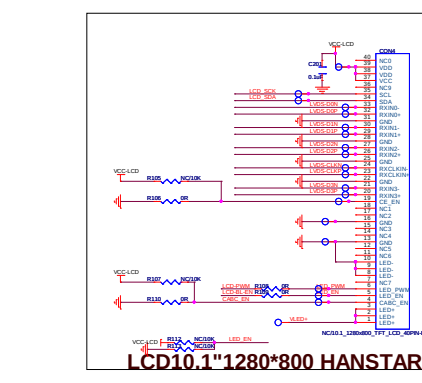
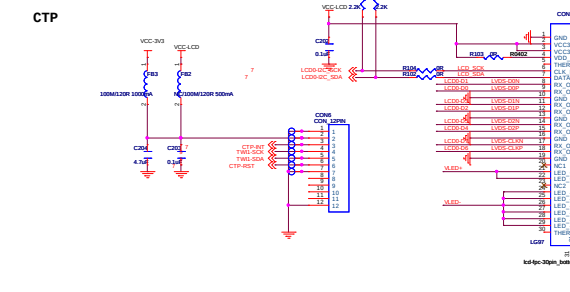
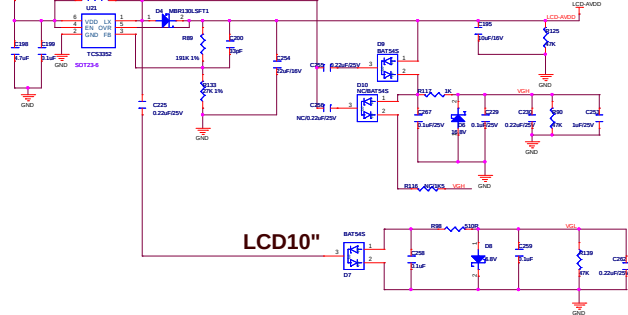
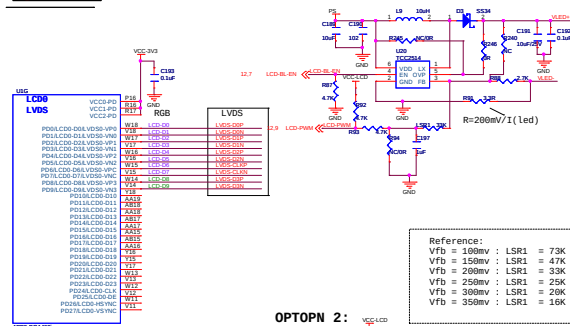
CAMERA



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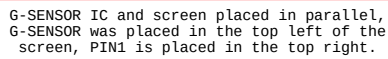


LCD-TP



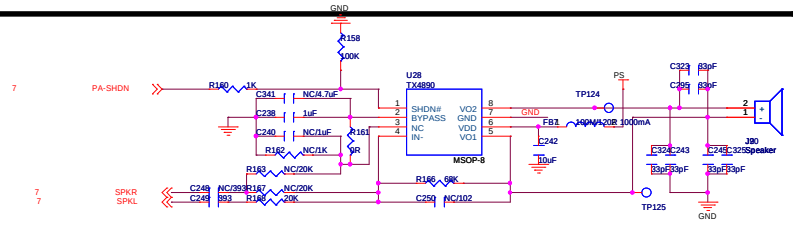
PAD APP4 STD					
A2 LCD					
Document Number: Monday, May 18, 2015					
APPROVE	CHECK	SUNING	DESIGN	SUNING	

SENSORS



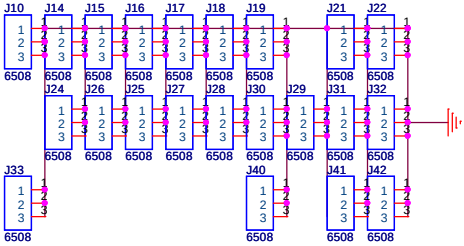
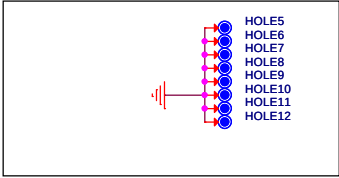
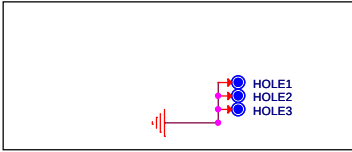
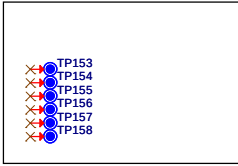
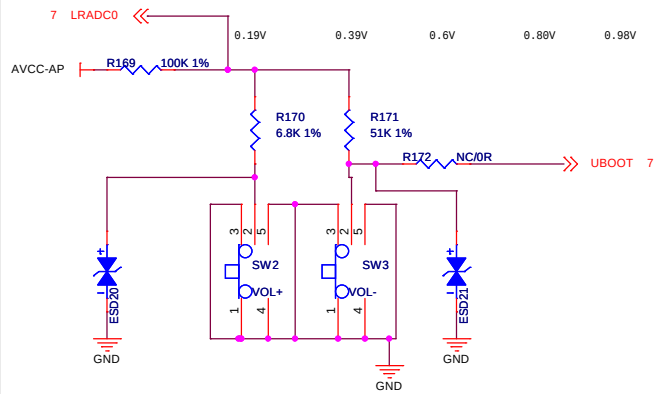
PAD APP4 STD				
Title A3 SENSORS				
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AUDIO IN/OUT



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KEY-IR-MISC



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A4 KEY-IR-MISC					
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