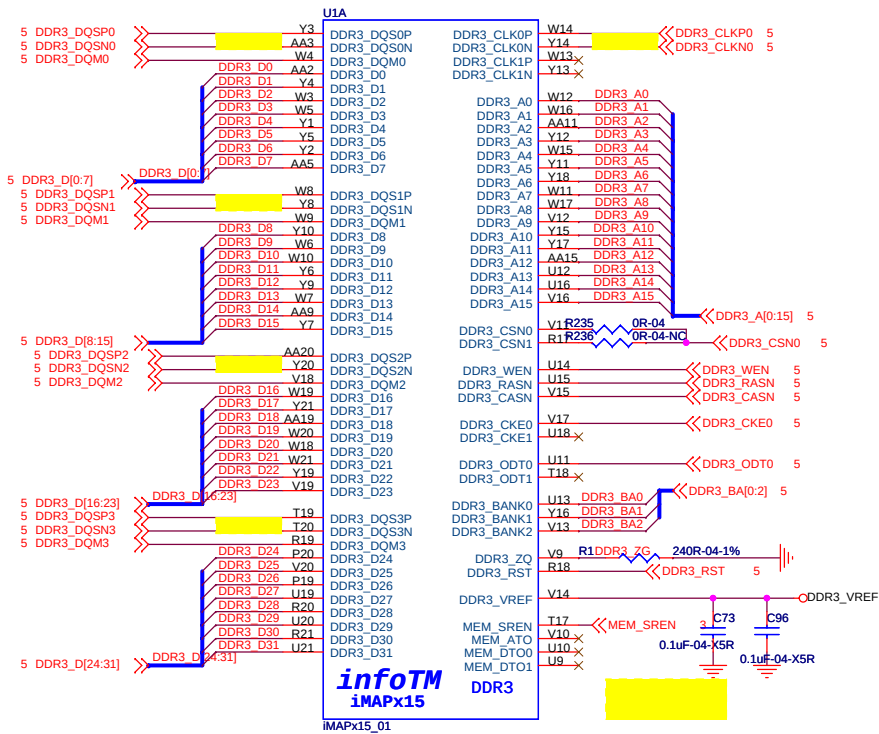
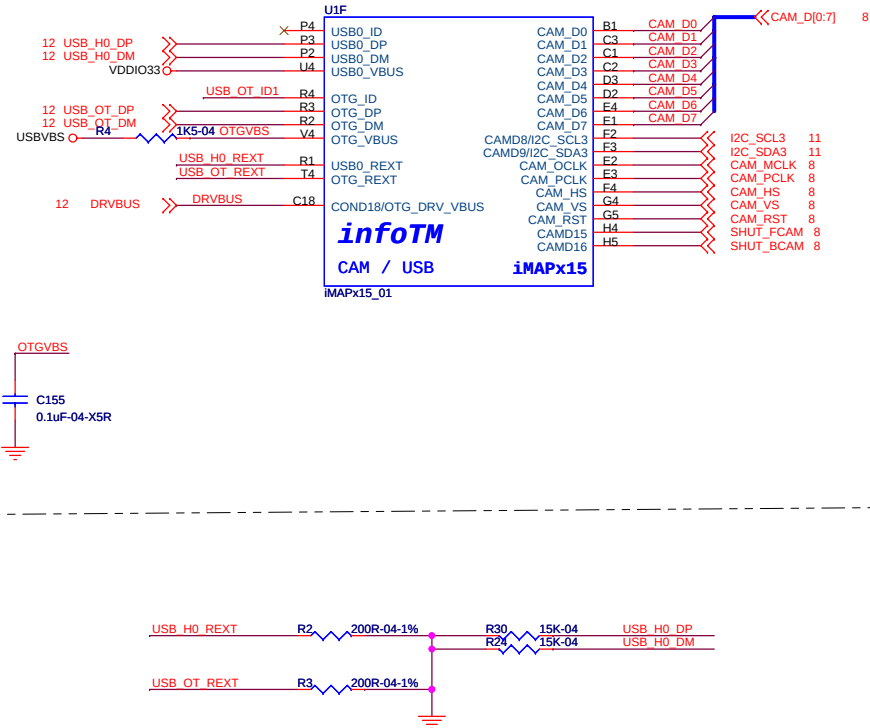


DDR3 Interface

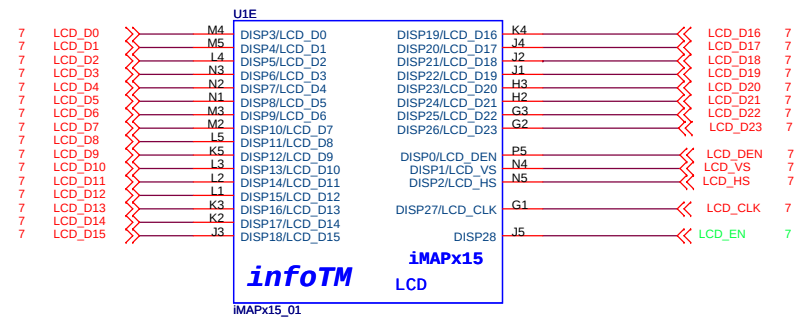


Difference line impedance for 100R

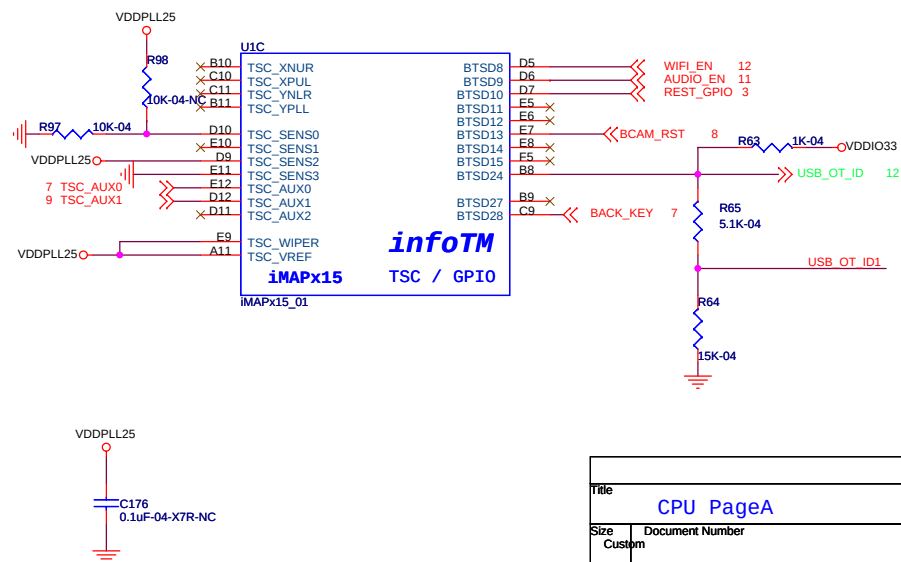
USB / Ethernet Interface



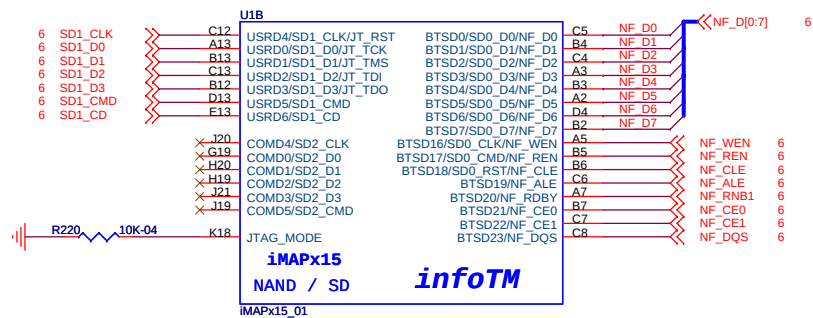
LCD / HDMI Interface



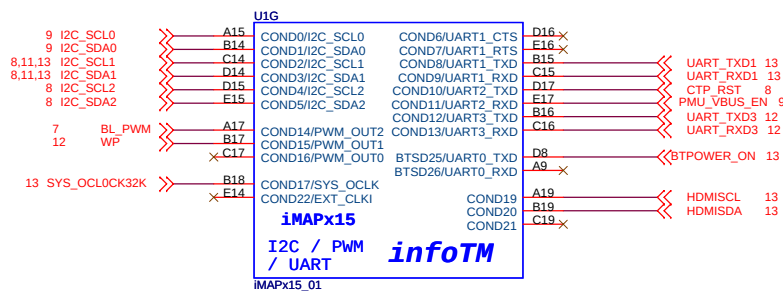
SATA /TP/ MIPI Interface



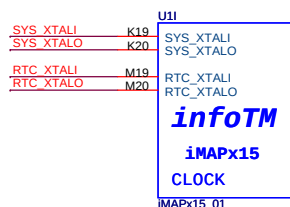
NAND / SD Interface



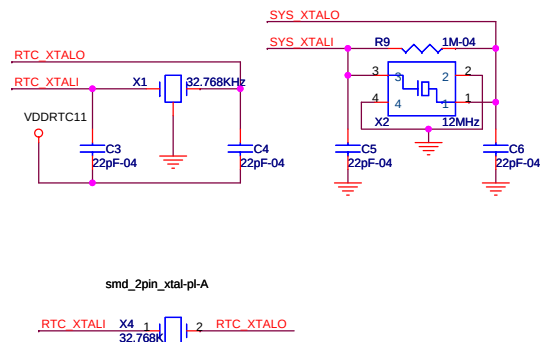
PWM / UART / GPS / I2C Interface



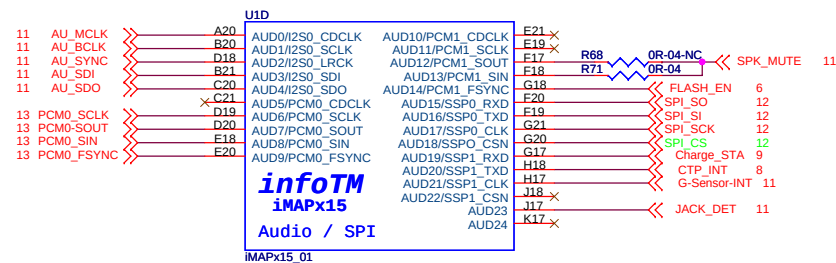
CLOCK Source



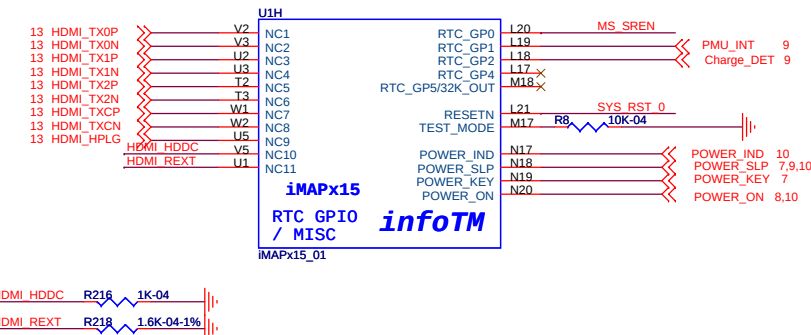
Crystal



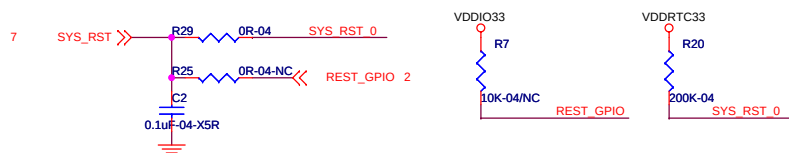
Audio / CAM Interface



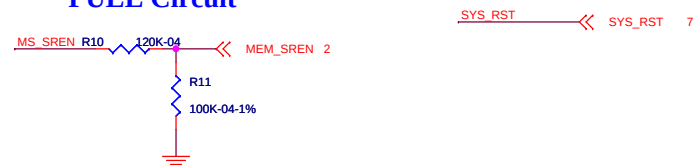
JTAG / Misc Interface



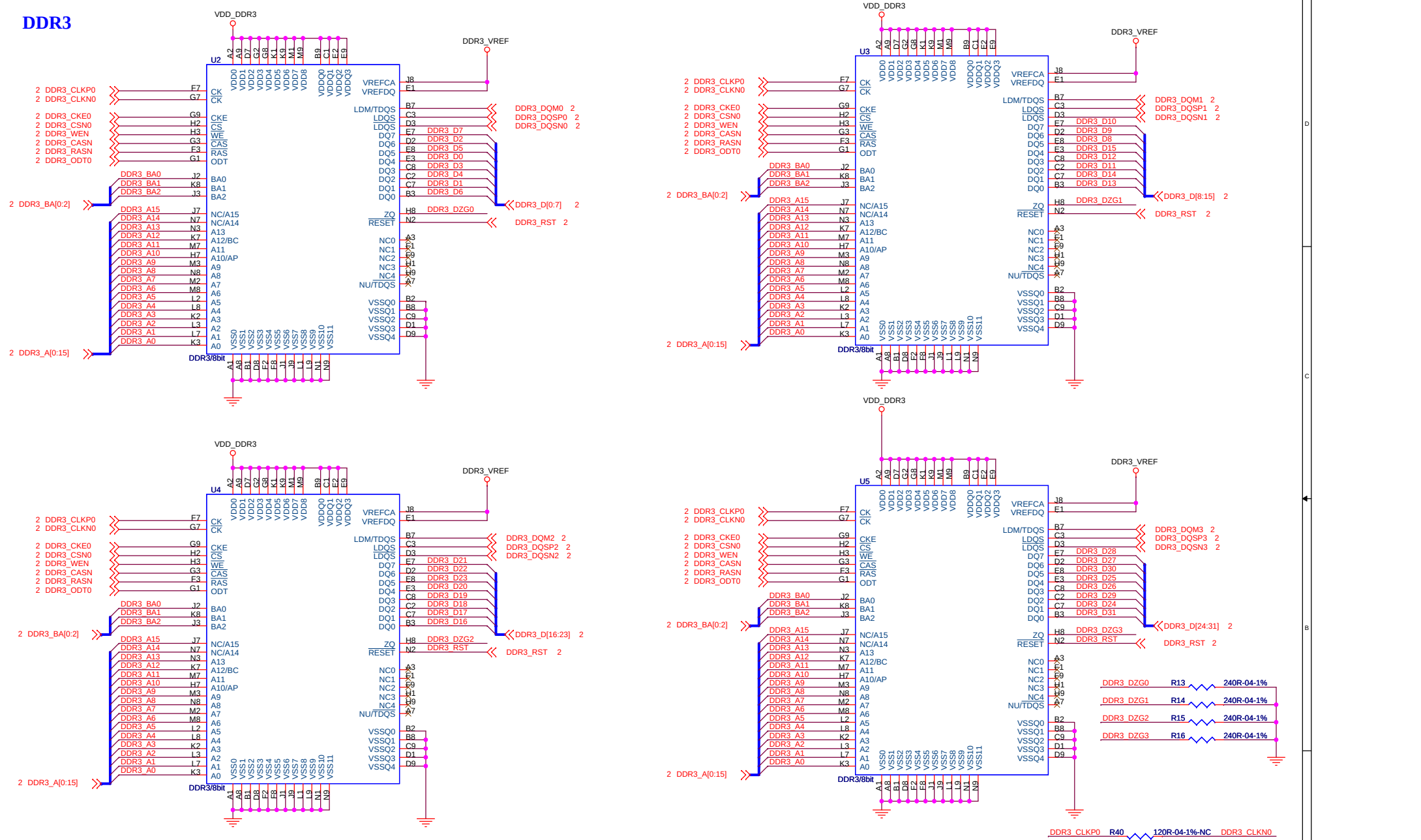
Reset Circuit



PULL Circuit

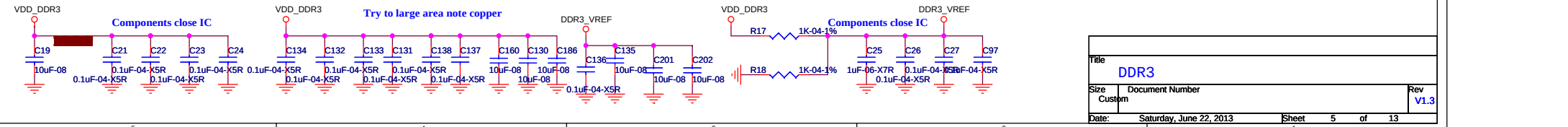


DDR3



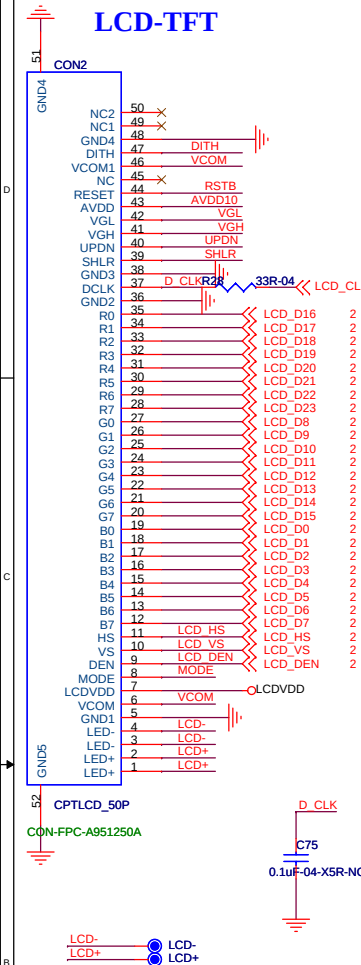
Modify DDR VREF power from V_DDR_CPU to VDD_DDR3 26129912

place to B0T side

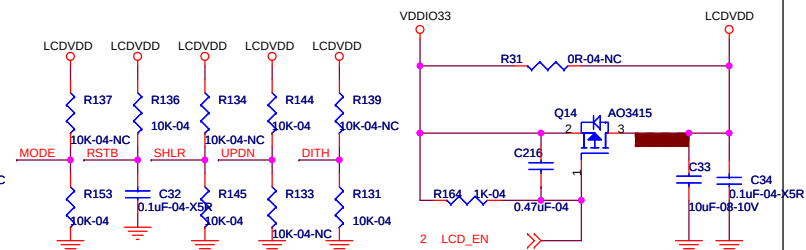
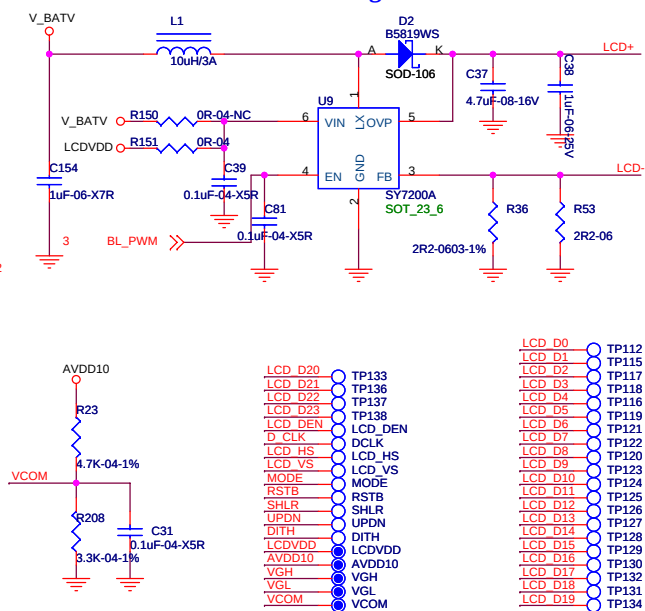


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DDR3		
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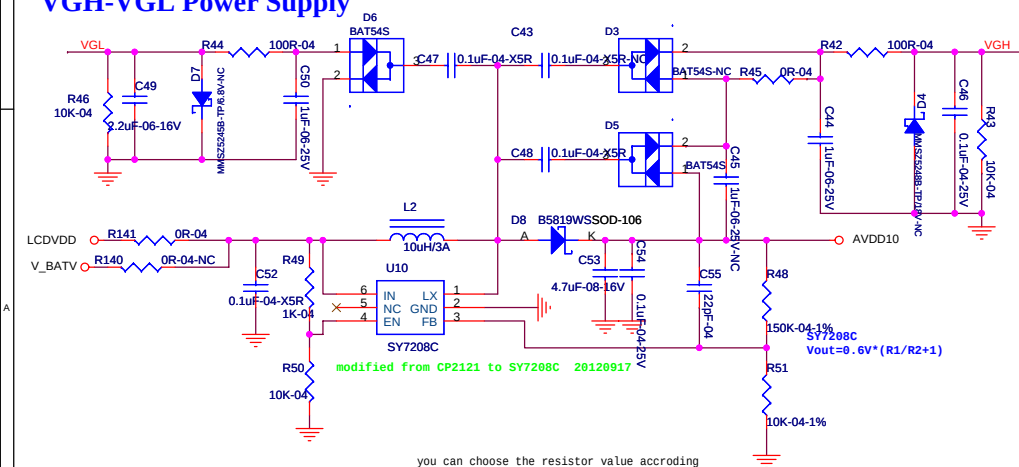
LCD-TFT



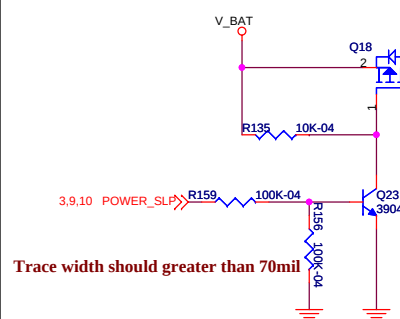
LVDS/LCD Power & Back Light Circuit



VGH-VGL Power Supply

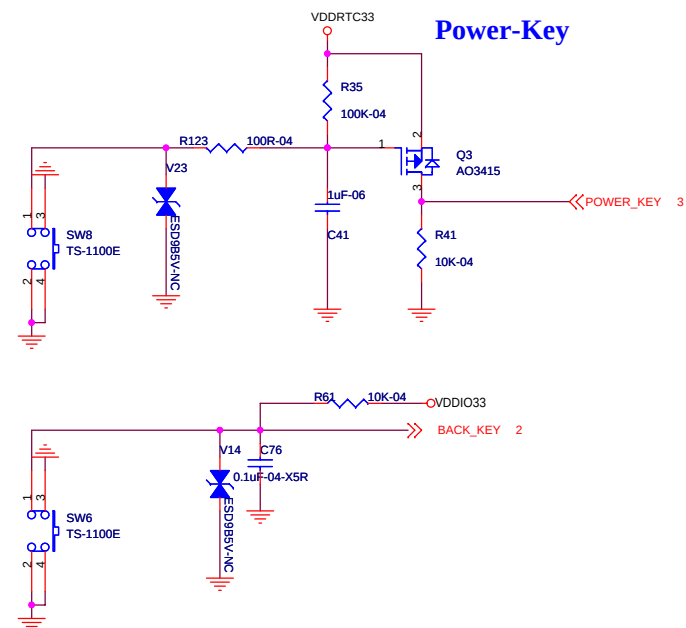


you can choose the resistor value according to the LCD SPEC.

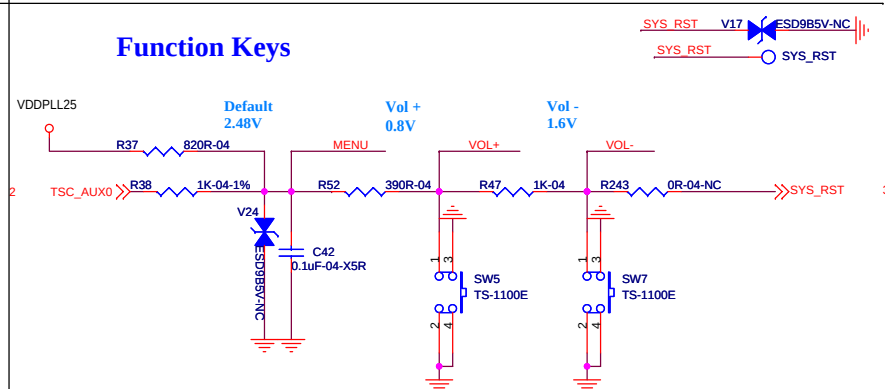


Trace width should greater than 70mil

Power-Key

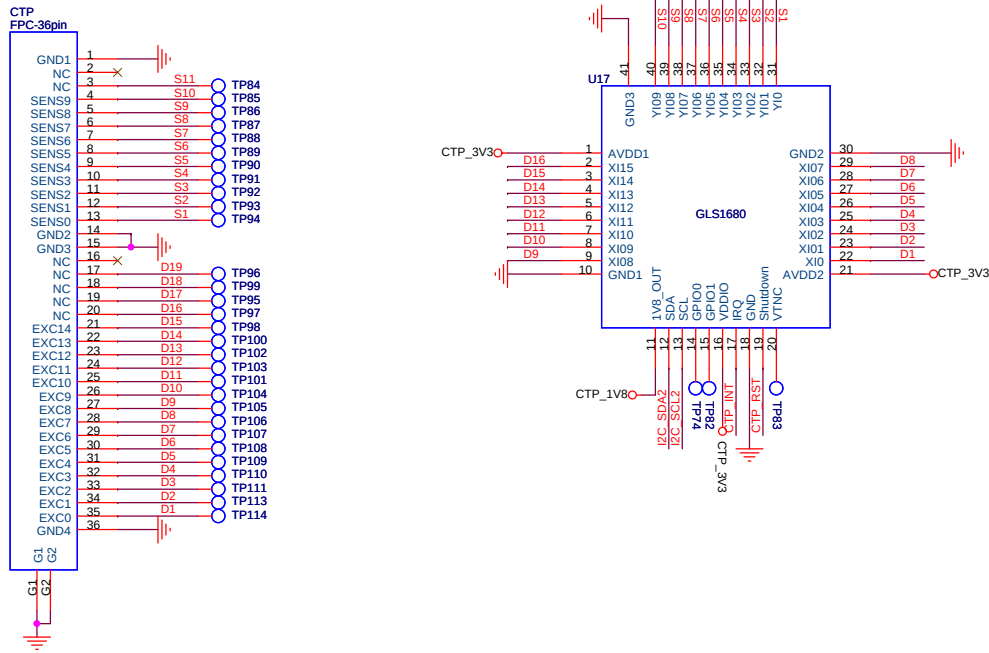


Function Keys



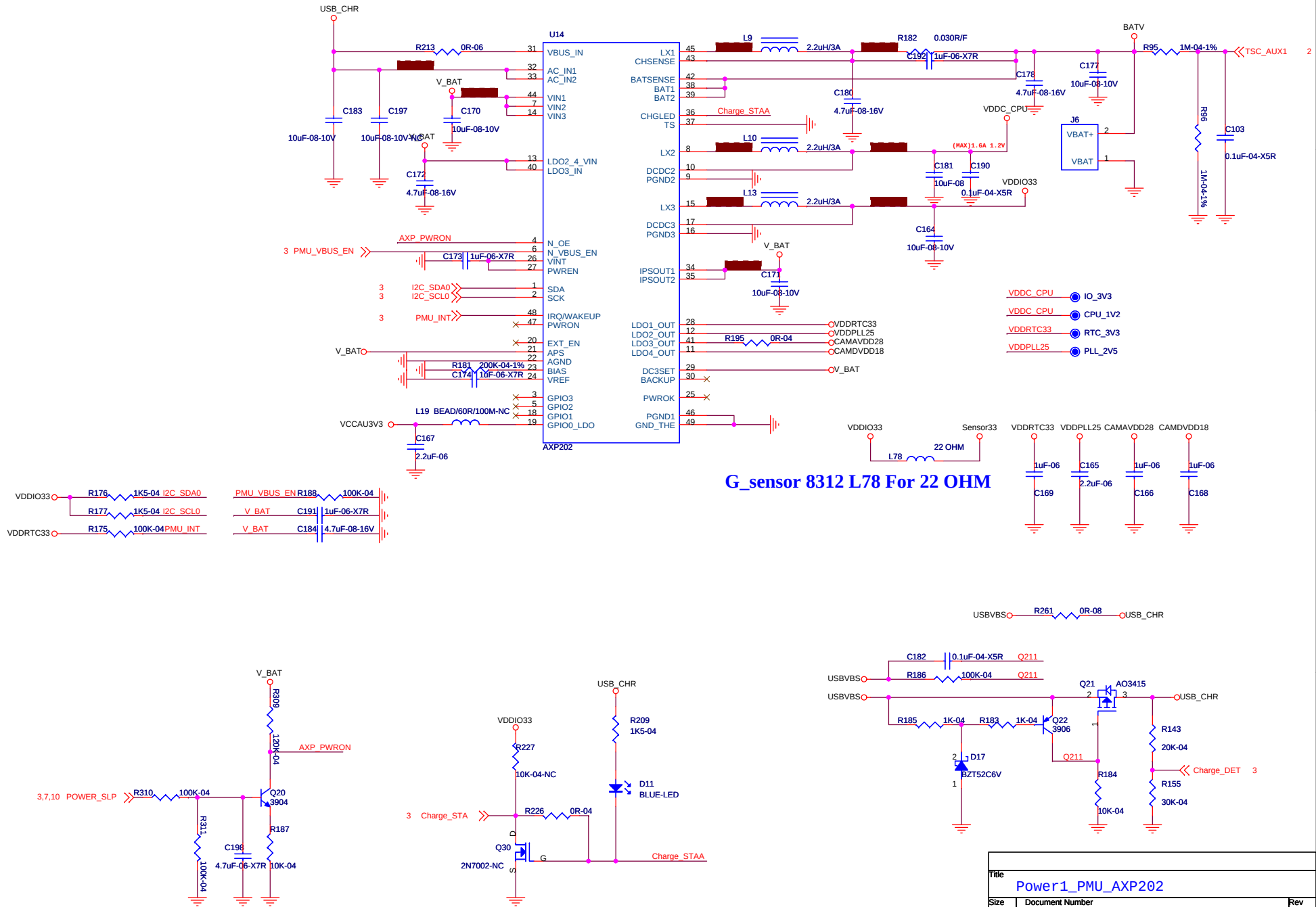
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LCD_KEY				
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Pull High Circuit



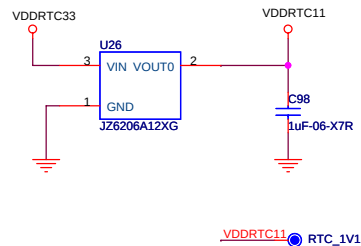
AXP202

- | | |
|---|---------------------------------------|
|  | Trace width should greater than 60mil |
|  | Trace width should greater than 30mil |
|  | Trace width should greater than 15mil |

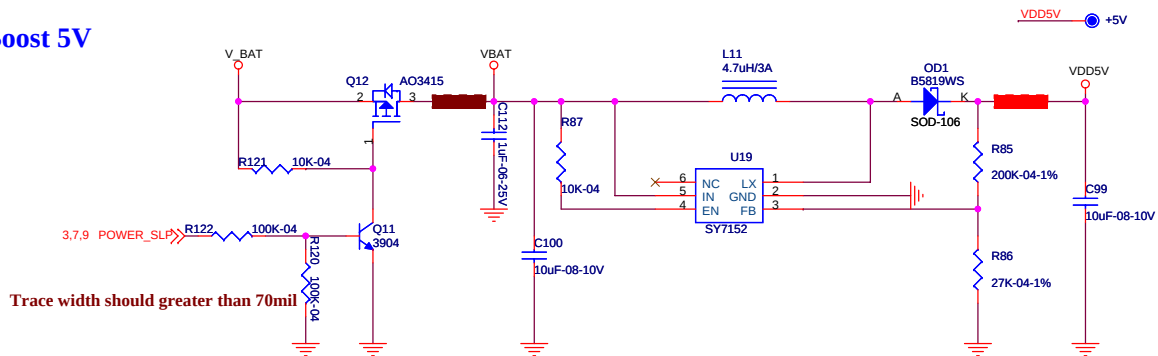


Title			
Power1_PMU_AXP202			
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DC Power Input

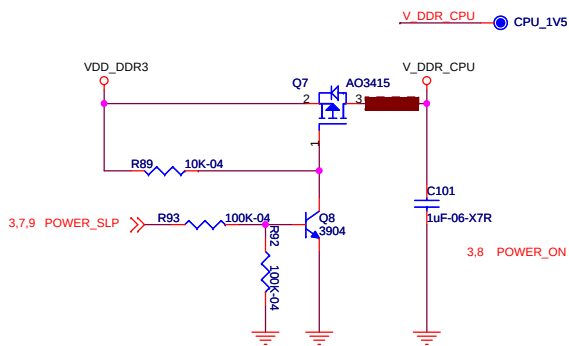


Boost 5V



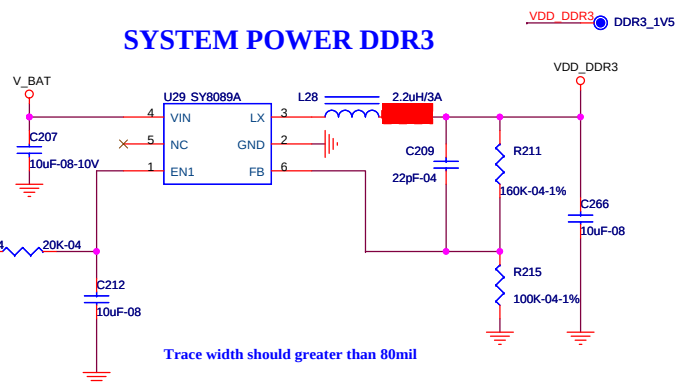
Trace width should greater than 70mil

V_DDR_CPU



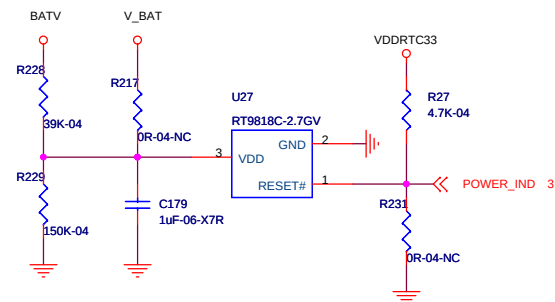
Trace width should greater than 60mil

SYSTEM POWER DDR3

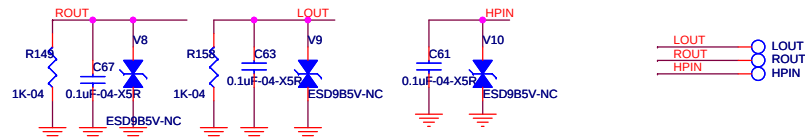
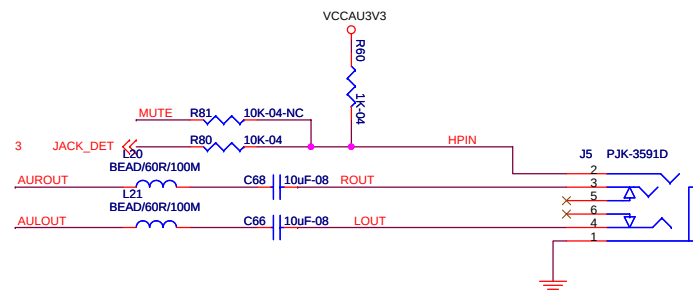
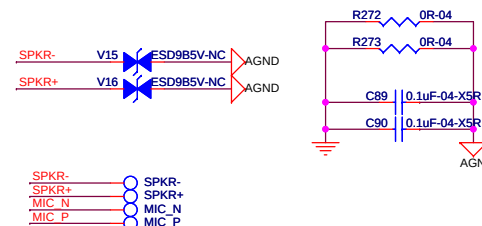
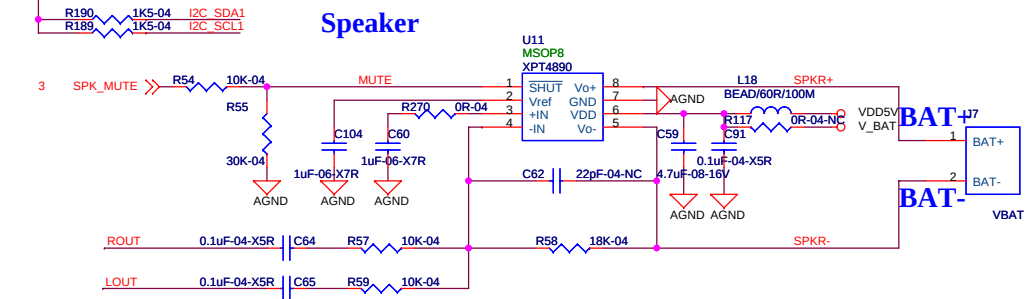
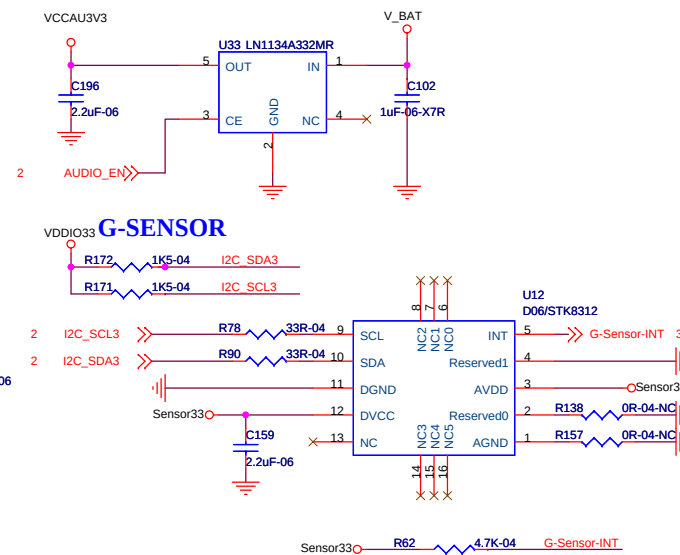
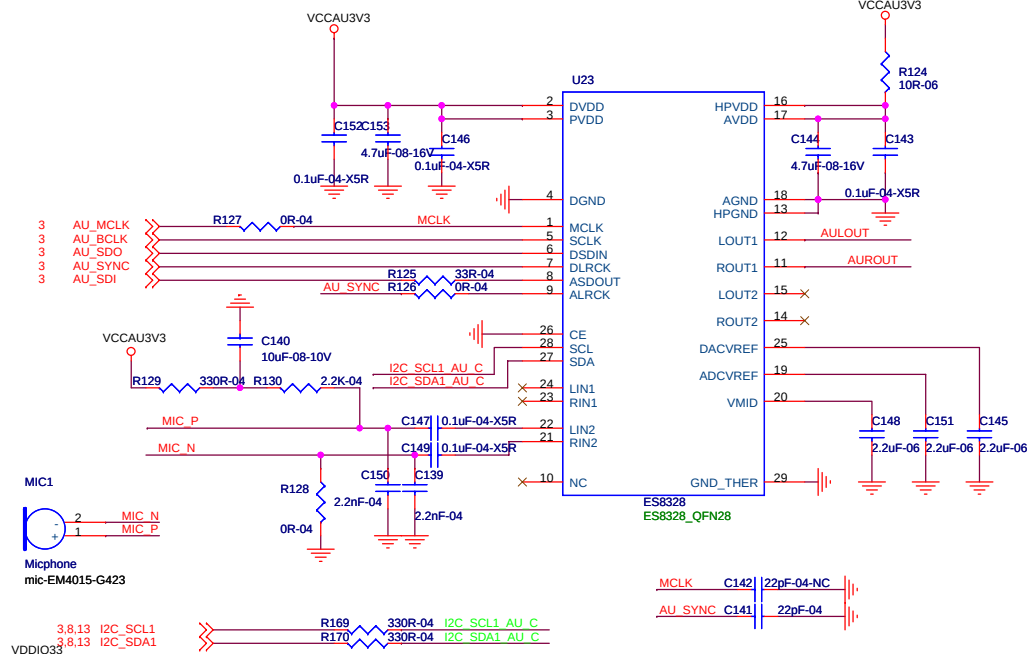


Trace width should greater than 80mil

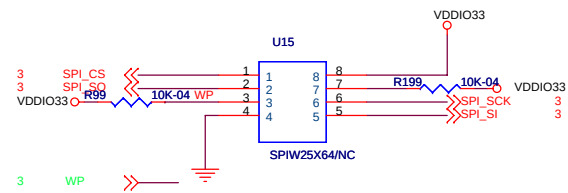
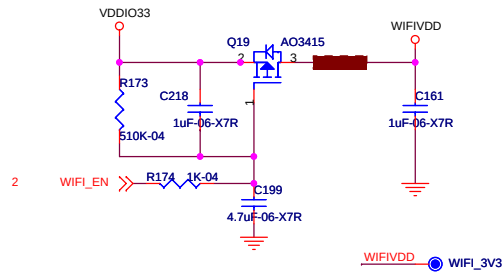
battery low voltage for 3.3v



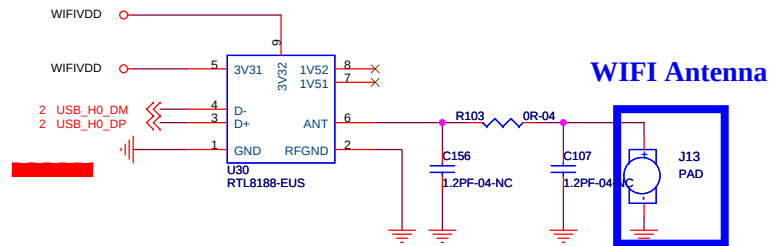
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Power2_DC to DC		
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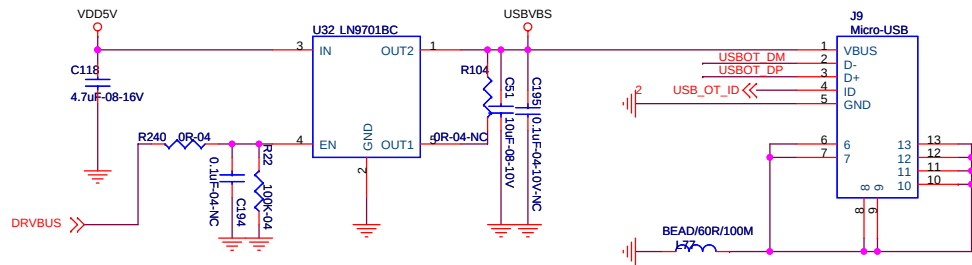
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AUDIO ES8328_SENSOR				
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USB WIFI

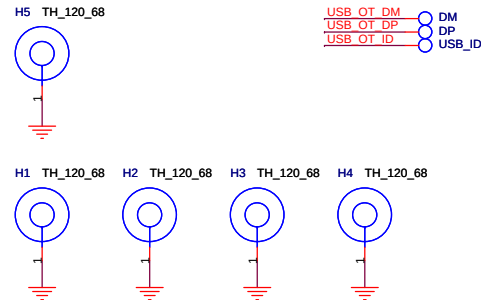
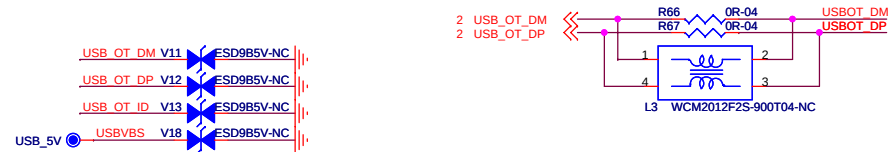
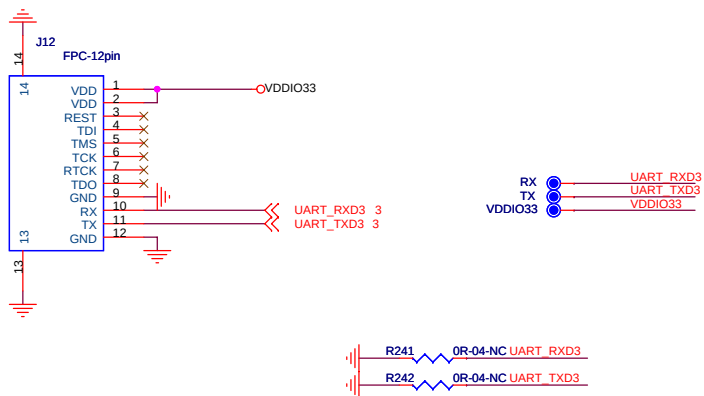


OTG/HOST/Charger



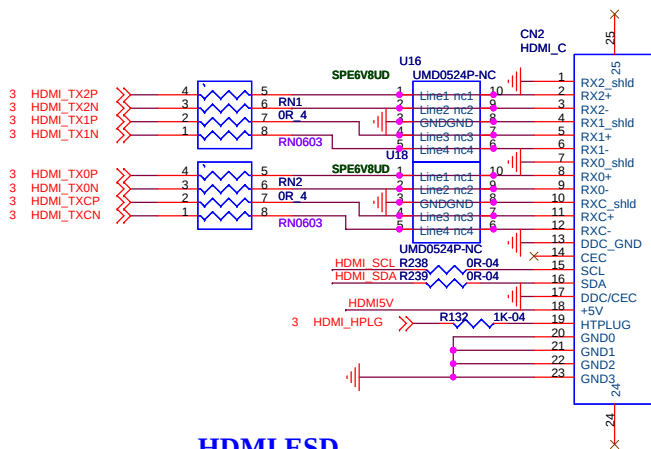
JTAG Connector

JTAG Connector

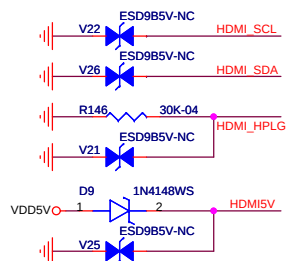


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WIFI SDOMTK5931_USB		
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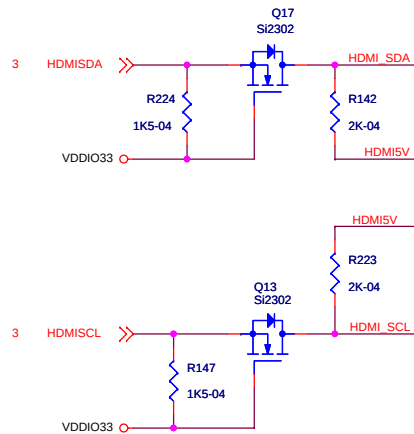
HDMI



HDMI ESD



HDMI LEVEL SHIFT



BT

