

APPENDIX FCC407 UNII 1

WIR128375

FCC407 UNII 1 Data

§ 15.203 Antenna Requirement

No appendix data.

§ 15. 403 26dB Bandwidth

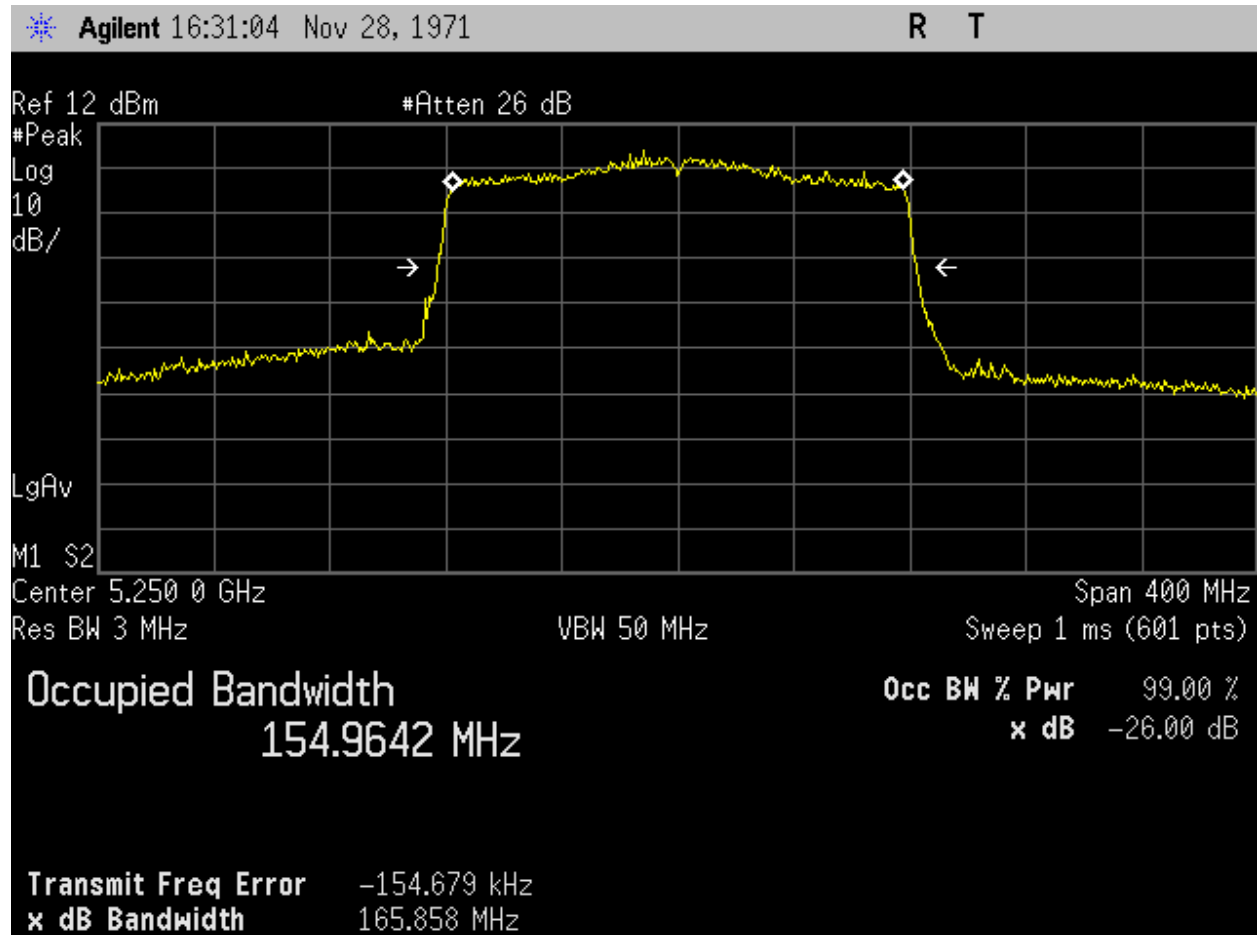


Figure 1: U-NII-1,2A_5250MHz_Mid_Mid Ch_50_160MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 1.

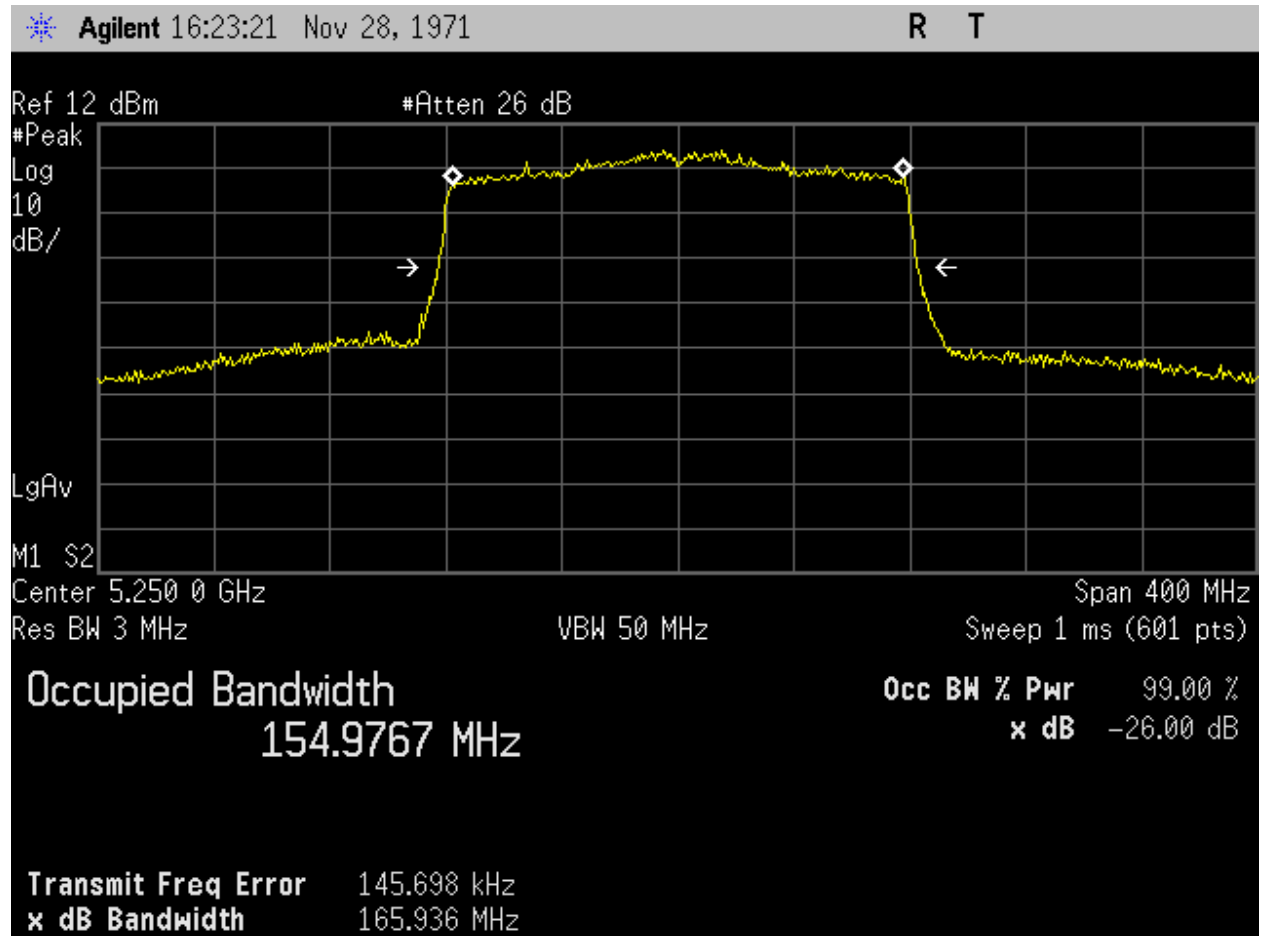


Figure 2: U-NII-1,2A_5250MHz_Mid_Mid Ch_50_160MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 2.

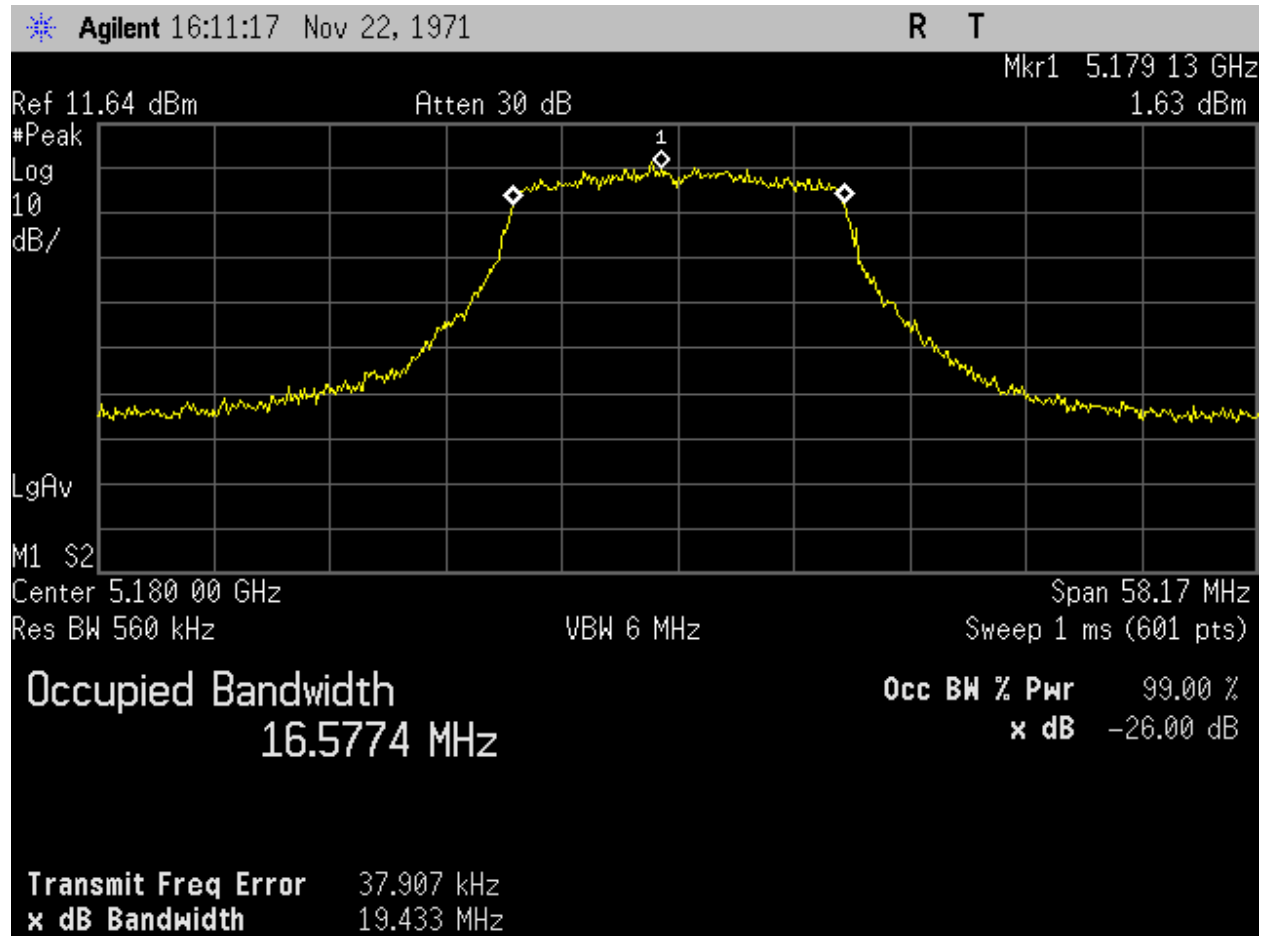


Figure 3: U-NII-1_5180MHz_Low Ch_36_20MHz BW_a-mode_Occupied Bandwidth_-26dB_Port 1.

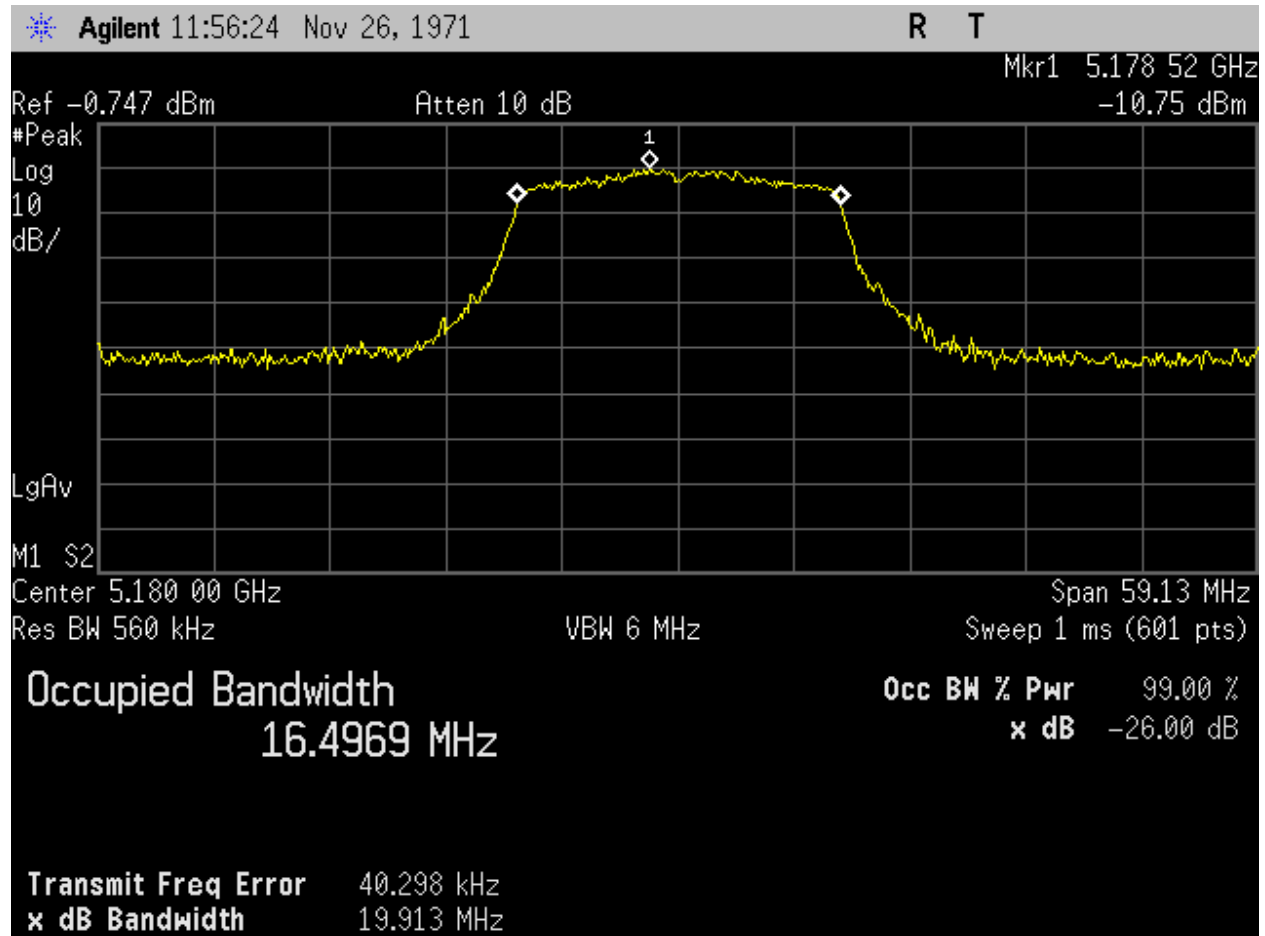


Figure 4: U-NII-1_5180MHz_Low Ch_36_20MHz BW_a-mode_Occupied Bandwidth_-26dB_Port 2.

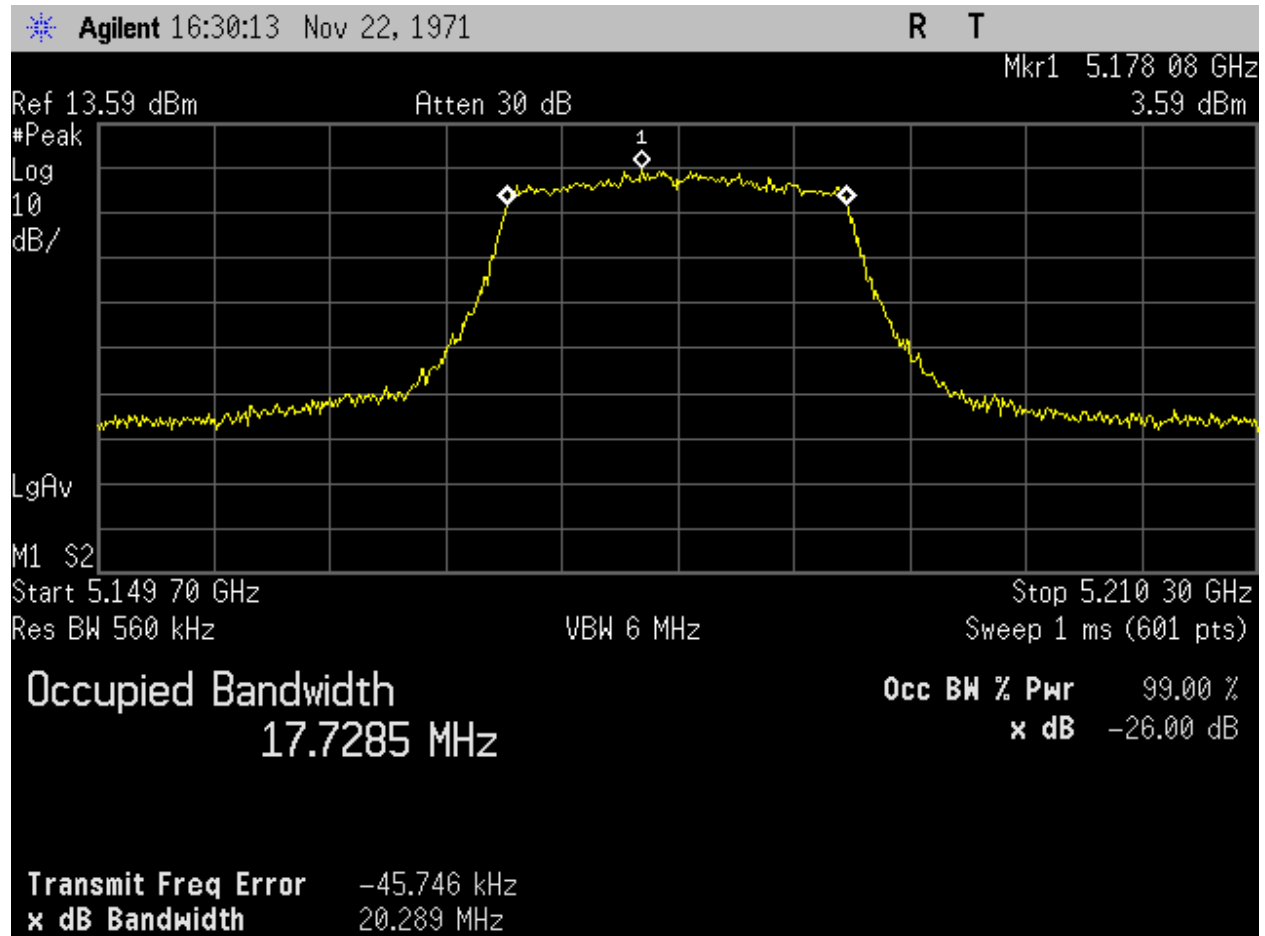


Figure 5: U-NII-1_5180MHz_Low Ch_36_20MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 1.

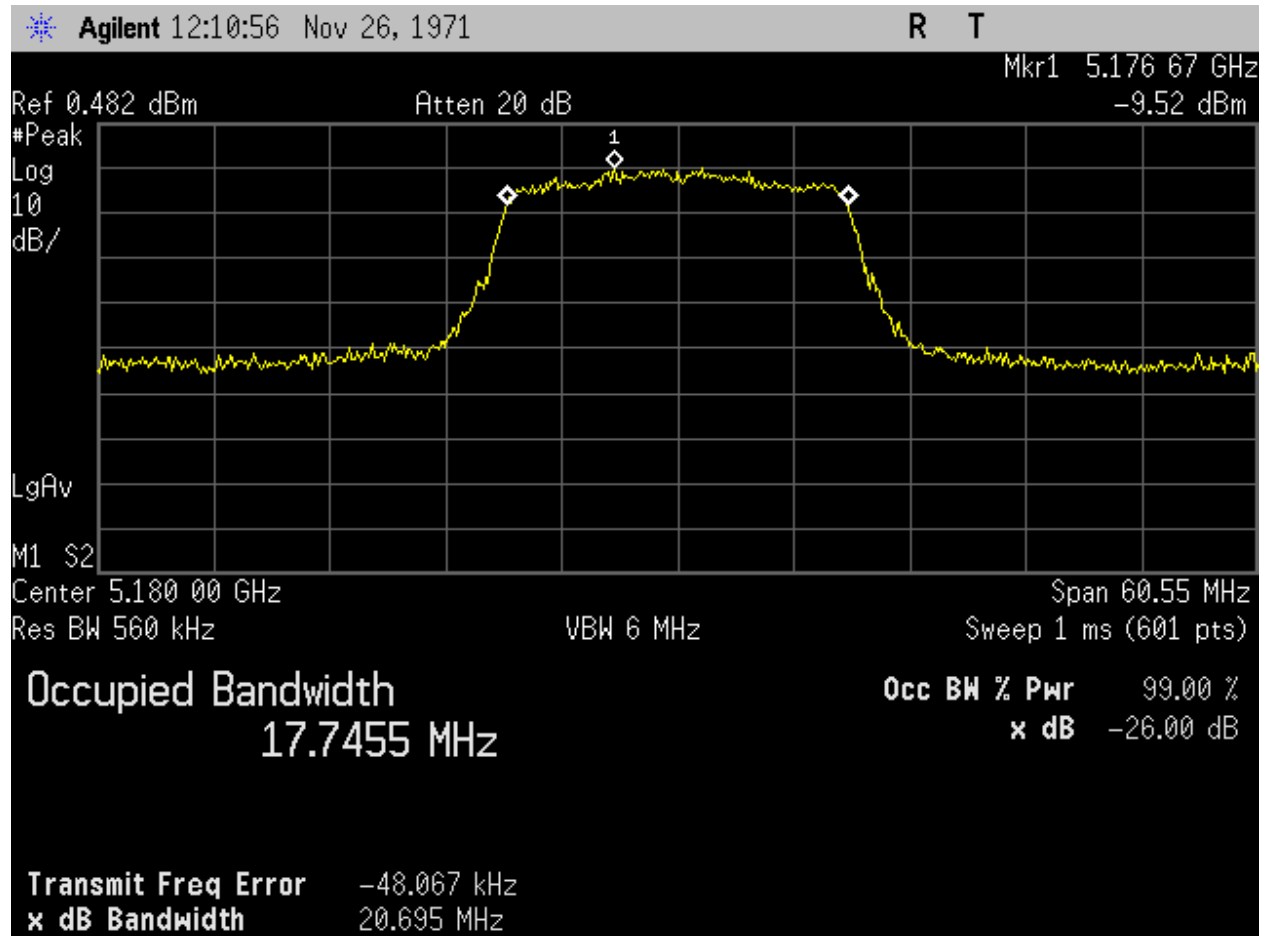


Figure 6: U-NII-1_5180MHz_Low Ch_36_20MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 2.

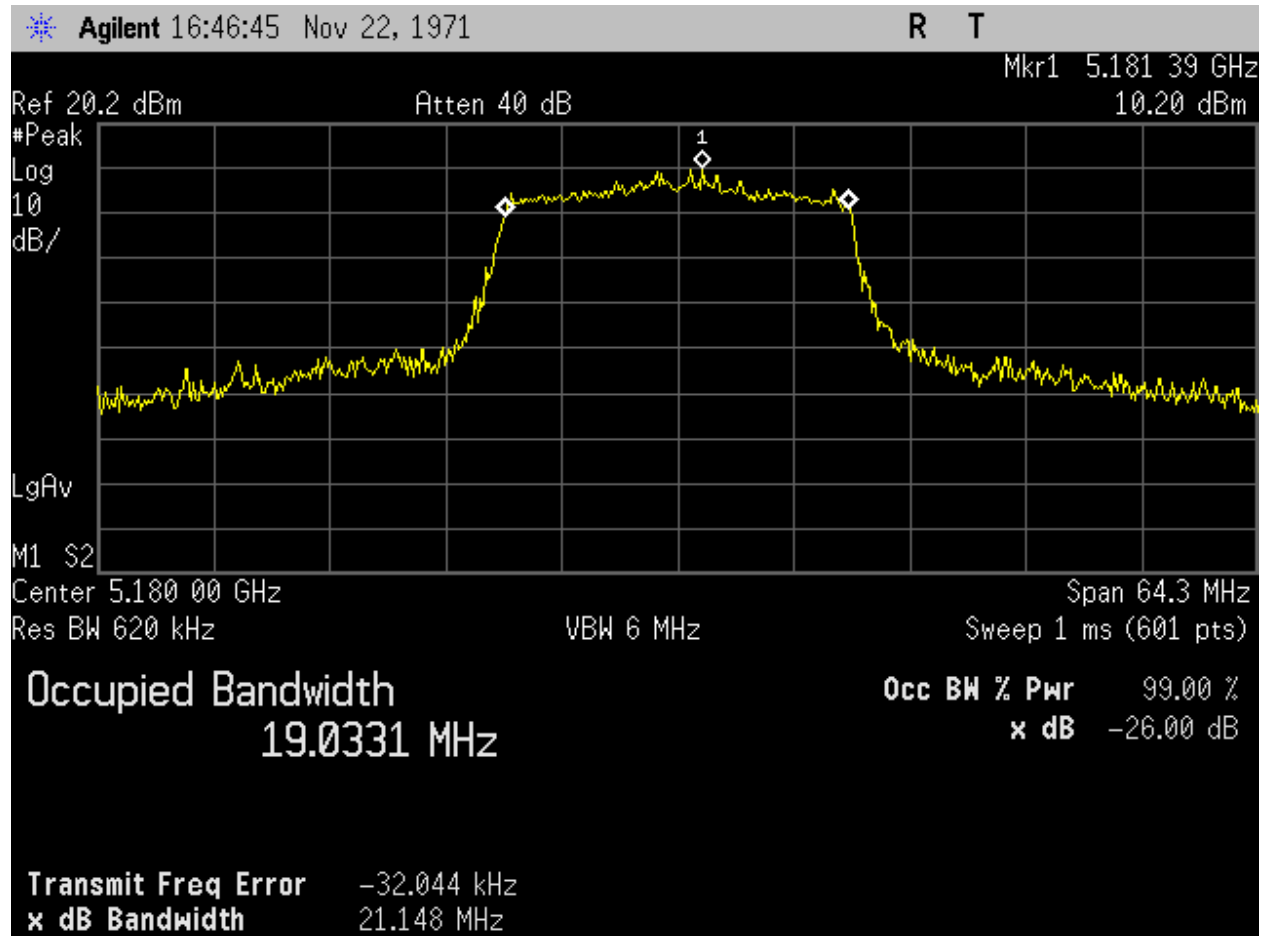


Figure 7: U-NII-1_5180MHz_Low Ch_36_20MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 1.

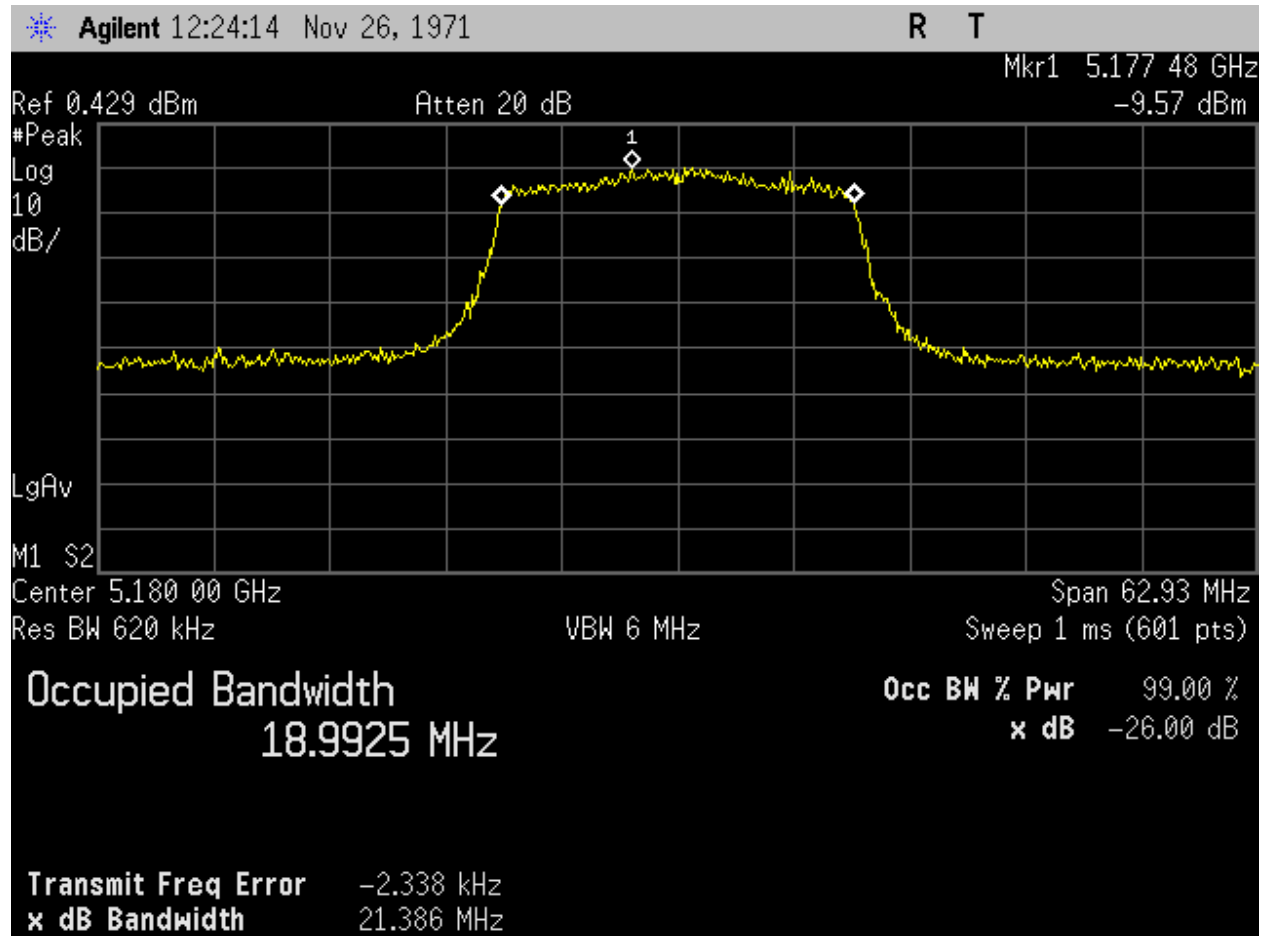


Figure 8: U-NII-1_5180MHz_Low Ch_36_20MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 2.

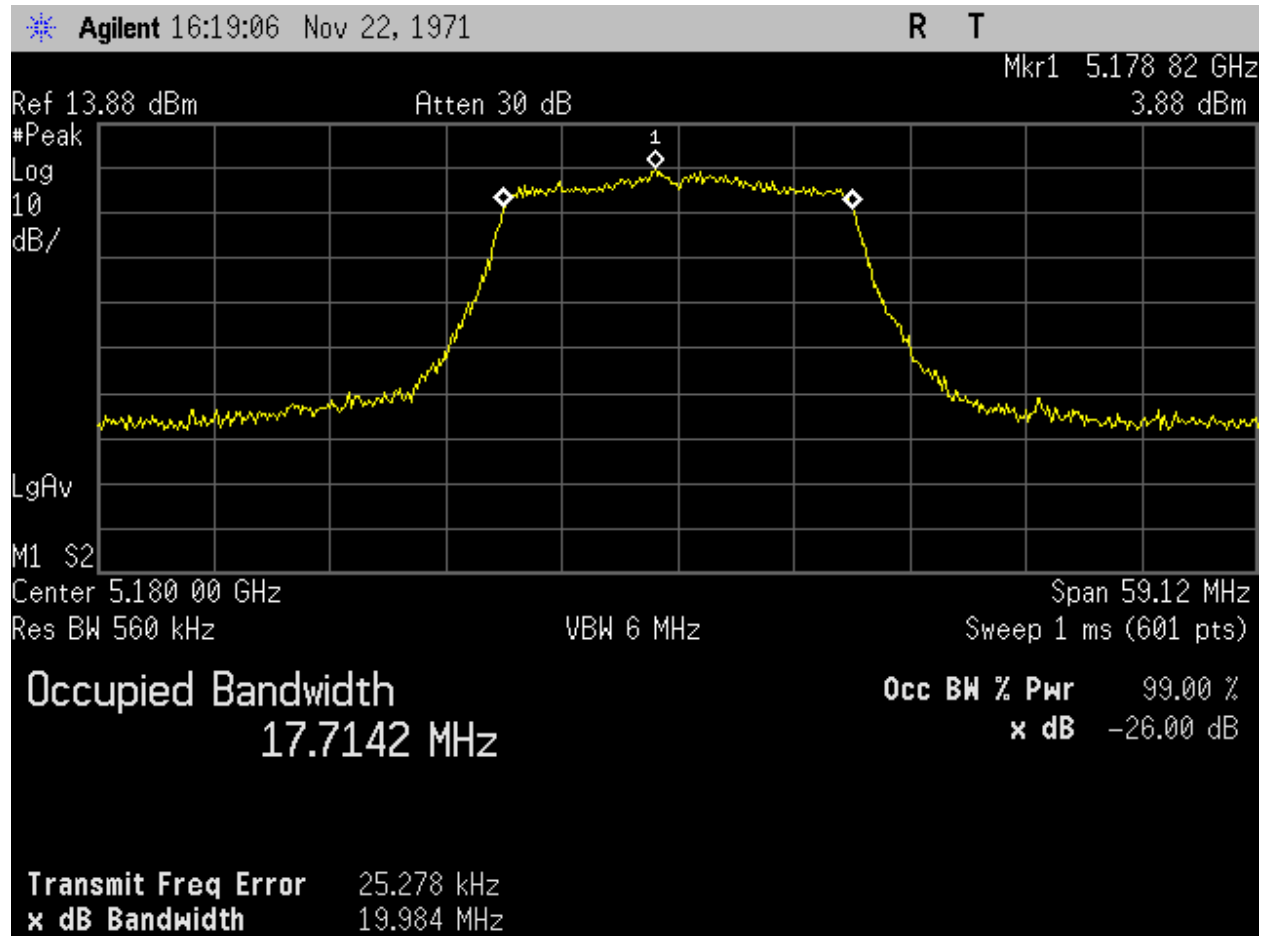


Figure 9: U-NII-1_5180MHz_Low Ch_36_20MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 1.

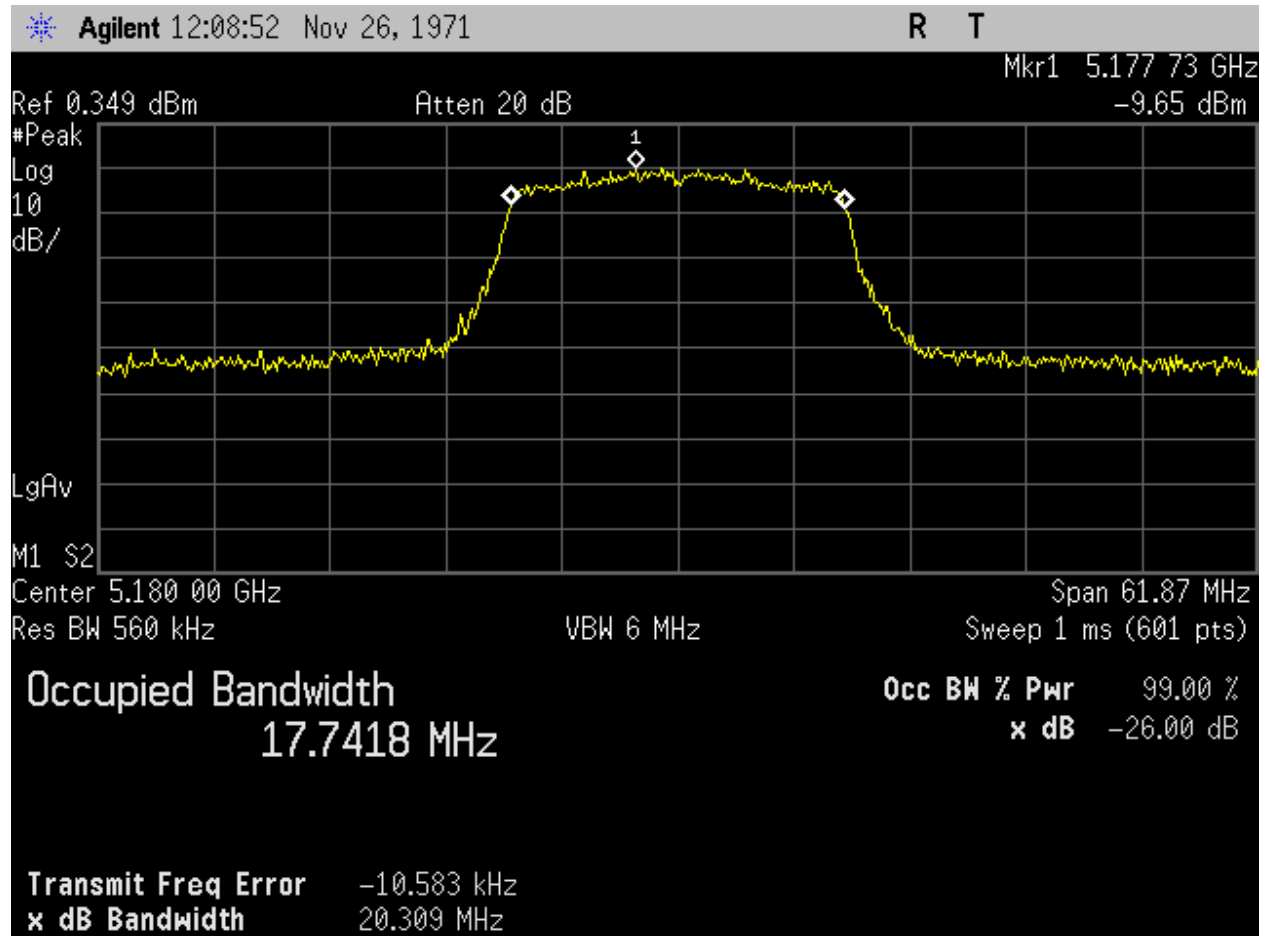


Figure 10: U-NII-1_5180MHz_Low Ch_36_20MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 2.

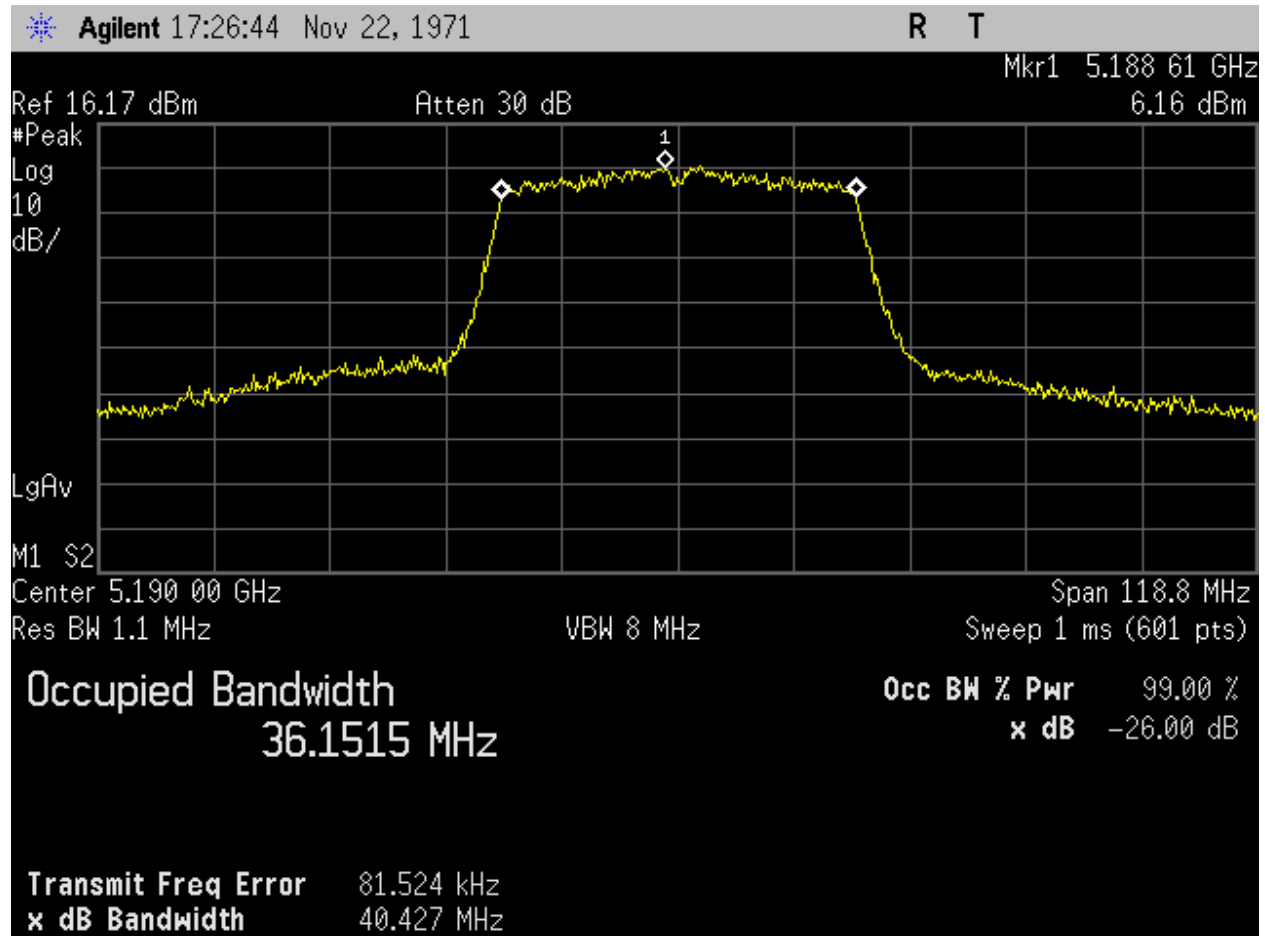


Figure 11: U-NII-1_5190MHz_Low Ch_38_40MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 1.

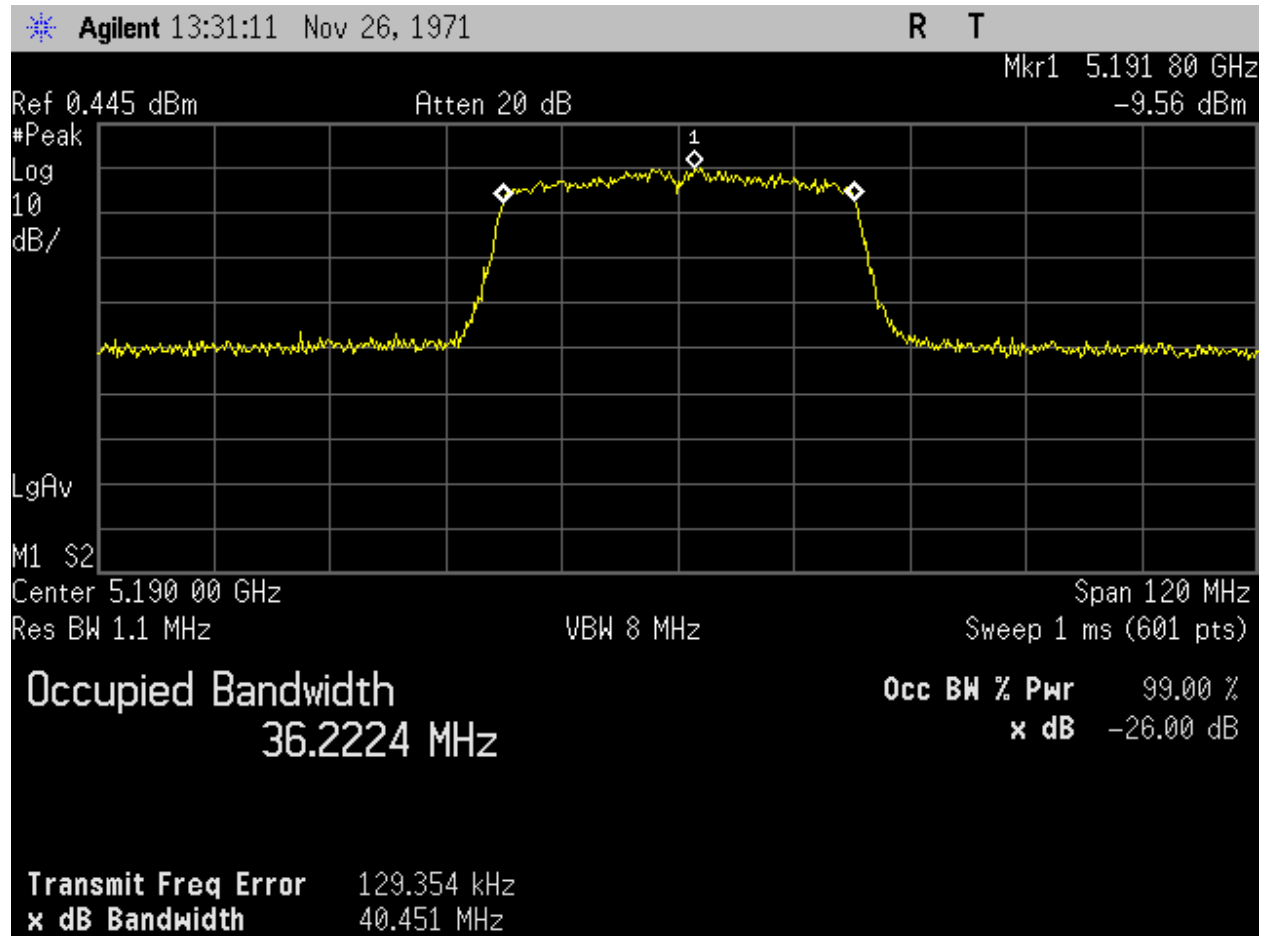


Figure 12: U-NII-1_5190MHz_Low Ch_38_40MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 2.

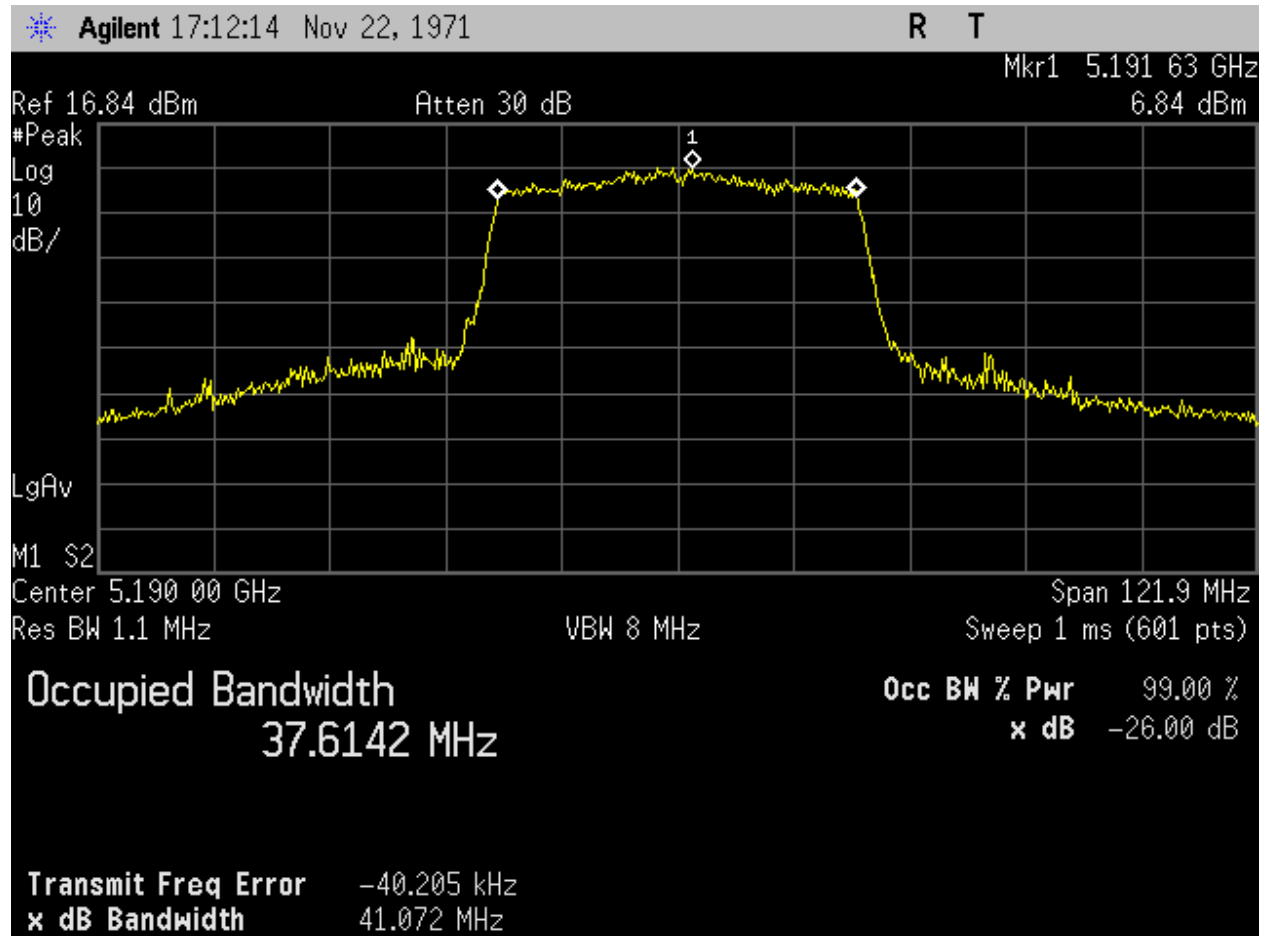


Figure 13: U-NII-1_5190MHz_Low Ch_38_40MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 1.

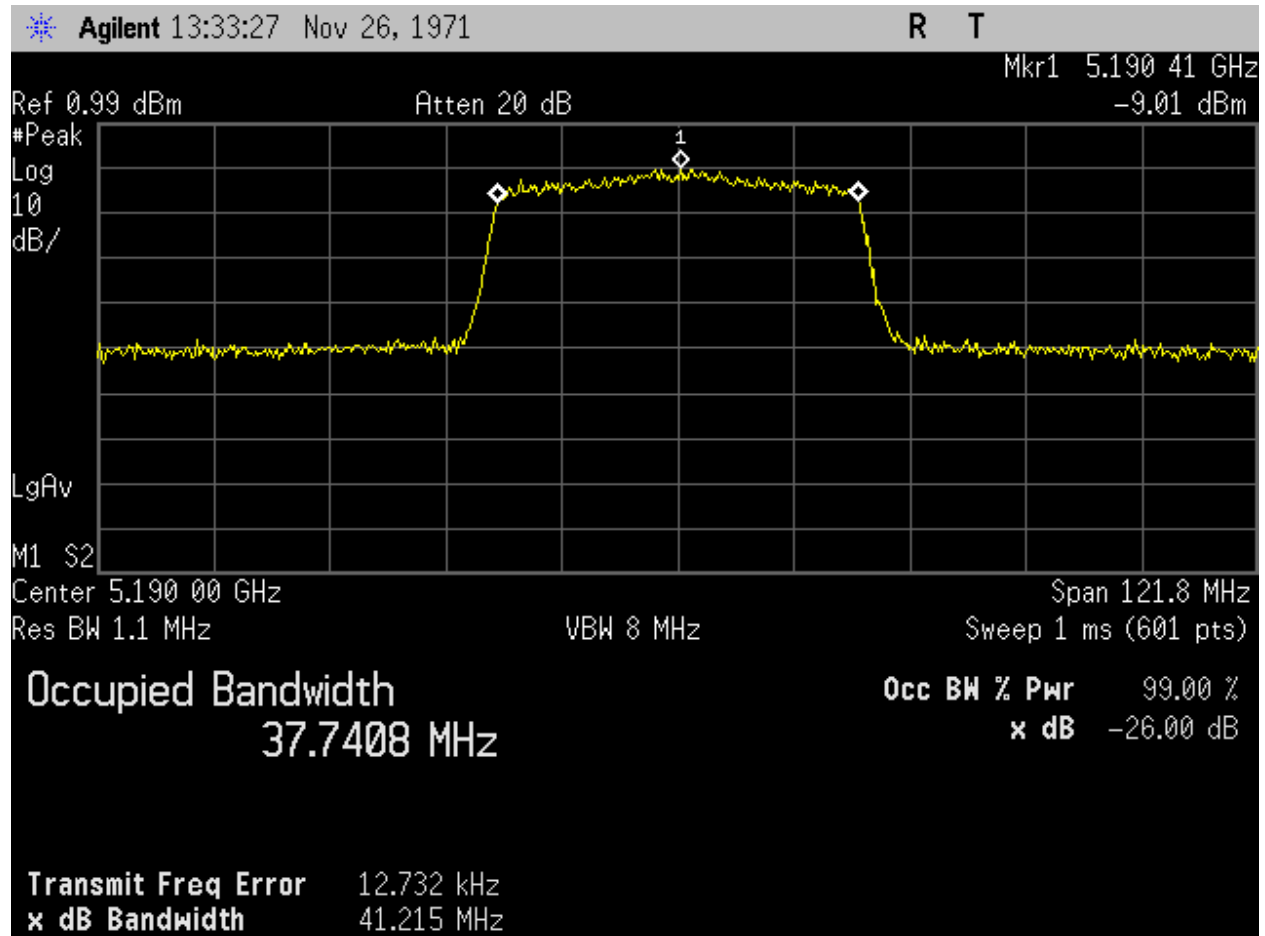


Figure 14: U-NII-1_5190MHz_Low Ch_38_40MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 2.

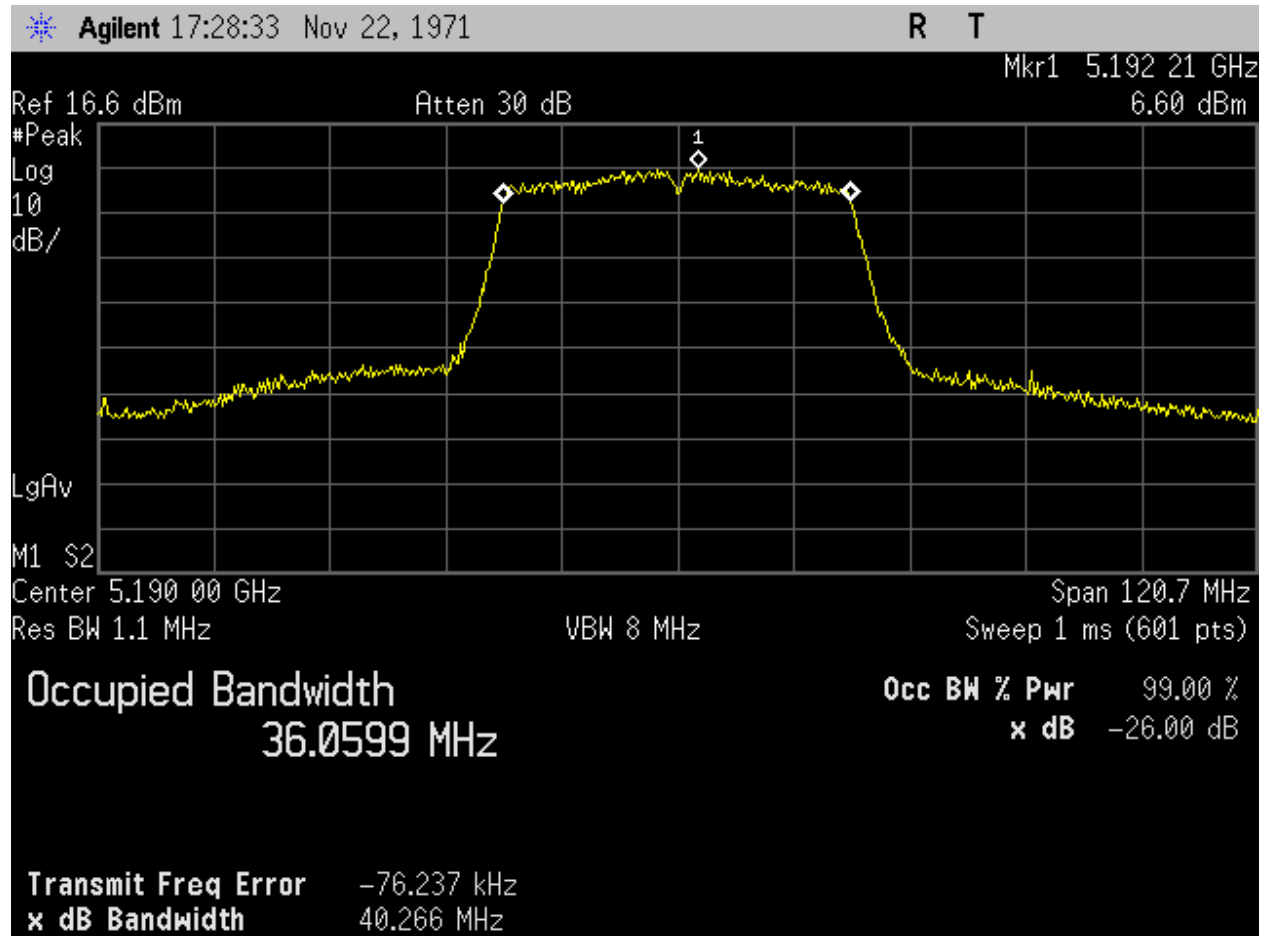


Figure 15: U-NII-1_5190MHz_Low Ch_38_40MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 1.

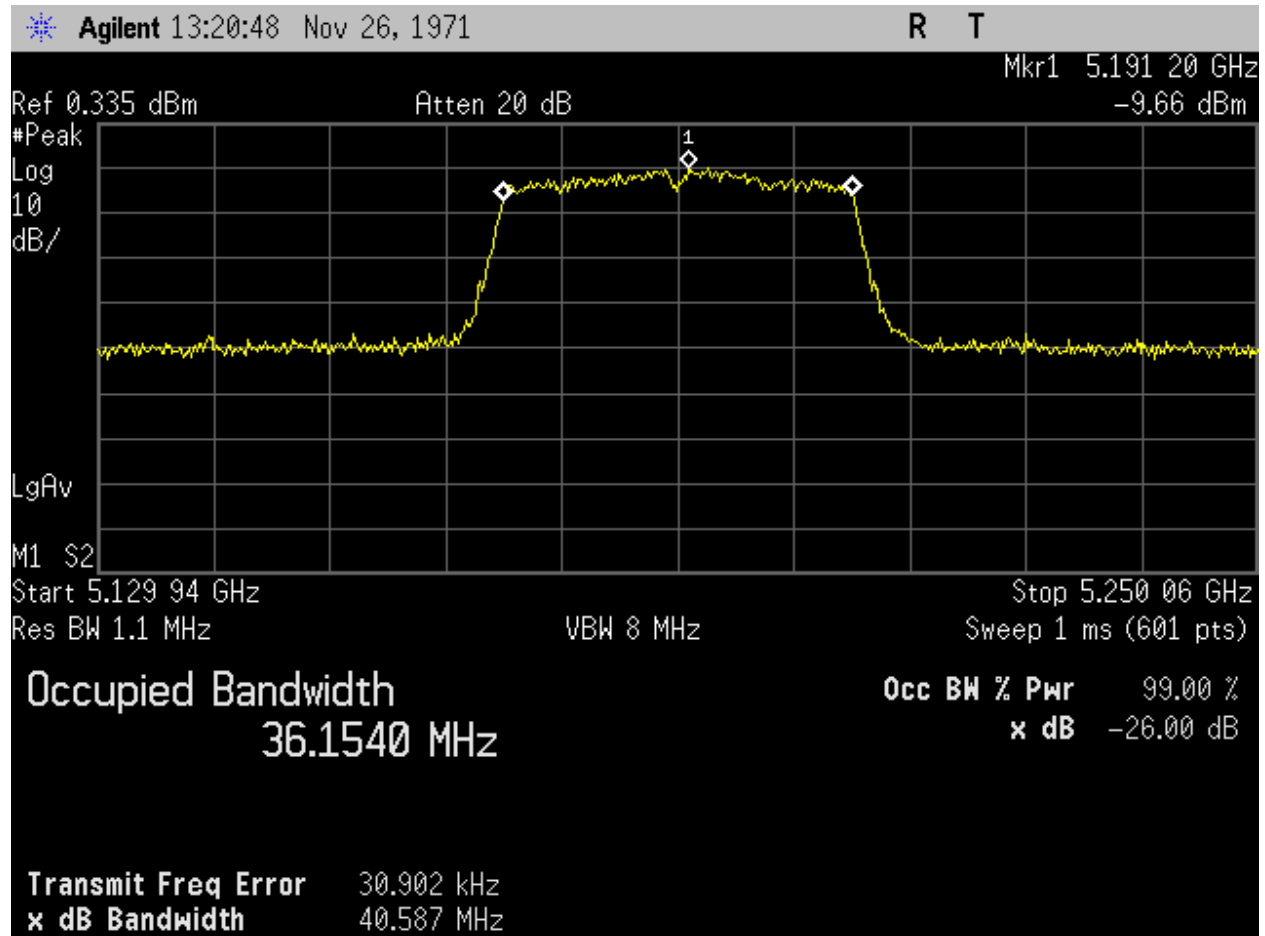


Figure 16: U-NII-1_5190MHz_Low Ch_38_40MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 2.

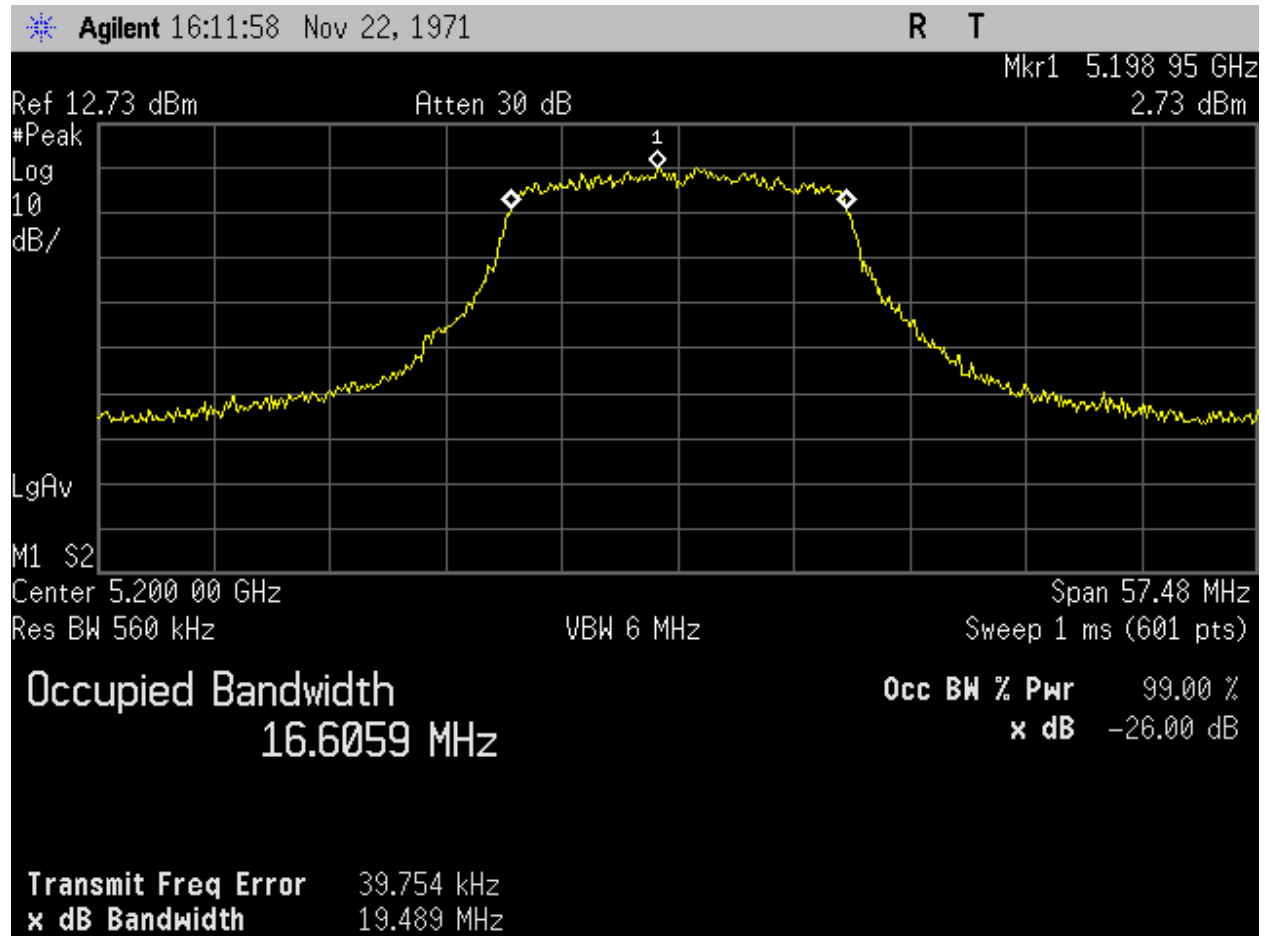


Figure 17: U-NII-1_5200MHz_low_Mid Ch_40_20MHz BW_a-mode_Occupied Bandwidth_-26dB_Port 1.

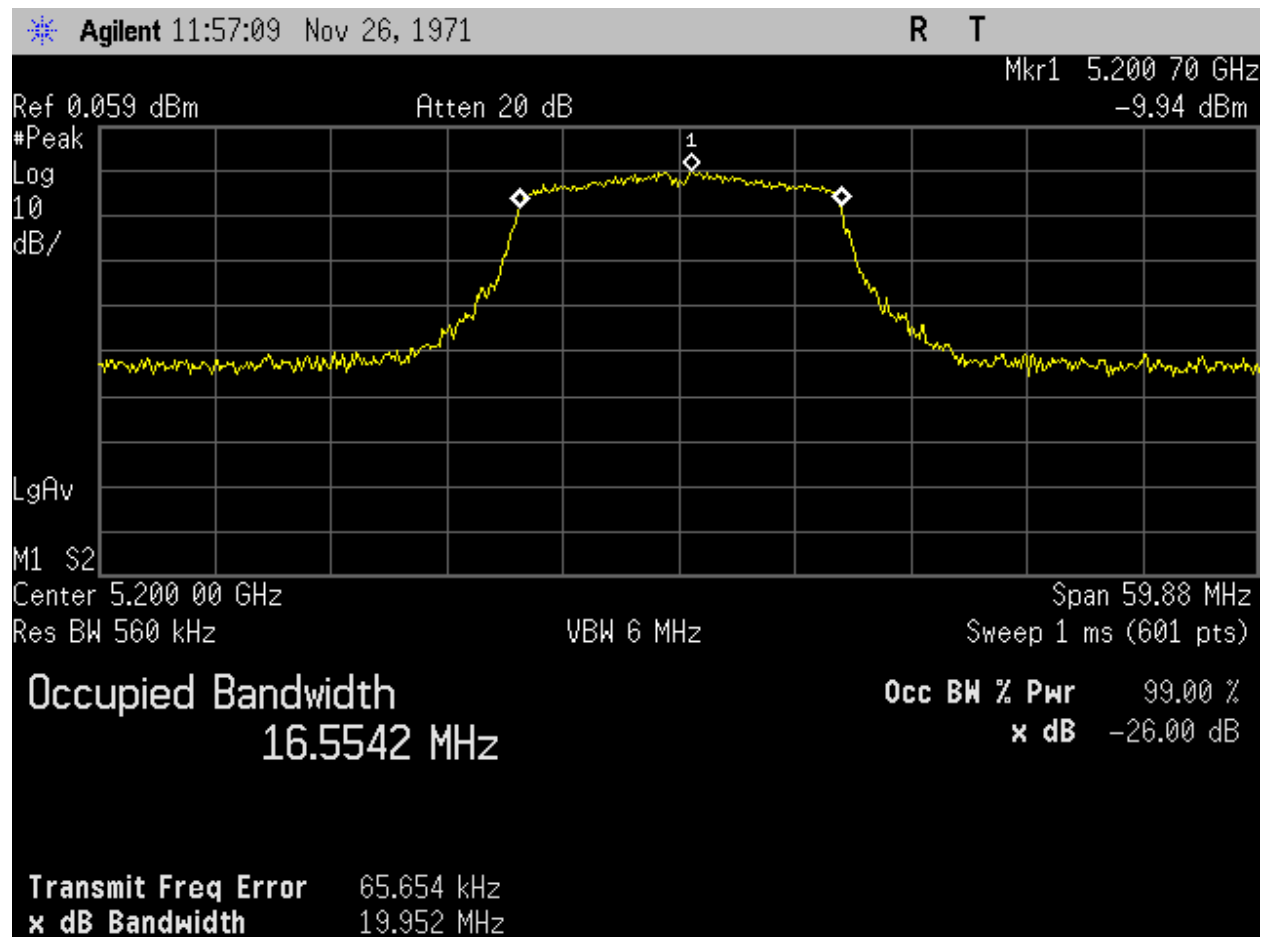


Figure 18: U-NII-1_5200MHz_low_Mid Ch_40_20MHz BW_a-mode_Occupied Bandwidth_-26dB_Port 2.

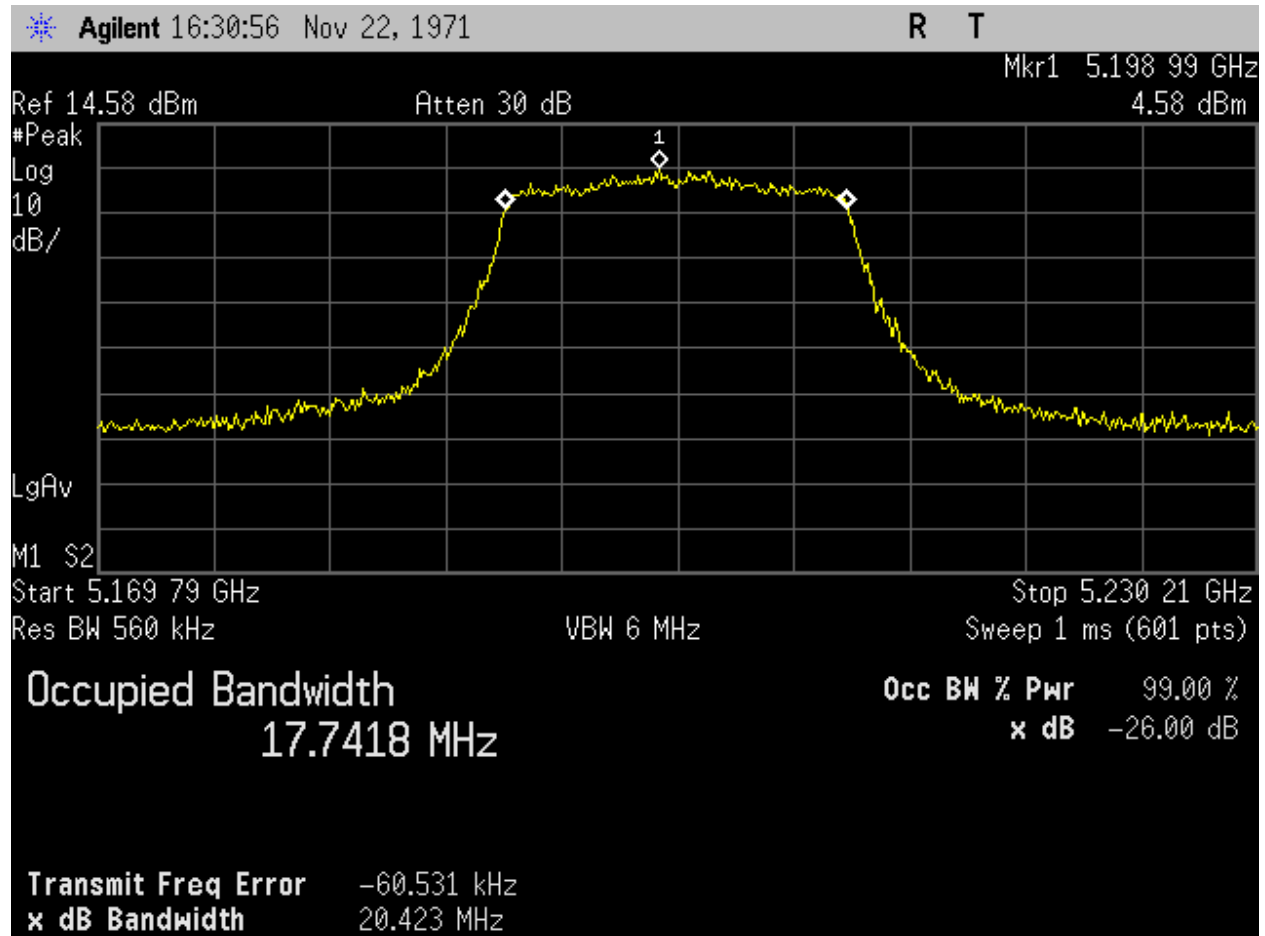


Figure 19: U-NII-1_5200MHz_low_Mid Ch_40_20MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 1.

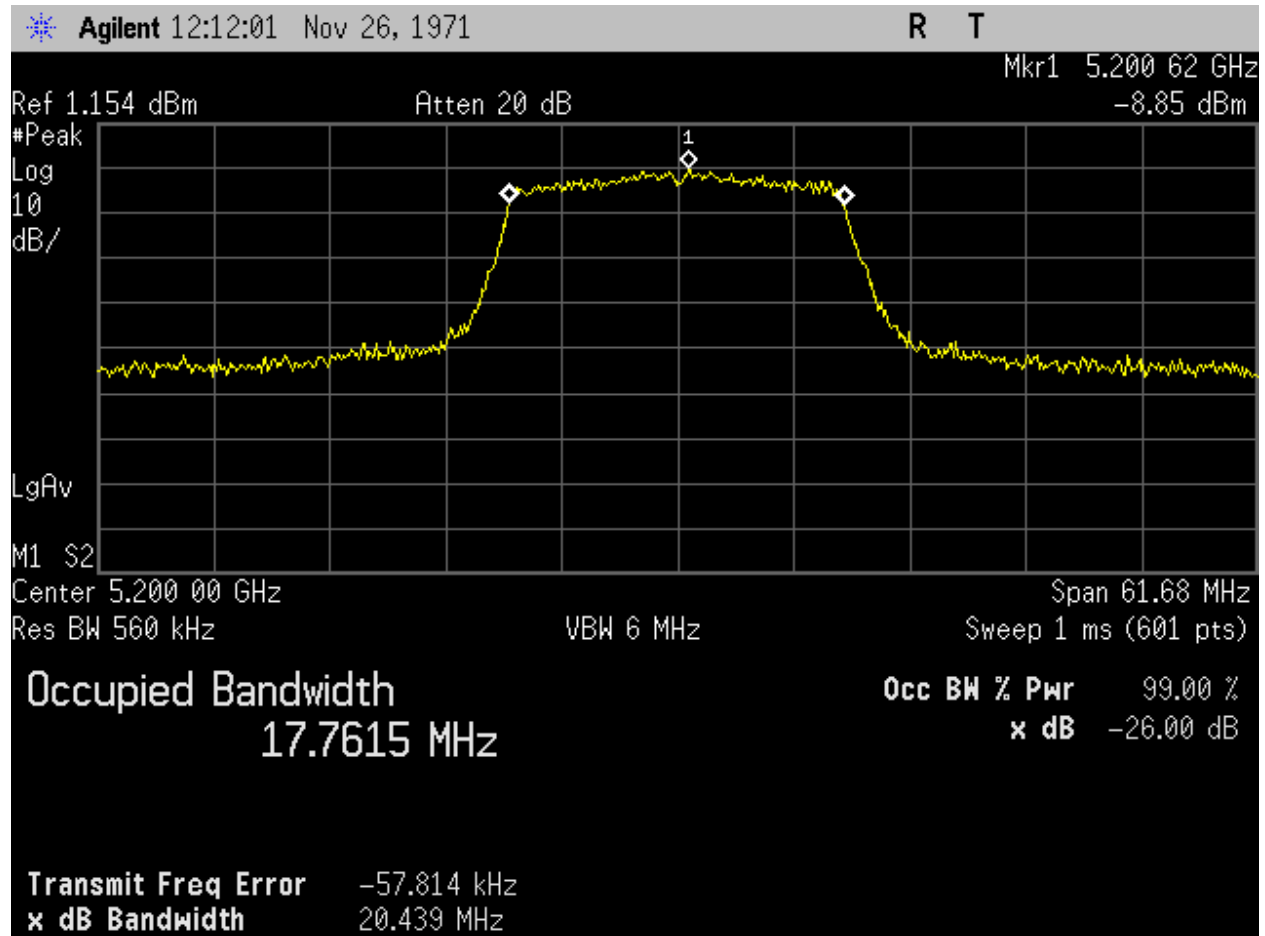


Figure 20: U-NII-1_5200MHz_low_Mid Ch_40_20MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 2.

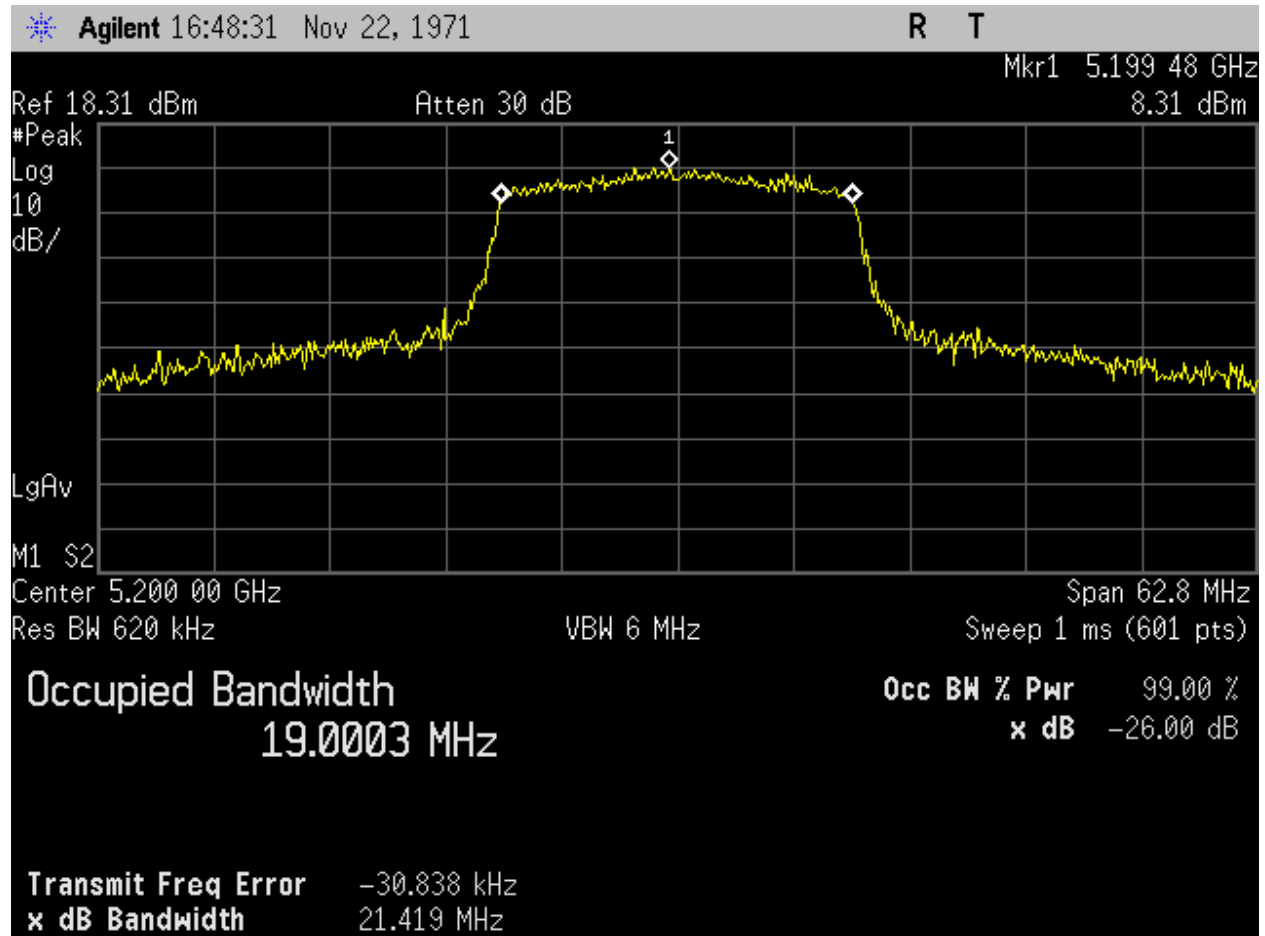


Figure 21: U-NII-1_5200MHz_low_Mid Ch_40_20MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 1.

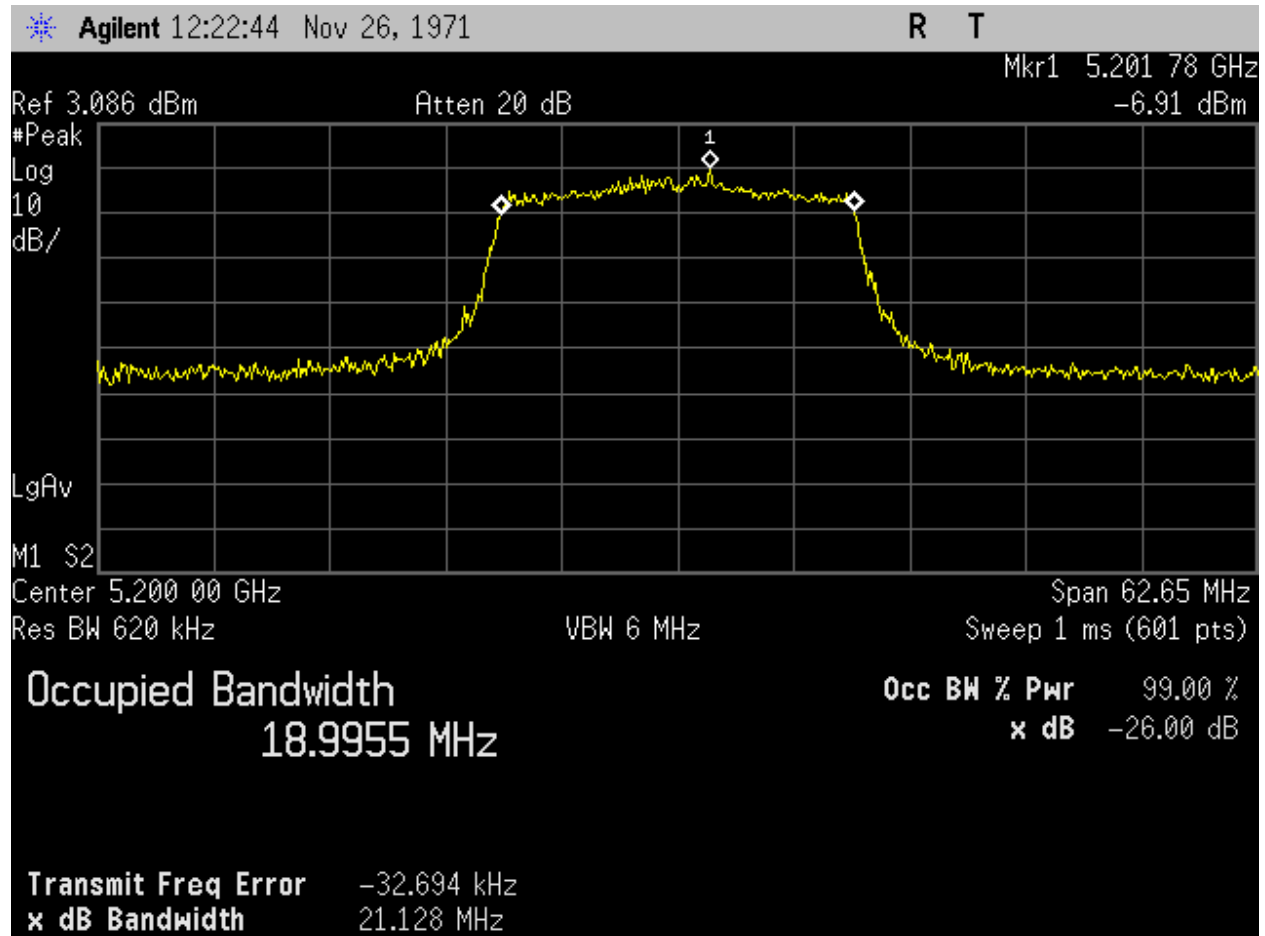


Figure 22: U-NII-1_5200MHz_low_Mid Ch_40_20MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 2.

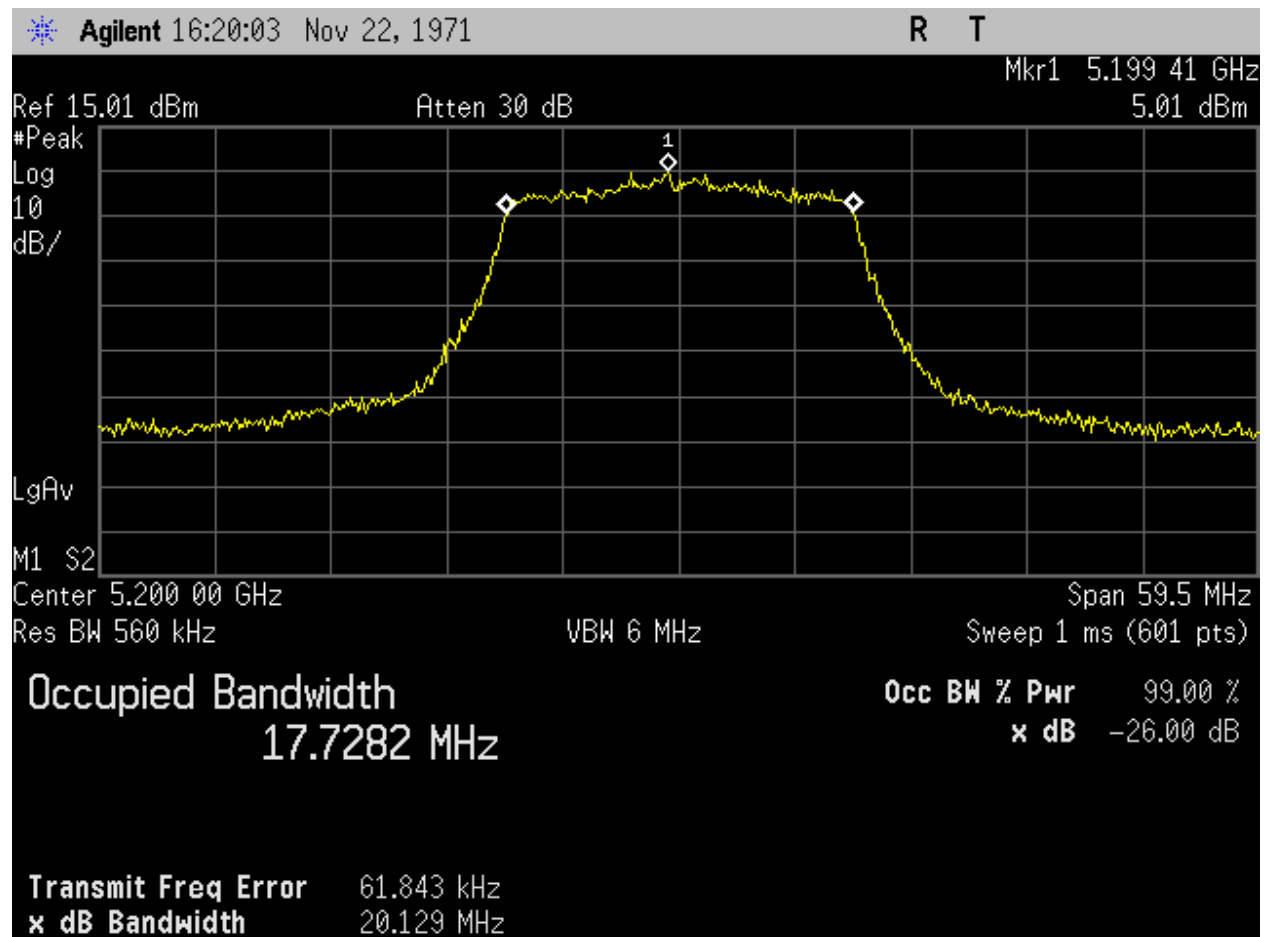


Figure 23: U-NII-1_5200MHz_low_Mid Ch_40_20MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 1.

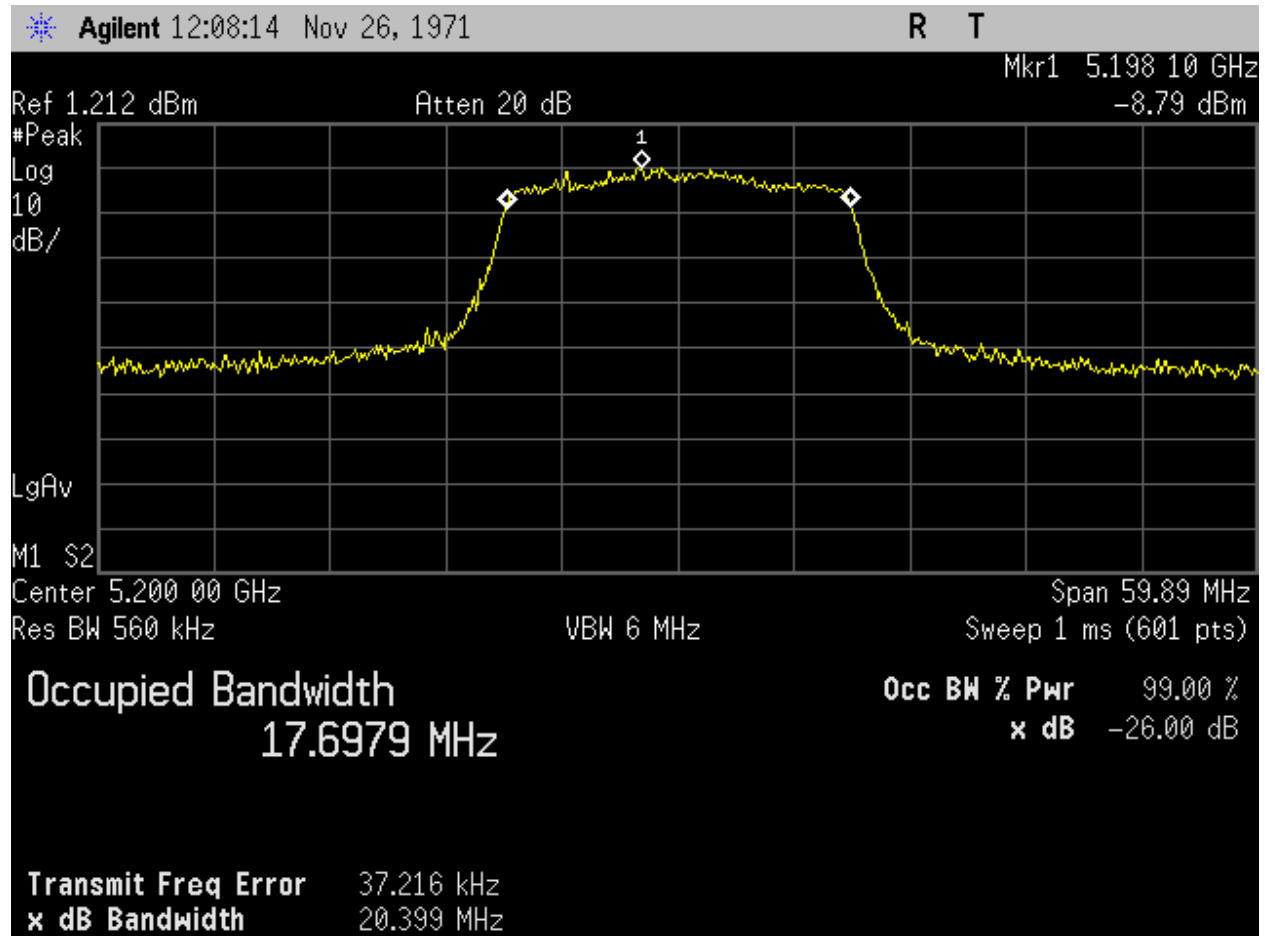


Figure 24: U-NII-1_5200MHz_low_Mid Ch_40_20MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 2.

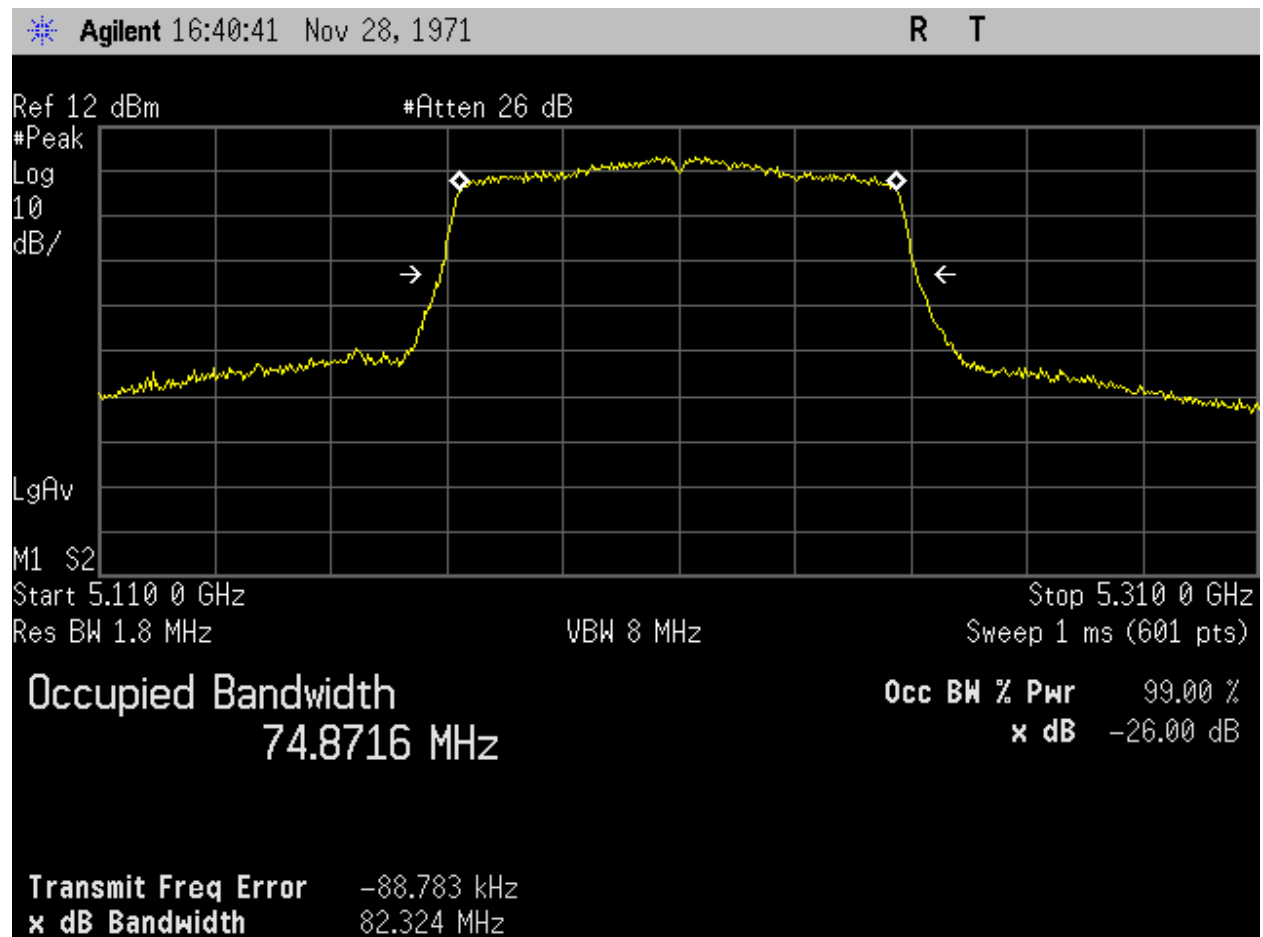


Figure 25: U-NII-1_5210MHz_Low_Mid Ch_42_80MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 1.

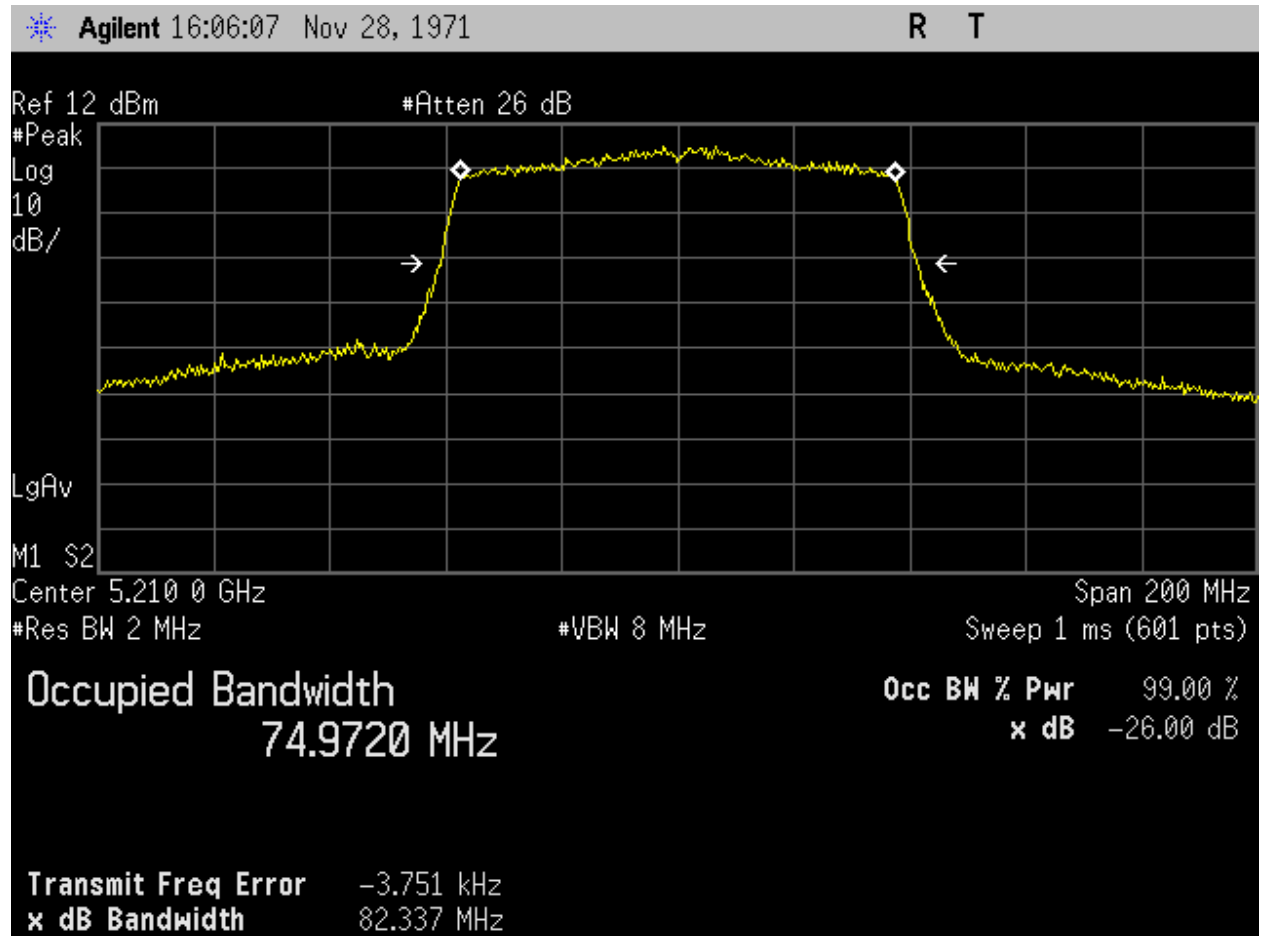


Figure 26: U-NII-1_5210MHz_Low_Mid Ch_42_80MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 2.

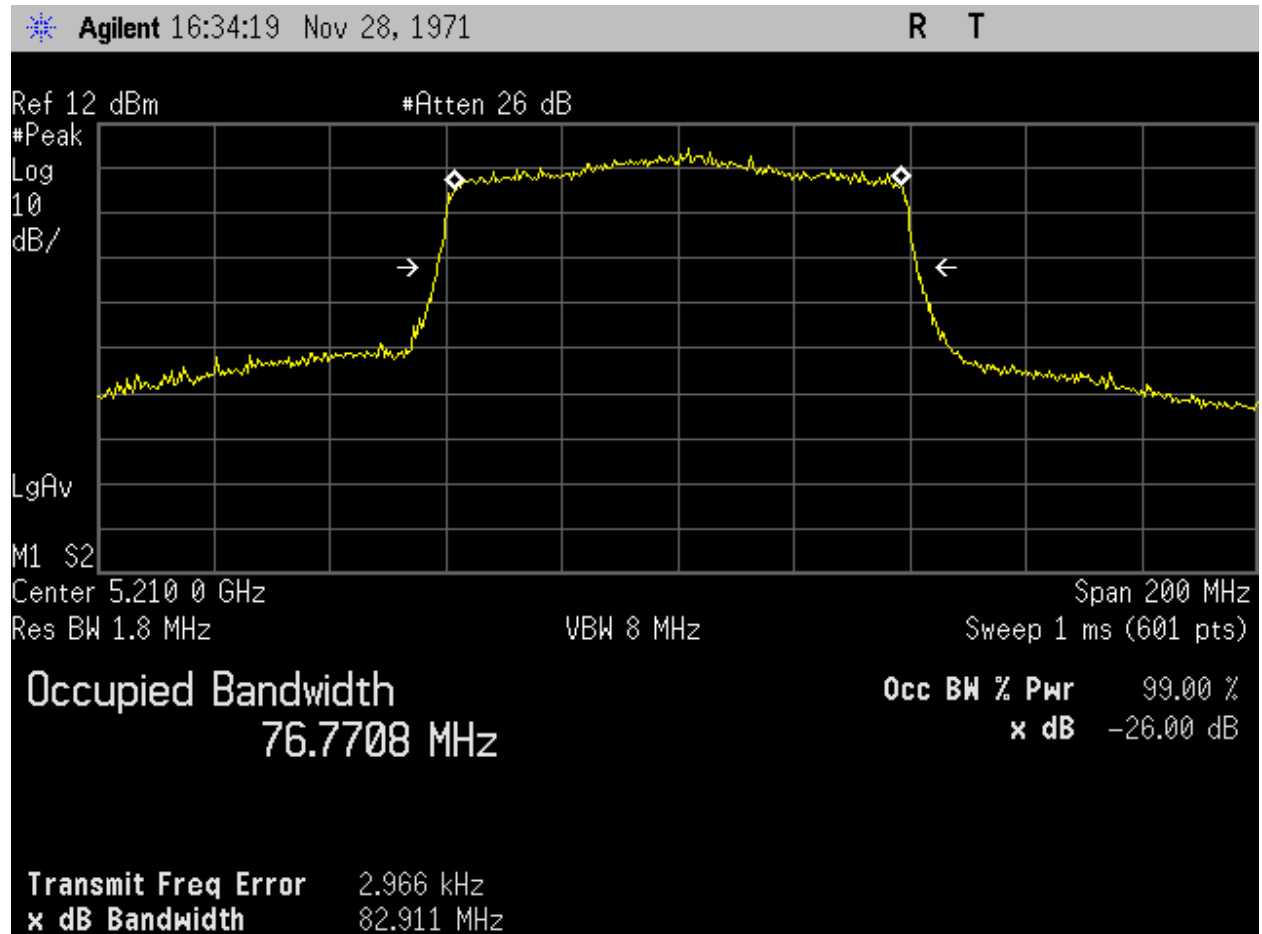


Figure 27: U-NII-1_5210MHz_Low_Mid Ch_42_80MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 1.

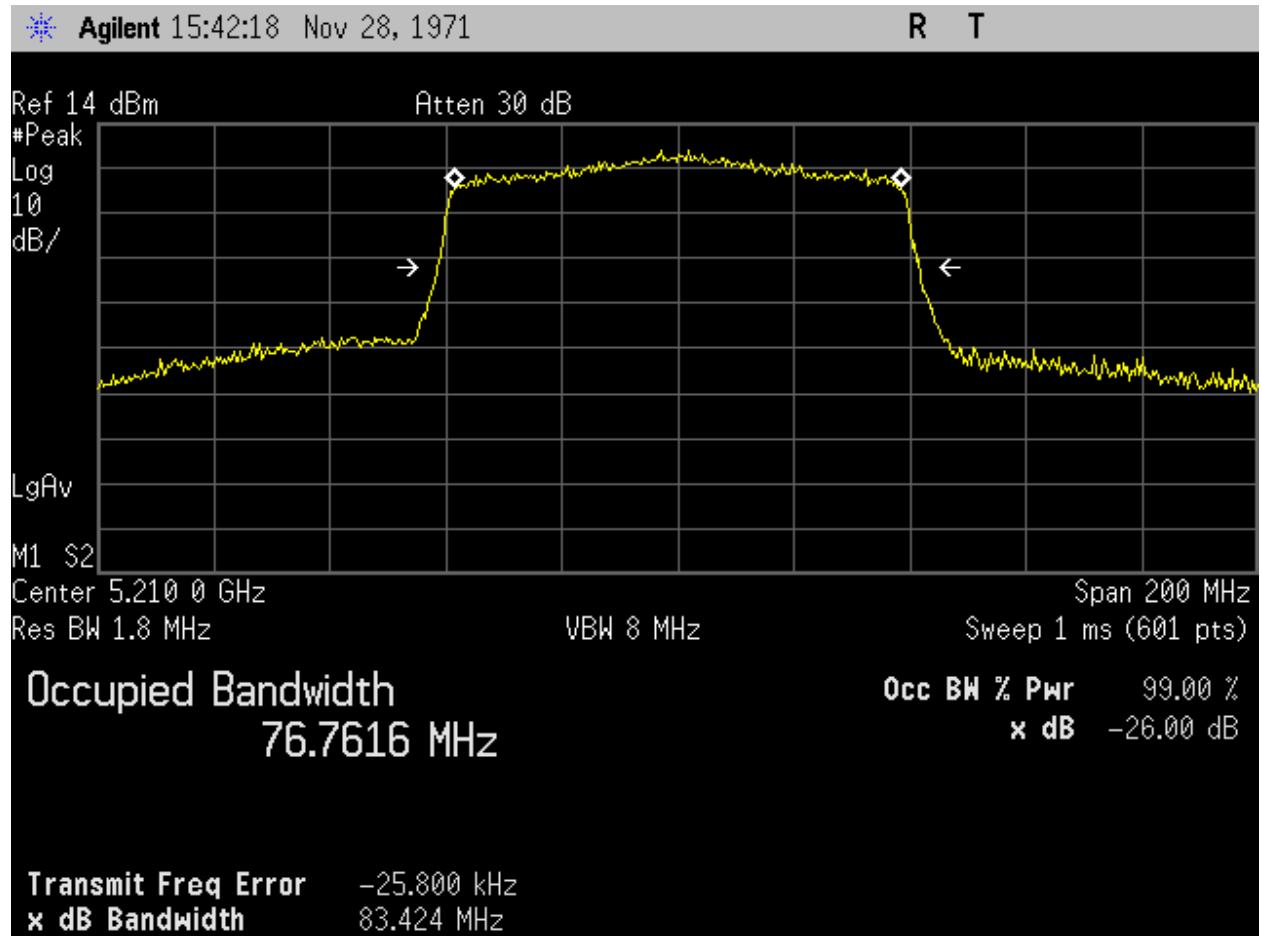


Figure 28: U-NII-1_5210MHz_Low_Mid Ch_42_80MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 2.

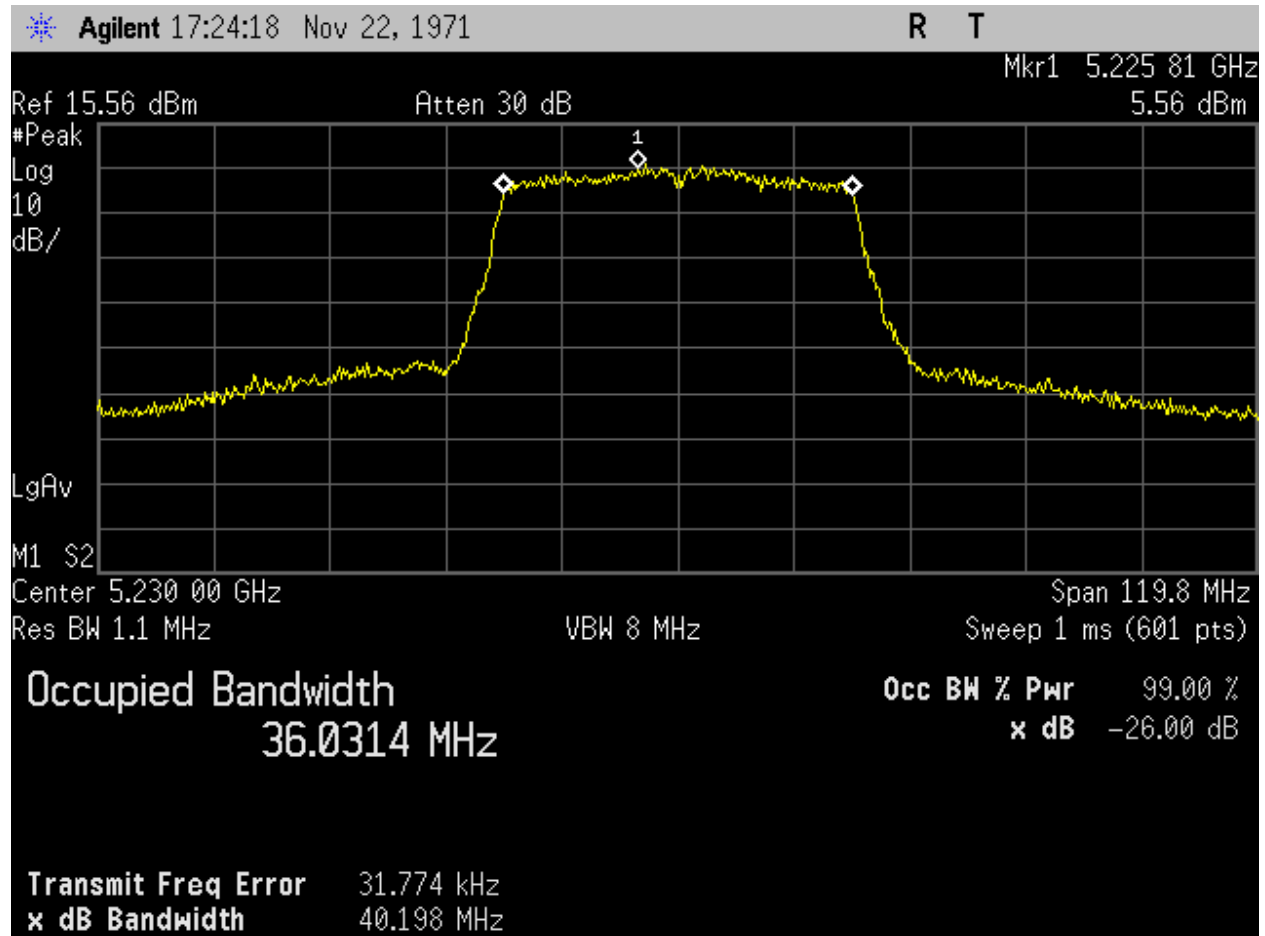


Figure 29: U-NII-1_5230MHz_high_Mid Ch_46_40MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 1.

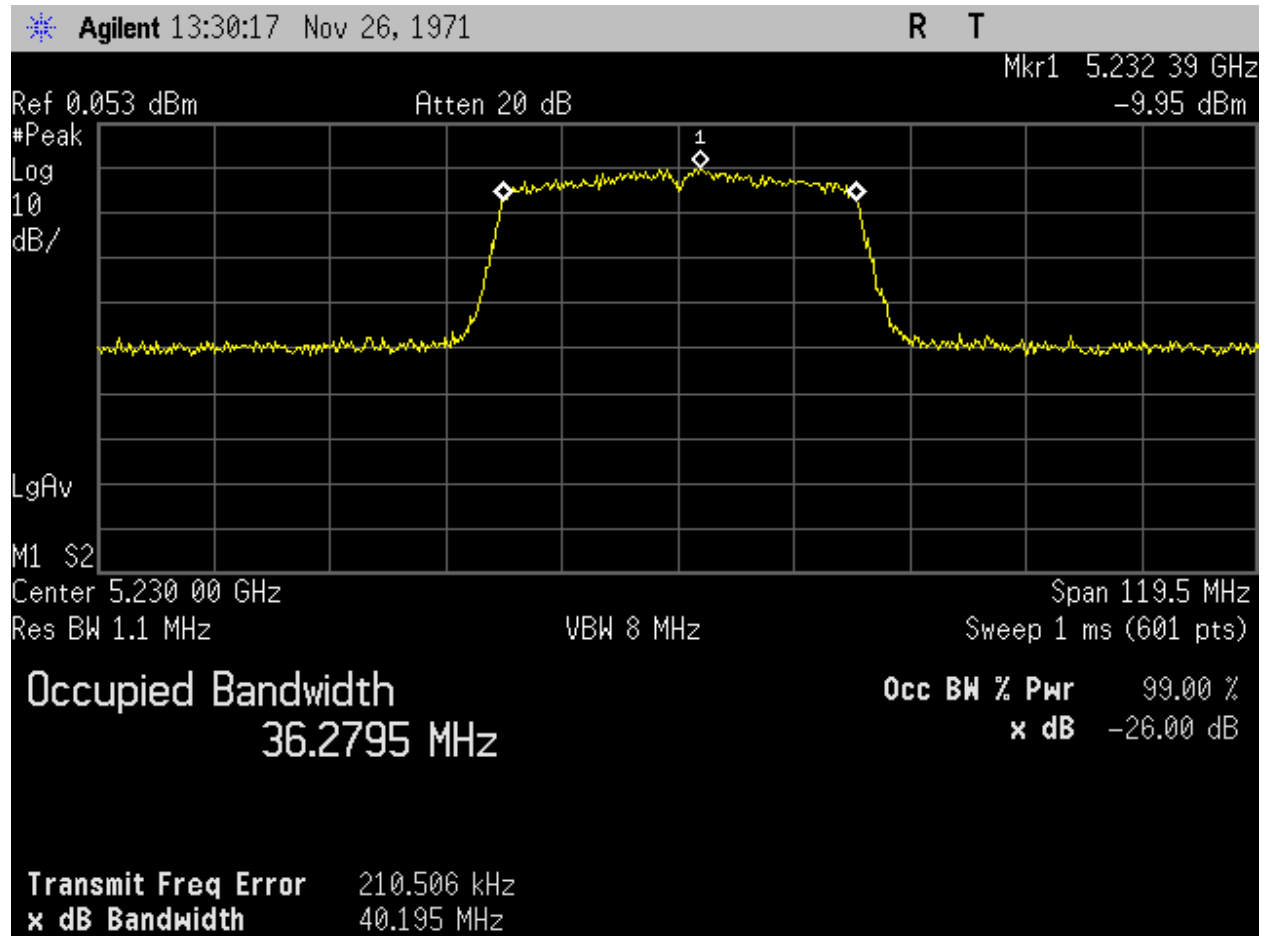


Figure 30: U-NII-1_5230MHz_high_Mid Ch_46_40MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 2.

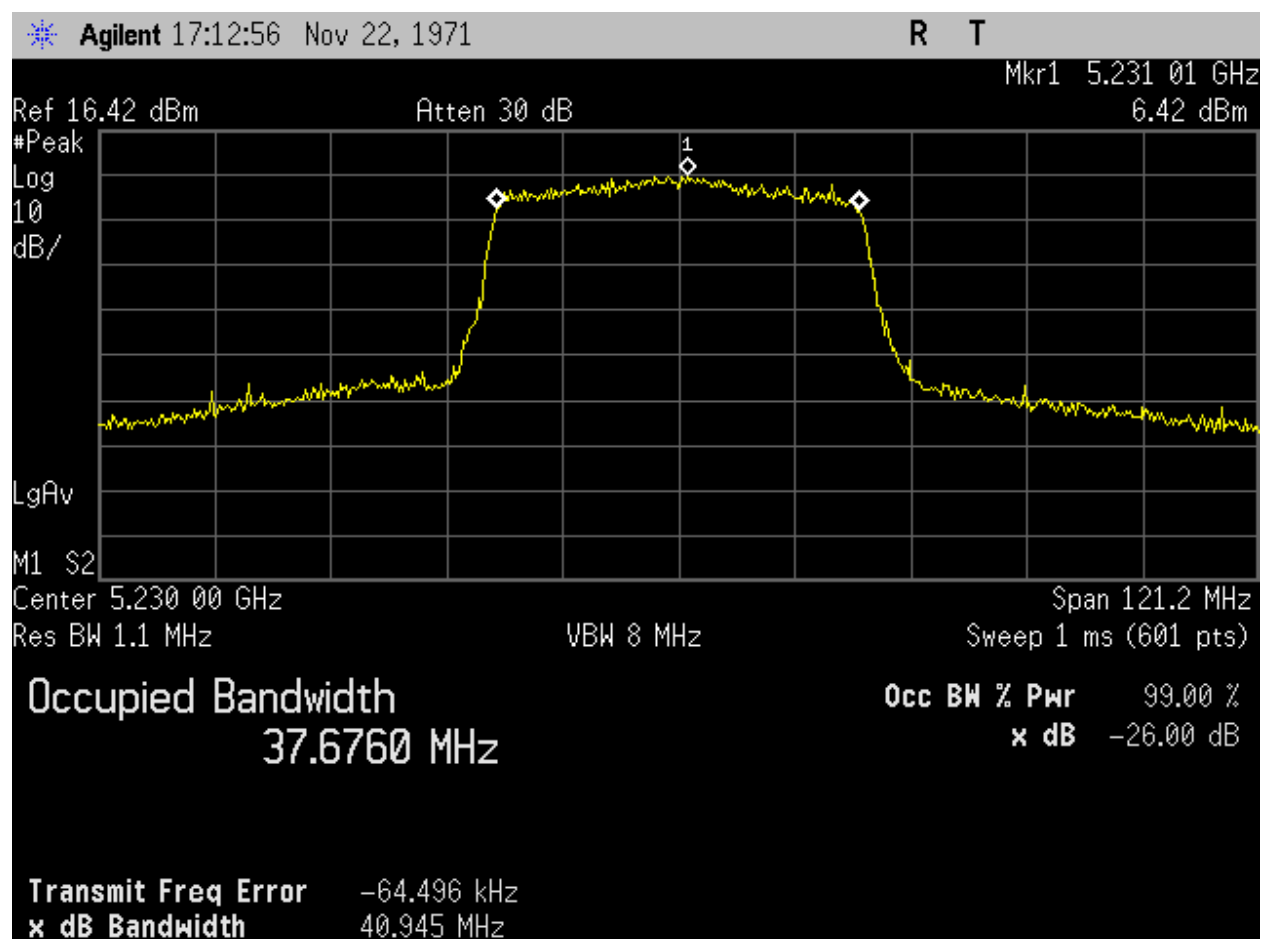


Figure 31: U-NII-1_5230MHz_high_Mid Ch_46_40MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 1.

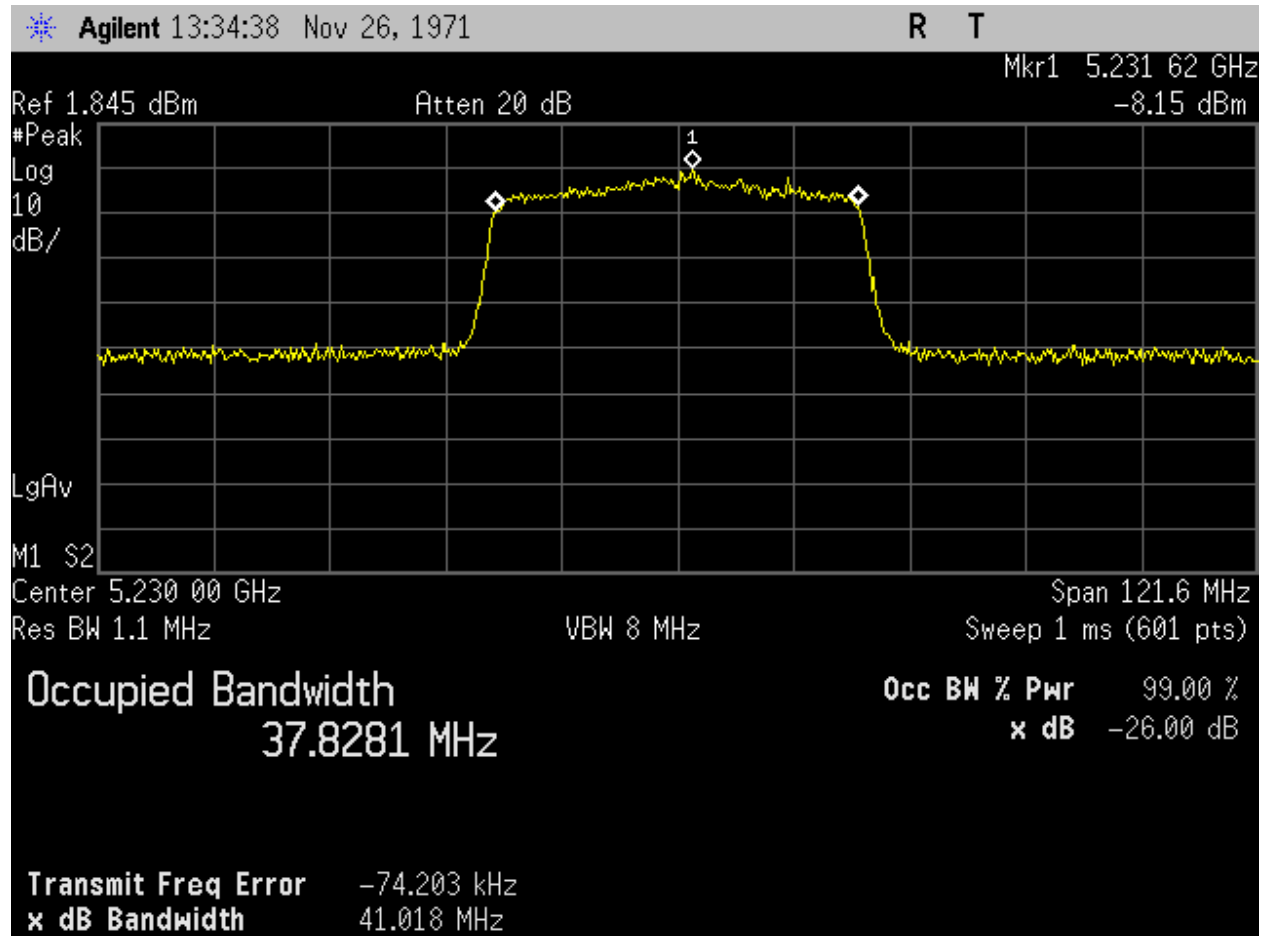


Figure 32: U-NII-1_5230MHz_high_Mid Ch_46_40MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 2.

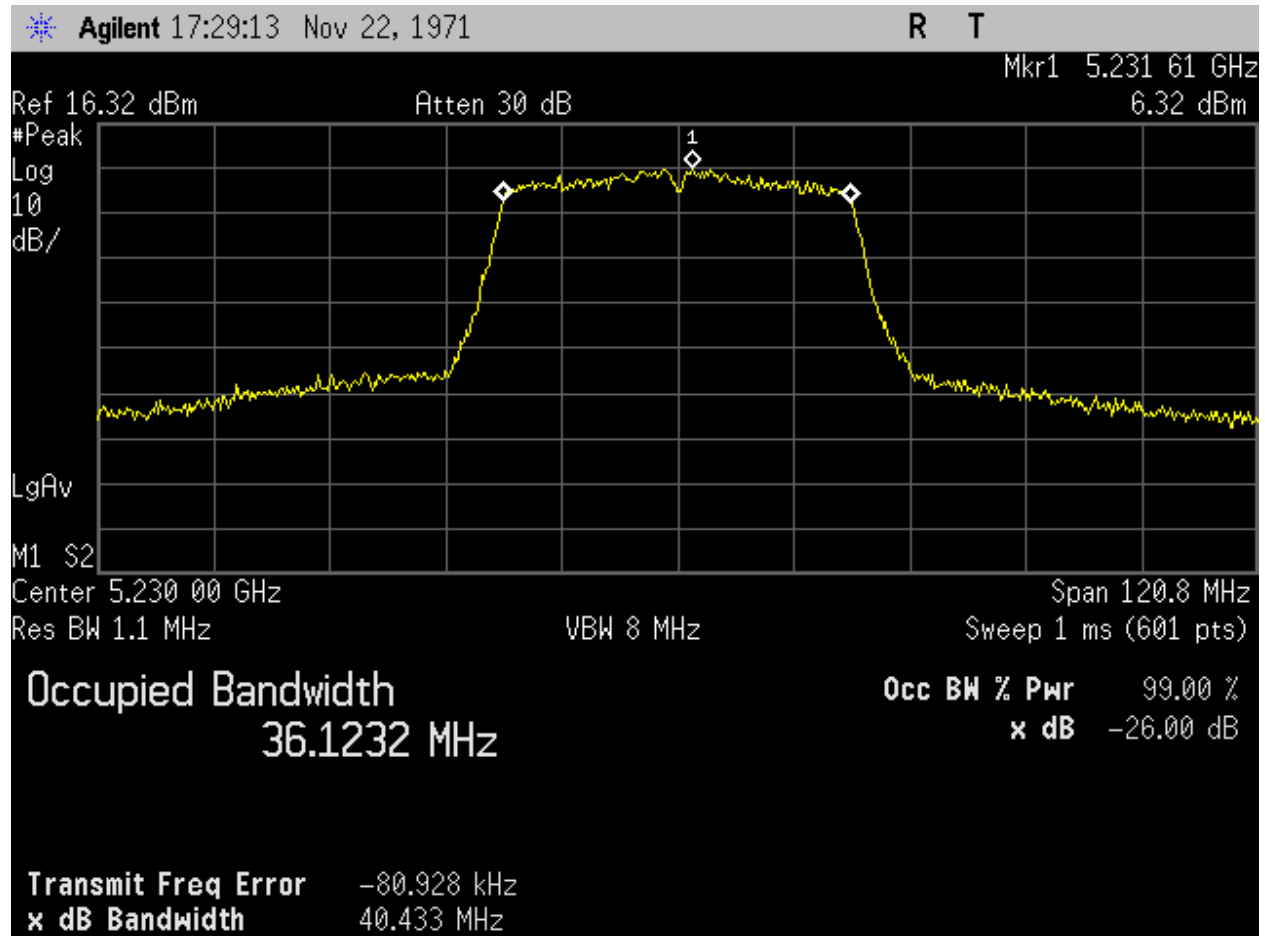


Figure 33: U-NII-1_5230MHz_high_Mid Ch_46_40MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 1.

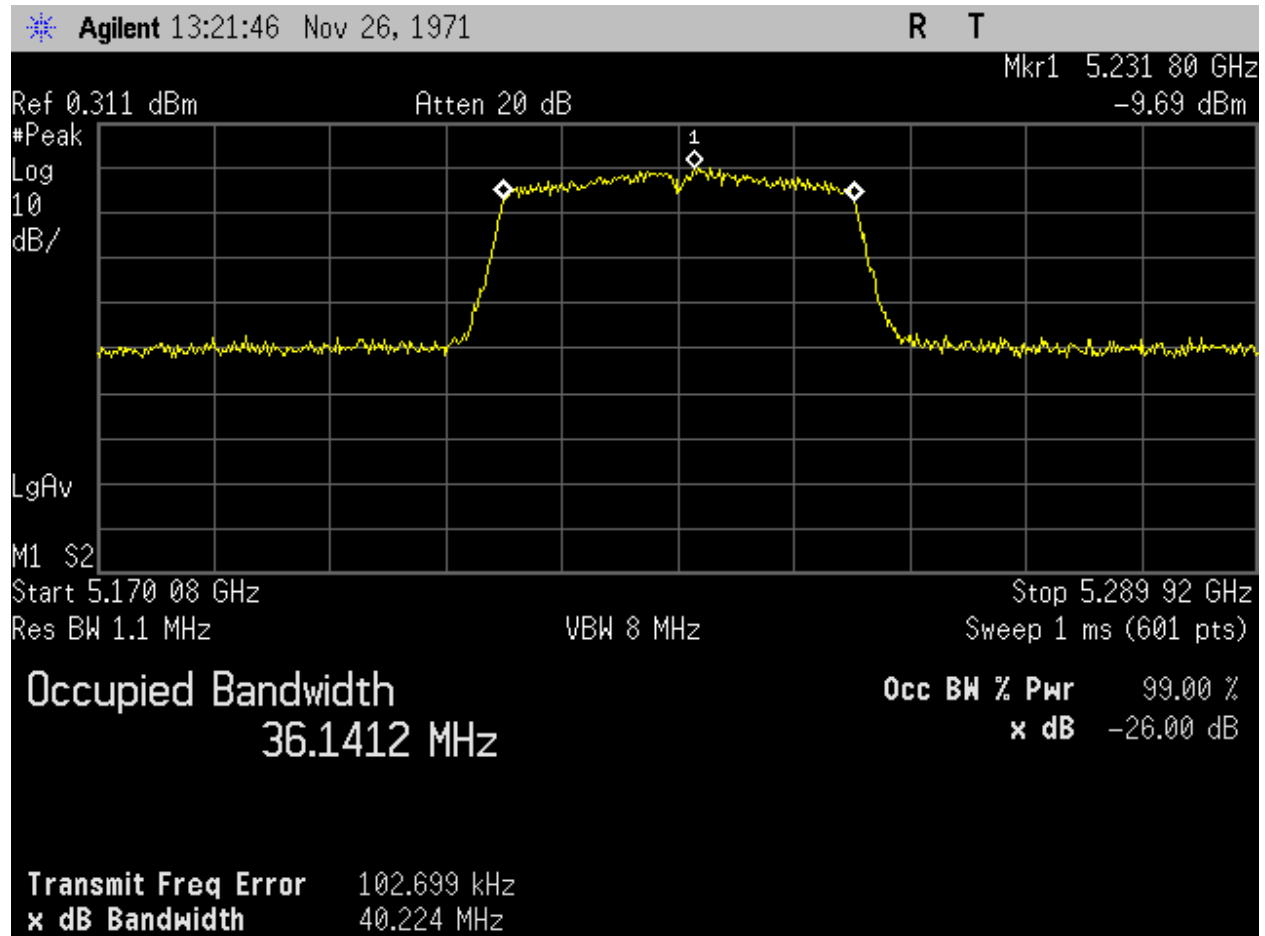


Figure 34: U-NII-1_5230MHz_high_Mid Ch_46_40MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 2.

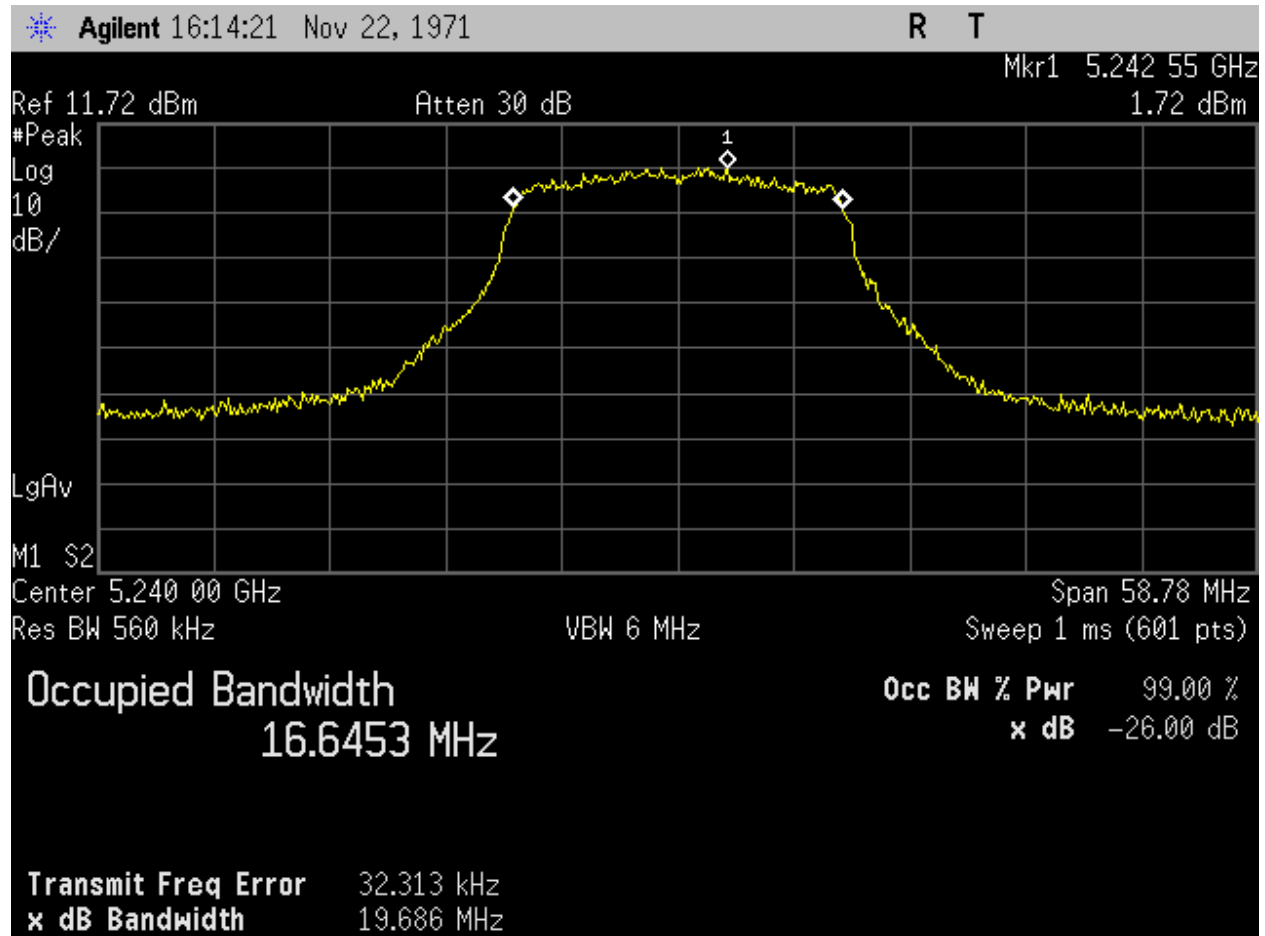


Figure 35: U-NII-1_5240MHz_high_mid Ch_48_20MHz BW_a-mode_Occupied Bandwidth_-26dB_Port 1.

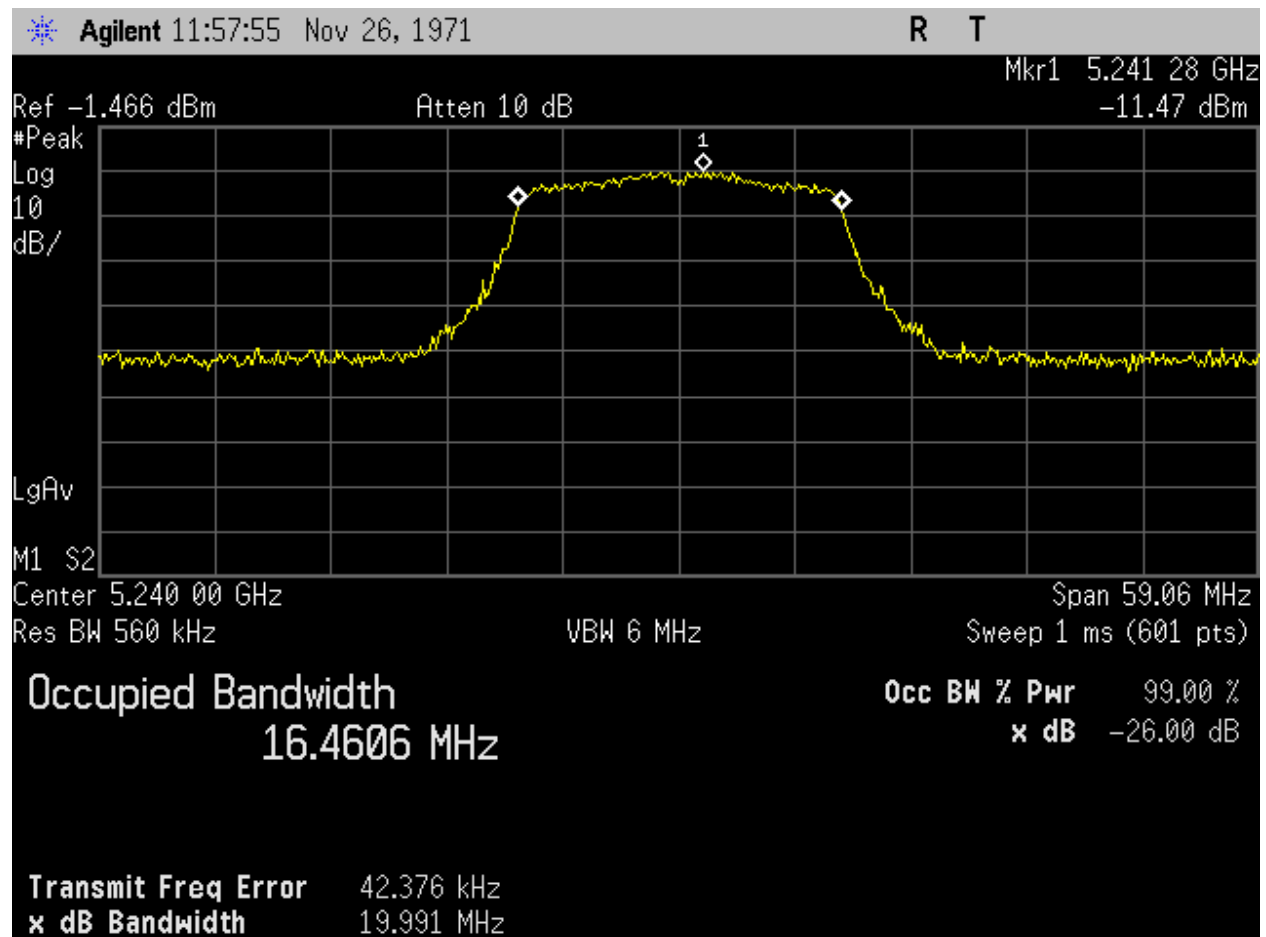


Figure 36: U-NII-1_5240MHz_high_mid Ch_48_20MHz BW_a-mode_Occupied Bandwidth_-26dB_Port 2.

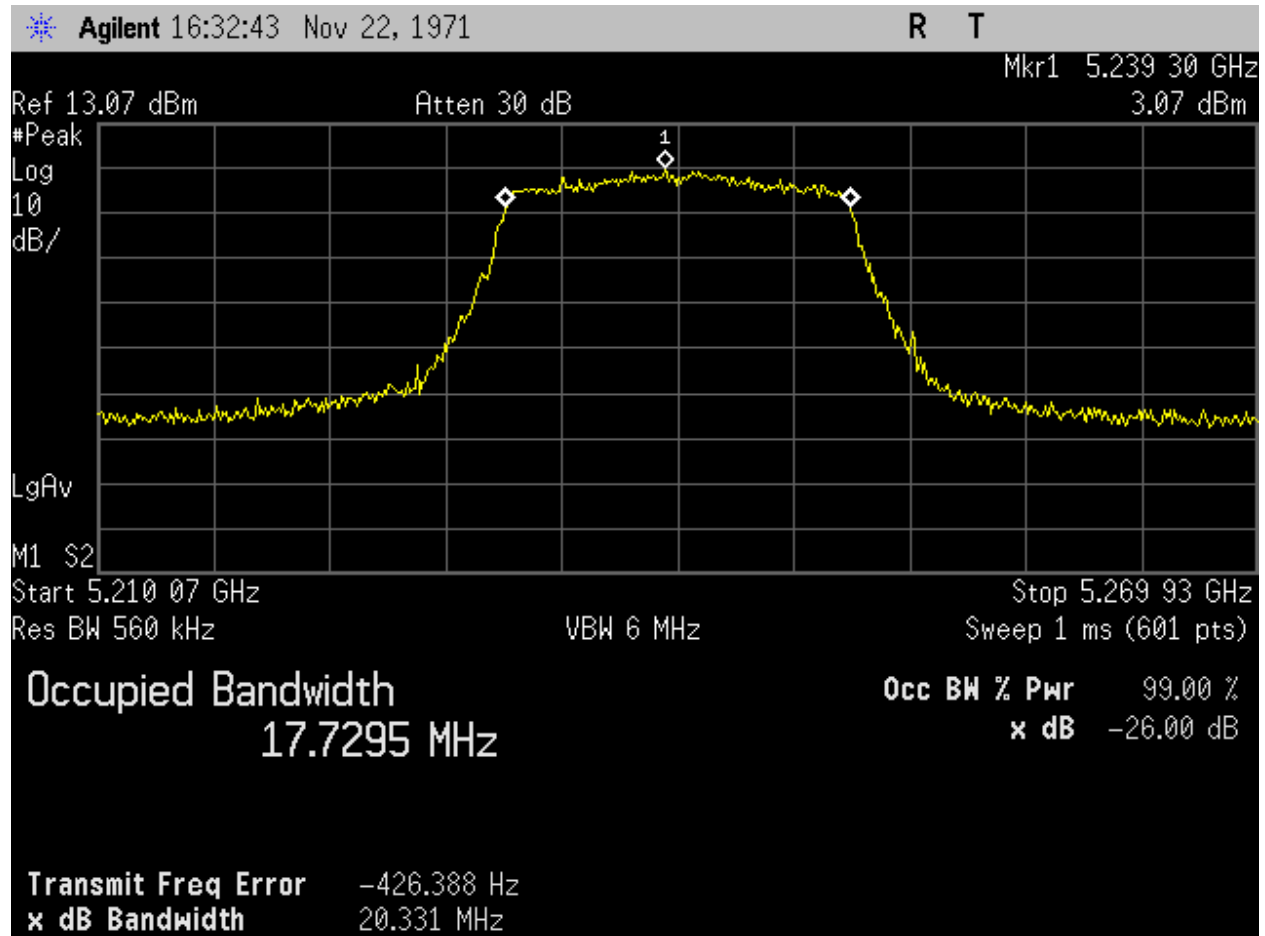


Figure 37: U-NII-1_5240MHz_high_mid Ch_48_20MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 1.

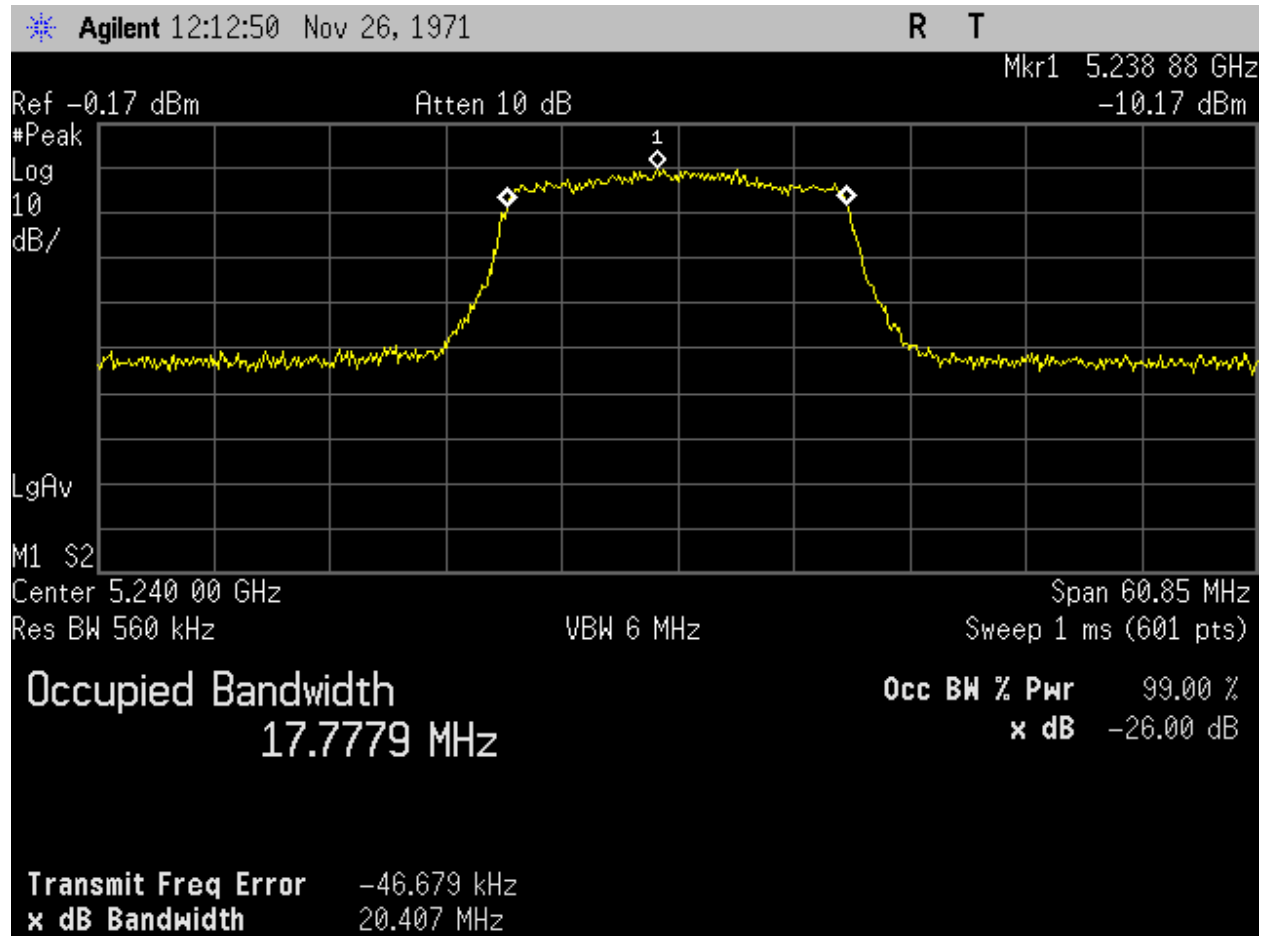


Figure 38: U-NII-1_5240MHz_high_mid Ch_48_20MHz BW_ac-mode_Occupied Bandwidth_-26dB_Port 2.

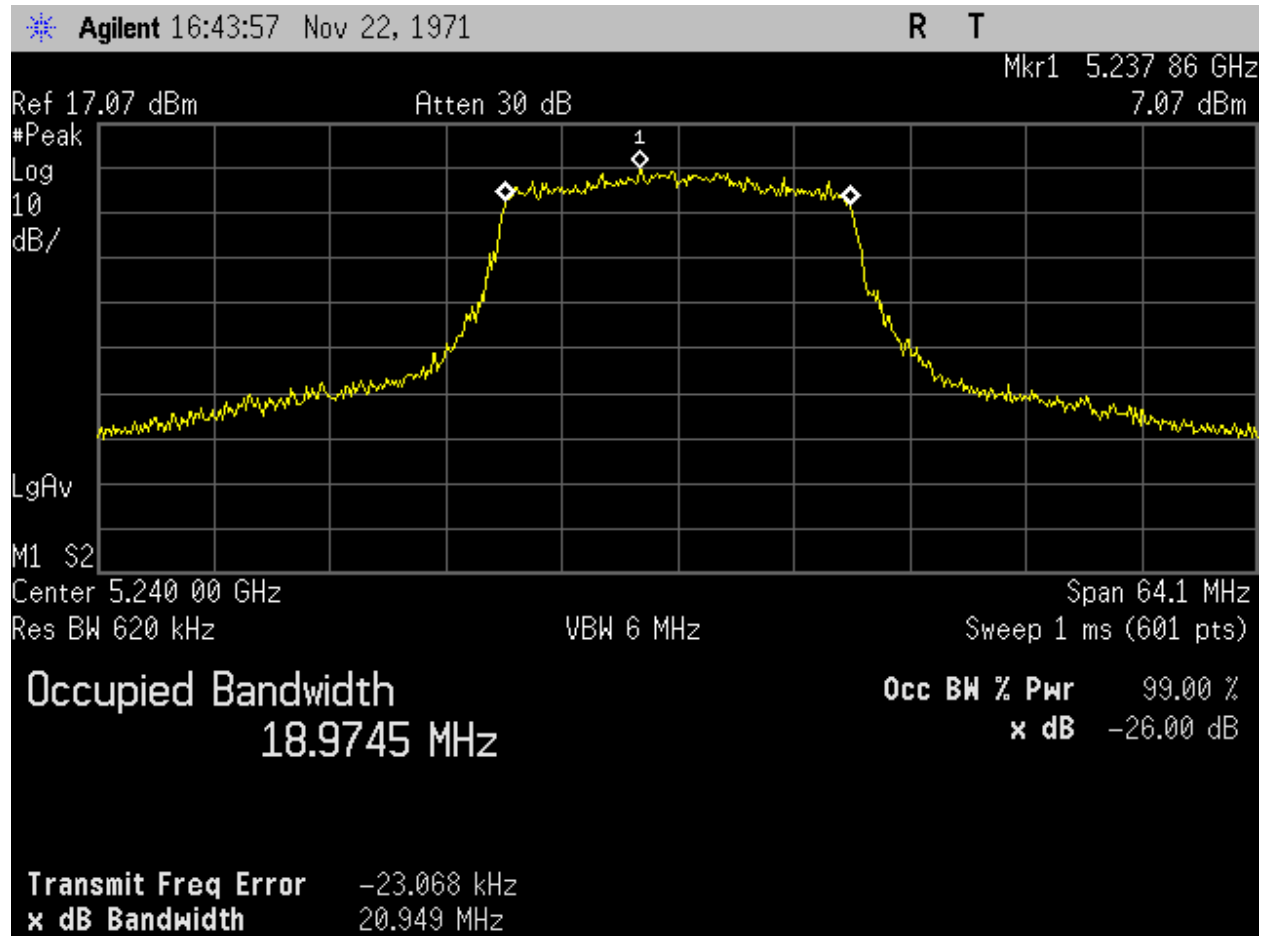


Figure 39: U-NII-1_5240MHz_high_mid Ch_48_20MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 1.

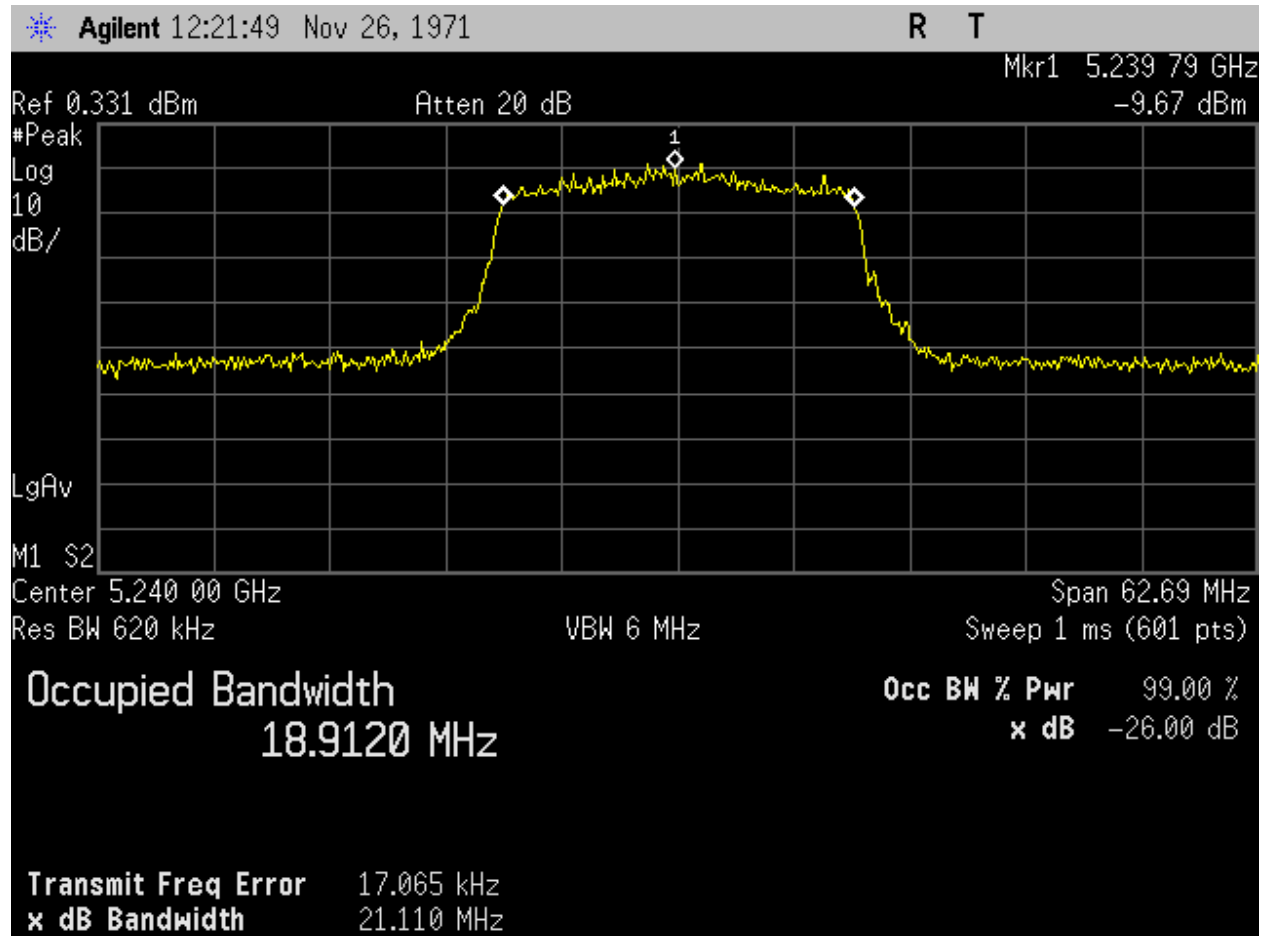


Figure 40: U-NII-1_5240MHz_high_mid Ch_48_20MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 2.

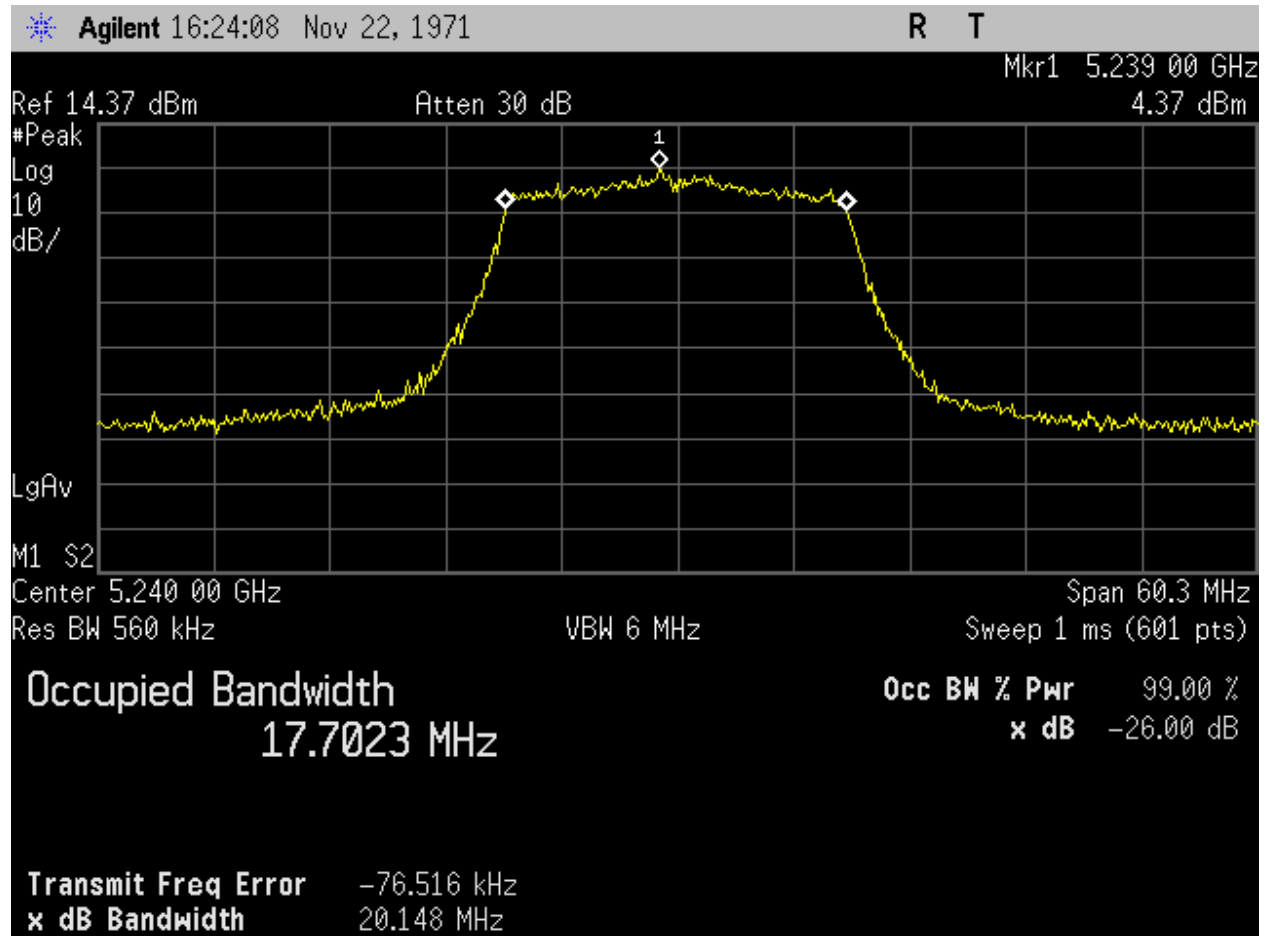


Figure 41: U-NII-1_5240MHz_high_mid Ch_48_20MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 1.

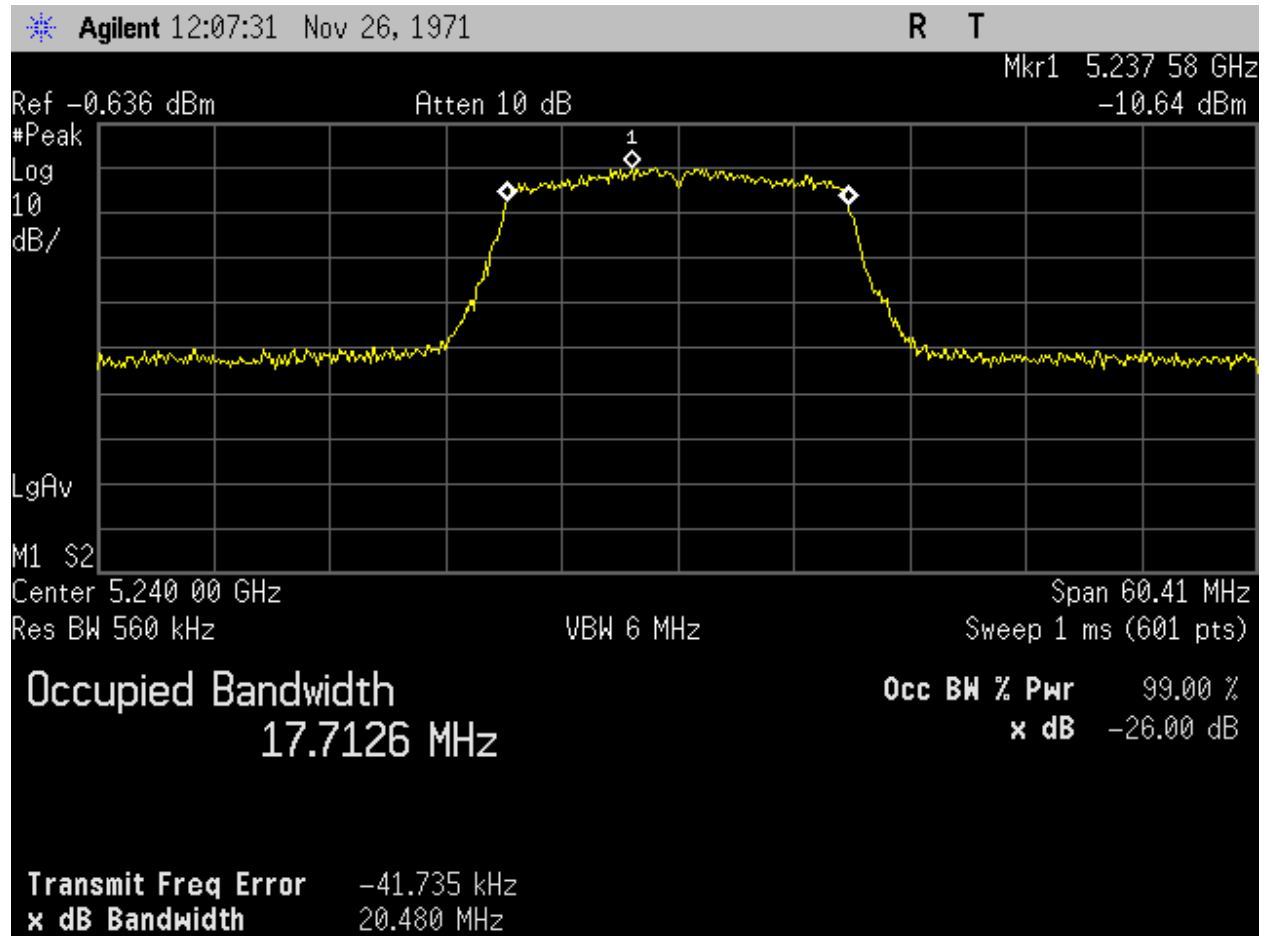


Figure 42: U-NII-1_5240MHz_high_mid Ch_48_20MHz BW_n-mode_Occupied Bandwidth_-26dB_Port 2.

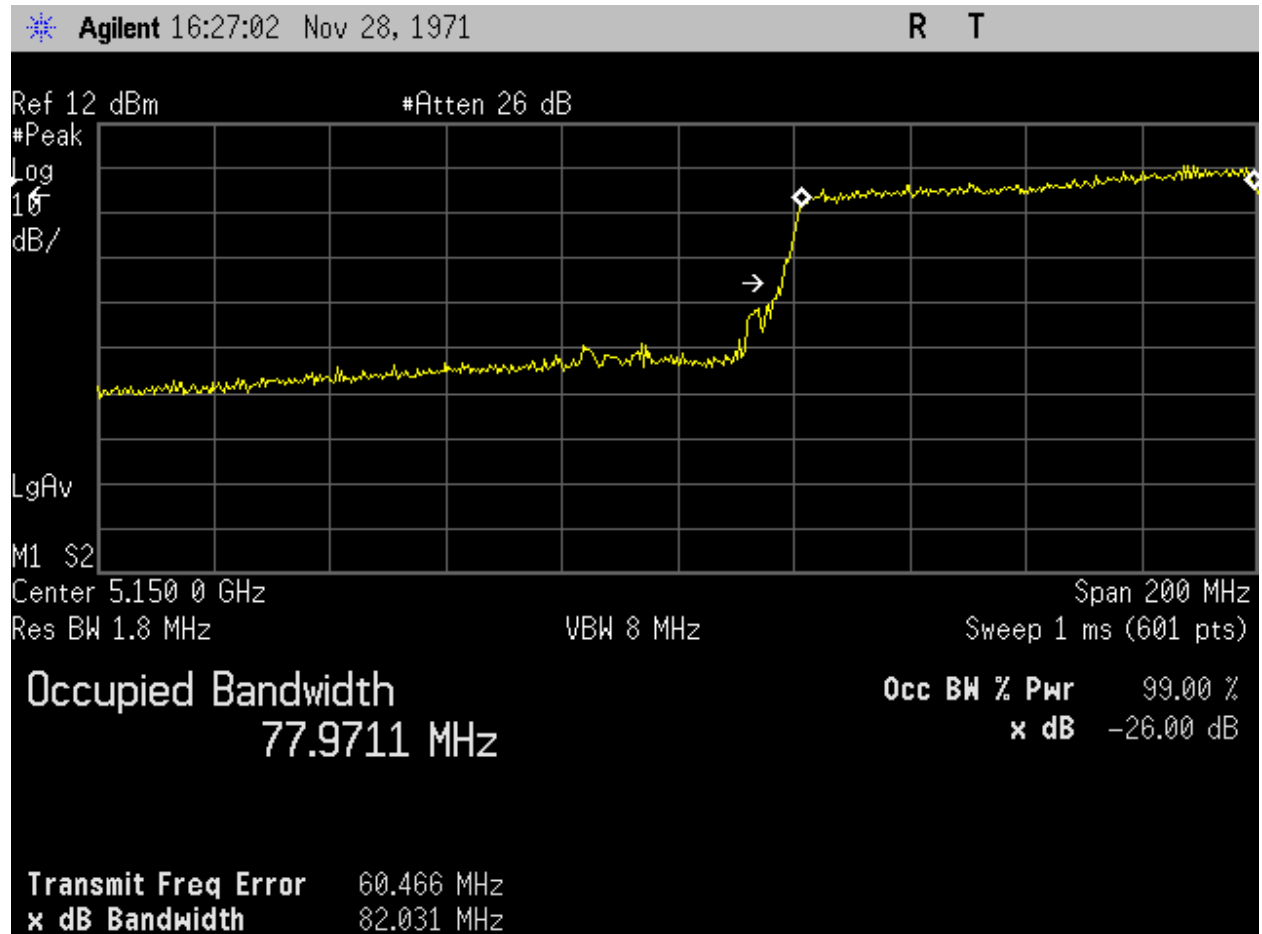


Figure 43: U-NII-1_5250MHz_Low_Mid Ch_50_160MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 1.

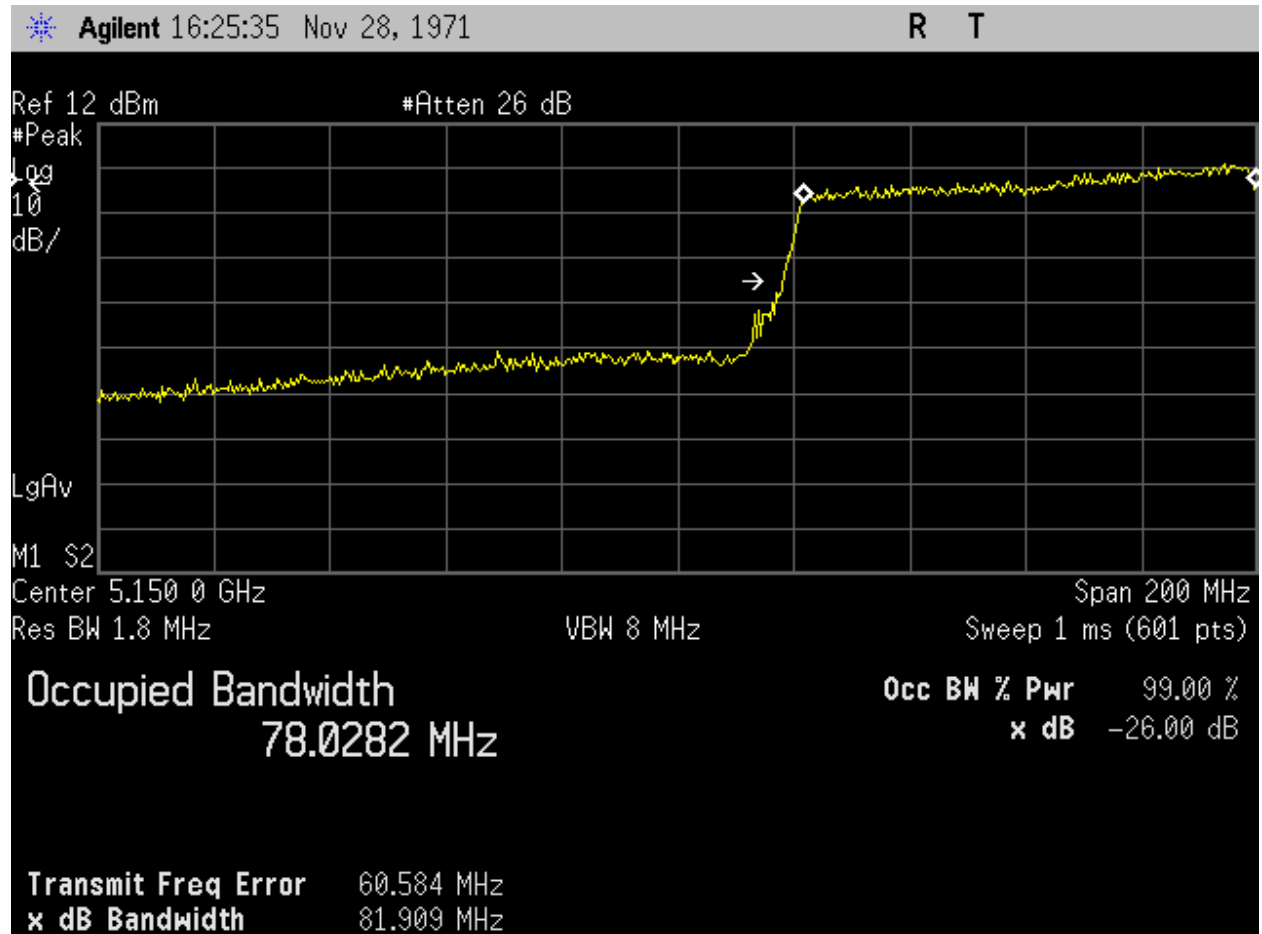


Figure 44: U-NII-1_5250MHz_Low_Mid Ch_50_160MHz BW_ax-mode_Occupied Bandwidth_-26dB_Port 2.

Duty Cycle

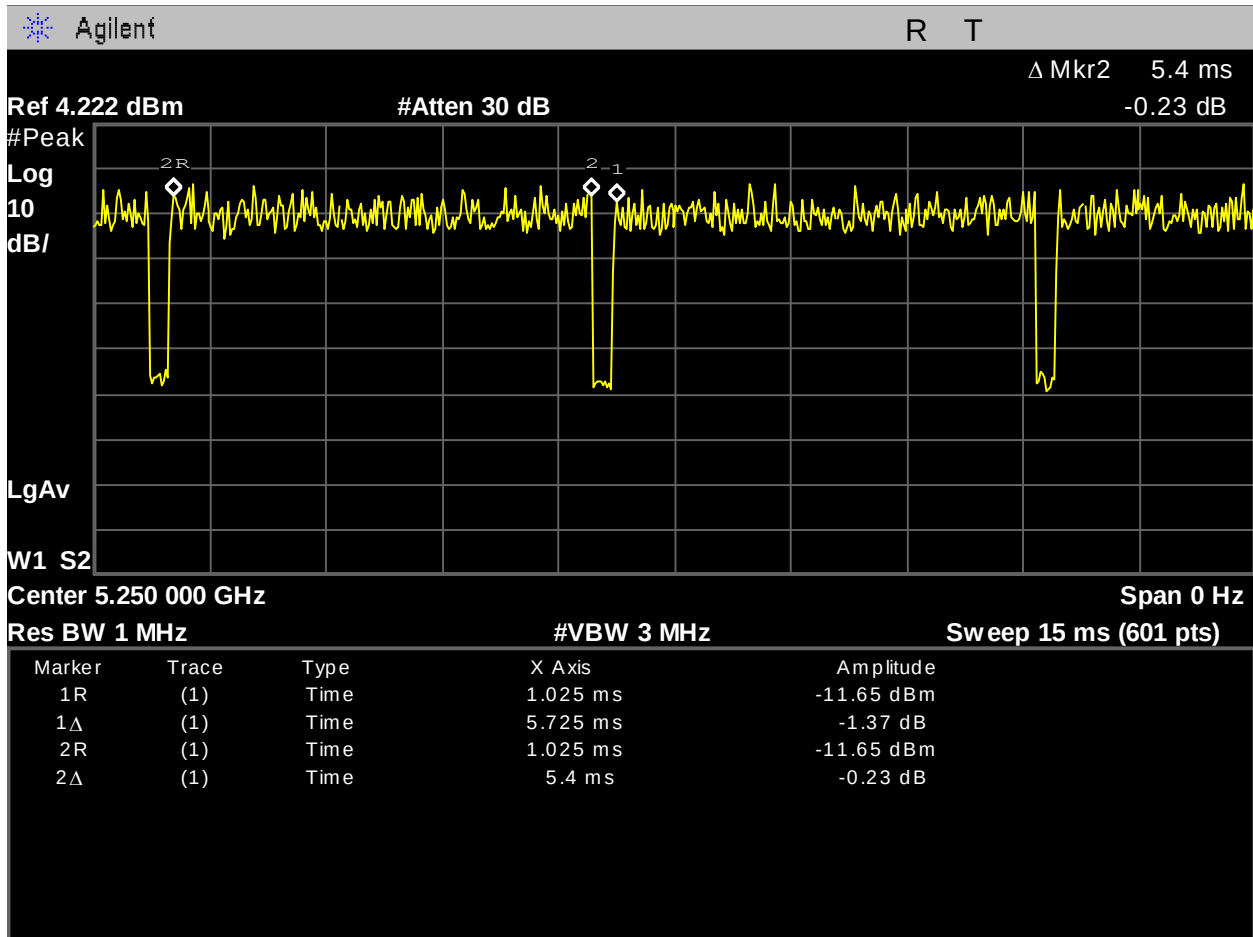


Figure 45: U-NII-1,2A_5250MHz_Mid Ch_50_160MHz BW_ax-mode_Duty Cycle_Port 1.

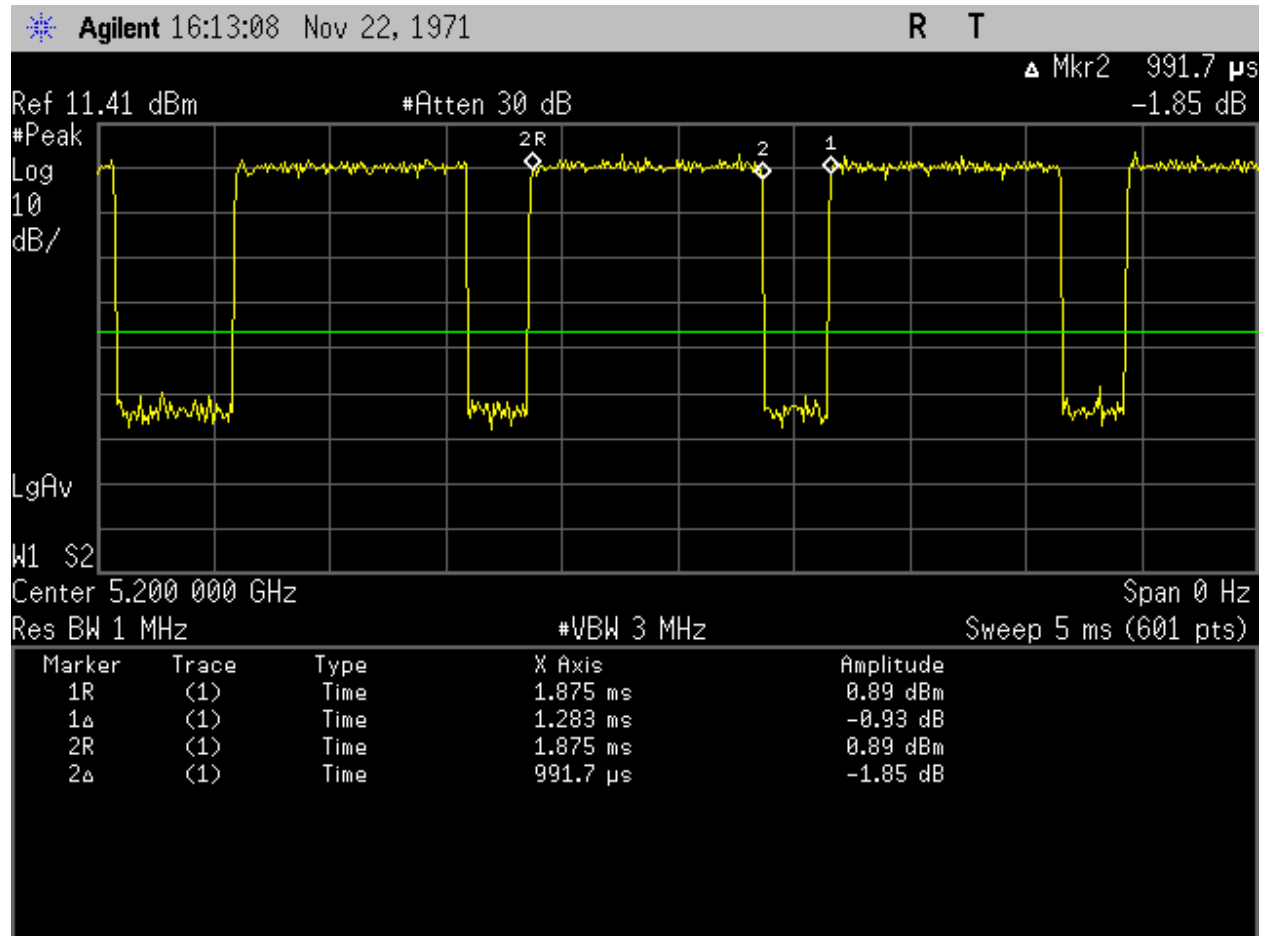


Figure 46: U-NII-1_5200MHz_low_Mid Ch_40_20MHz BW_a-mode_Duty Cycle_Port 1.

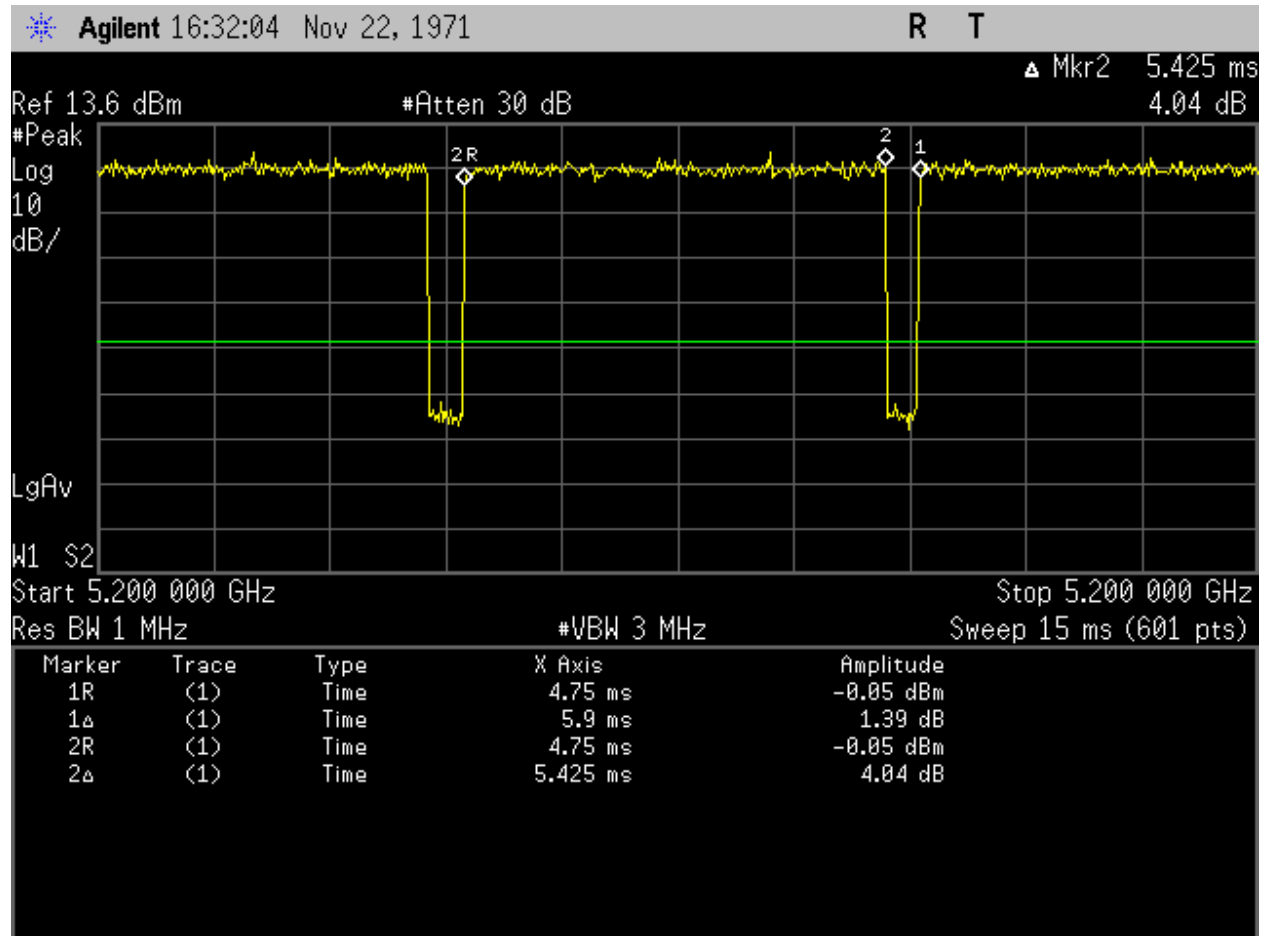


Figure 47: U-NII-1_5200MHz_low_Mid Ch_40_20MHz BW_ac-mode_Duty Cycle_Port 1.

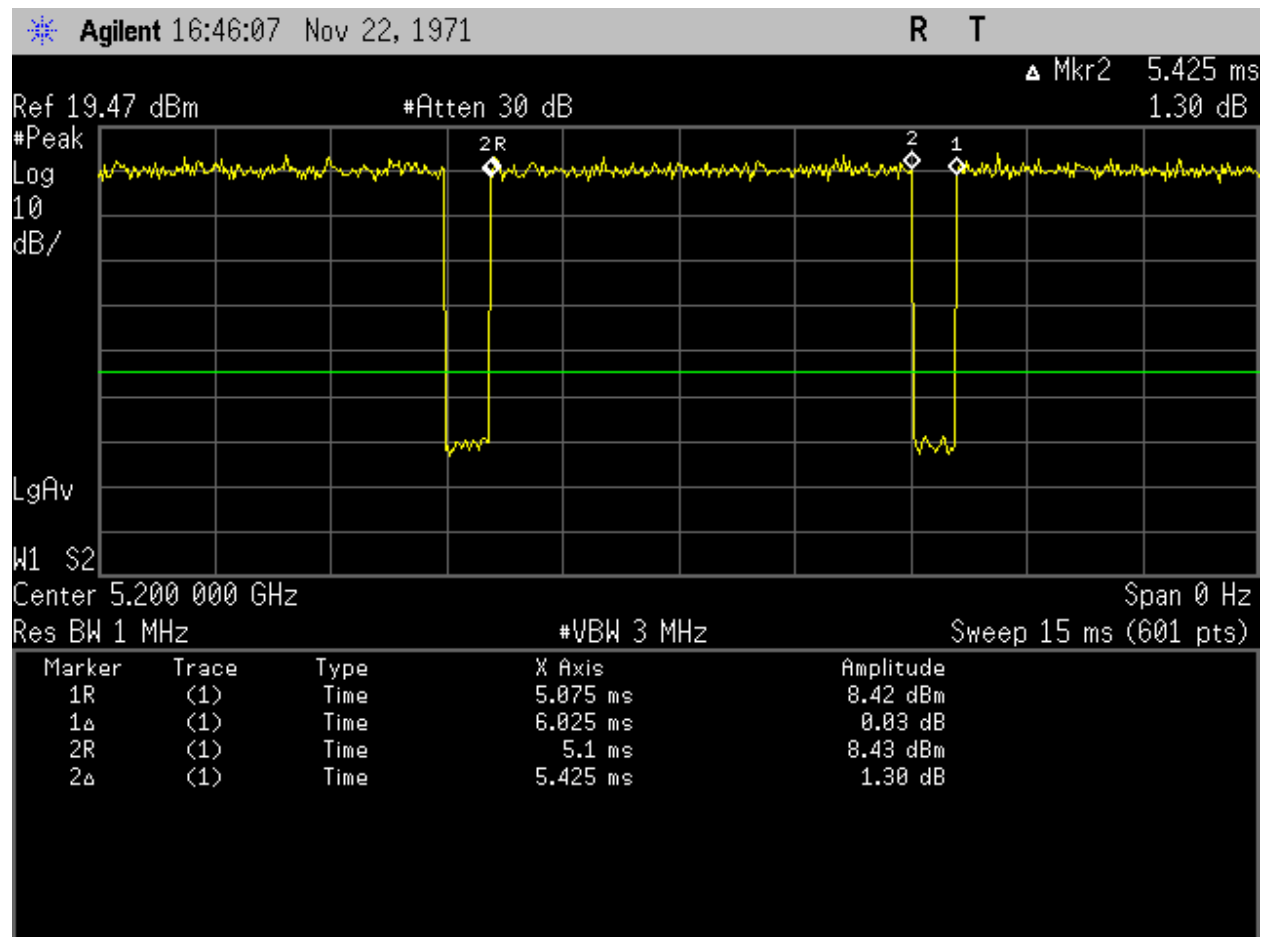


Figure 48: U-NII-1_5200MHz_low_Mid Ch_40_20MHz BW_ax-mode_Duty Cycle_Port 1.

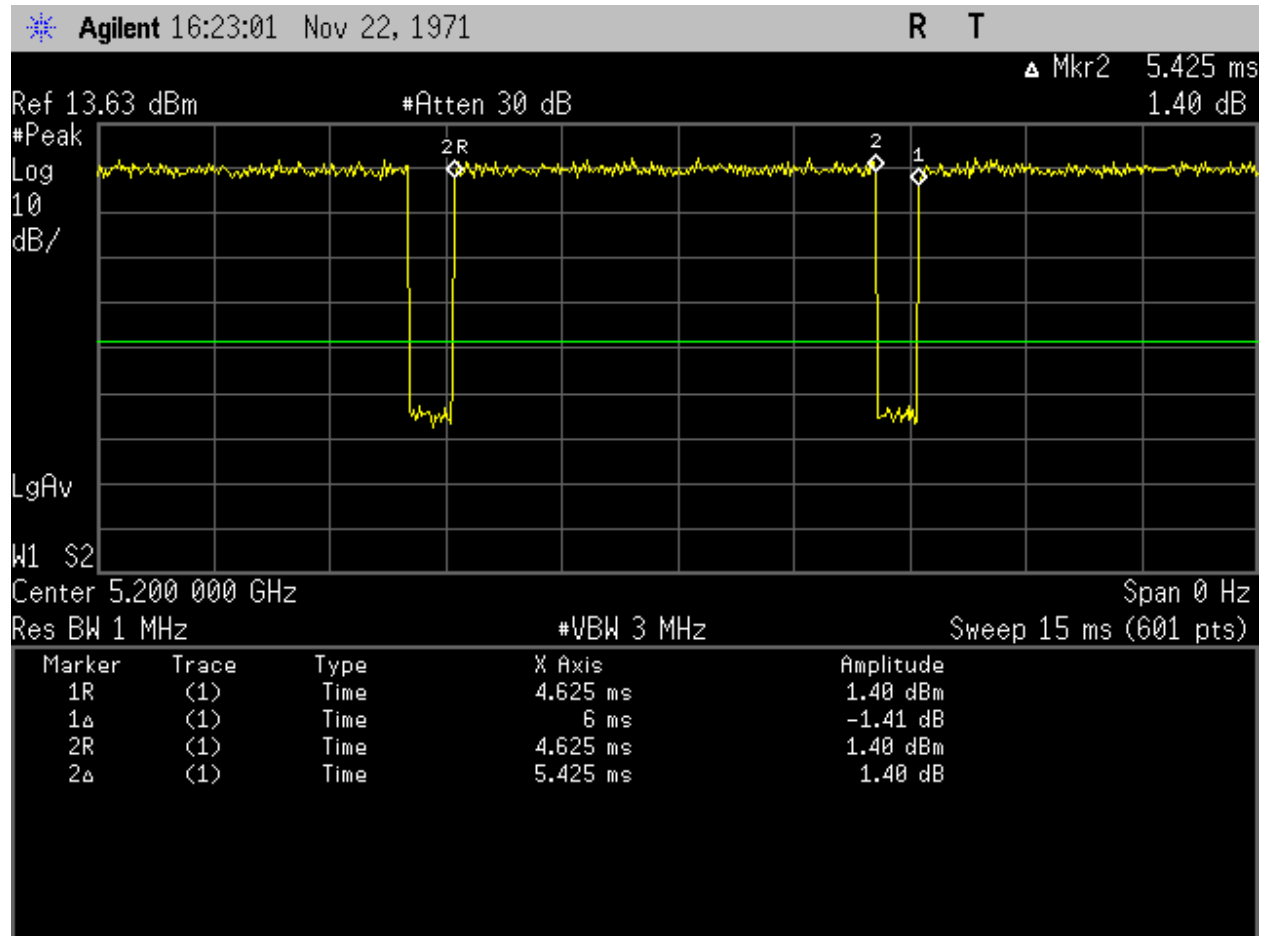


Figure 49: U-NII-1_5200MHz_low_Mid Ch_40_20MHz BW_n-mode_Duty Cycle_Port 1.

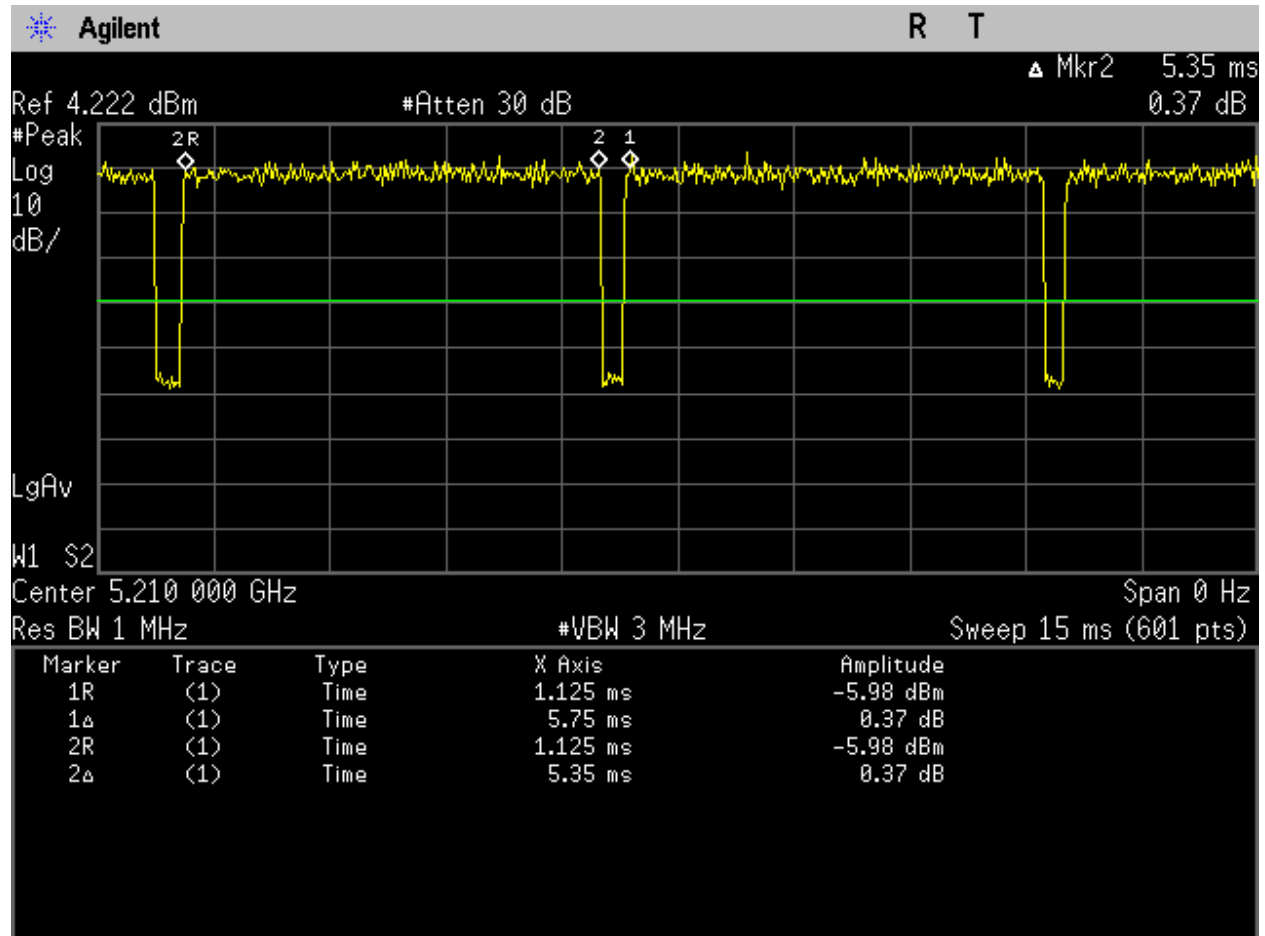


Figure 50: U-NII-1_5210MHz_Low Mid Ch_42_80MHz BW_ac-mode_Duty Cycle_Port 1.

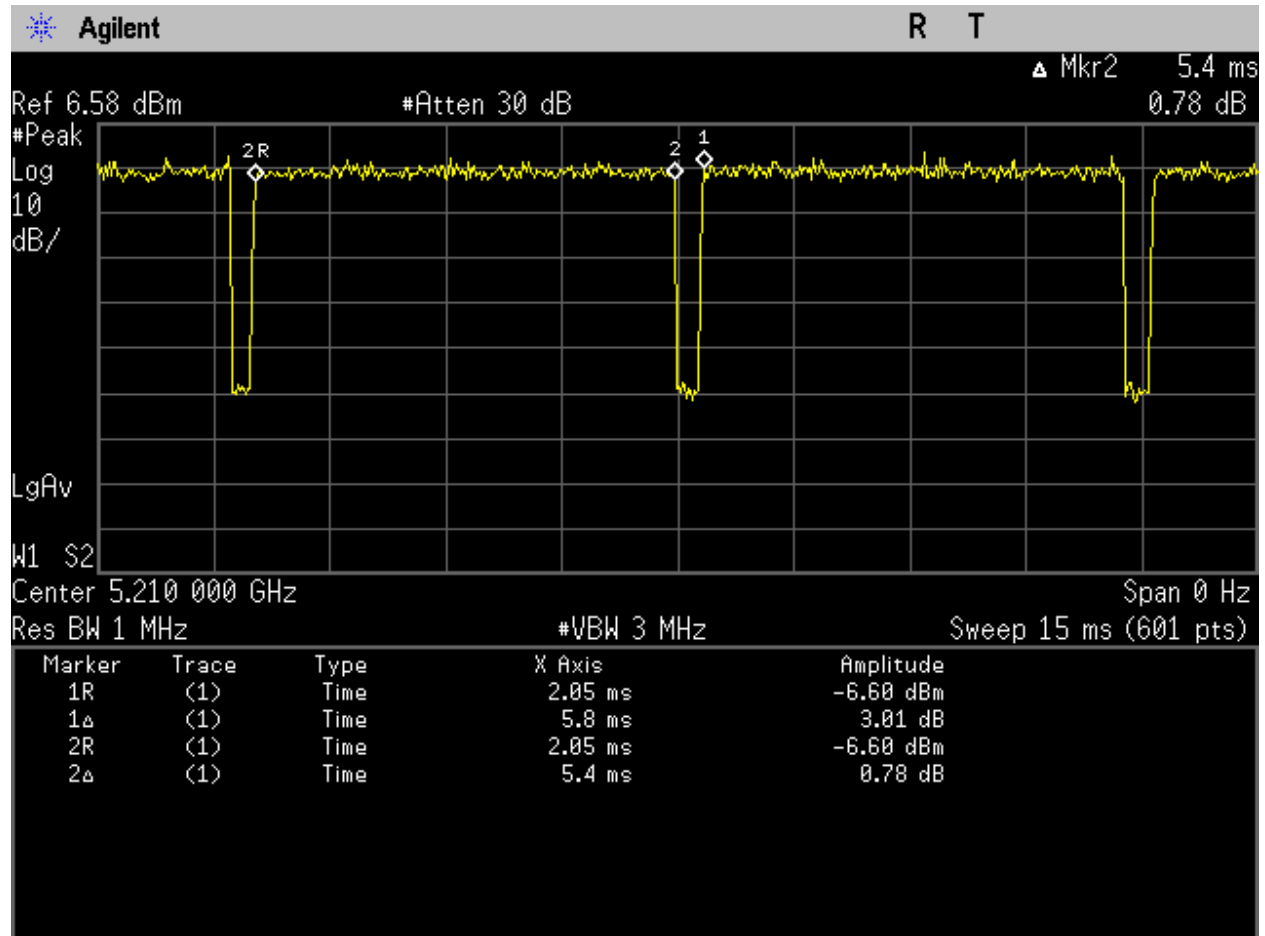


Figure 51: U-NII-1_5210MHz_Low Mid Ch_42_80MHz BW_ax-mode_Duty Cycle_Port 1.

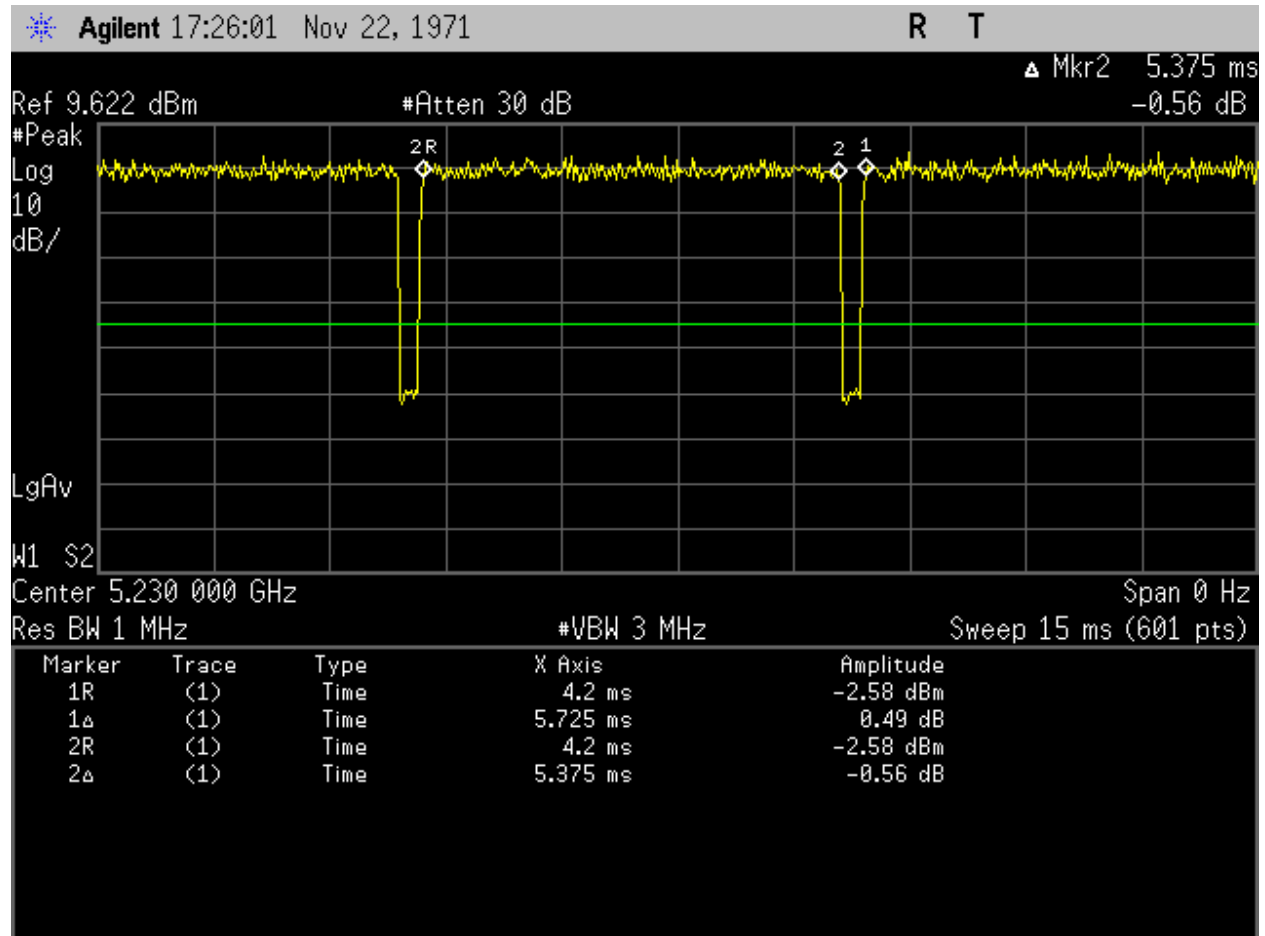


Figure 52: U-NII-1_5230MHz_high_Mid Ch_46_40MHz BW_ac-mode_Duty Cycle_Port 1.

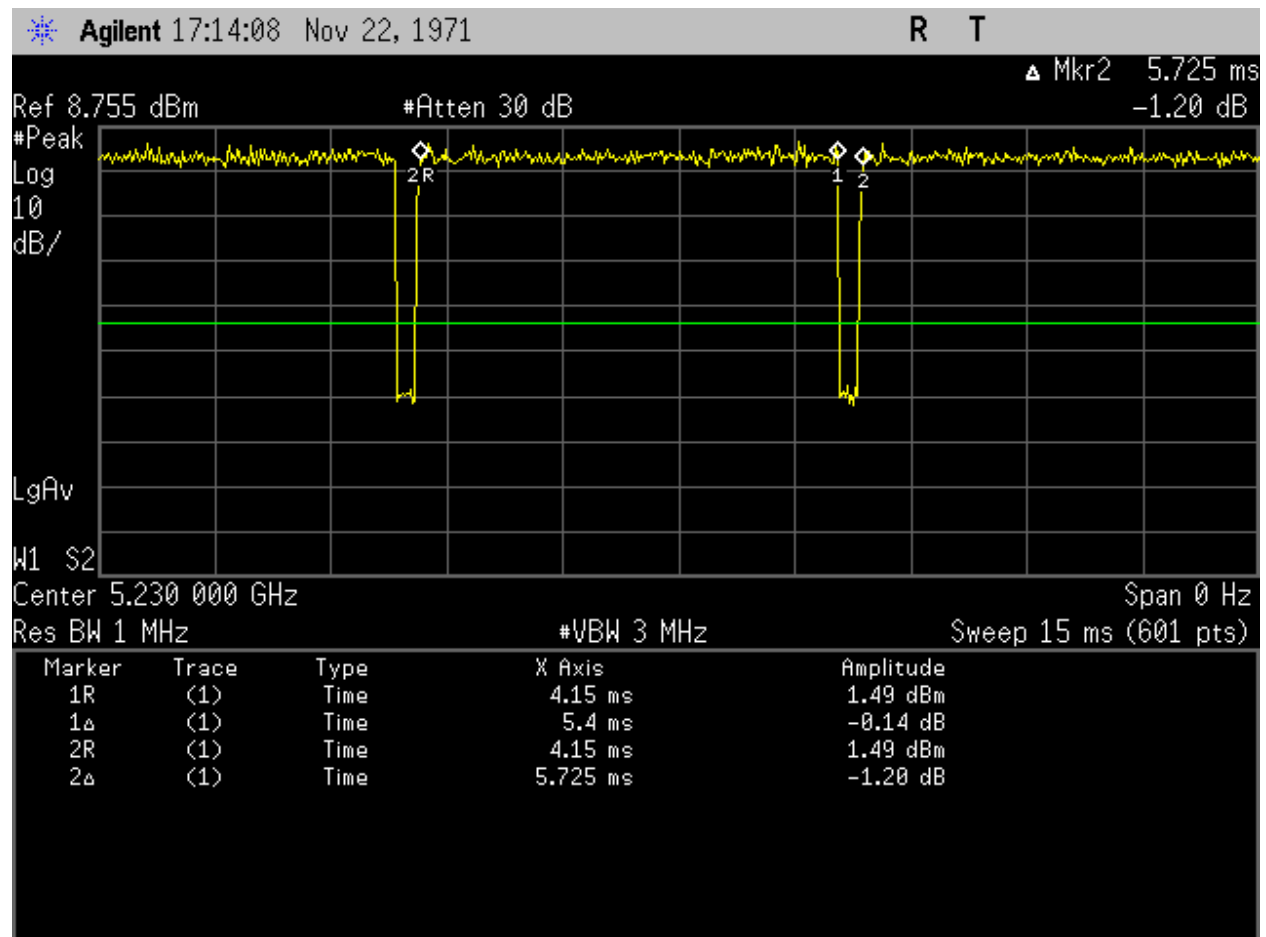


Figure 53: U-NII-1_5230MHz_high_Mid Ch_46_40MHz BW_ax-mode_Duty Cycle_Port 1.

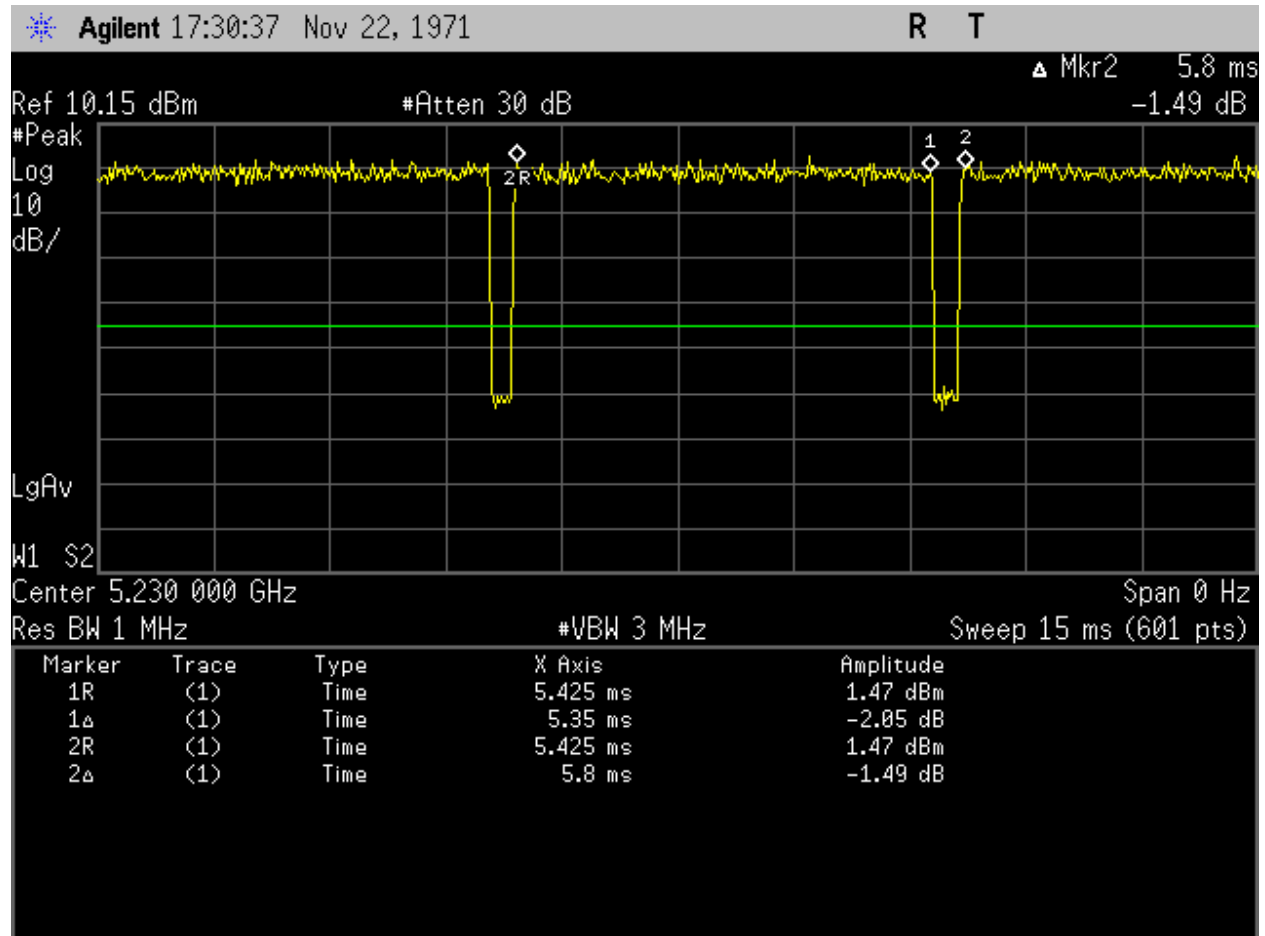


Figure 54: U-NII-1_5230MHz_high_Mid Ch_46_40MHz BW_n-mode_Duty Cycle_Port 1.

§15. 407(a)(1)(ii) Maximum Conducted Output Power

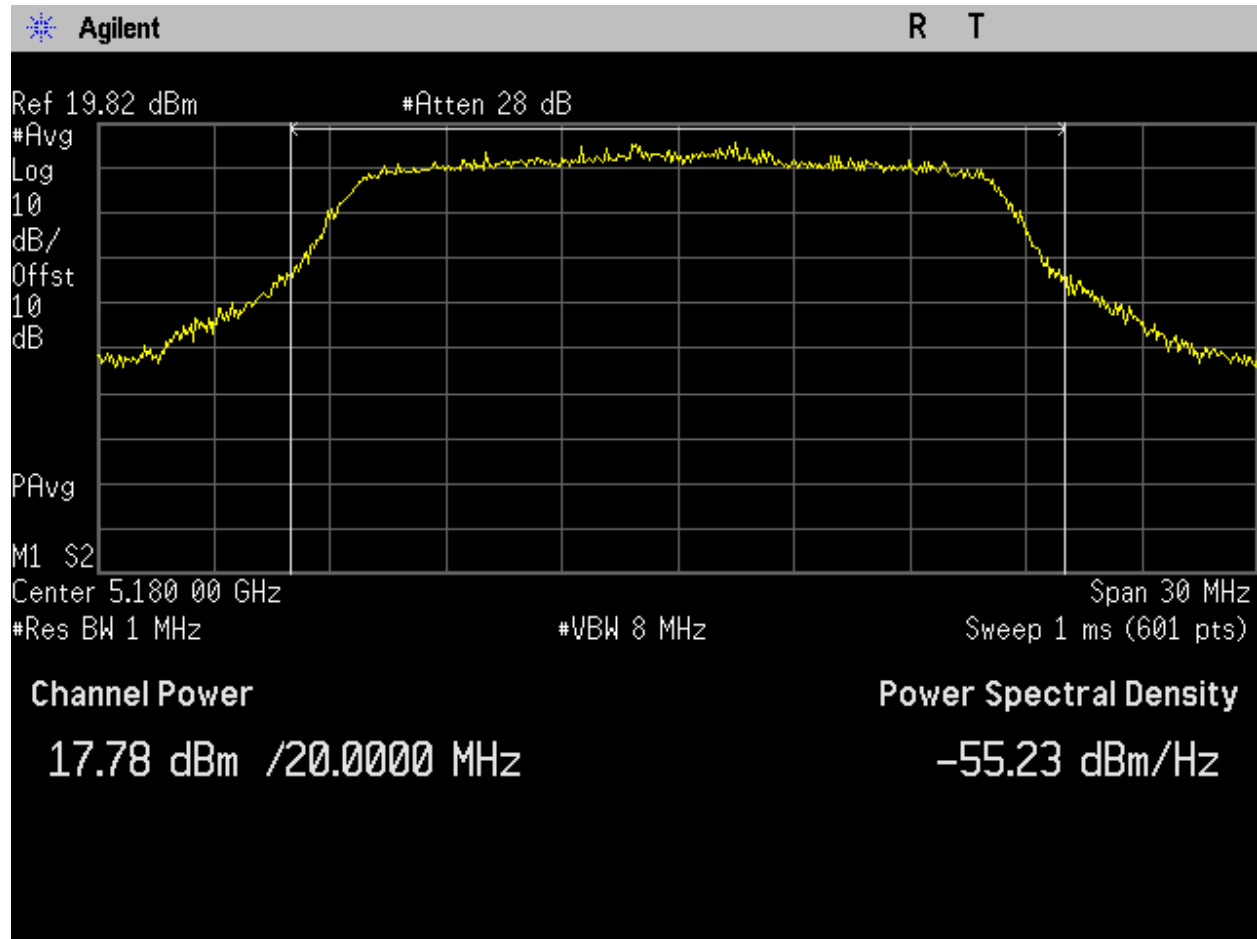


Figure 55: U-NII-1_5180MHz_Low Ch_36_20MHz BW_a-mode_Output Power_Port 1.

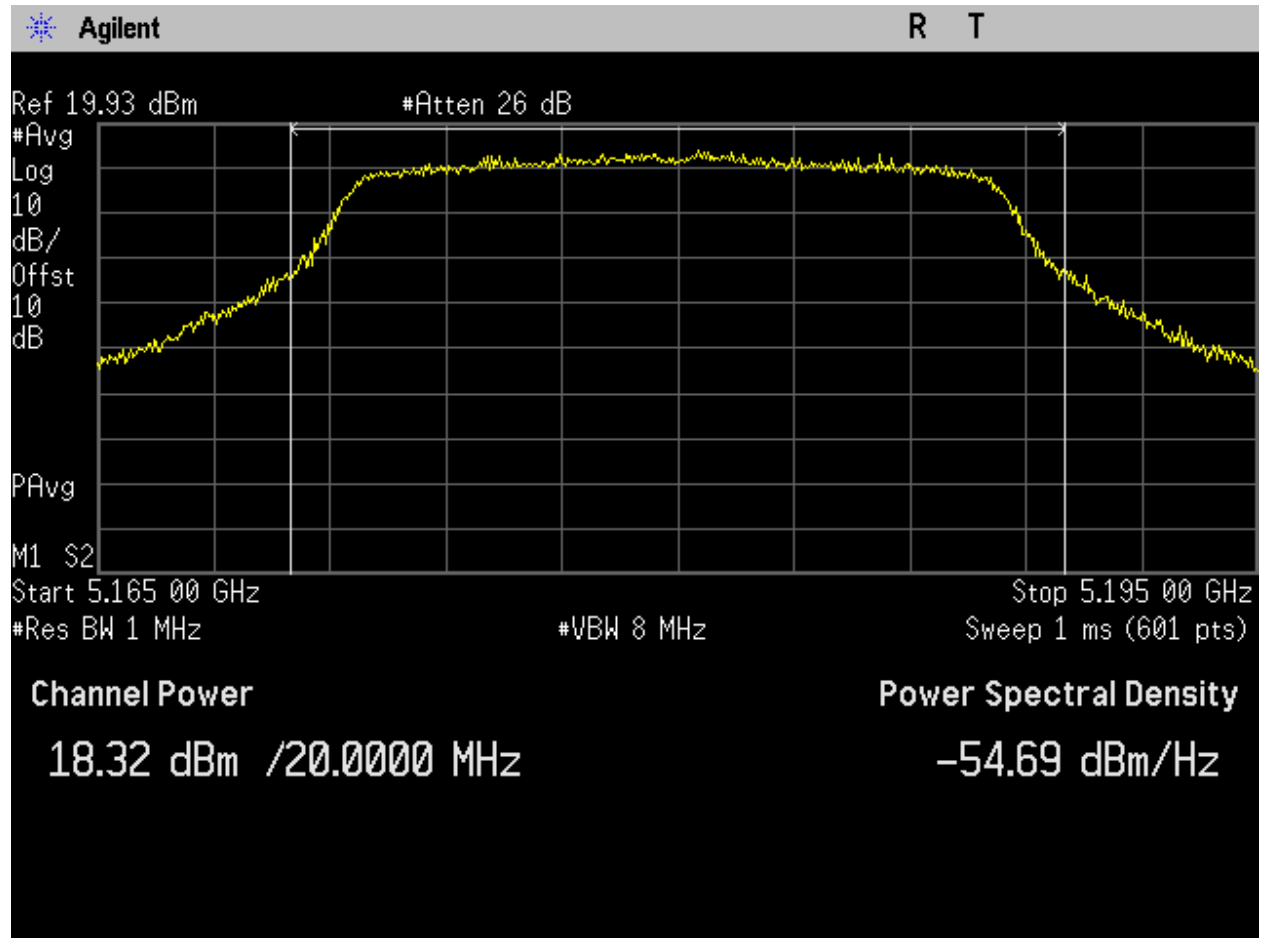


Figure 56: U-NII-1_5180MHz_Low Ch_36_20MHz BW_a-mode_Output Power_Port 2.

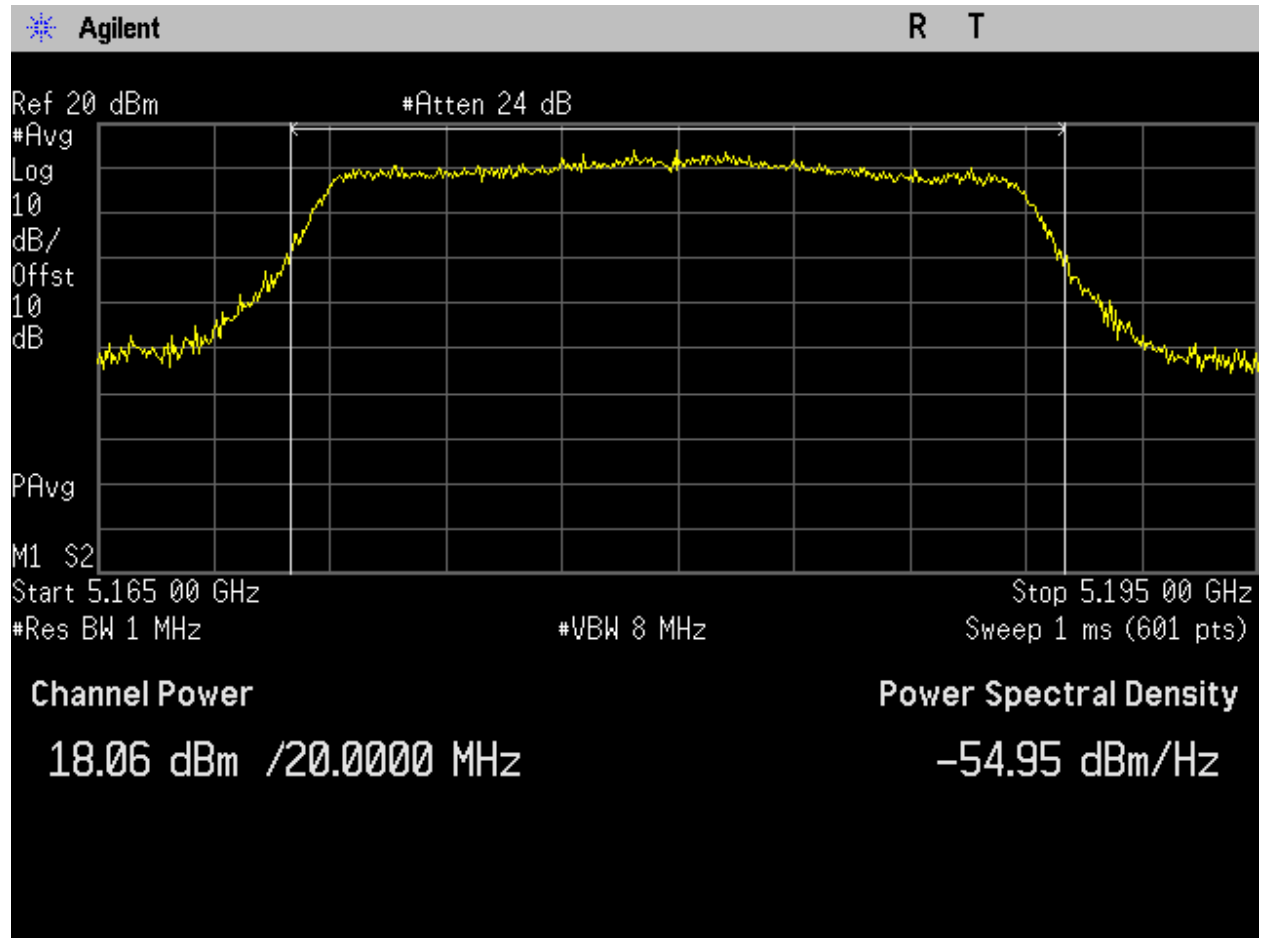


Figure 57: U-NII-1_5180MHz_Low Ch_36_20MHz BW_ac-mode_Output Power_Port 1.

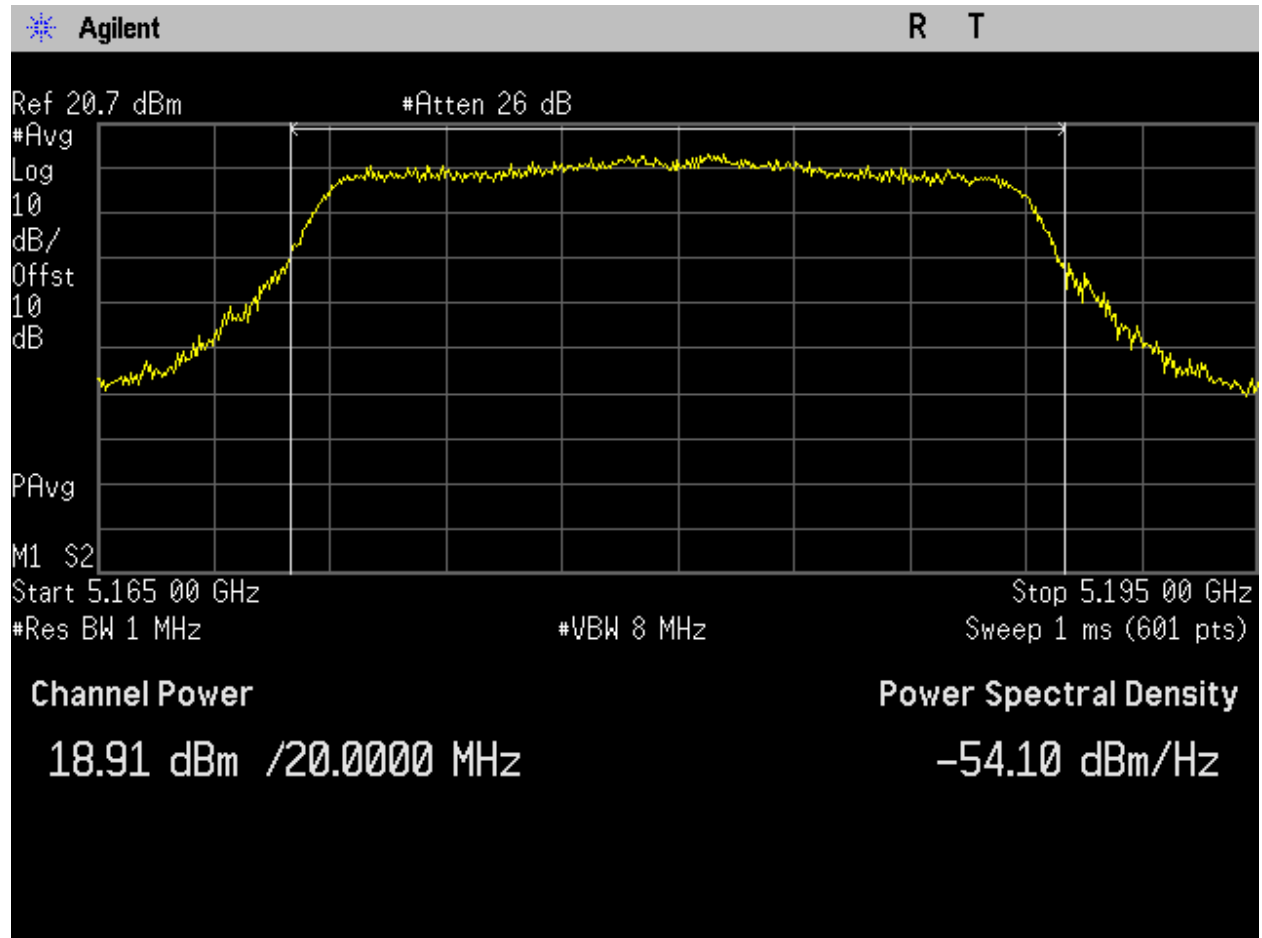


Figure 58: U-NII-1_5180MHz_Low Ch_36_20MHz BW_ac-mode_Output Power_Port 2.

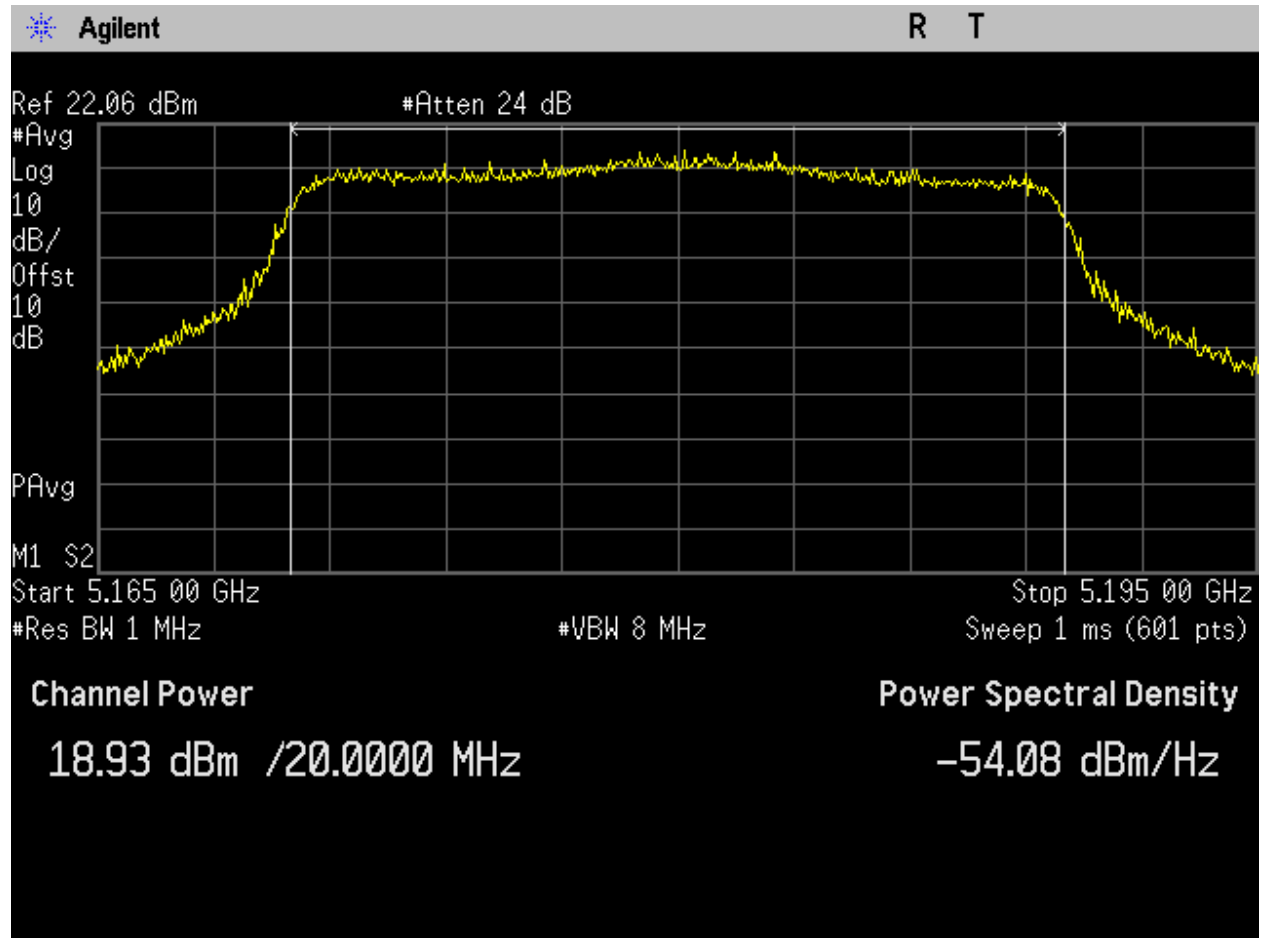


Figure 59: U-NII-1_5180MHz_Low Ch_36_20MHz BW_ax-mode_Output Power_Port 1.

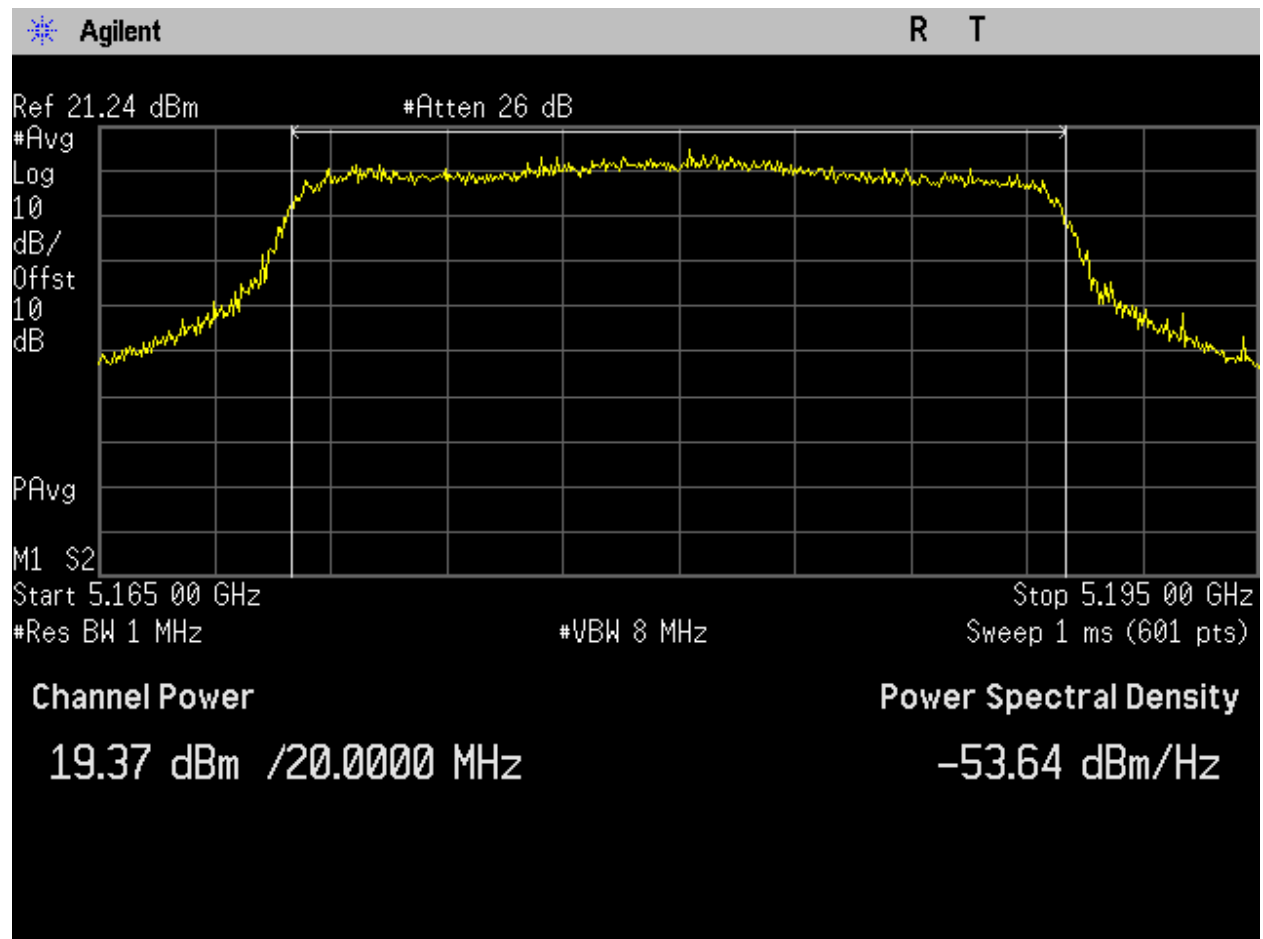


Figure 60: U-NII-1_5180MHz_Low Ch_36_20MHz BW_ax-mode_Output Power_Port 2.

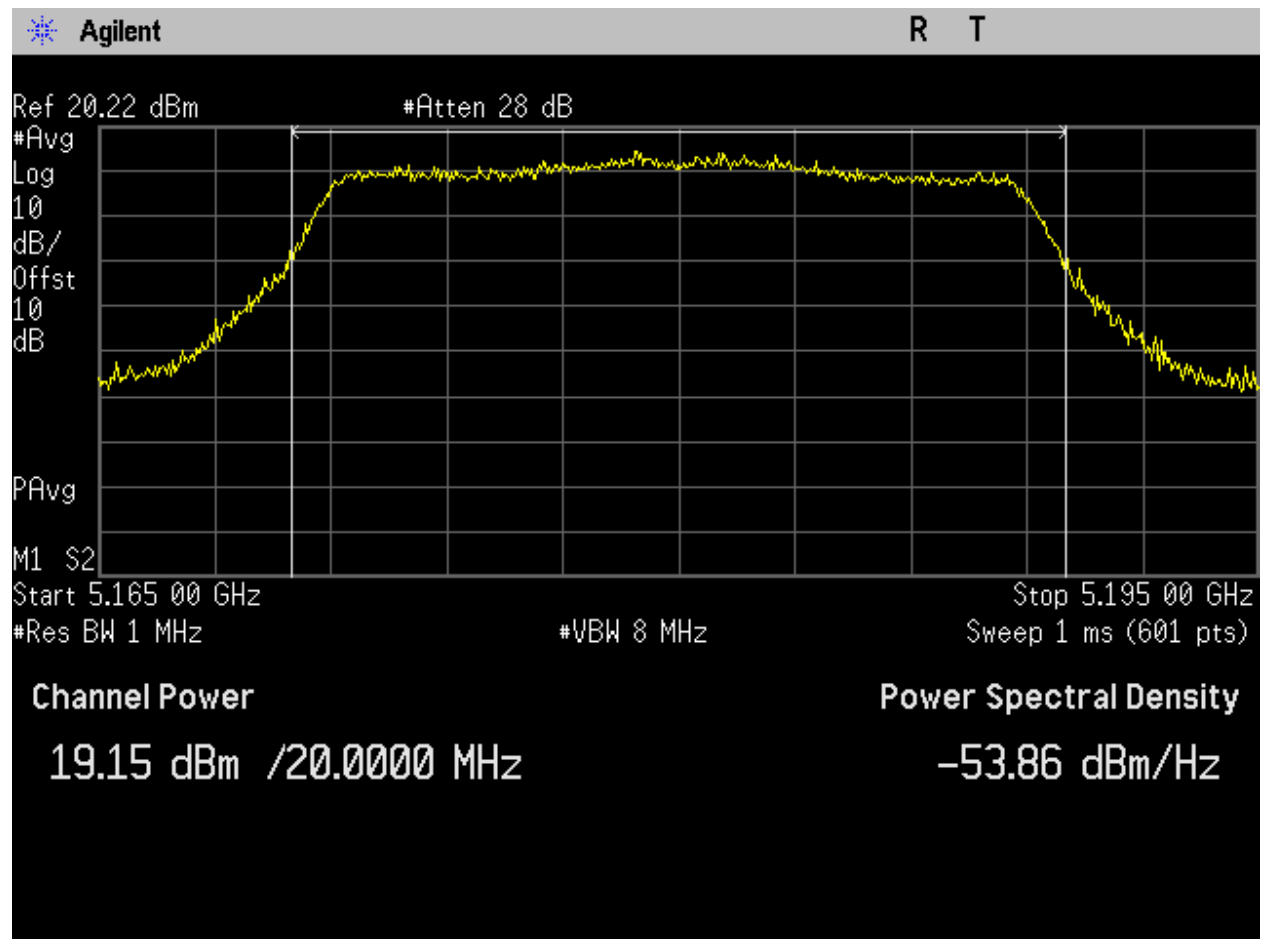


Figure 61: U-NII-1_5180MHz_Low Ch_36_20MHz BW_n-mode_Output Power_Port 1.

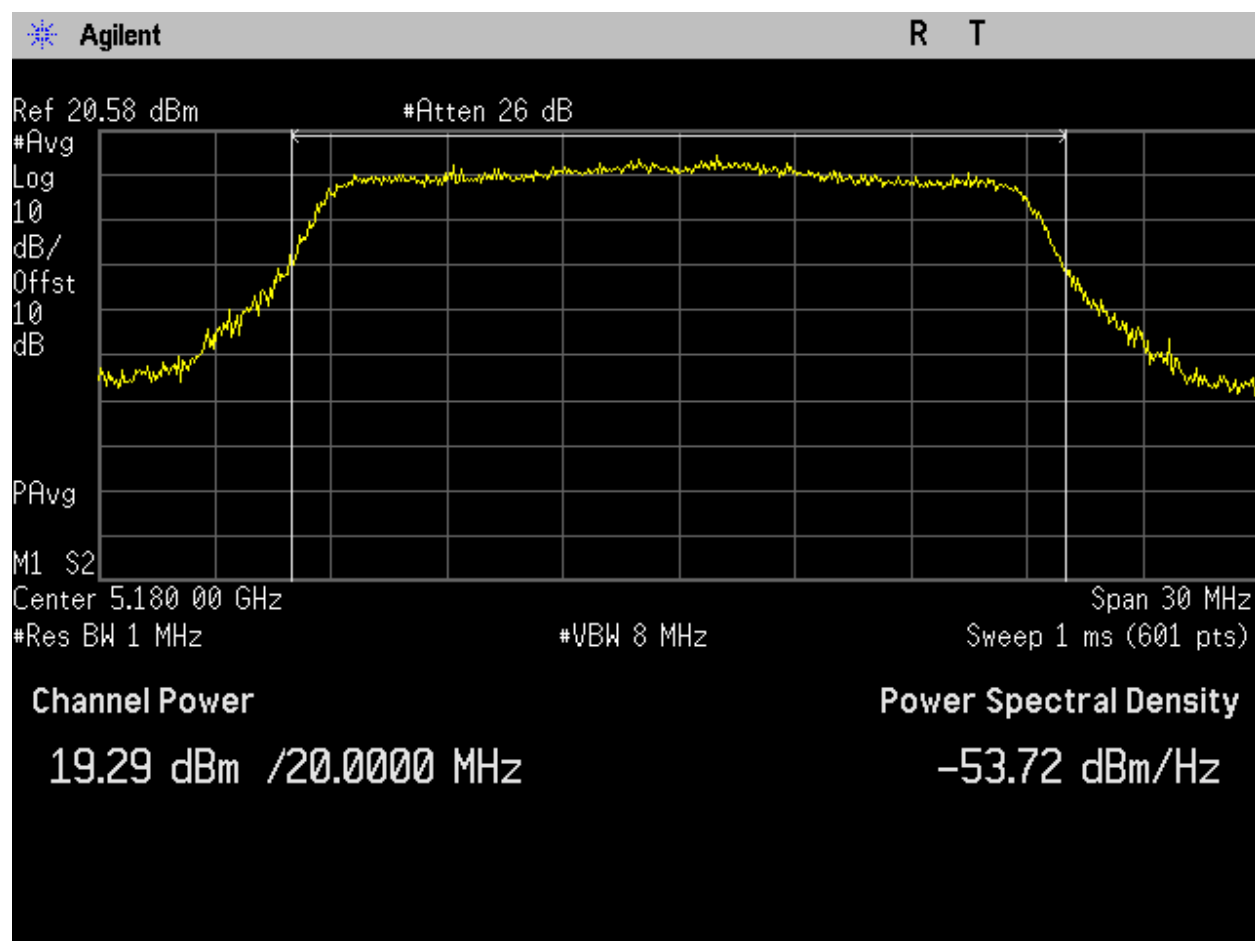


Figure 62: U-NII-1_5180MHz_Low Ch_36_20MHz BW_n-mode_Output Power_Port 2.

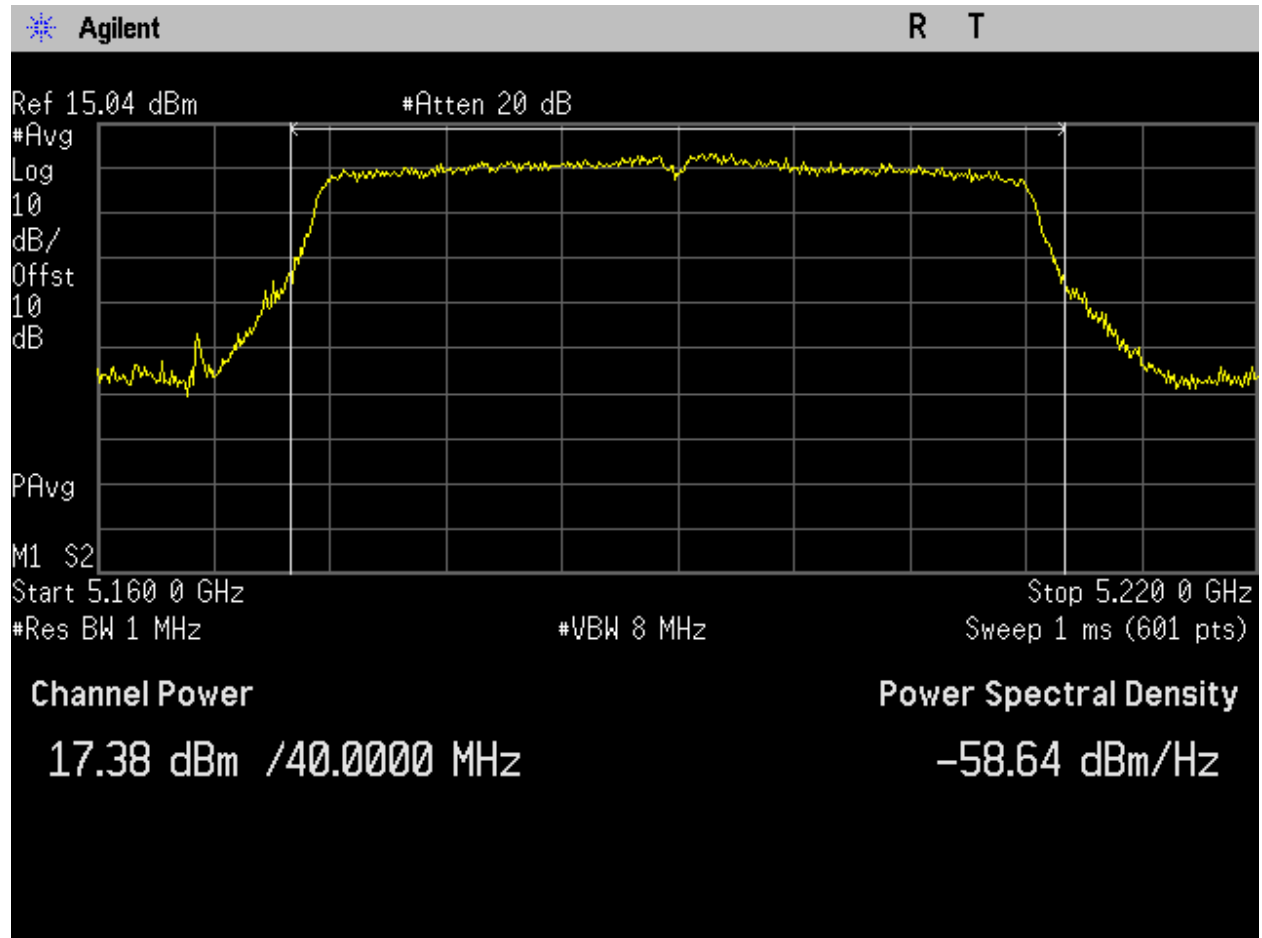


Figure 63: U-NII-1_5190MHz_Low Ch_38_40MHz BW_ac-mode_Output Power_Port 1.

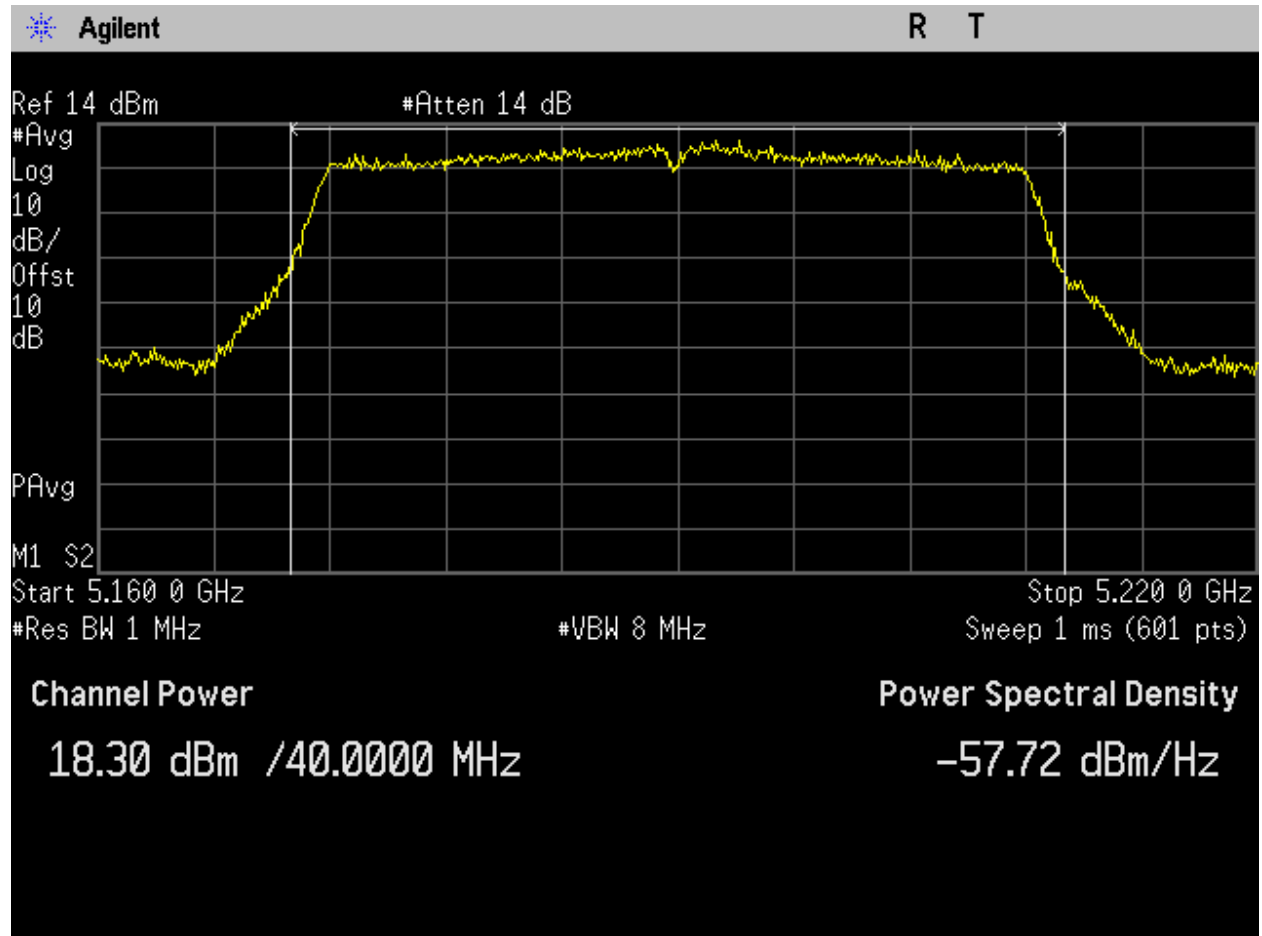


Figure 64: U-NII-1_5190MHz_Low Ch_38_40MHz BW_ac-mode_Output Power_Port 2.

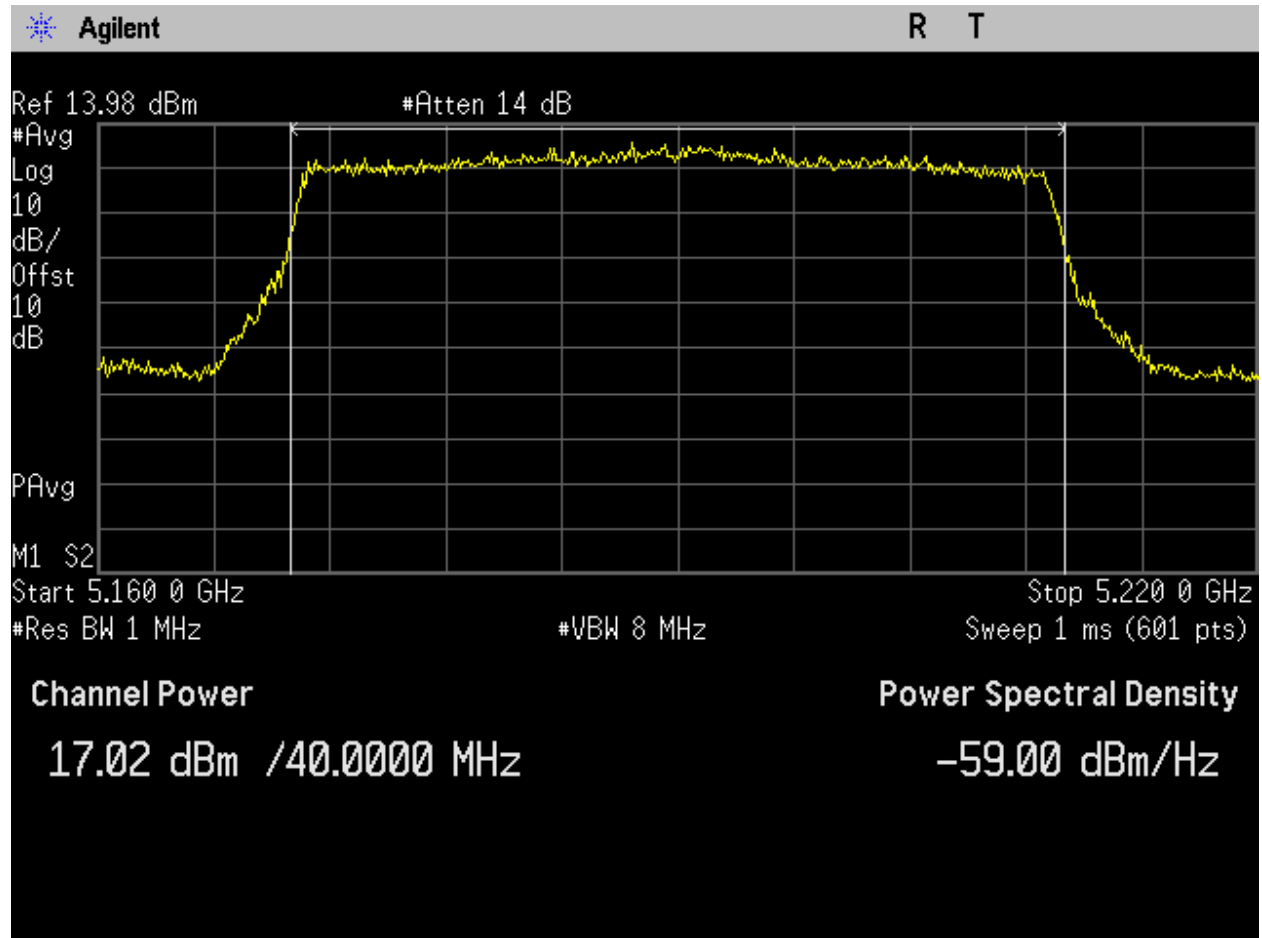


Figure 65: U-NII-1_5190MHz_Low Ch_38_40MHz BW_ax-mode_Output Power_Port 1.

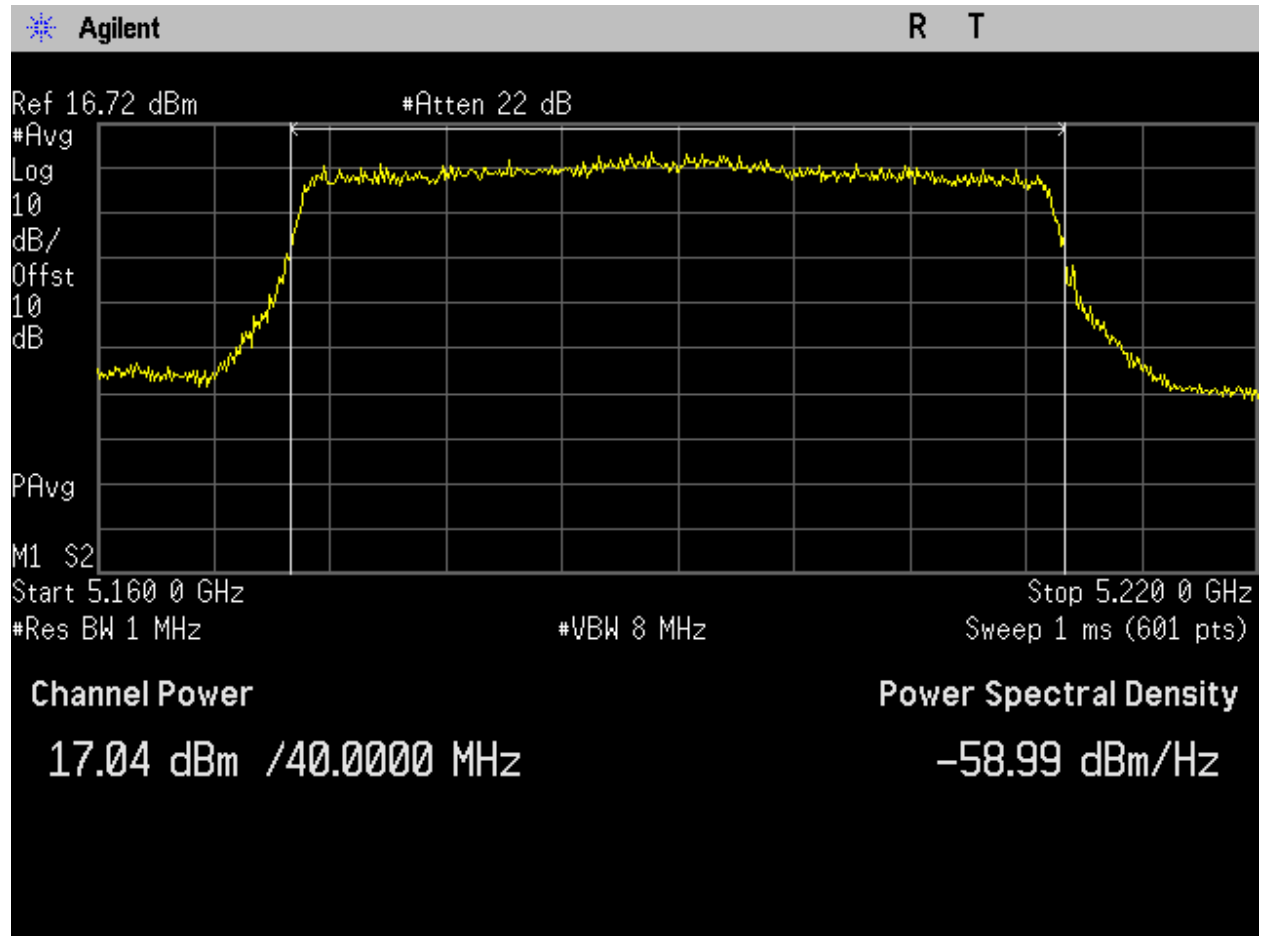


Figure 66: U-NII-1_5190MHz_Low Ch_38_40MHz BW_ax-mode_Output Power_Port 2.

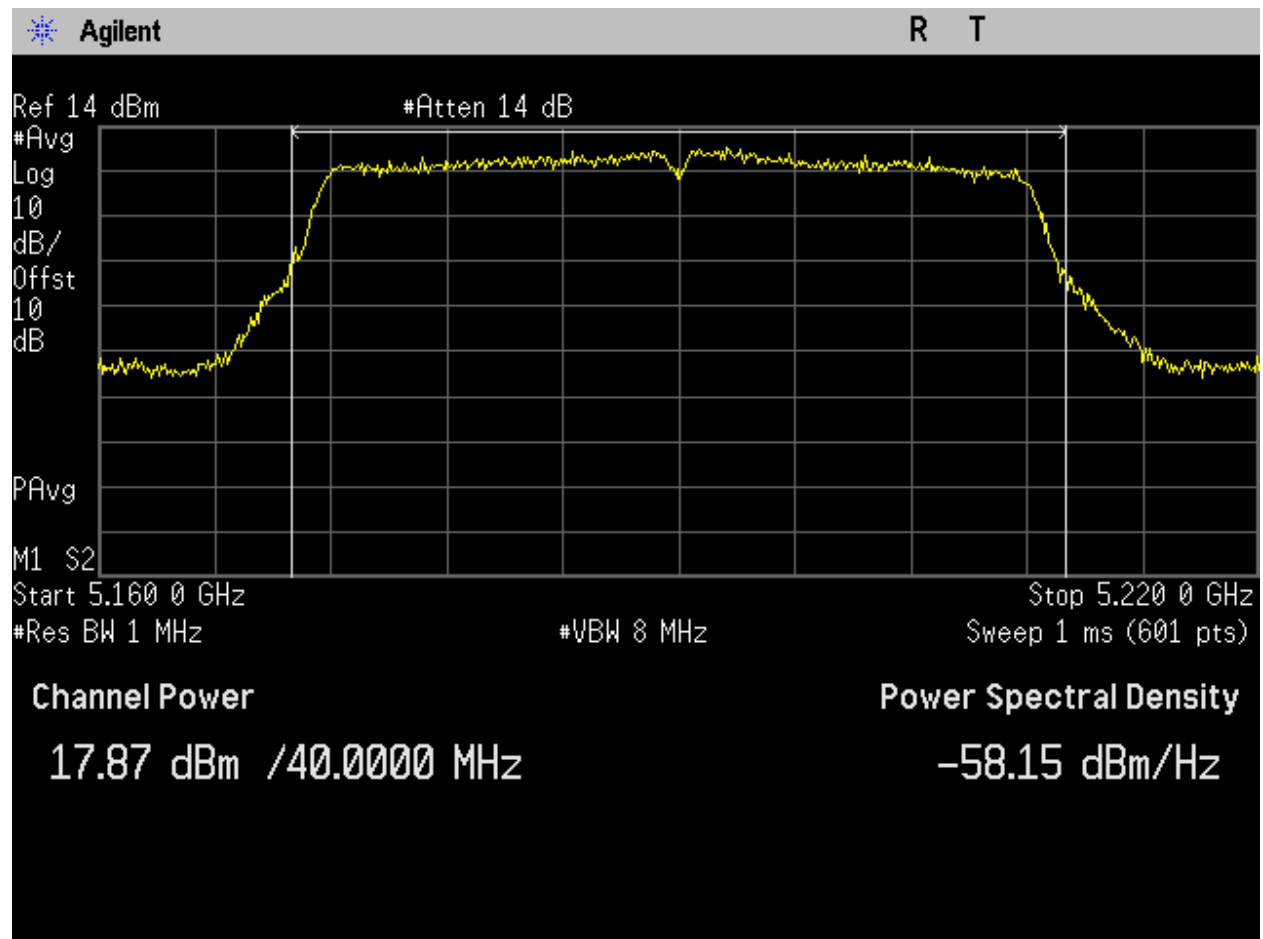


Figure 67: U-NII-1_5190MHz_Low Ch_38_40MHz BW_n-mode_Output Power_Port 1.

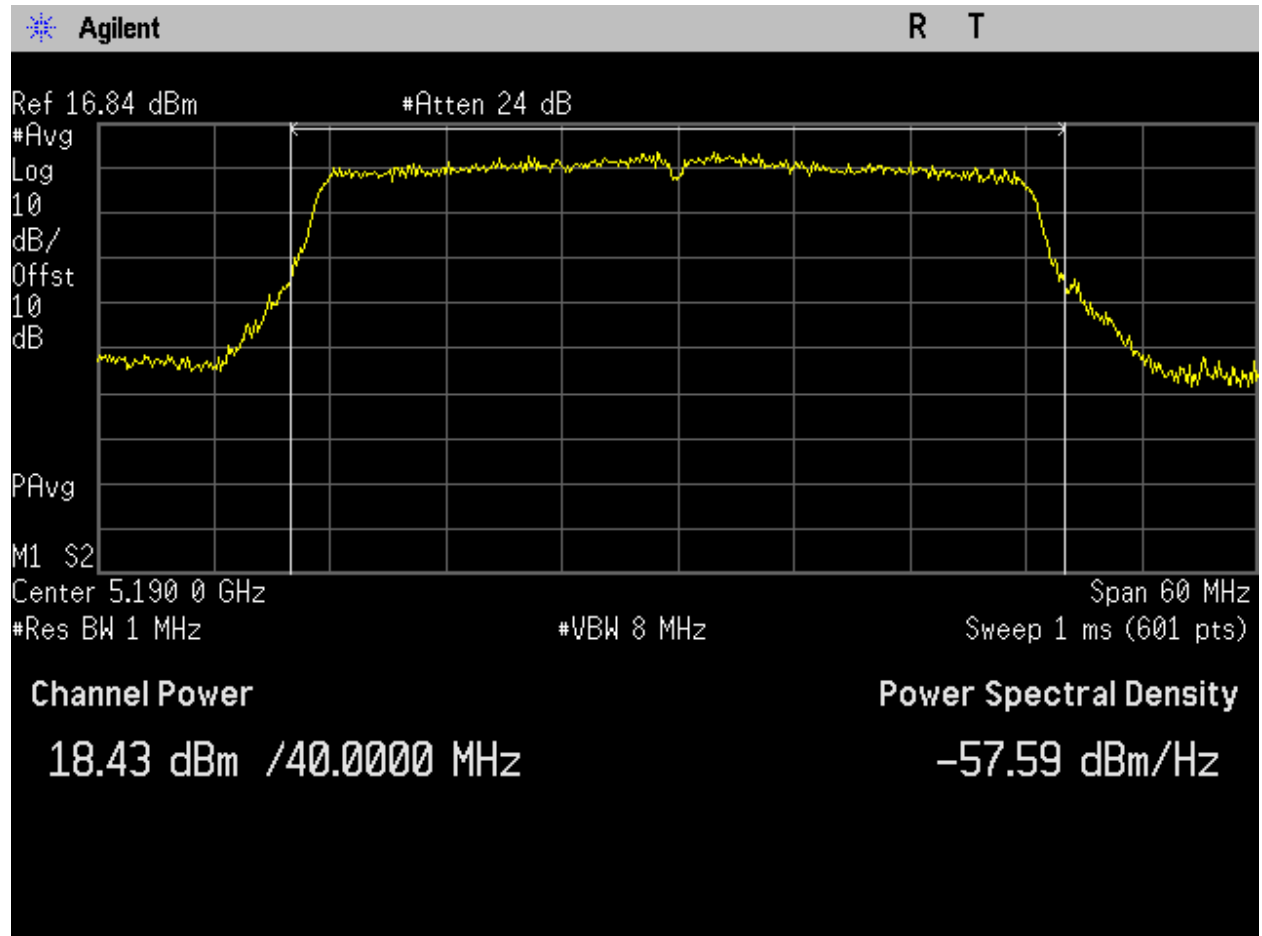


Figure 68: U-NII-1_5190MHz_Low Ch_38_40MHz BW_n-mode_Output Power_Port 2.



Figure 69: U-NII-1_5200MHz_low_Mid Ch_40_20MHz BW_a-mode_Output Power_Port 1.

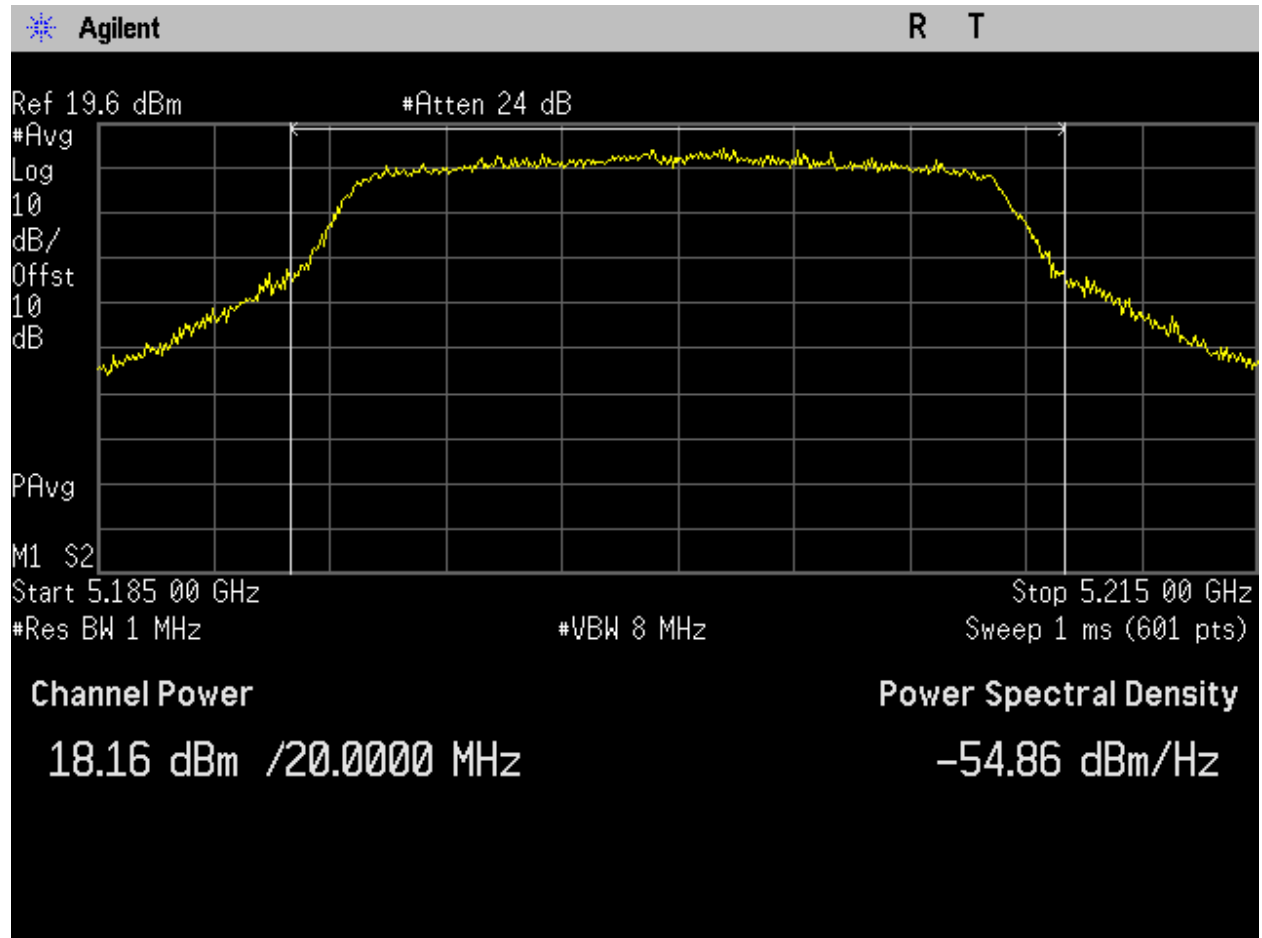


Figure 70: U-NII-1_5200MHz_low_Mid Ch_40_20MHz BW_a-mode_Output Power_Port 2.

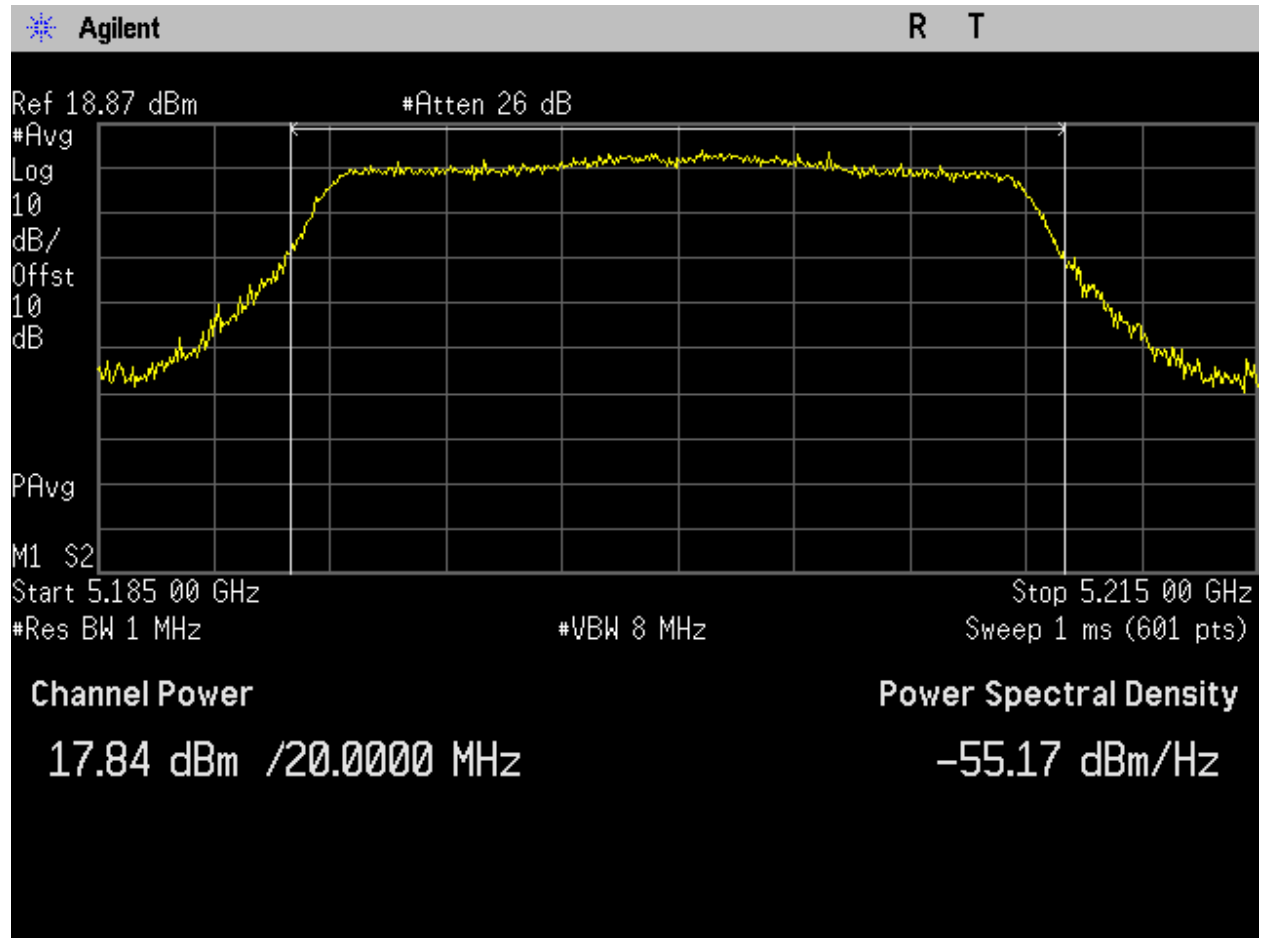


Figure 71: U-NII-1_5200MHz_low_Mid Ch_40_20MHz BW_ac-mode_Output Power_Port 1.

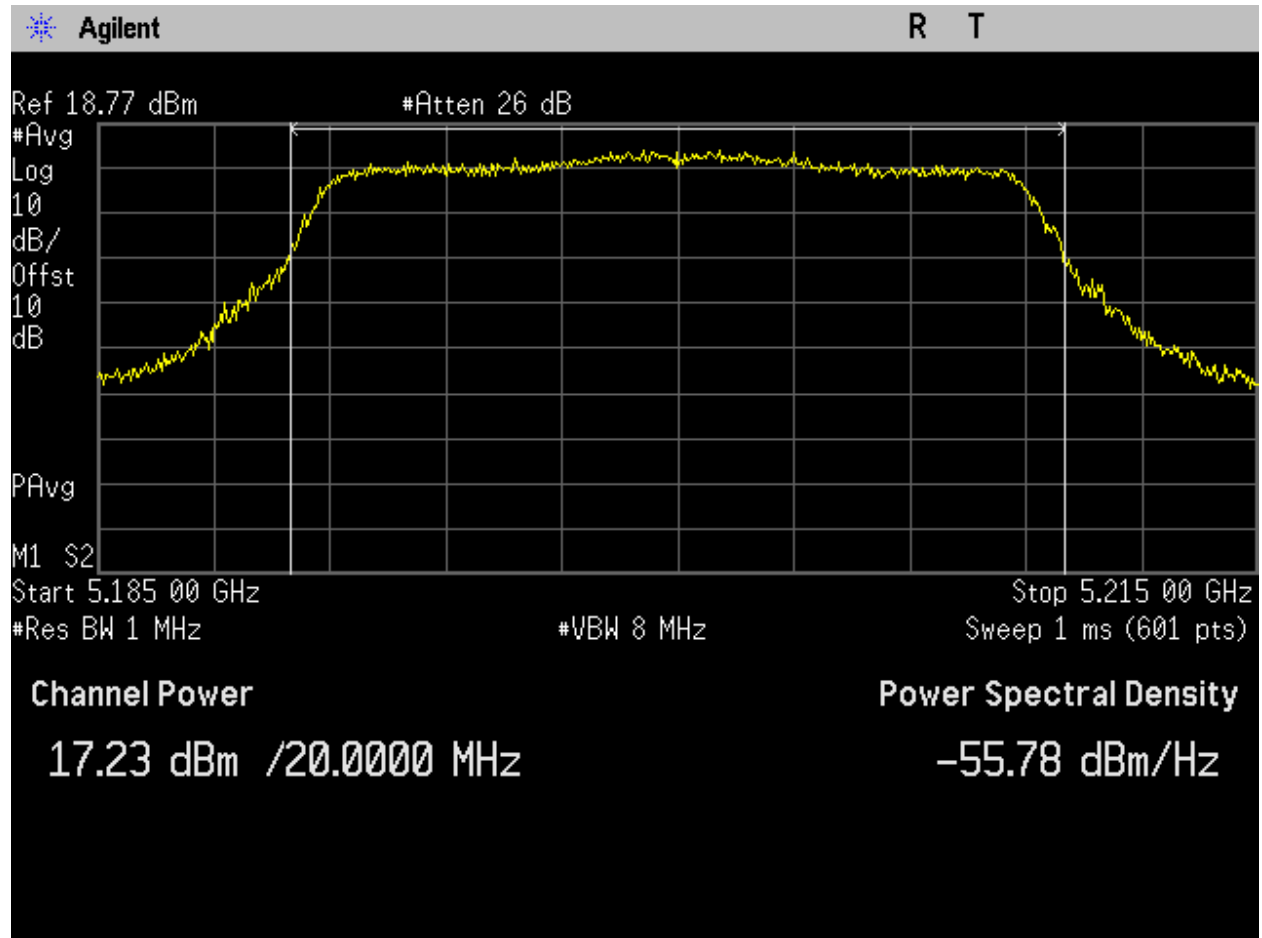


Figure 72: U-NII-1_5200MHz_low_Mid Ch_40_20MHz BW_ac-mode_Output Power_Port 2.

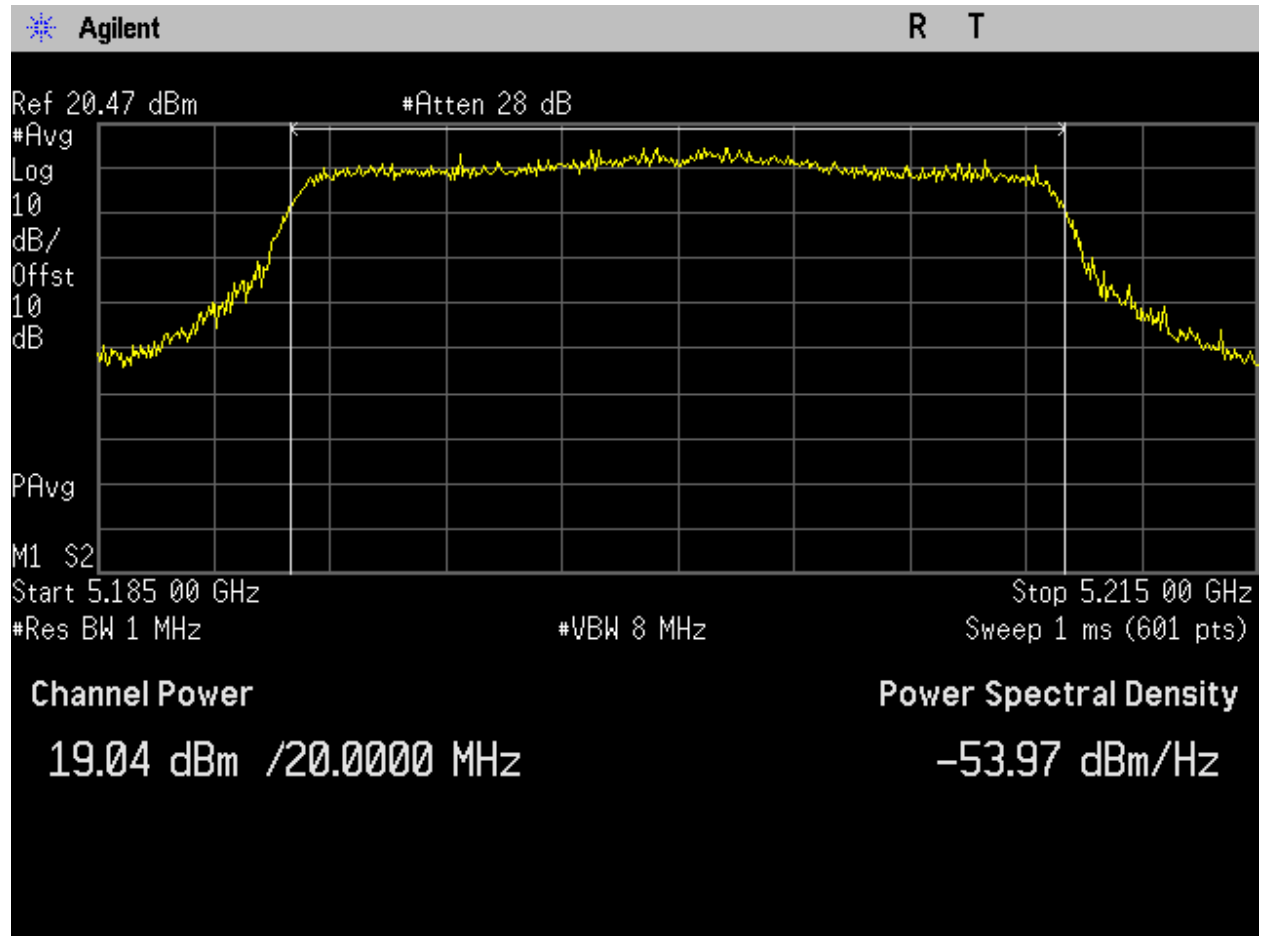


Figure 73: U-NII-1_5200MHz_low_Mid Ch_40_20MHz BW_ax-mode_Output Power_Port 1.

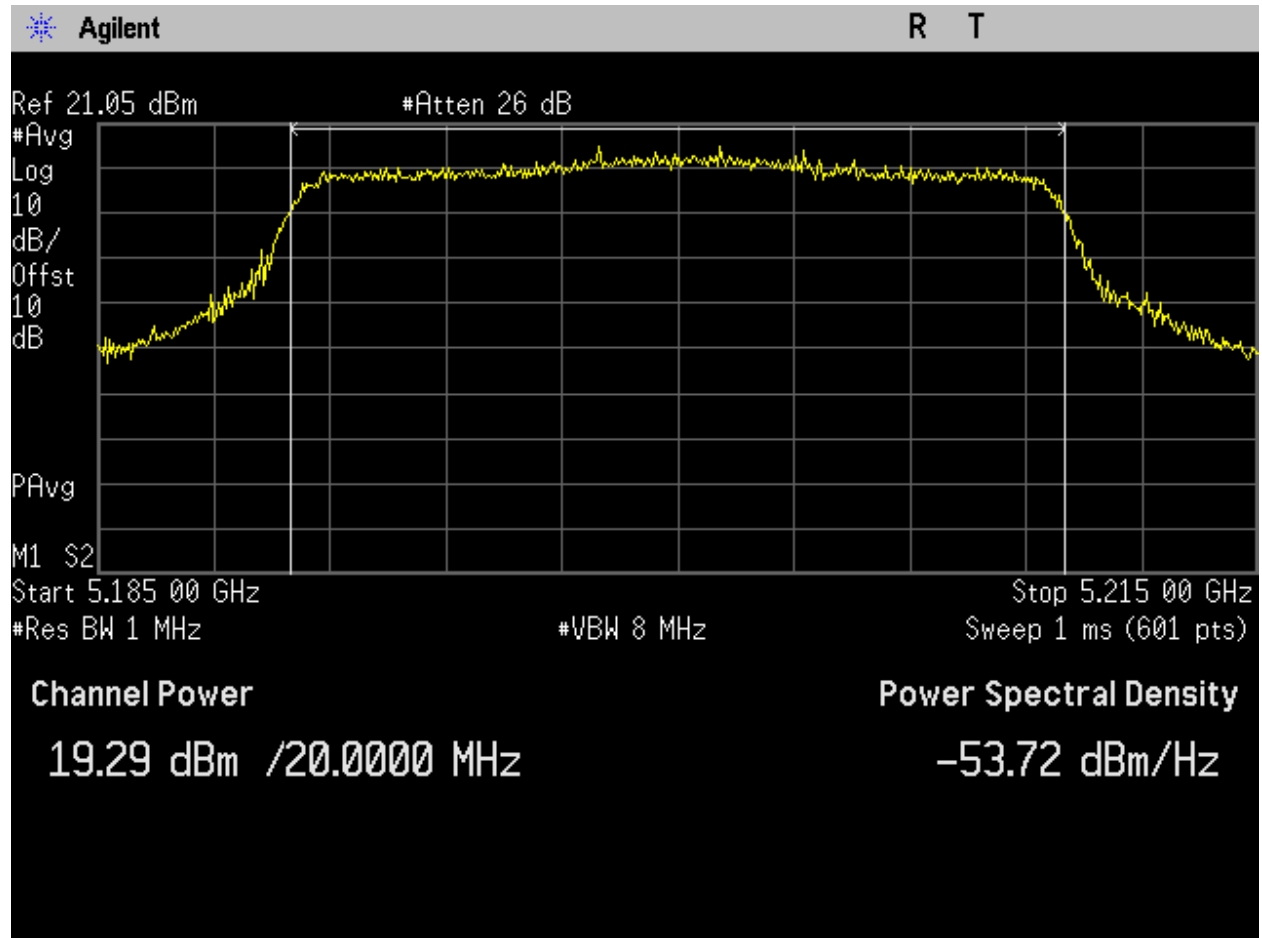


Figure 74: U-NII-1_5200MHz_low_Mid Ch_40_20MHz BW_ax-mode_Output Power_Port 2.

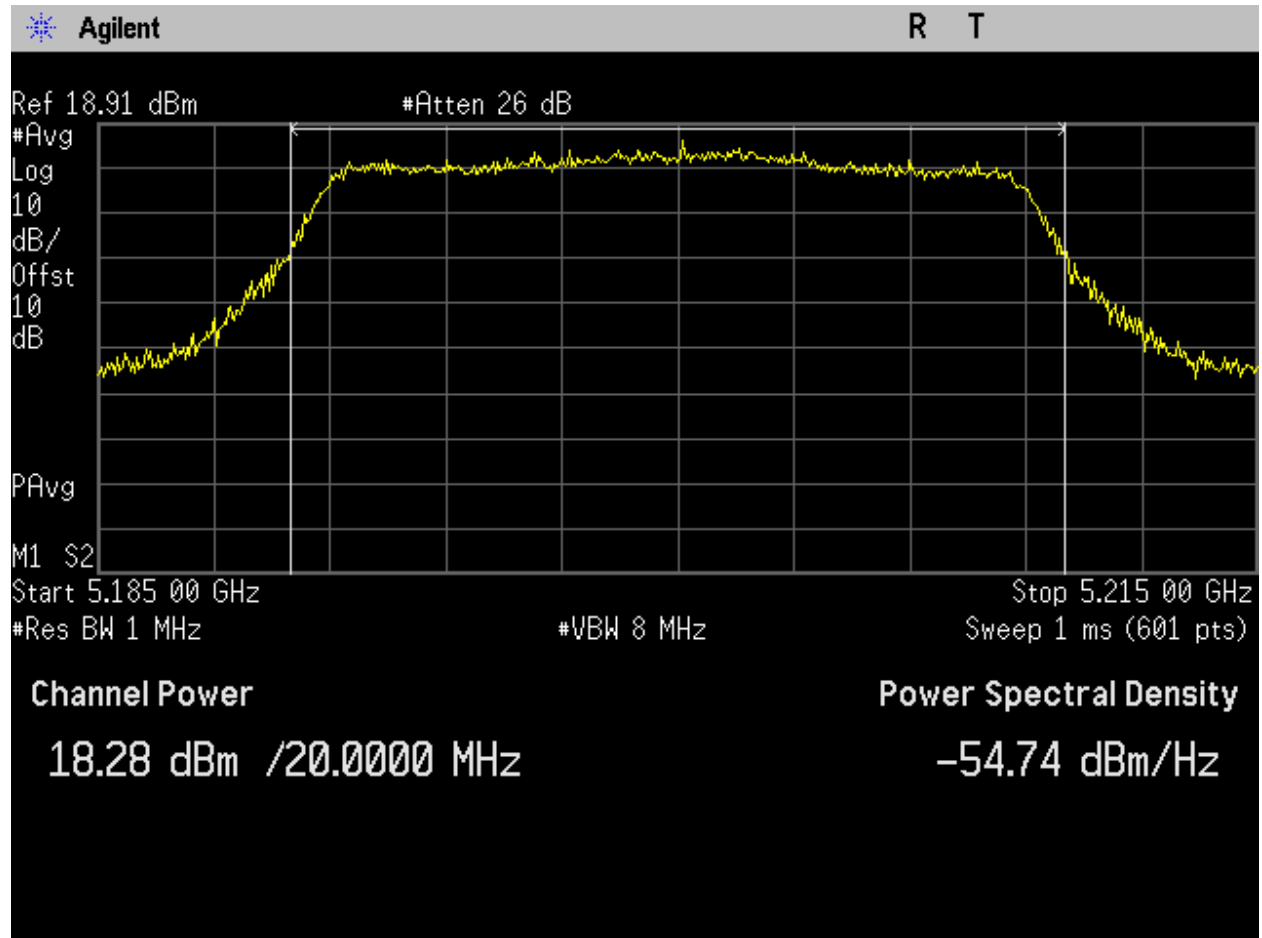


Figure 75: U-NII-1_5200MHz_low_Mid Ch_40_20MHz BW_n-mode_Output Power_Port 1.

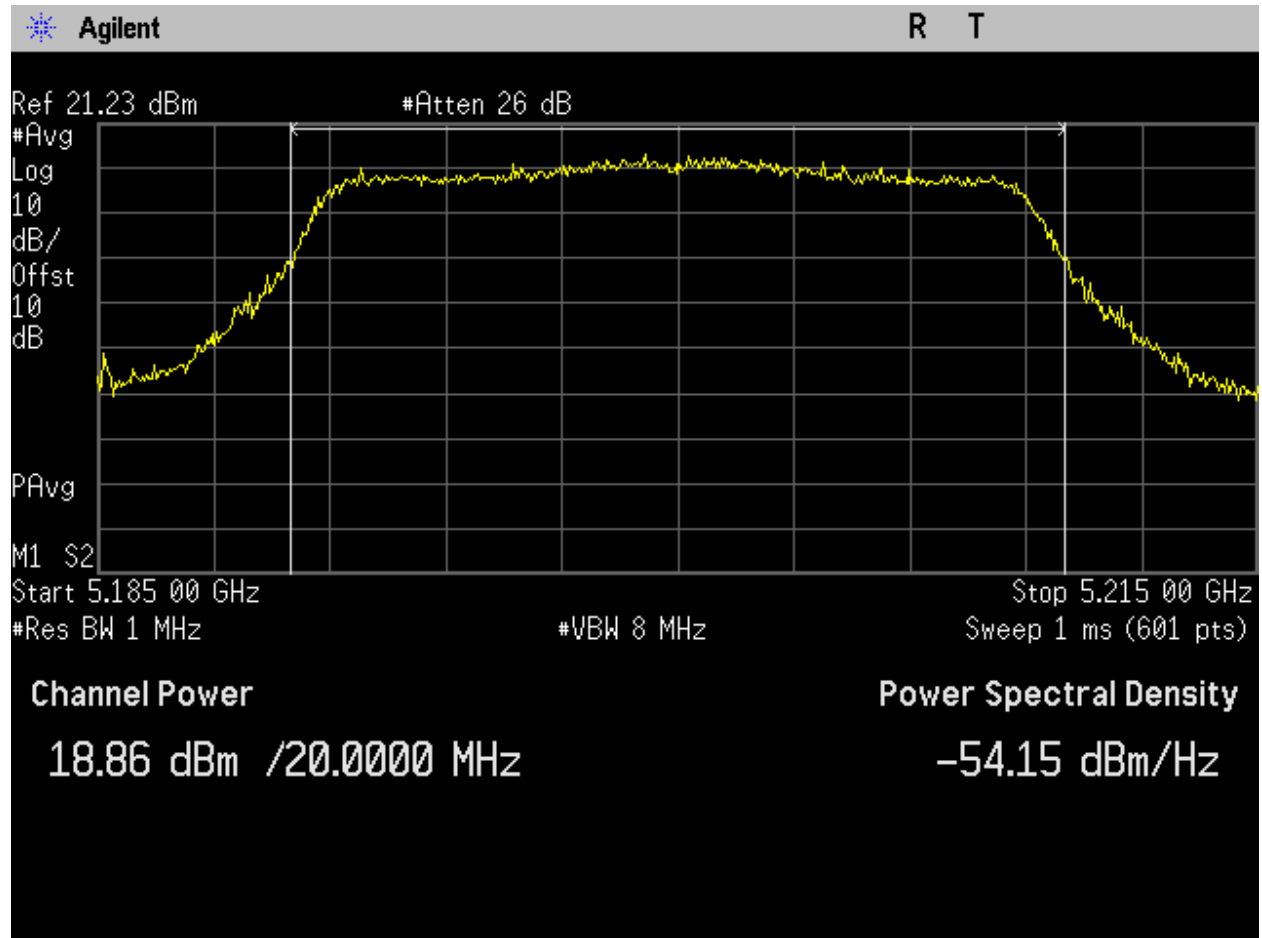


Figure 76: U-NII-1_5200MHz_low_Mid Ch_40_20MHz BW_n-mode_Output Power_Port 2.

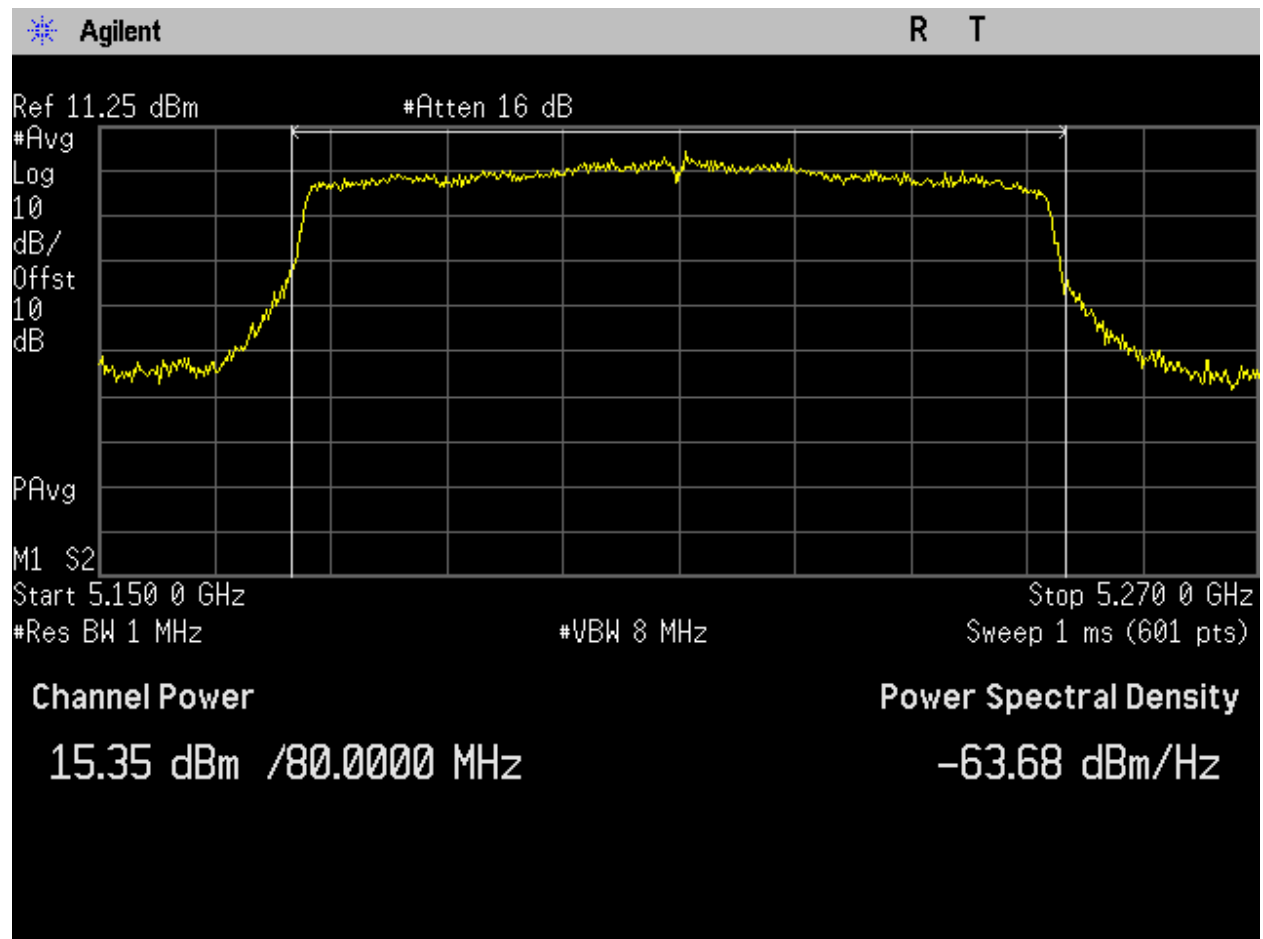


Figure 77: U-NII-1_5210MHz_Low Mid Ch_42_80MHz BW_ac-mode_Output Power_Port 1.

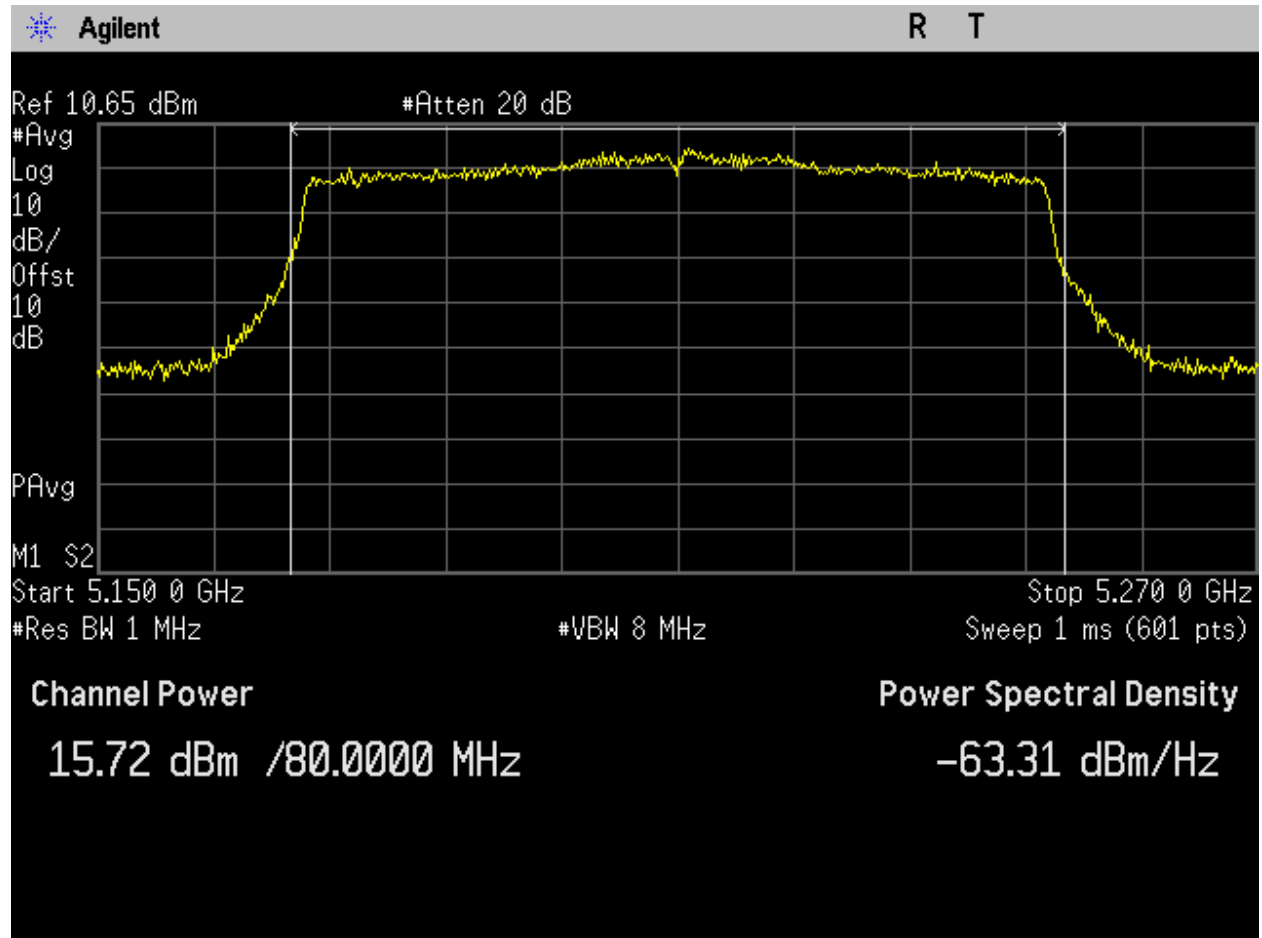


Figure 78: U-NII-1_5210MHz_Low Mid Ch_42_80MHz BW_ac-mode_Output Power_Port 2.

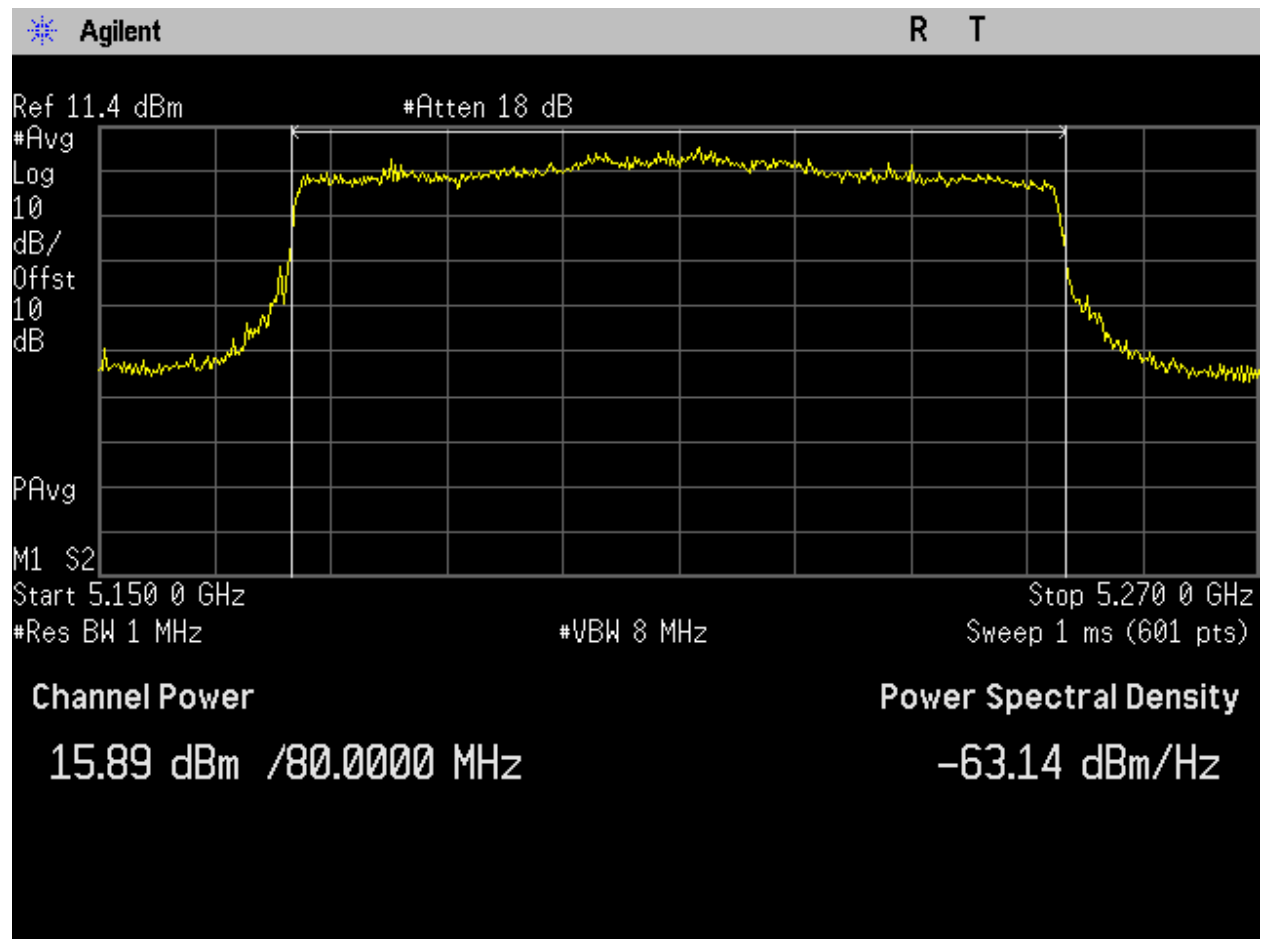


Figure 79: U-NII-1_5210MHz_Low Mid Ch_42_80MHz BW_ax-mode_Output Power_Port 1.

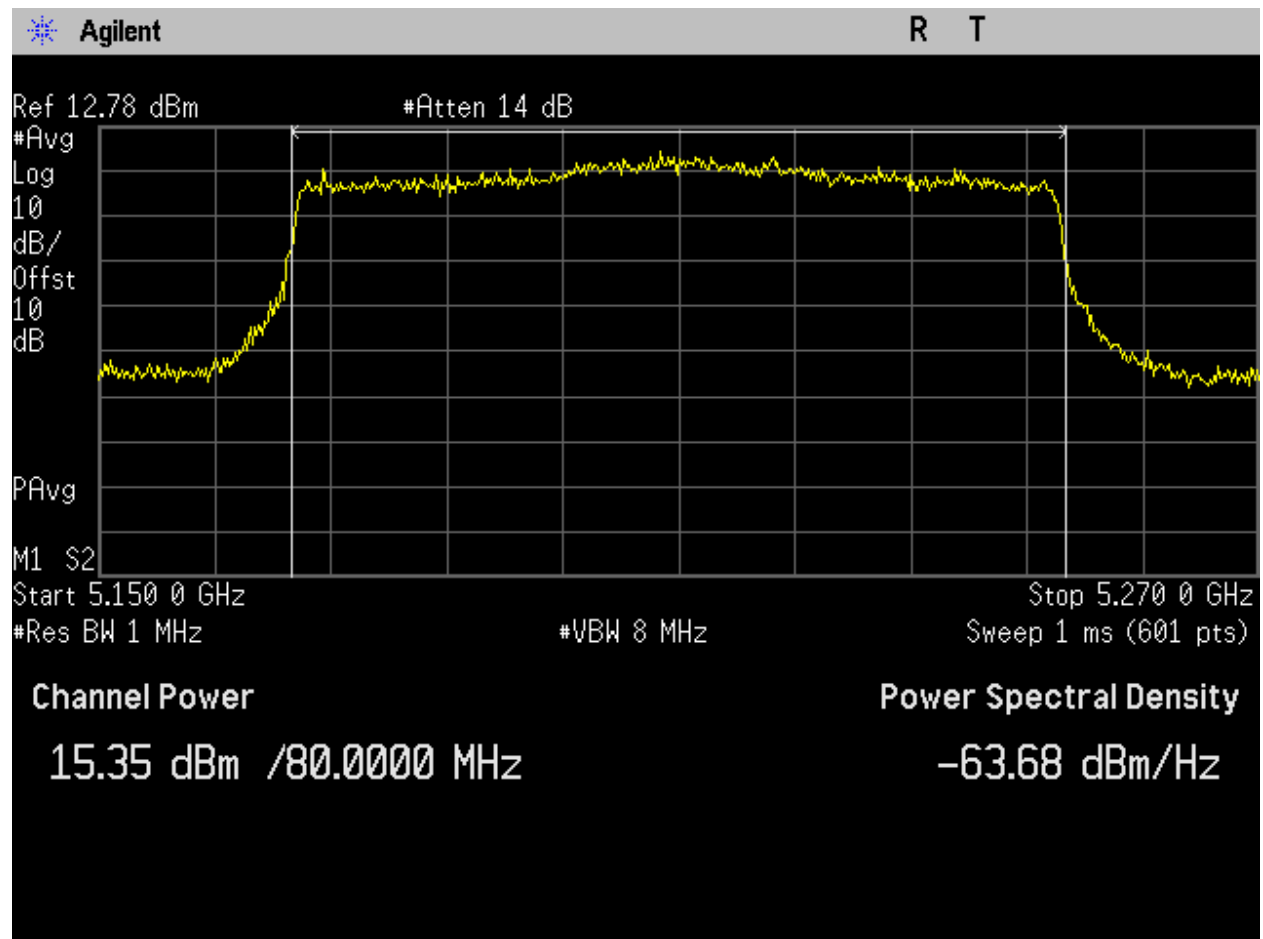


Figure 80: U-NII-1_5210MHz_Low Mid Ch_42_80MHz BW_ax-mode_Output Power_Port 2.

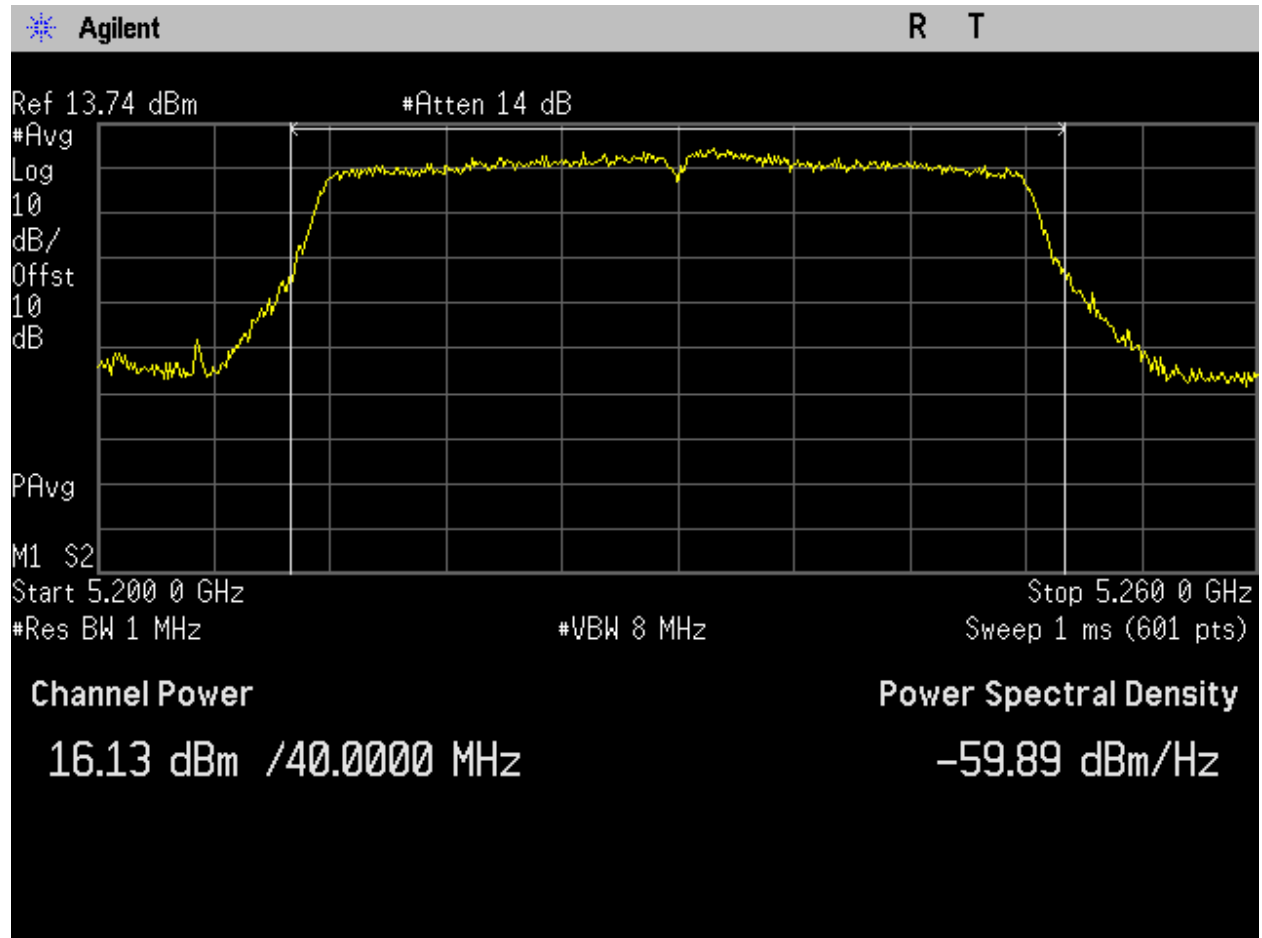


Figure 81: U-NII-1_5230MHz_high_Mid Ch_46_40MHz BW_ac-mode_Output Power_Port 1.

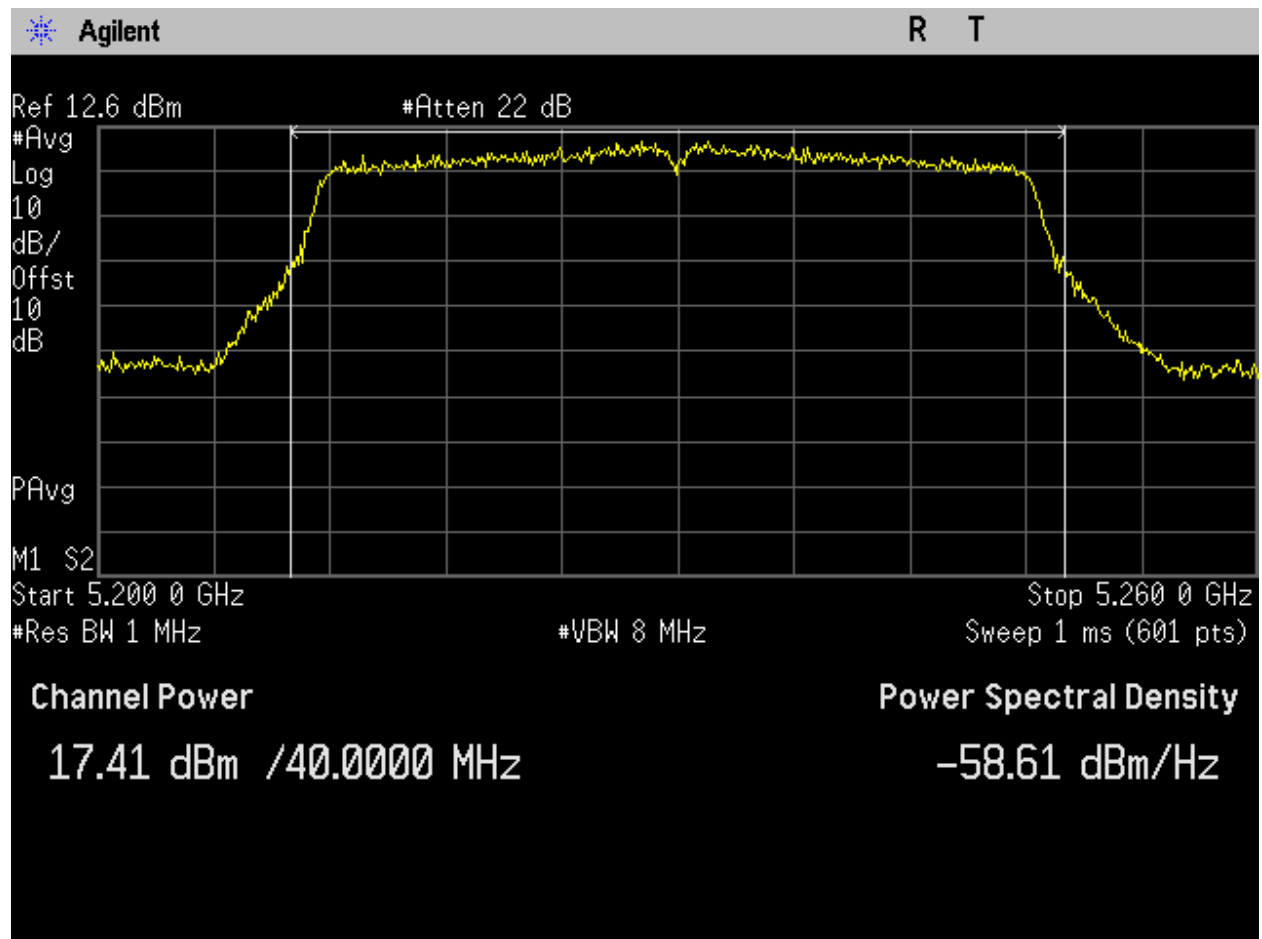


Figure 82: U-NII-1_5230MHz_high_Mid Ch_46_40MHz BW_ac-mode_Output Power_Port 2.

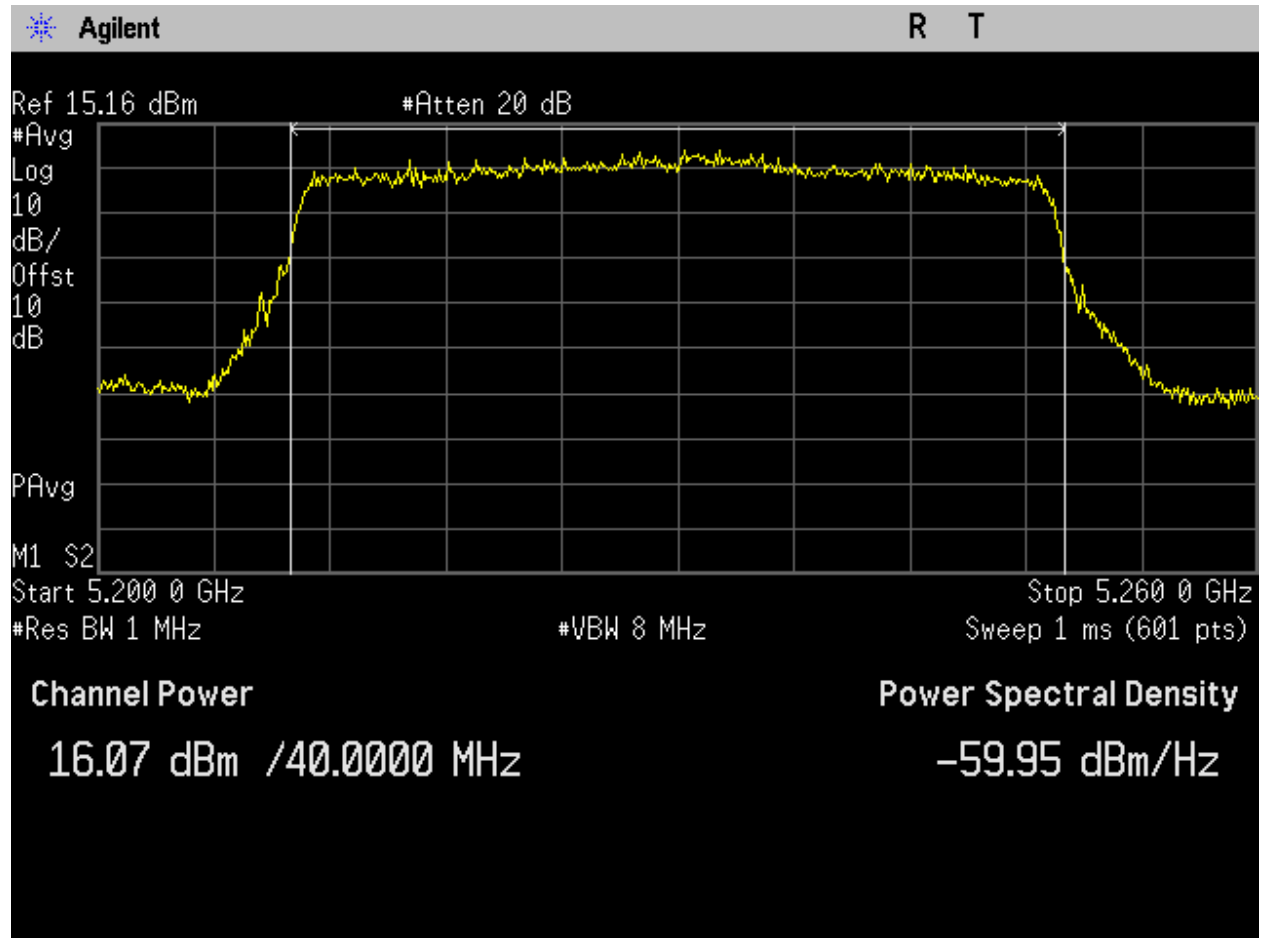


Figure 83: U-NII-1_5230MHz_high_Mid Ch_46_40MHz BW_ax-mode_Output Power_Port 1.

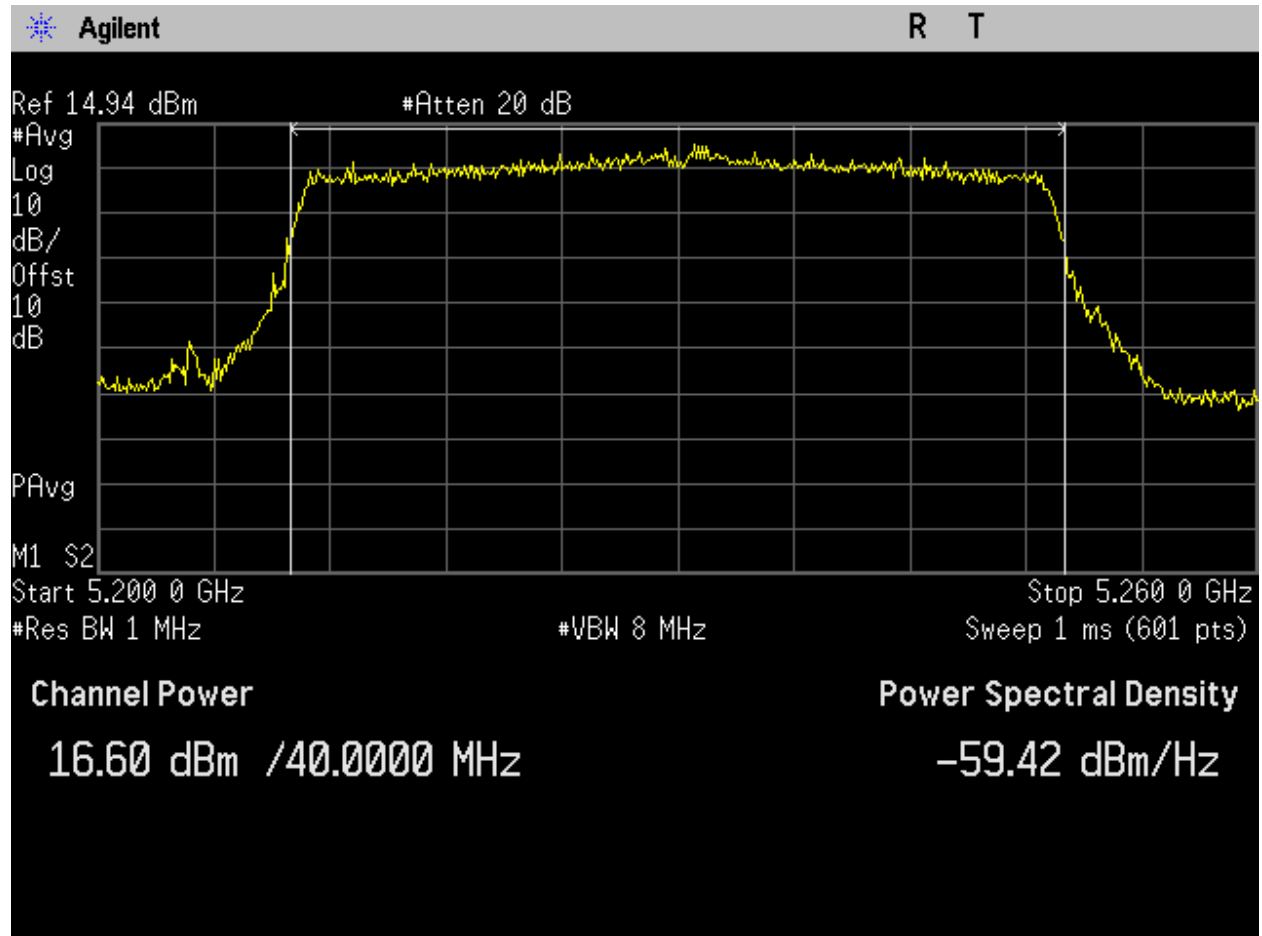


Figure 84: U-NII-1_5230MHz_high_Mid Ch_46_40MHz BW_ax-mode_Output Power_Port 2.

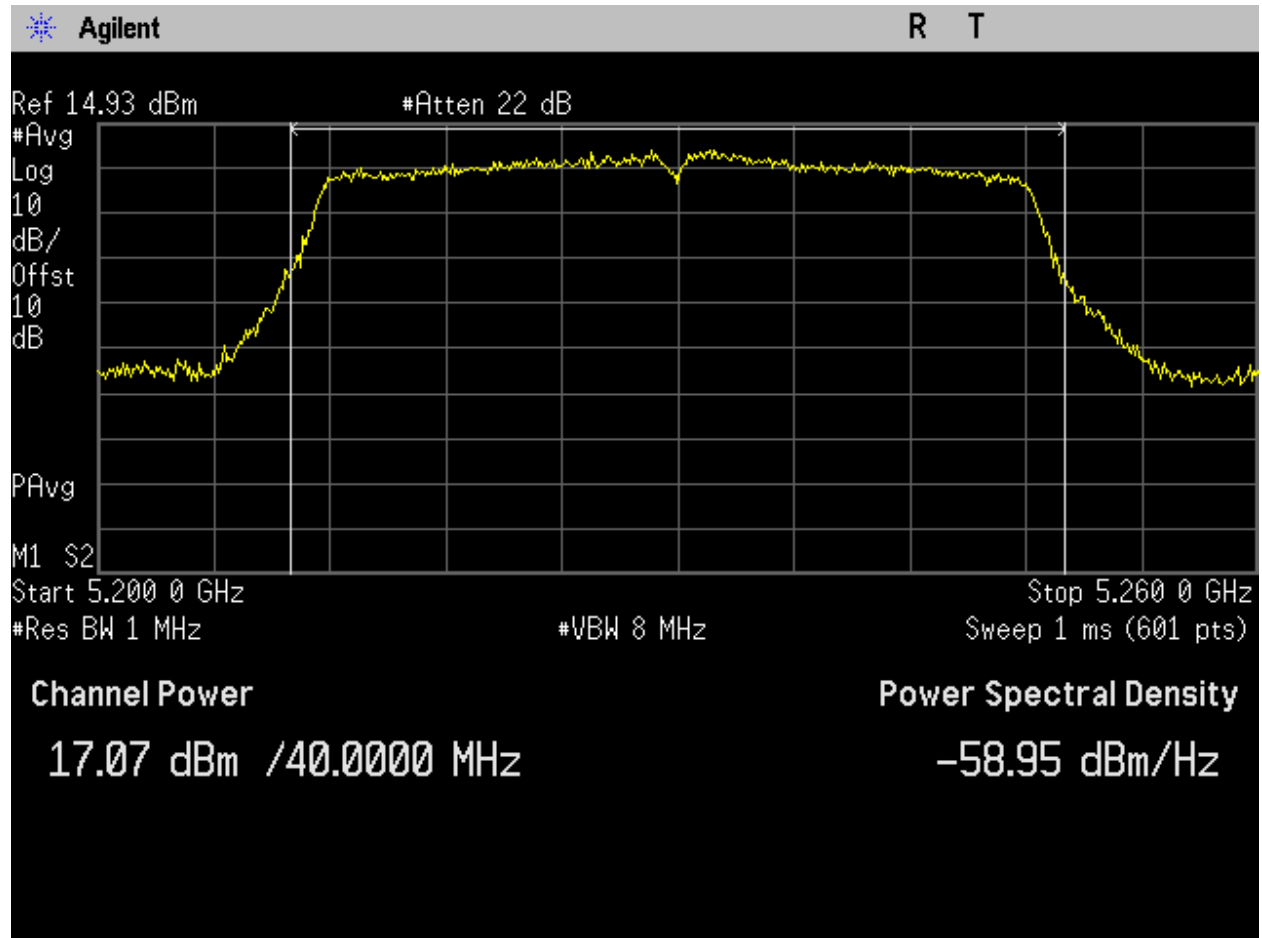


Figure 85: U-NII-1_5230MHz_high_Mid Ch_46_40MHz BW_n-mode_Output Power_Port 1.

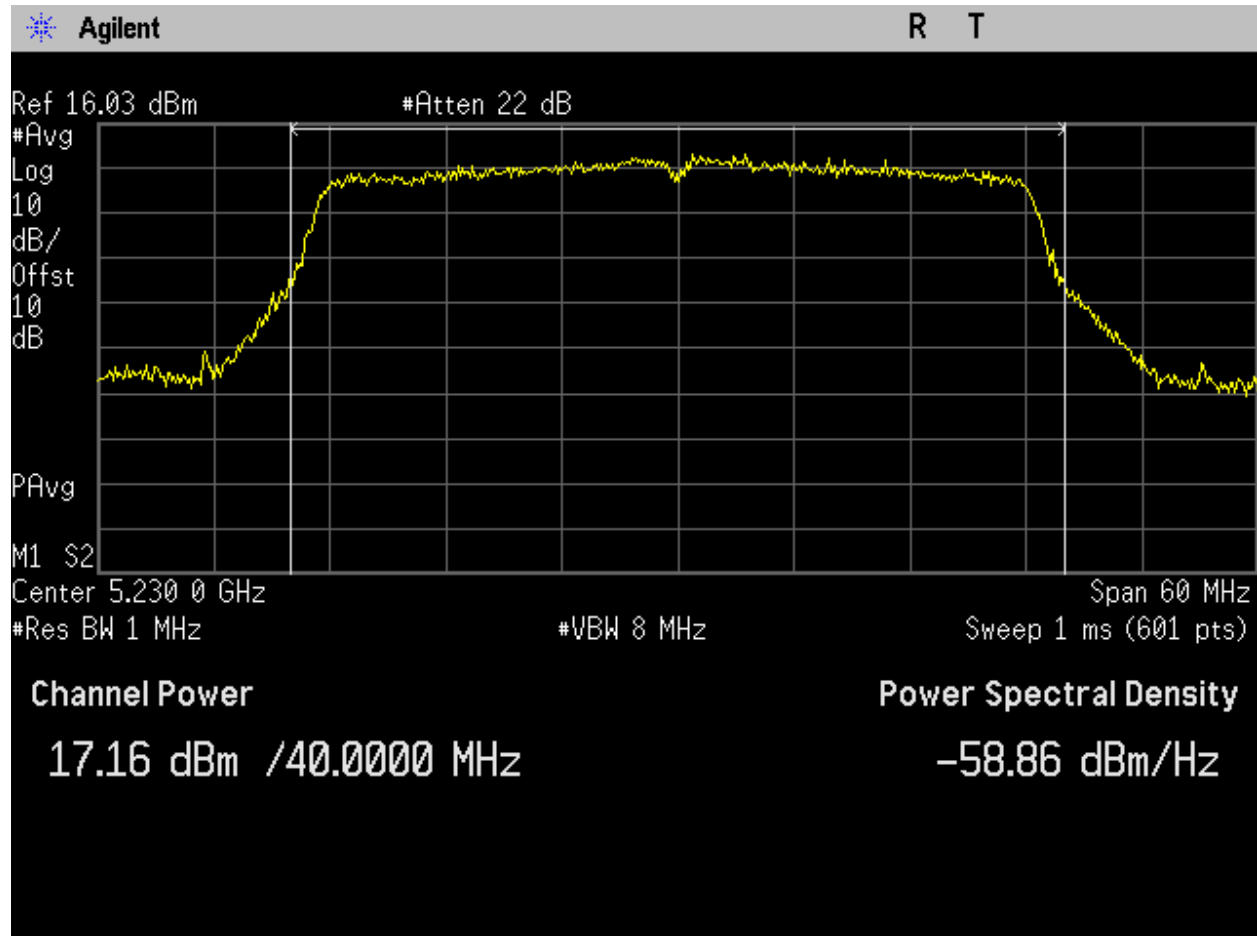


Figure 86: U-NII-1_5230MHz_high_Mid Ch_46_40MHz BW_n-mode_Output Power_Port 2.

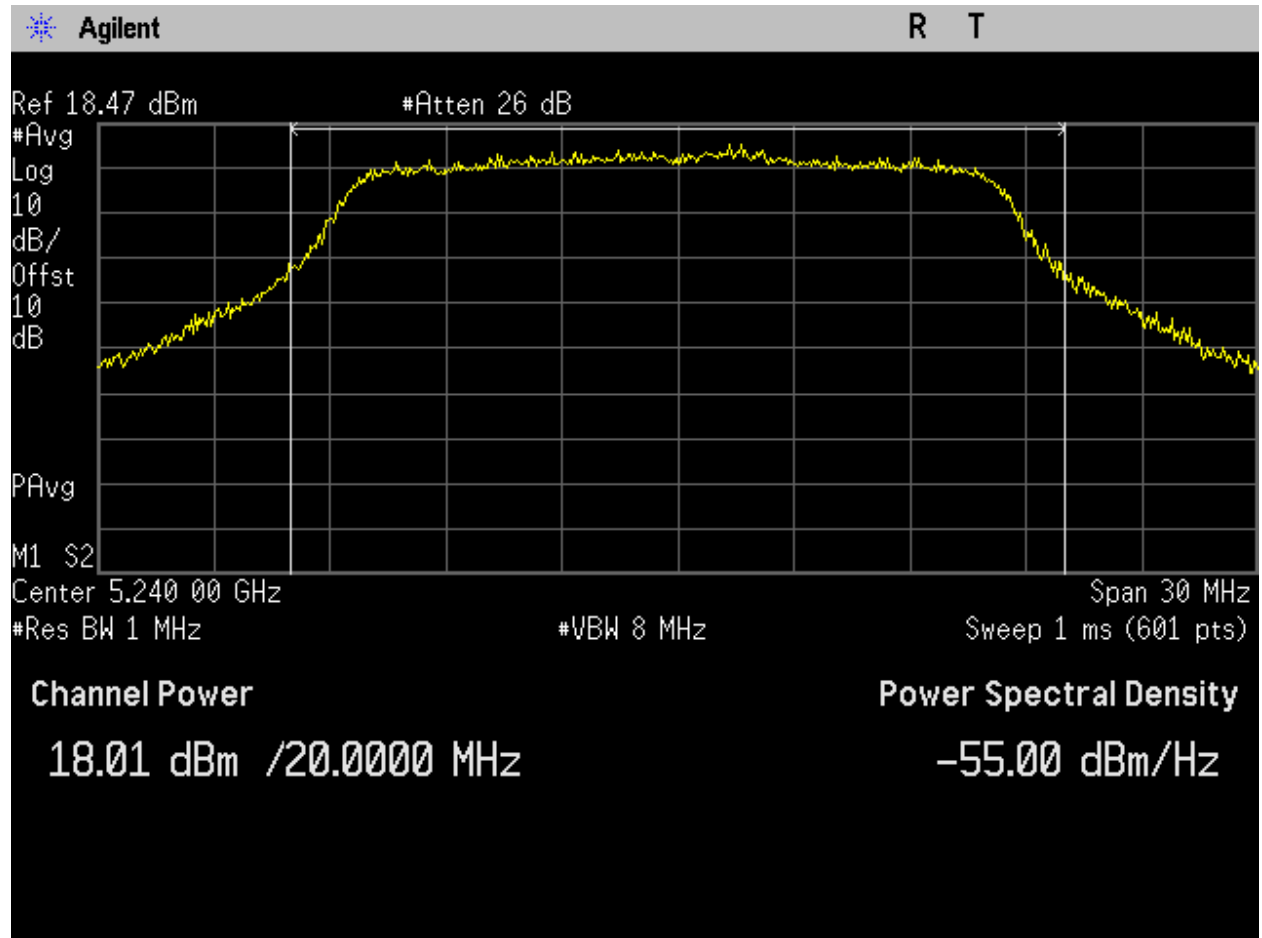


Figure 87: U-NII-1_5240MHz_high_mid Ch_48_20MHz BW_a-mode_Output Power_Port 1.

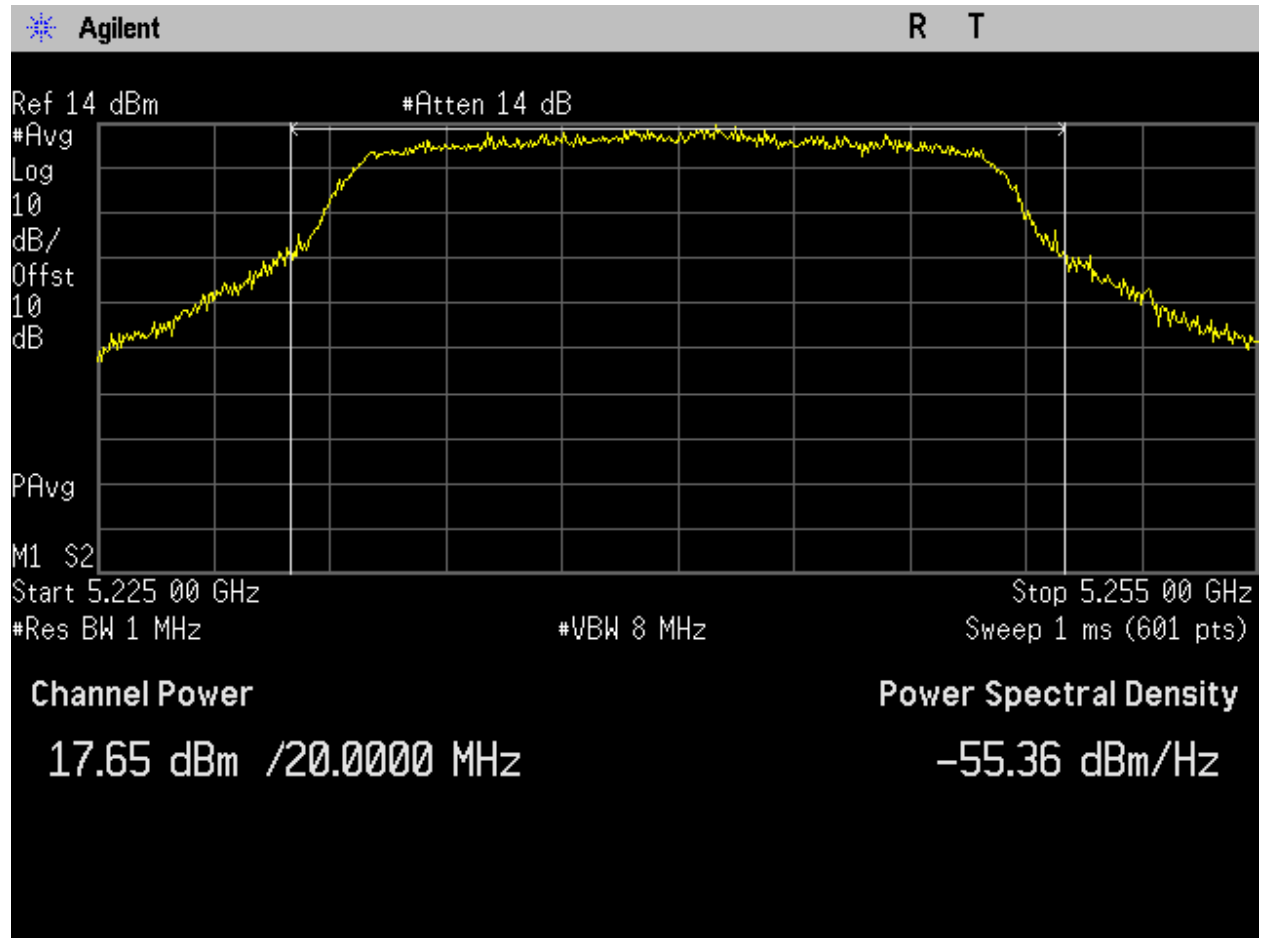


Figure 88: U-NII-1_5240MHz_high_mid Ch_48_20MHz BW_a-mode_Output Power_Port 2.

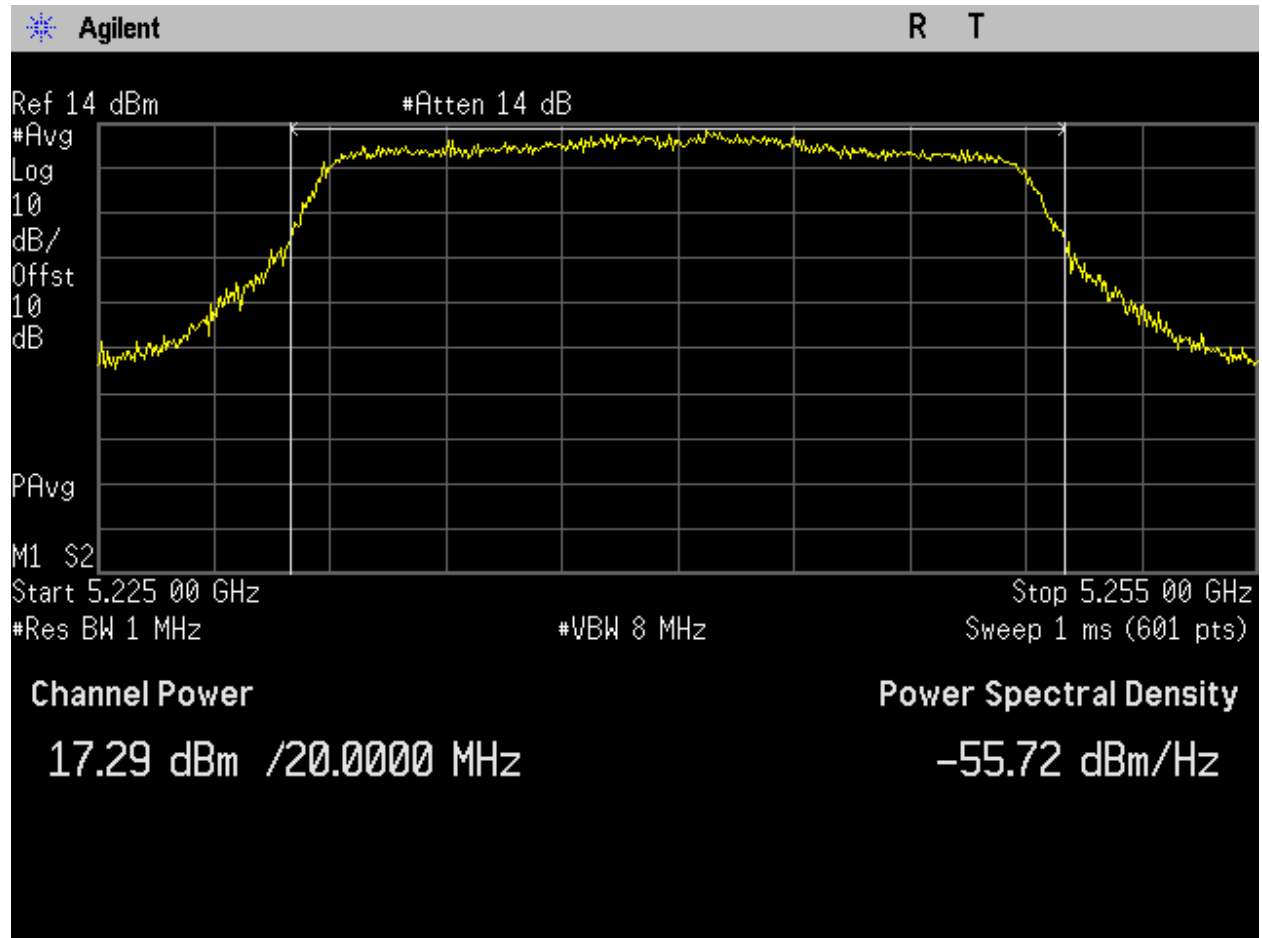


Figure 89: U-NII-1_5240MHz_high_mid Ch_48_20MHz BW_ac-mode_Output Power_Port 1.

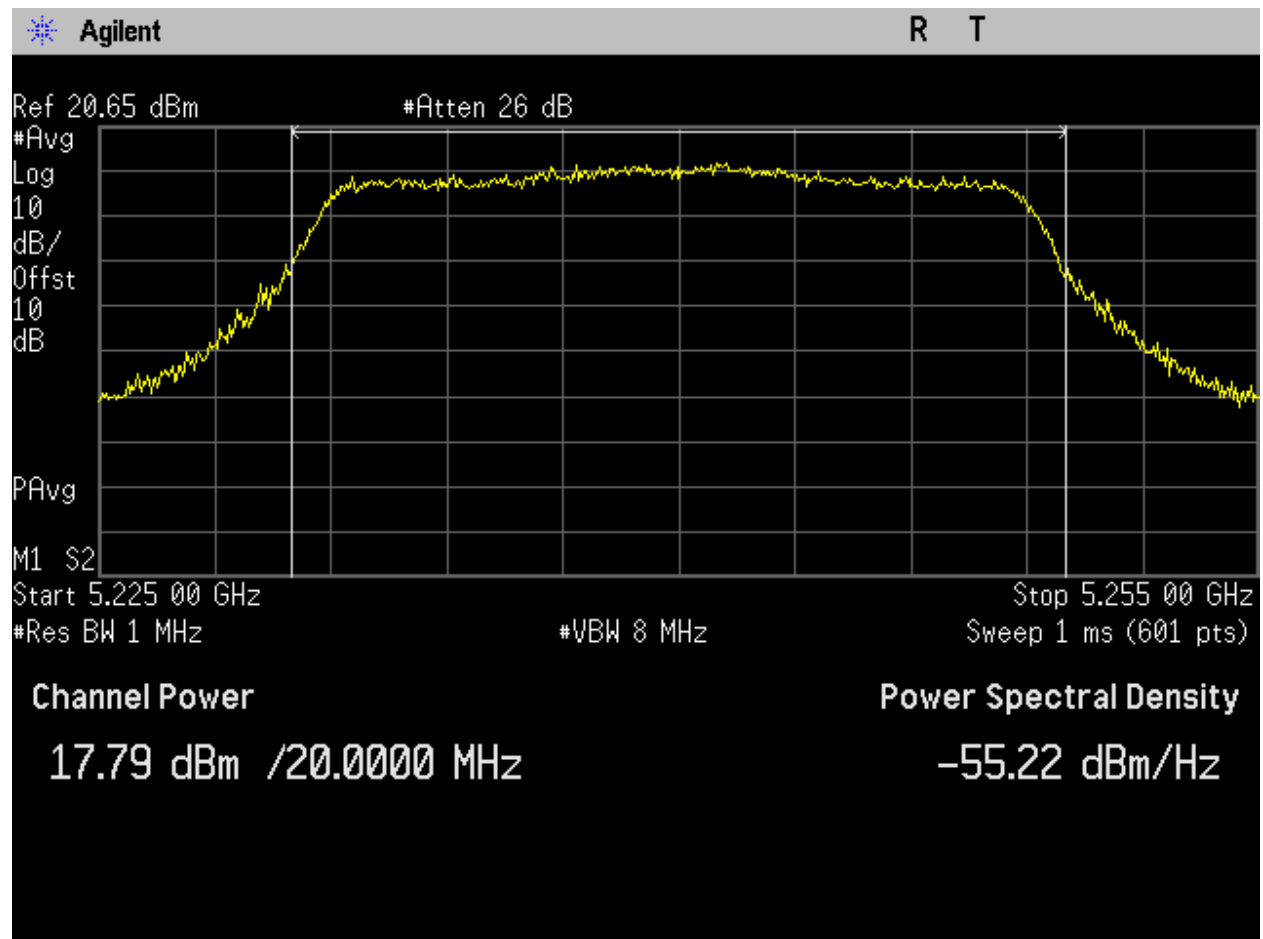


Figure 90: U-NII-1_5240MHz_high_mid Ch_48_20MHz BW_ac-mode_Output Power_Port 2.

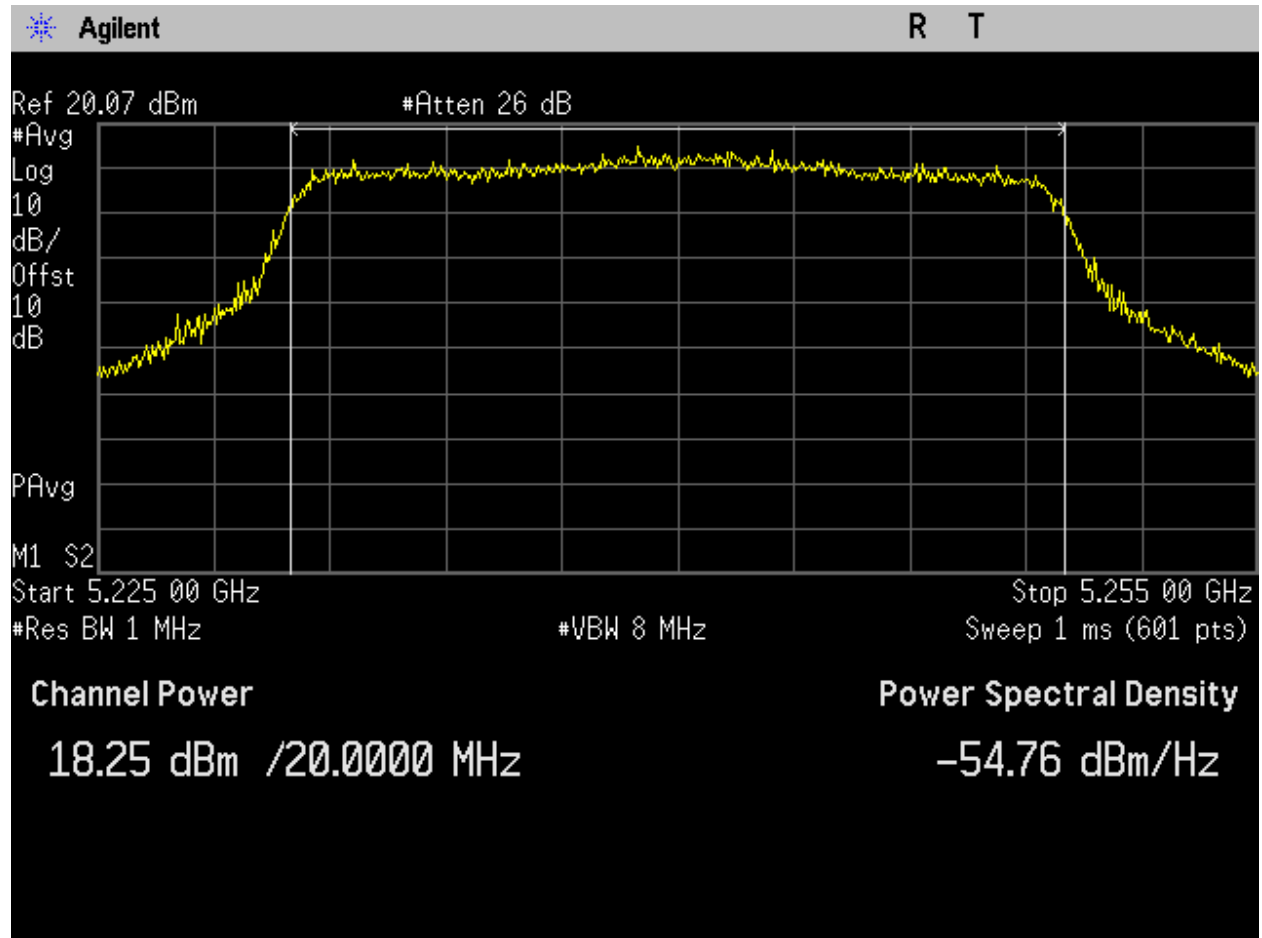


Figure 91: U-NII-1_5240MHz_high_mid Ch_48_20MHz BW_ax-mode_Output Power_Port 1.

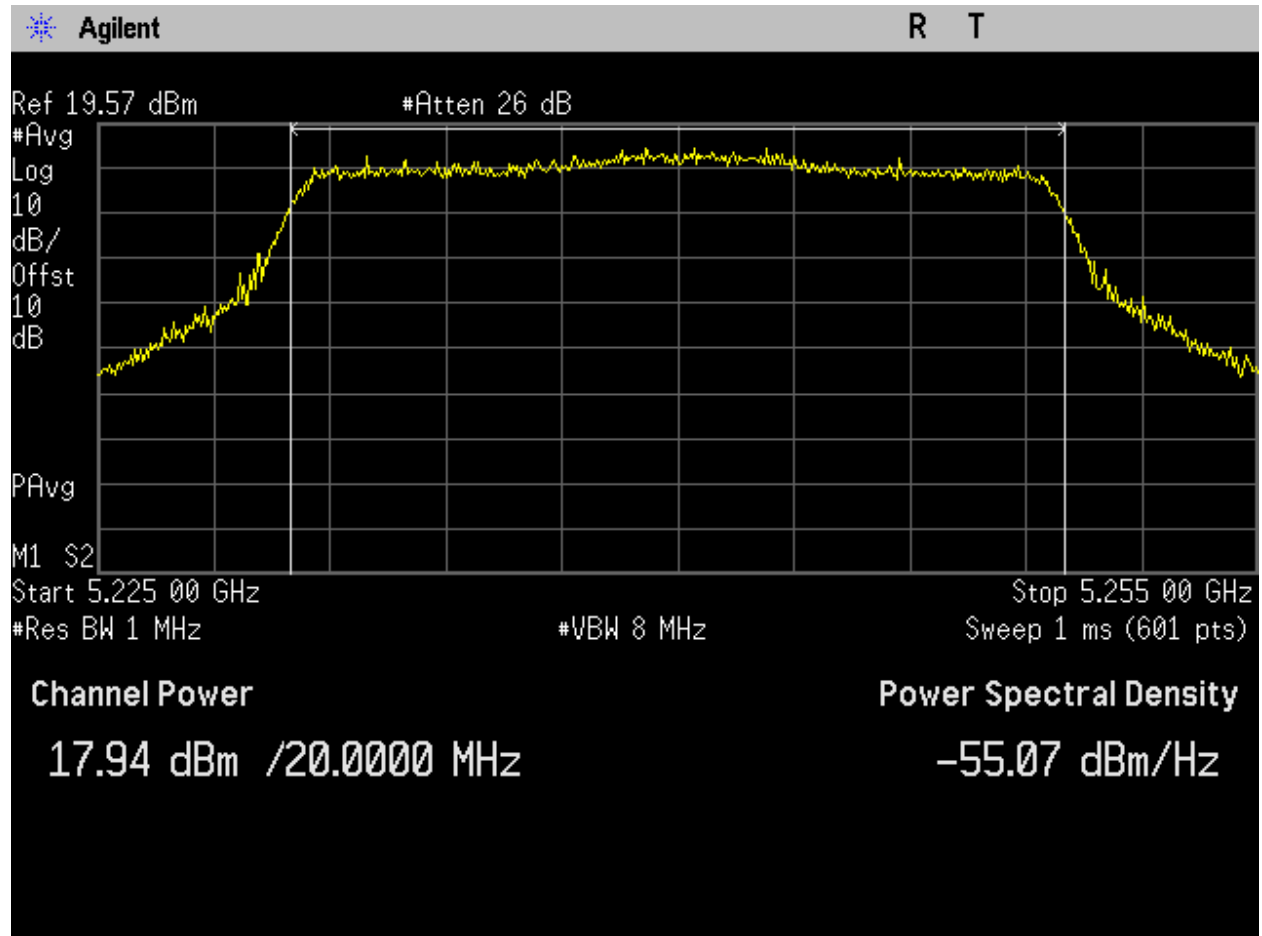


Figure 92: U-NII-1_5240MHz_high_mid Ch_48_20MHz BW_ax-mode_Output Power_Port 2.

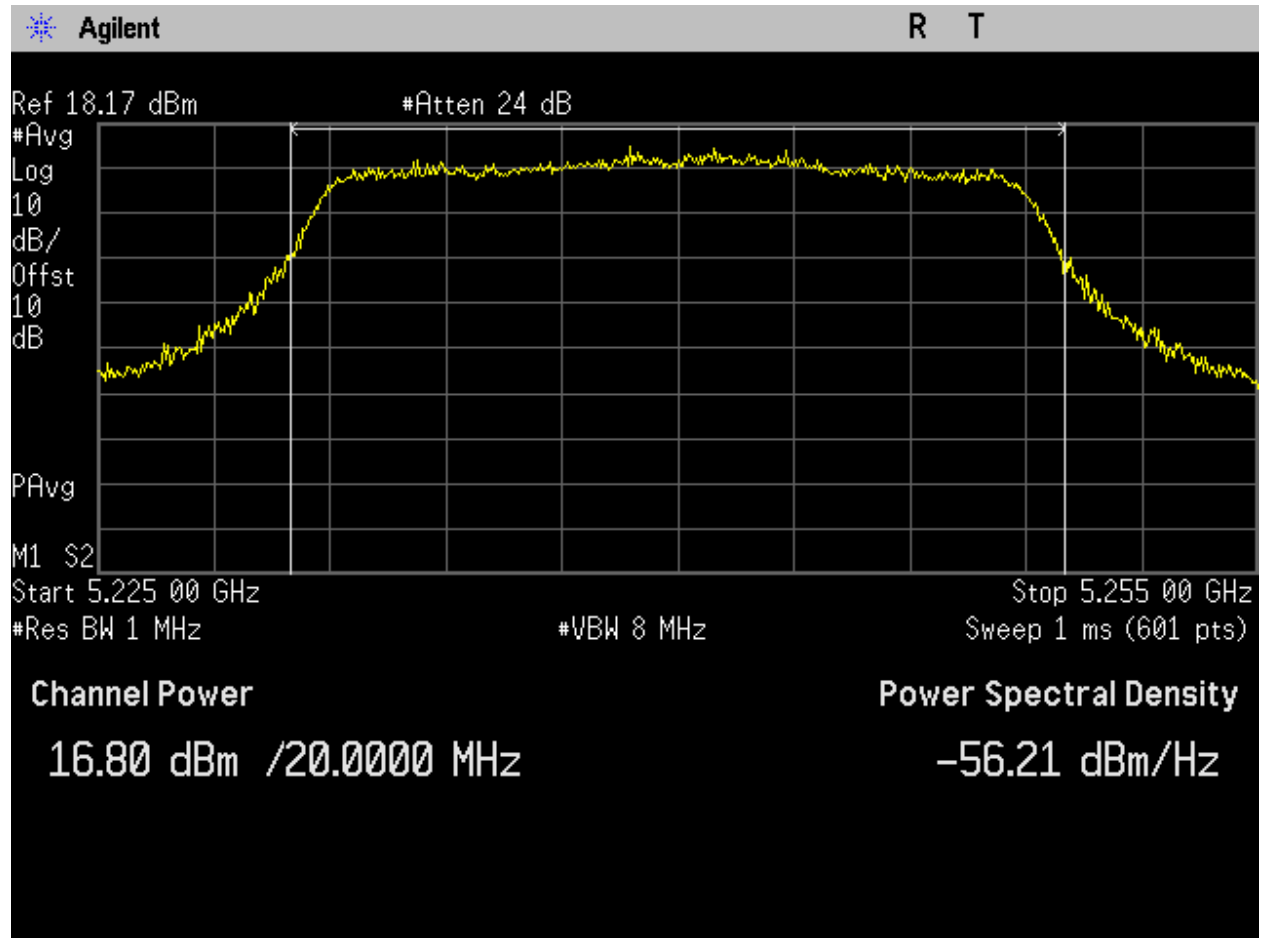


Figure 93: U-NII-1_5240MHz_high_mid Ch_48_20MHz BW_n-mode_Output Power_Port 1.

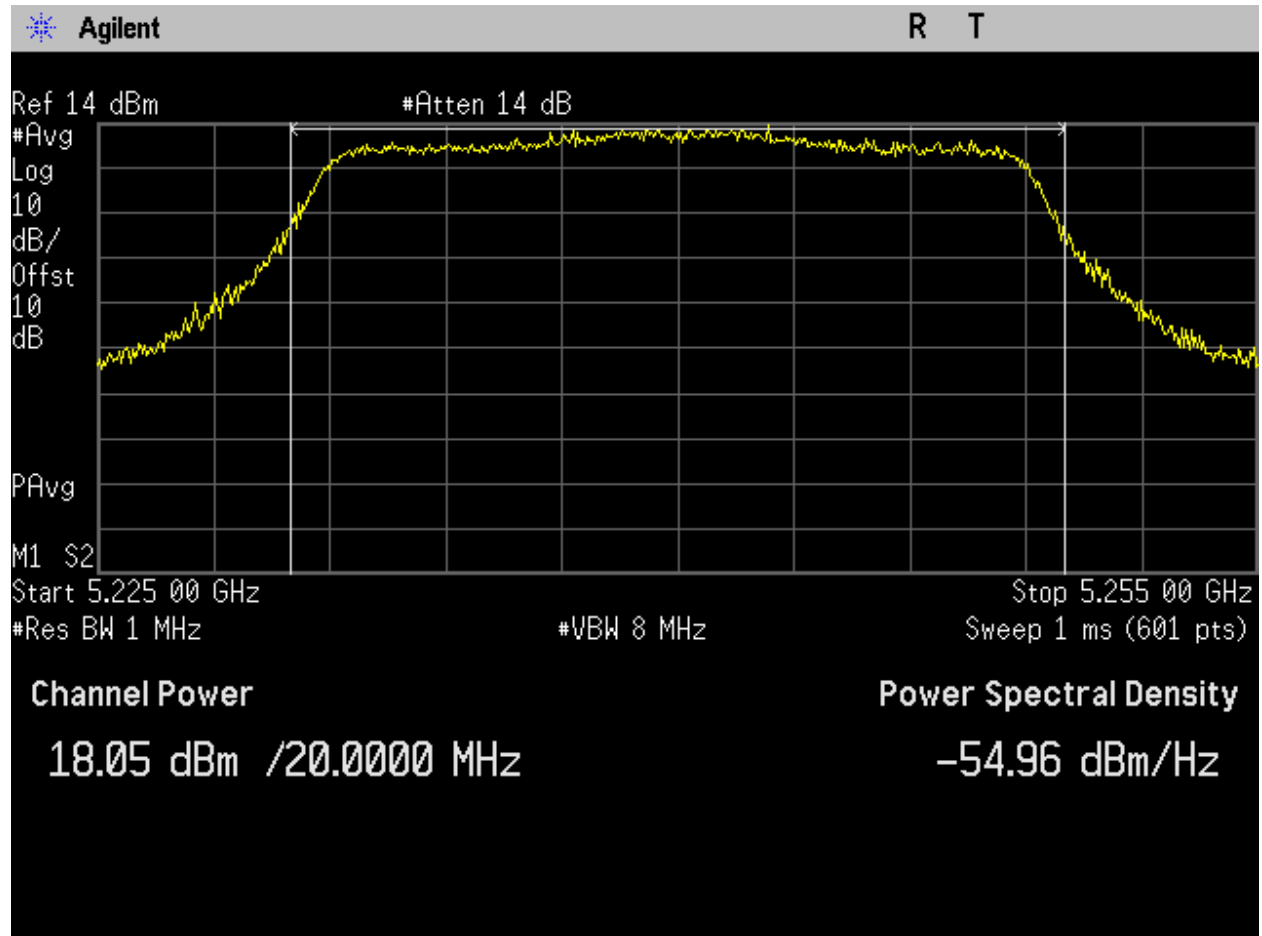


Figure 94: U-NII-1_5240MHz_high_mid Ch_48_20MHz BW_n-mode_Output Power_Port 2.

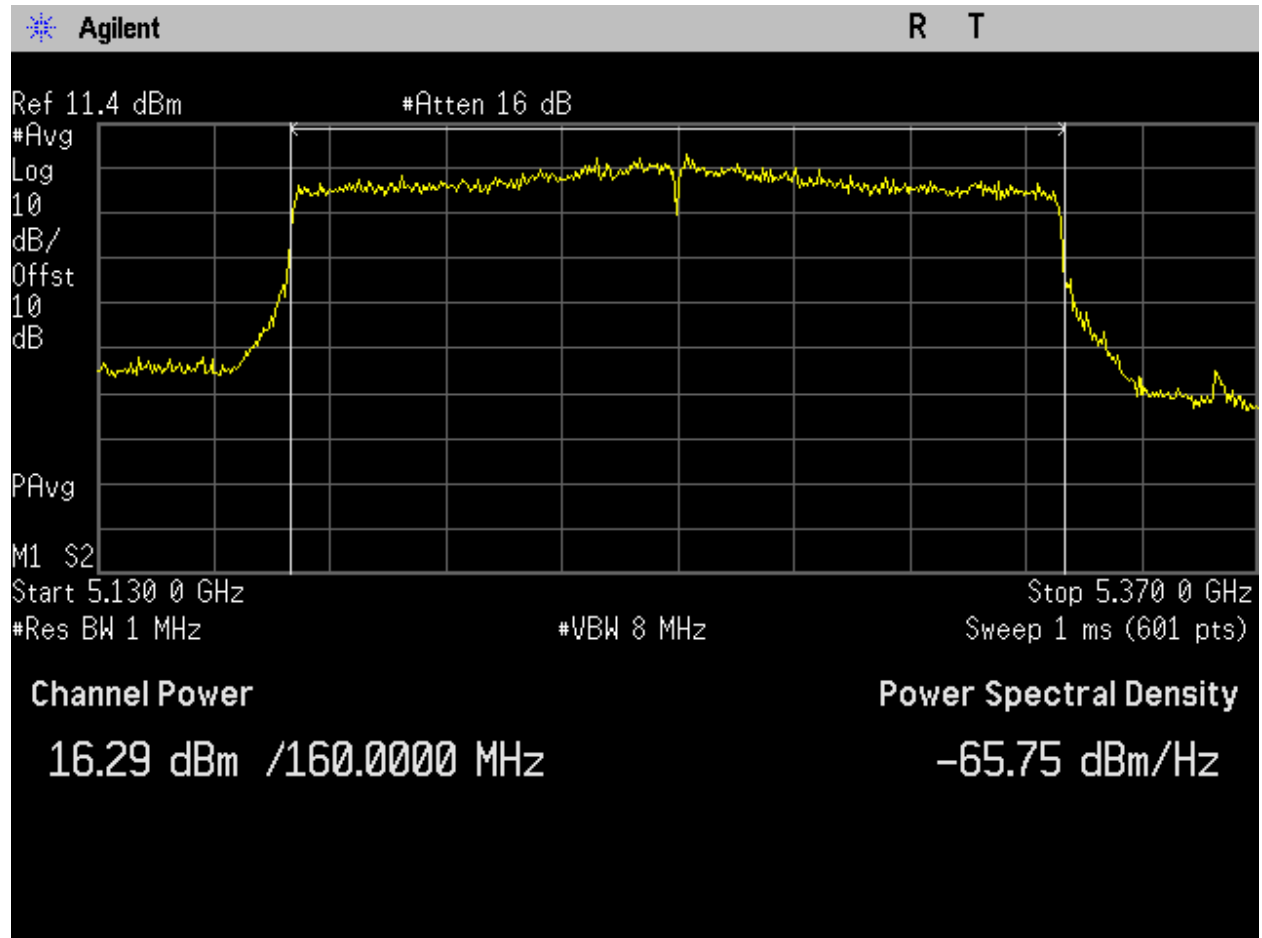


Figure 95: U-NII-2A_5250MHz_High_Mid Ch_50_160MHz BW_ac-mode_Output Power_Port 1.

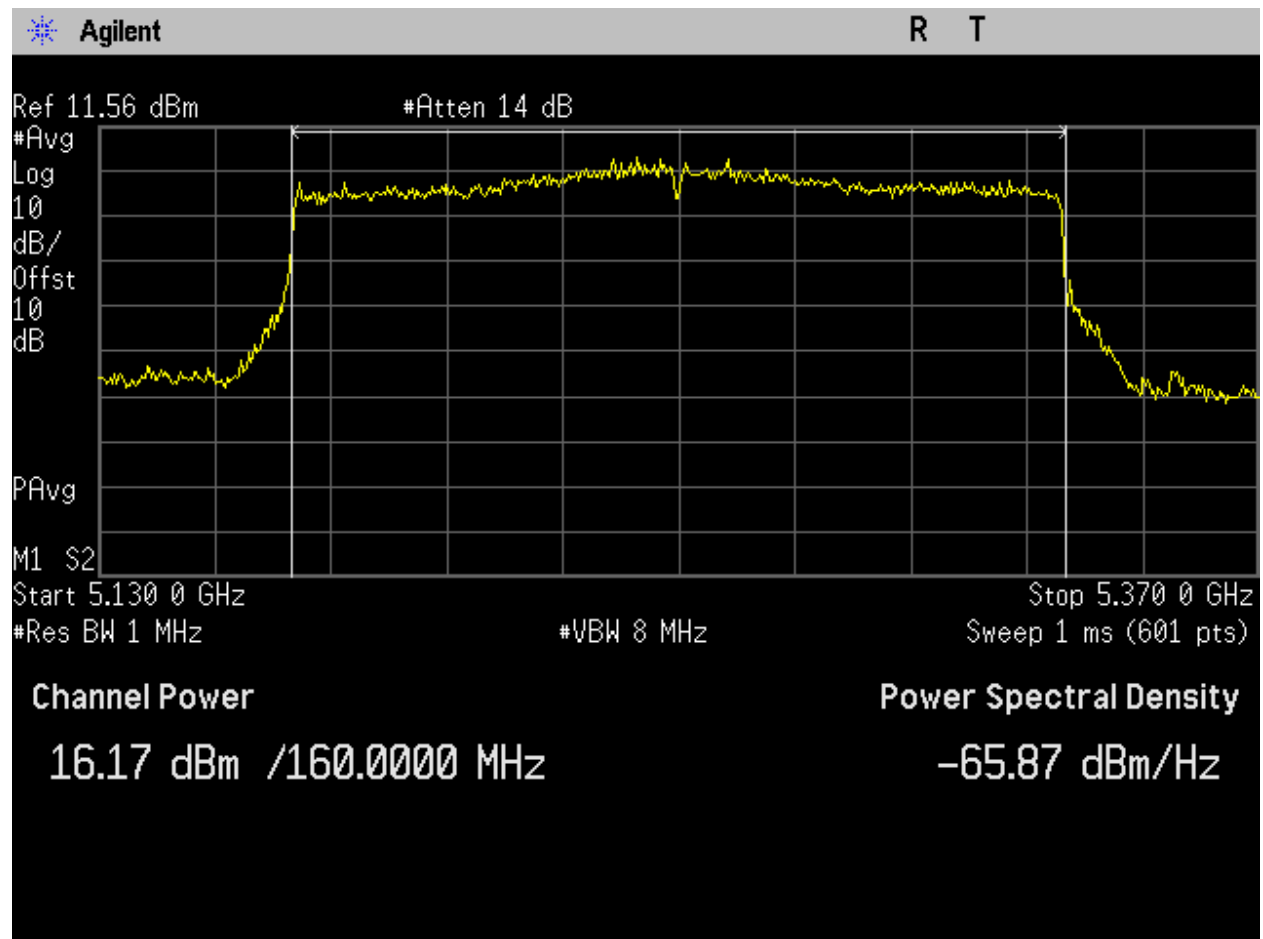


Figure 96: U-NII-2A_5250MHz_High_Mid Ch_50_160MHz BW_ac-mode_Output Power_Port 2.

§15.407(a)(1)(ii) Maximum Power Spectral Density

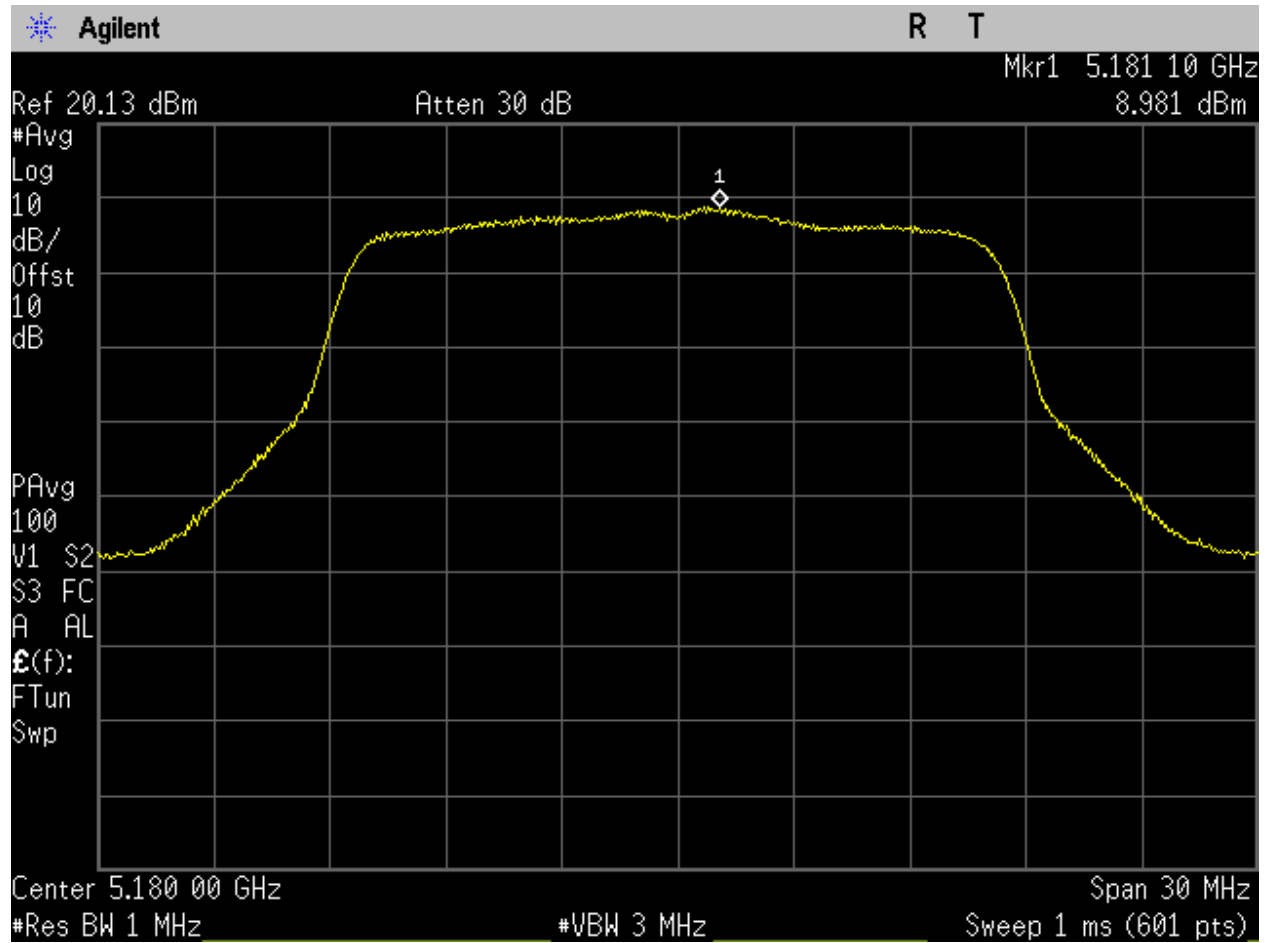


Figure 97: U-NII-1_5180MHz_Low Ch_36_20MHz BW_a-mode_Spectral Density_Port 1.

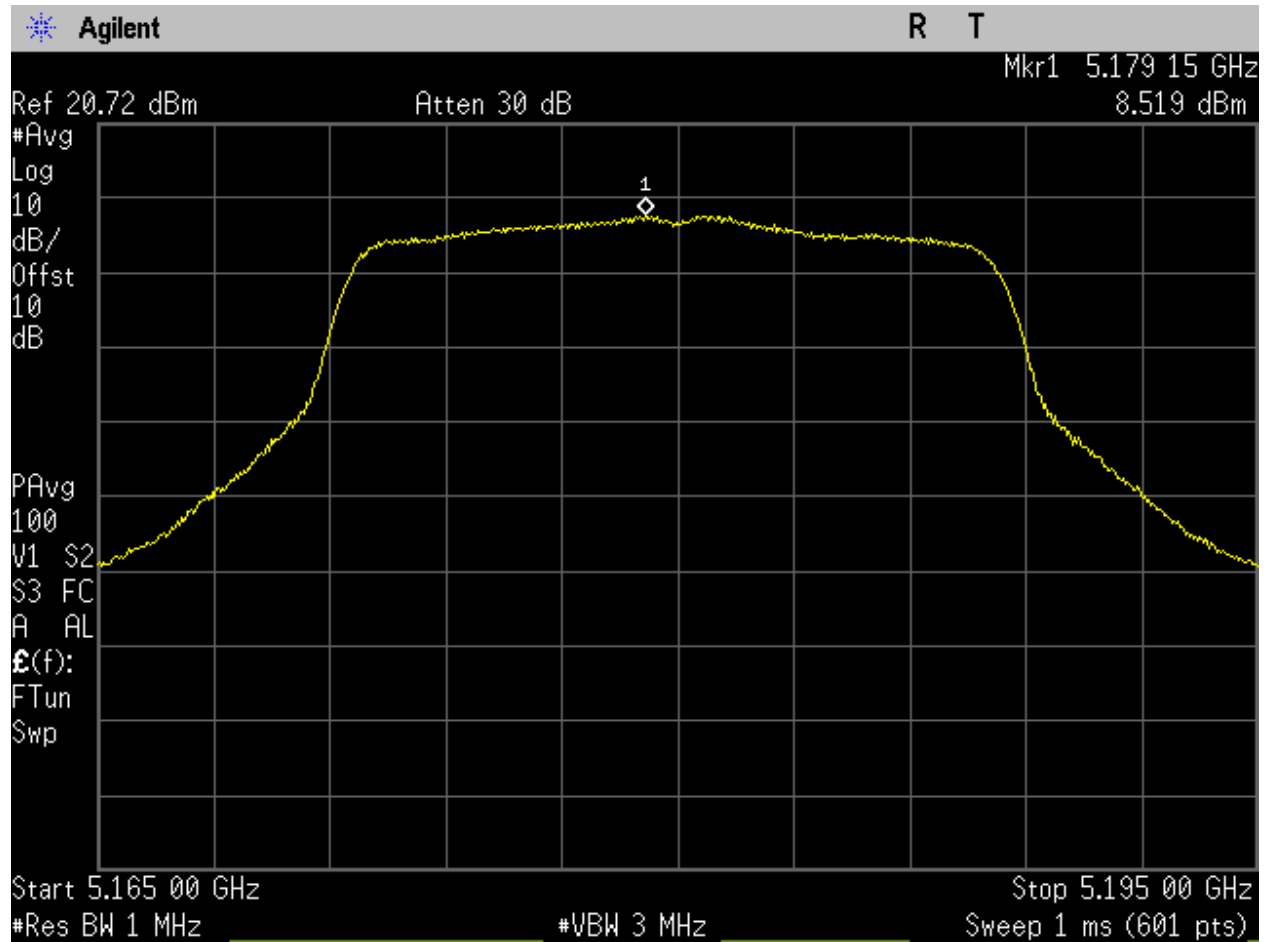


Figure 98: U-NII-1_5180MHz_Low Ch_36_20MHz BW_a-mode_Spectral Density_Port 2.

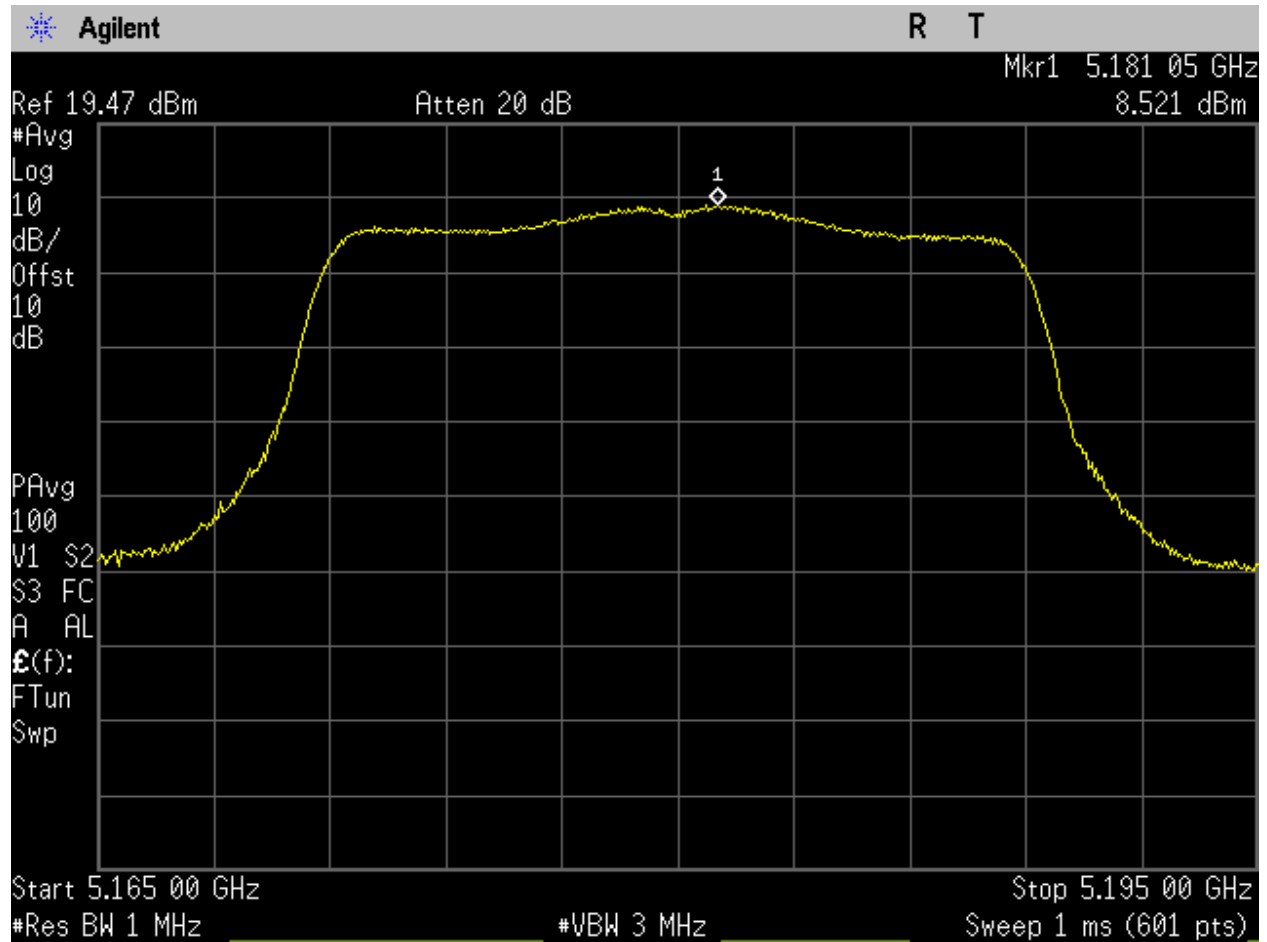


Figure 99: U-NII-1_5180MHz_Low Ch_36_20MHz BW_ac-mode_Spectral Density_Port 1.

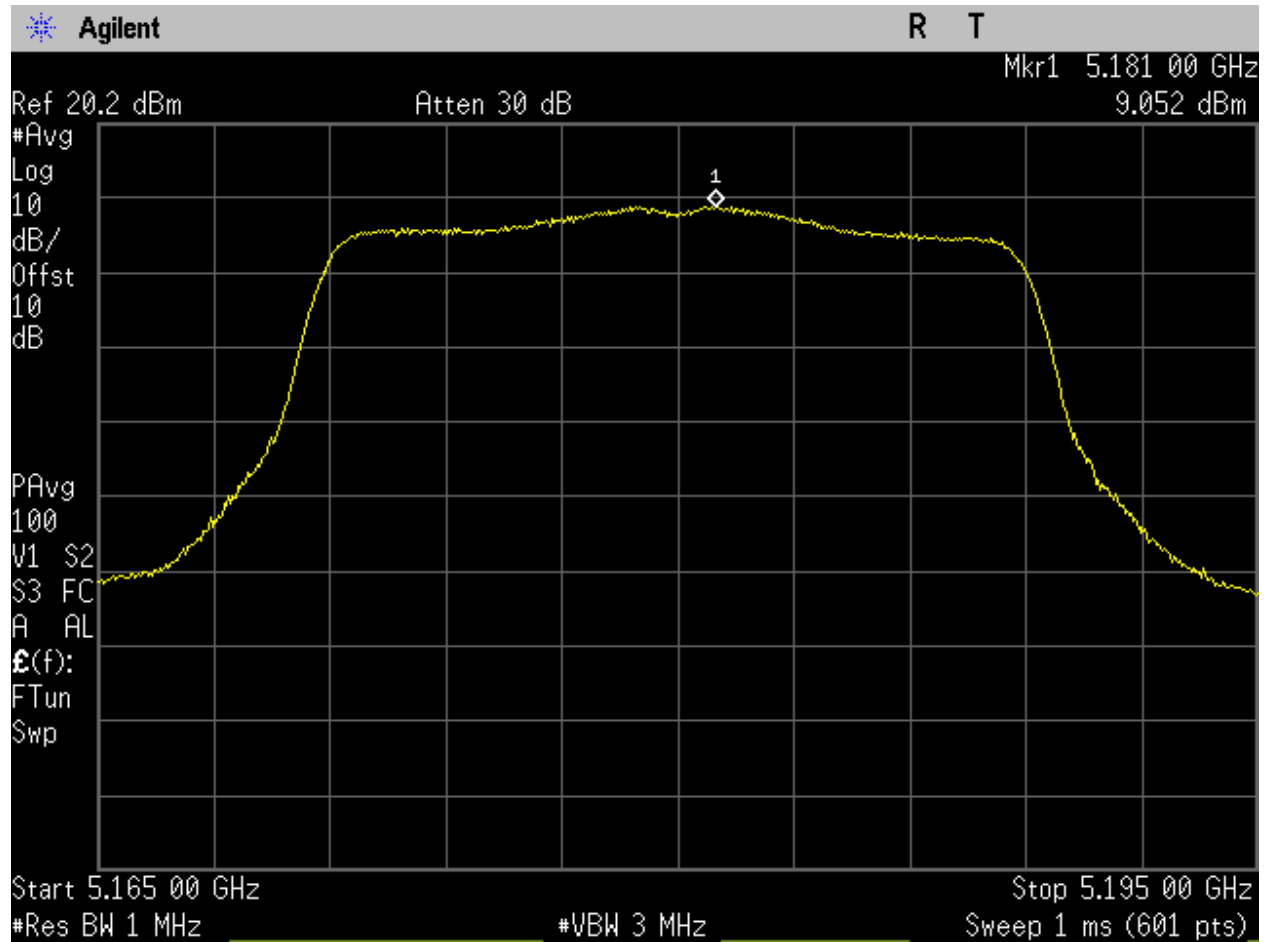


Figure 100: U-NII-1_5180MHz_Low Ch_36_20MHz BW_ac-mode_Spectral Density_Port 2.

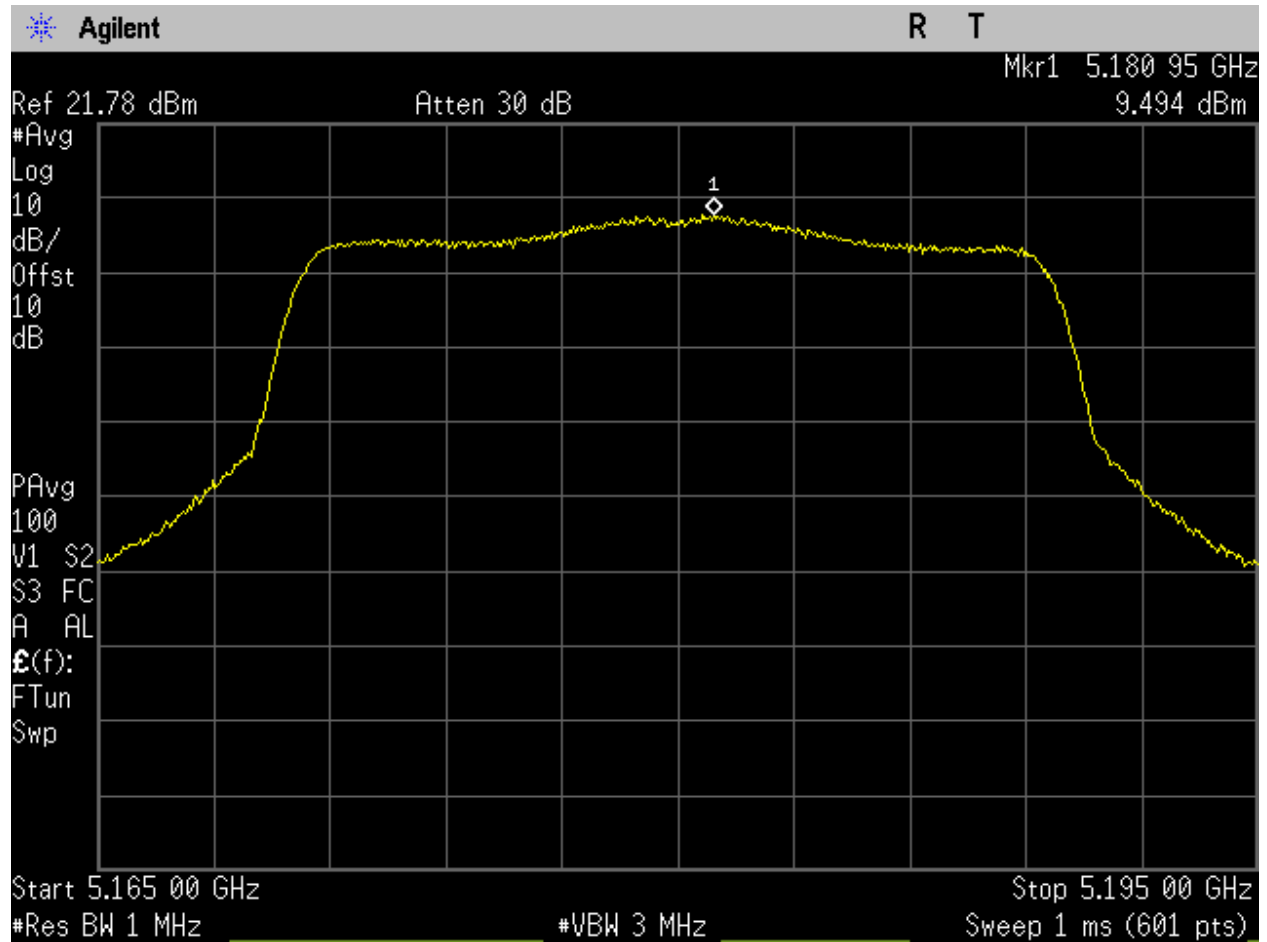


Figure 101: U-NII-1_5180MHz_Low Ch_36_20MHz BW_ax-mode_Spectral Density_Port 1.

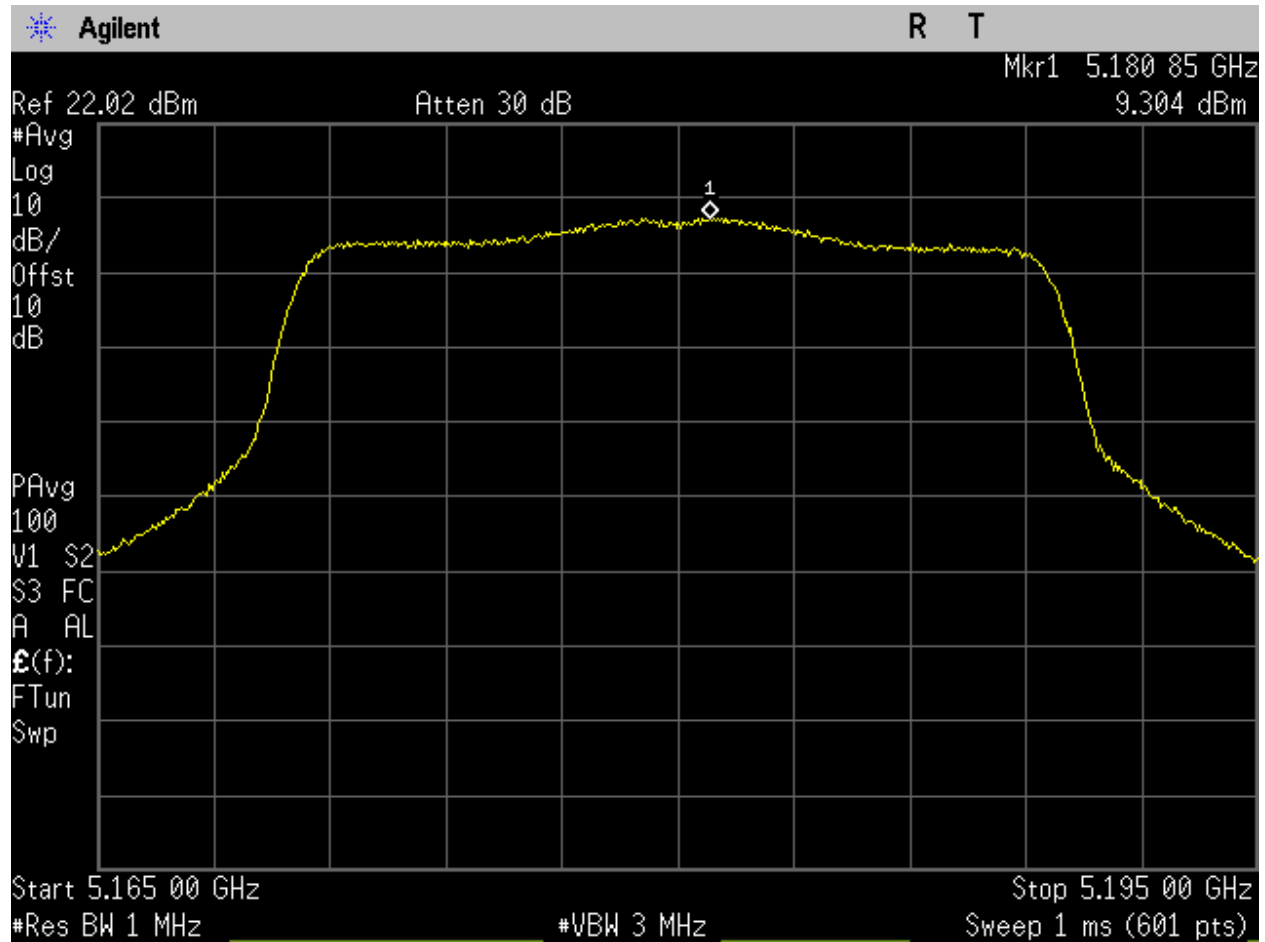


Figure 102: U-NII-1_5180MHz_Low Ch_36_20MHz BW_ax-mode_Spectral Density_Port 2.

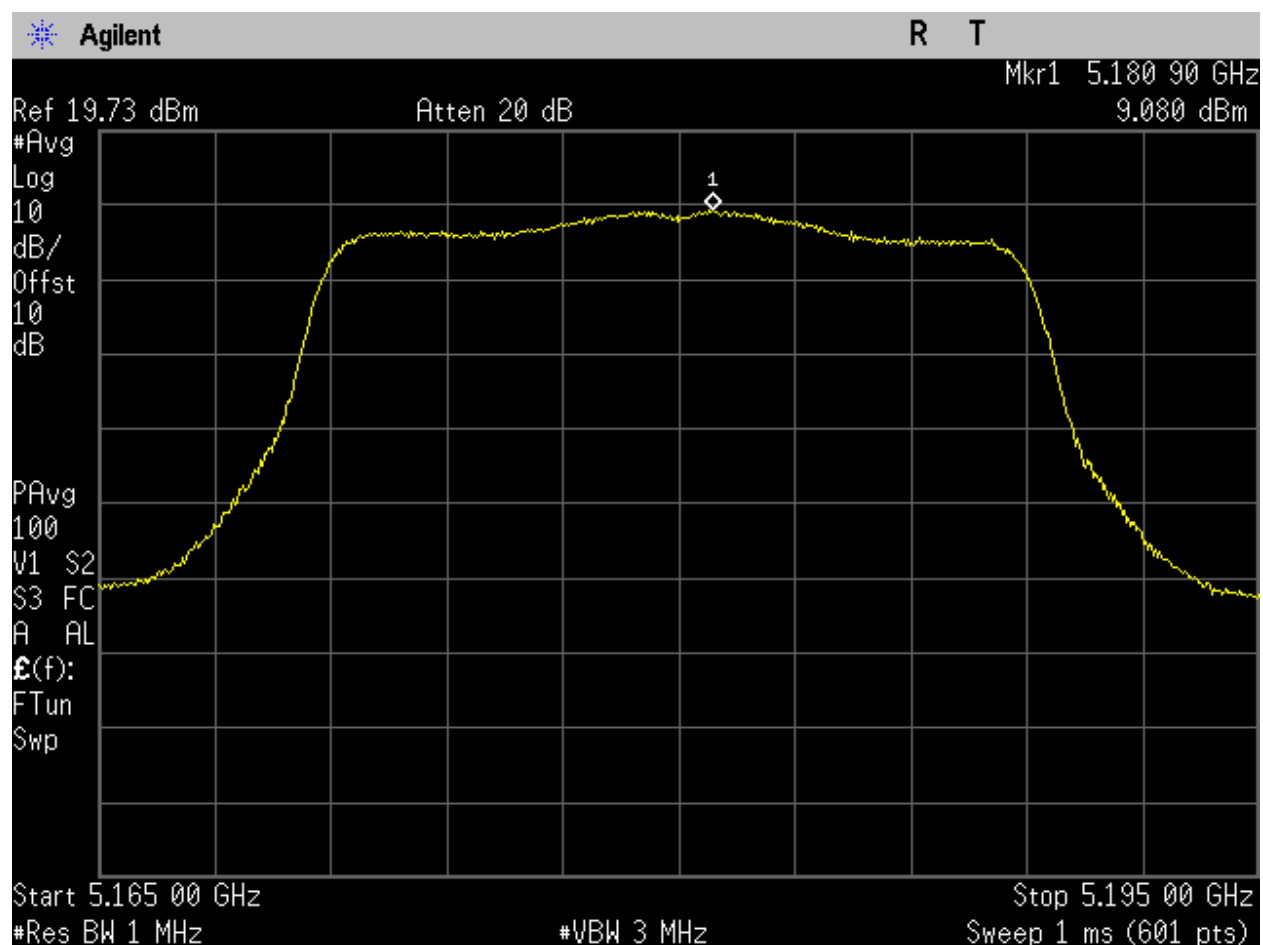


Figure 103: U-NII-1_5180MHz_Low Ch_36_20MHz BW_n-mode_Spectral Density_Port 1.

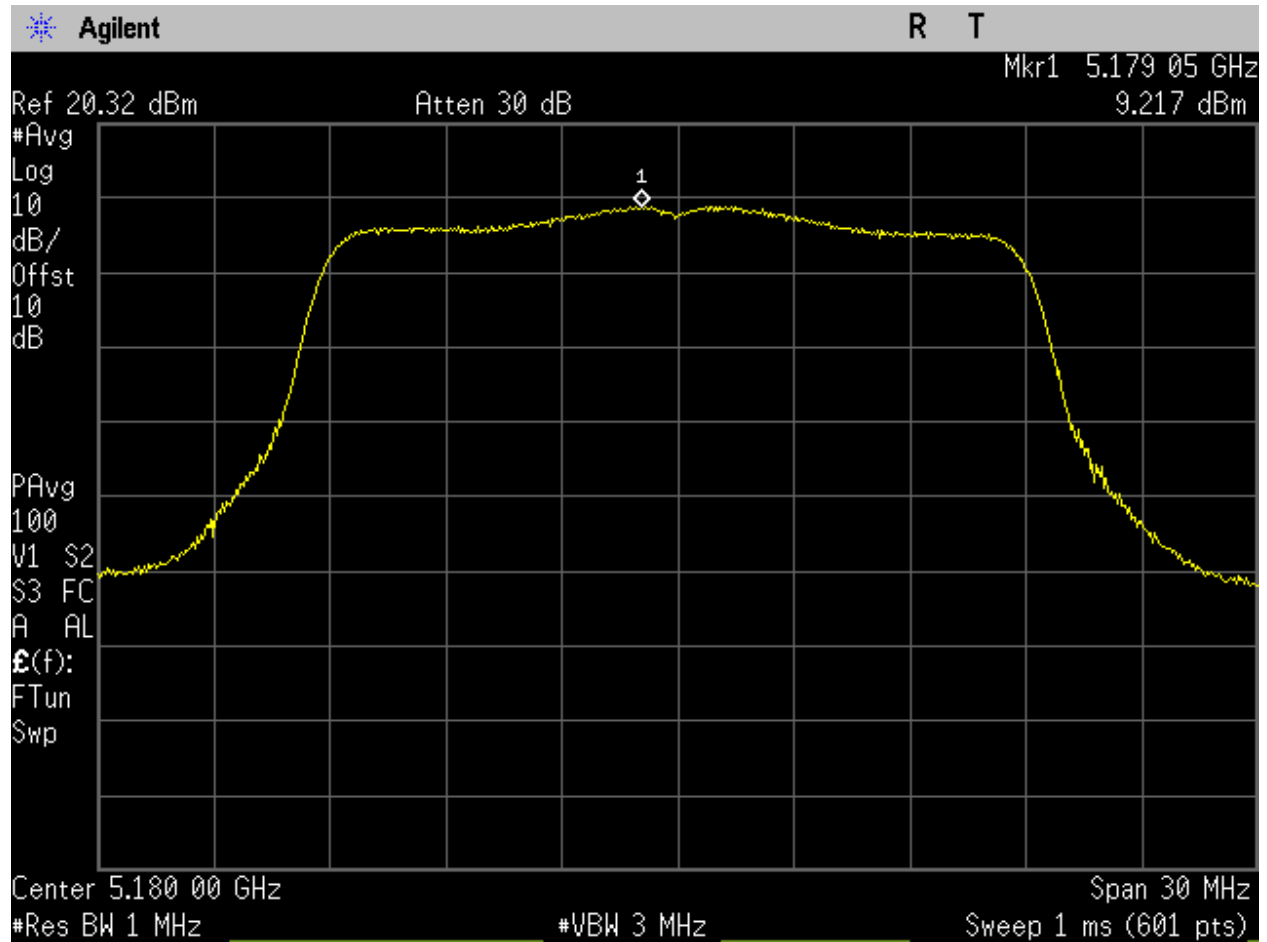


Figure 104: U-NII-1_5180MHz_Low Ch_36_20MHz BW_n-mode_Spectral Density_Port 2.

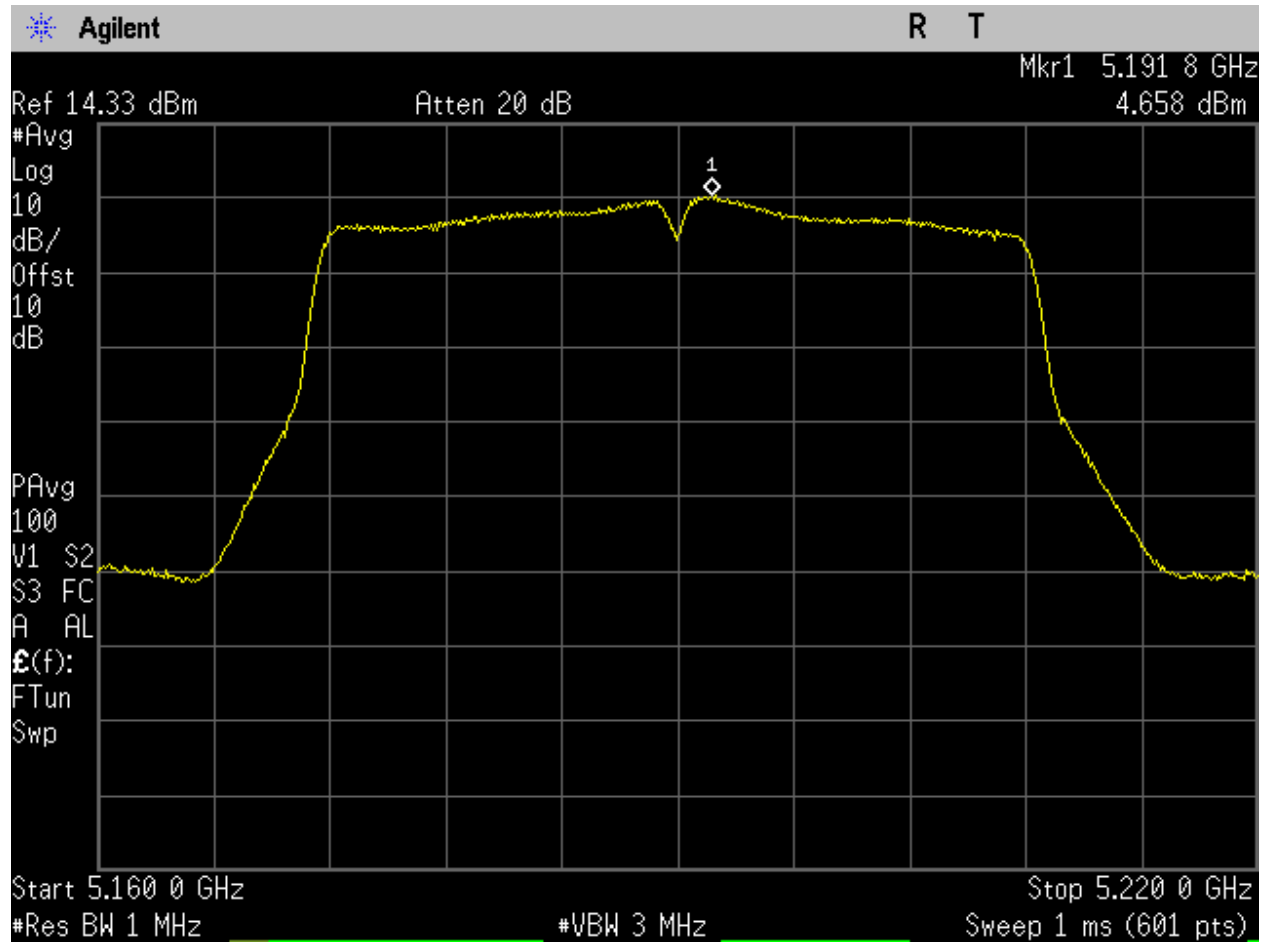


Figure 105: U-NII-1_5190MHz_Low Ch_38_40MHz BW_ac-mode_Spectral Density_Port 1.

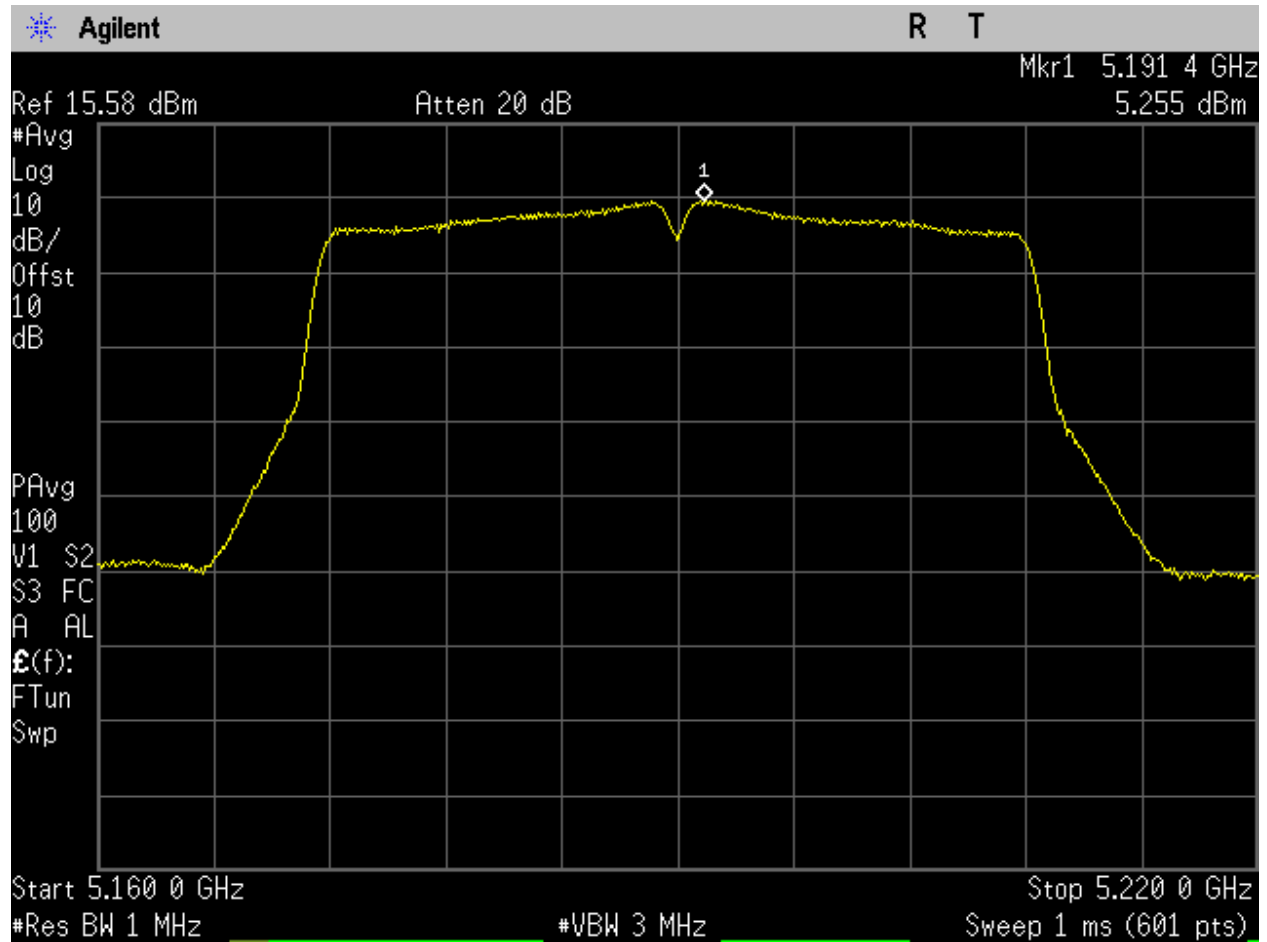


Figure 106: U-NII-1_5190MHz_Low Ch_38_40MHz BW_ac-mode_Spectral Density_Port 2.

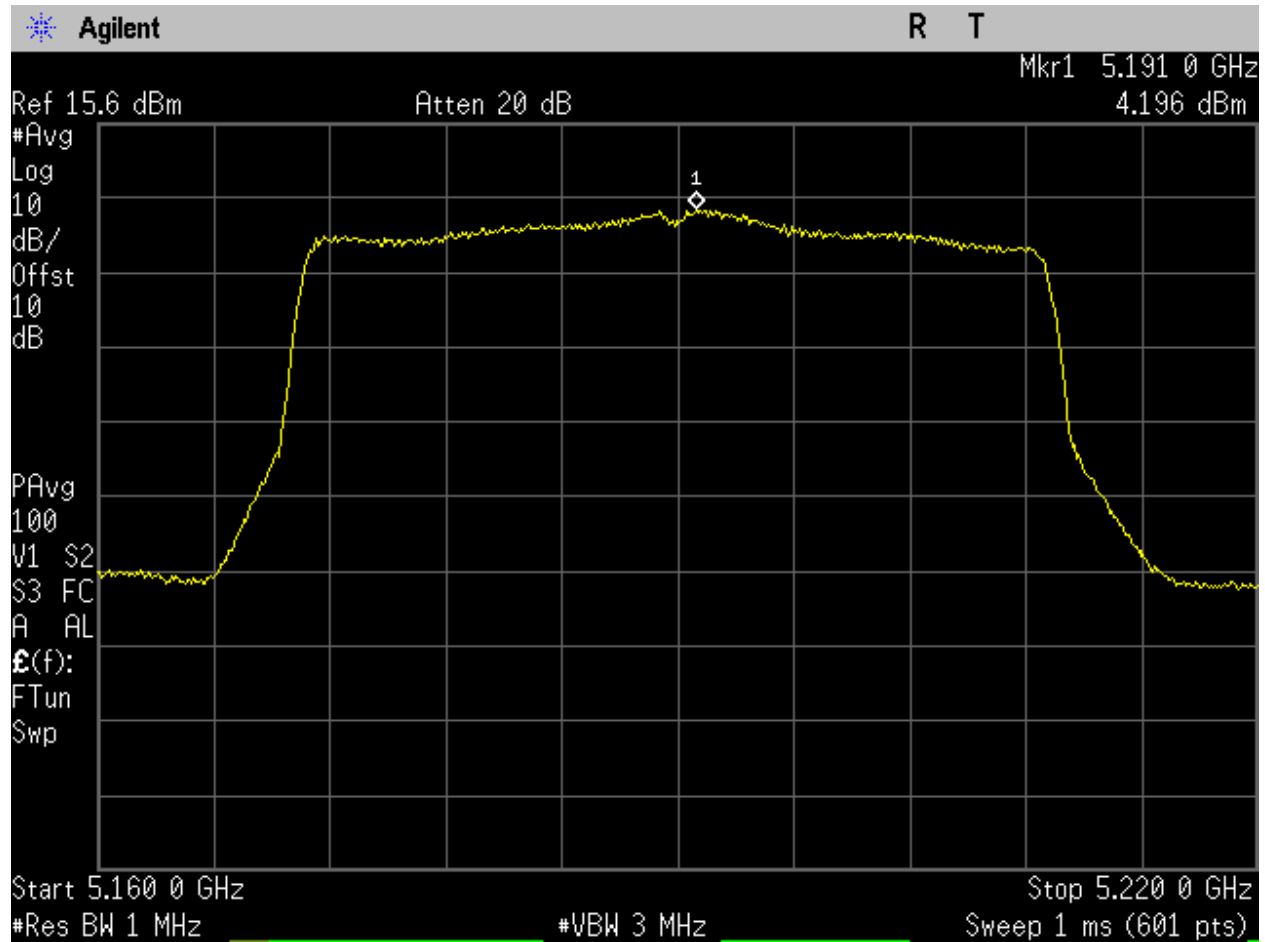


Figure 107: U-NII-1_5190MHz_Low Ch_38_40MHz BW_ax-mode_Spectral Density_Port 1.

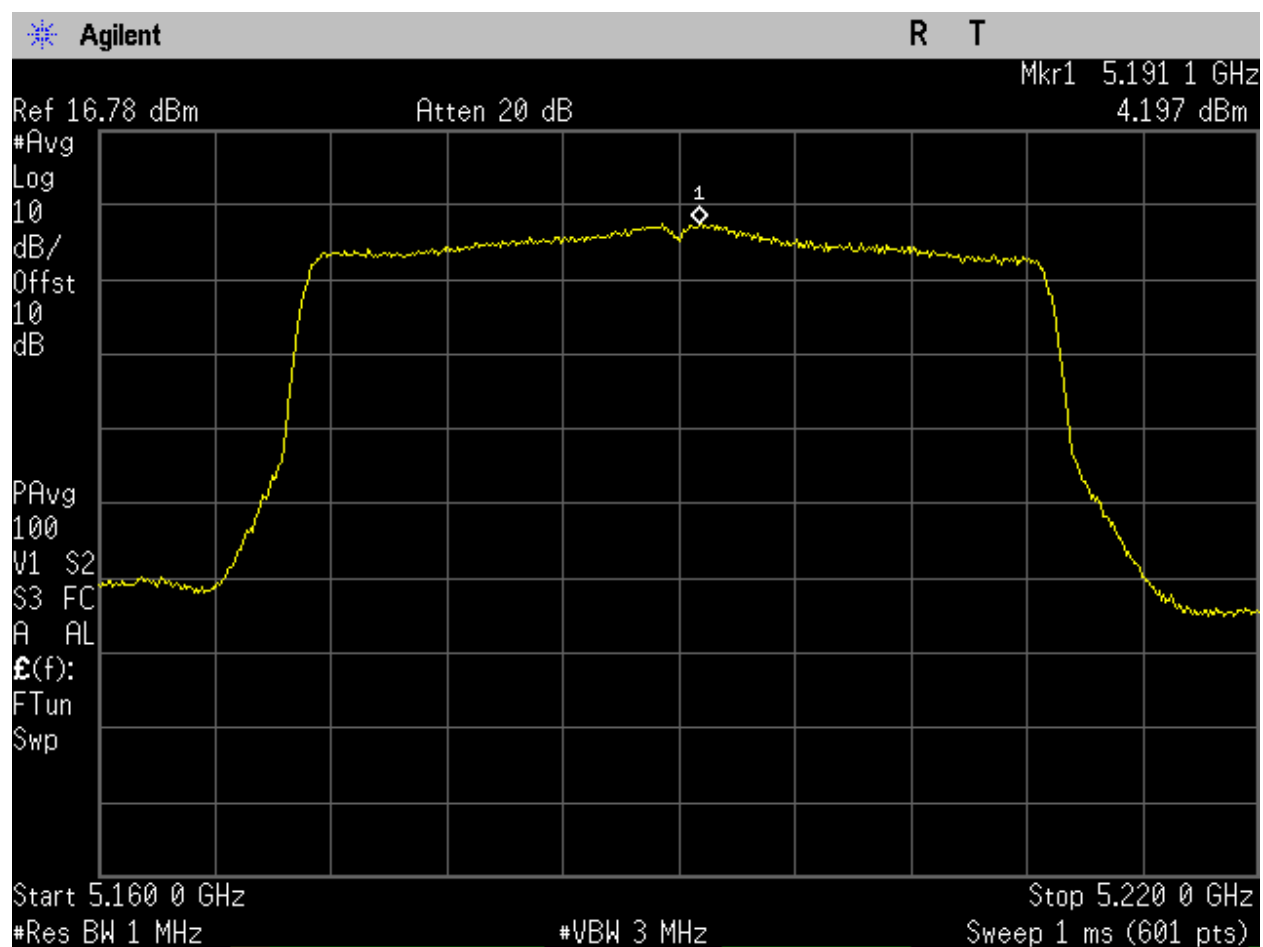


Figure 108: U-NII-1_5190MHz_Low Ch_38_40MHz BW_ax-mode_Spectral Density_Port 2.

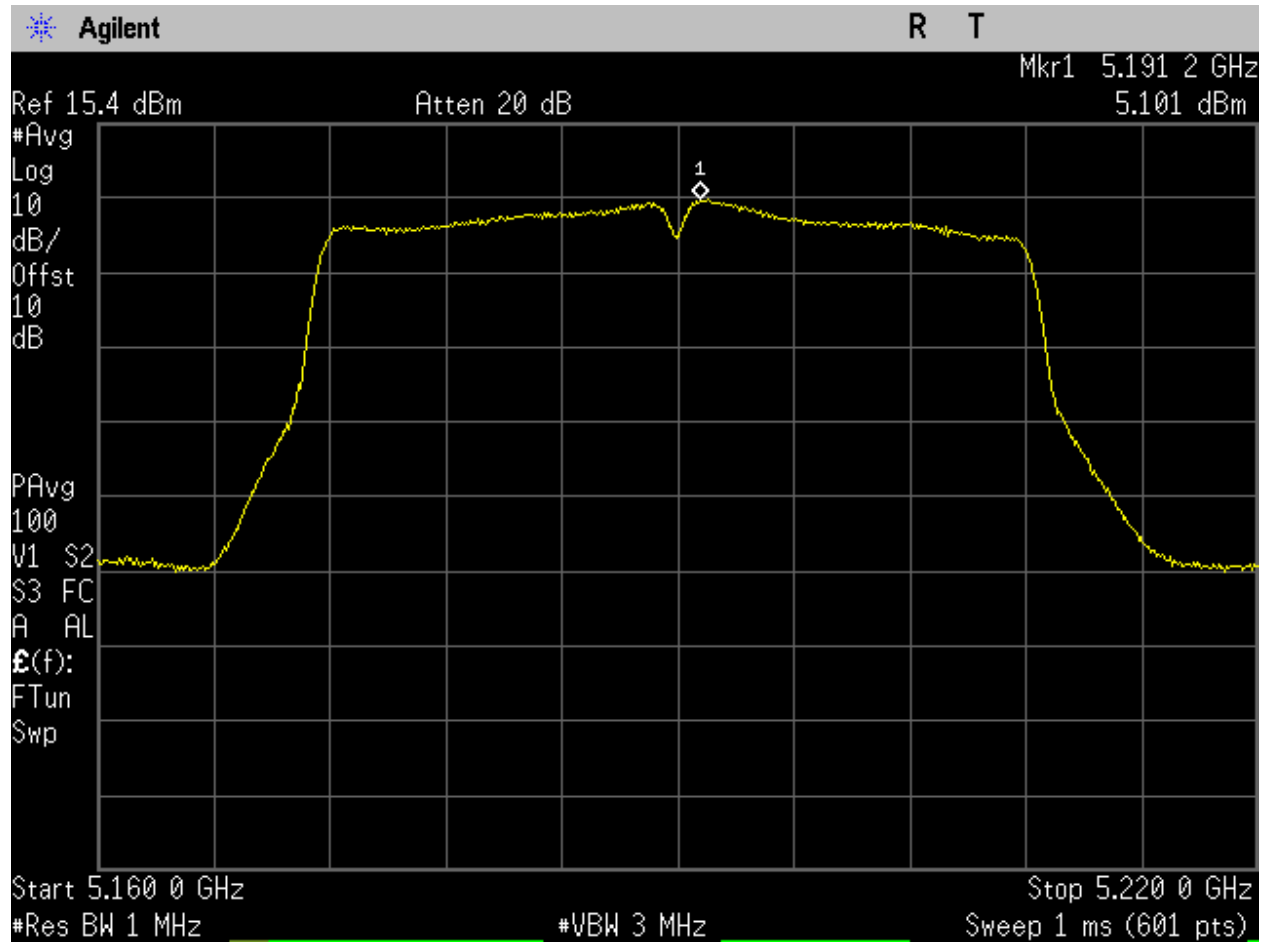


Figure 109: U-NII-1_5190MHz_Low Ch_38_40MHz BW_n-mode_Spectral Density_Port 1.

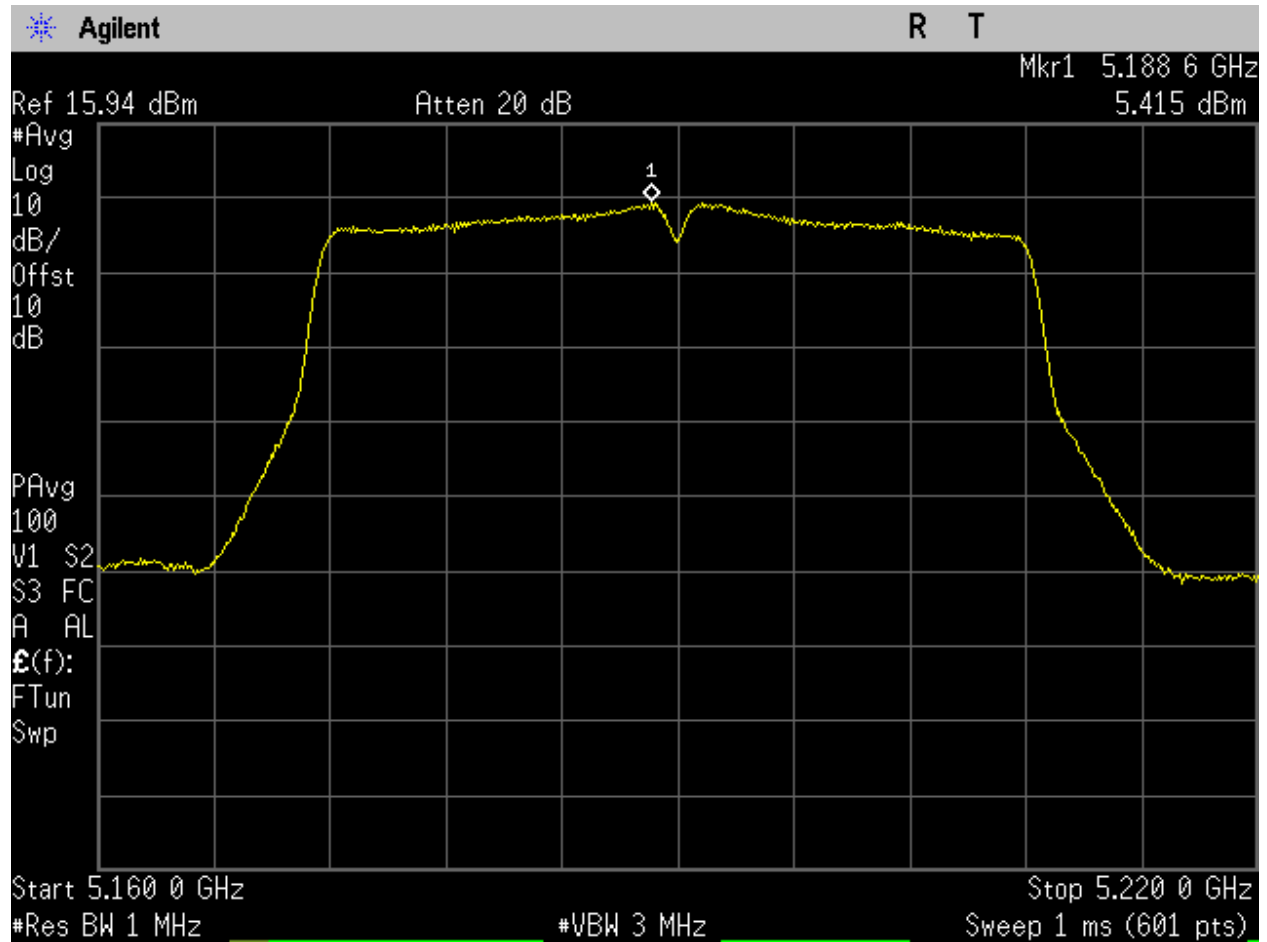


Figure 110: U-NII-1_5190MHz_Low Ch_38_40MHz BW_n-mode_Spectral Density_Port 2.

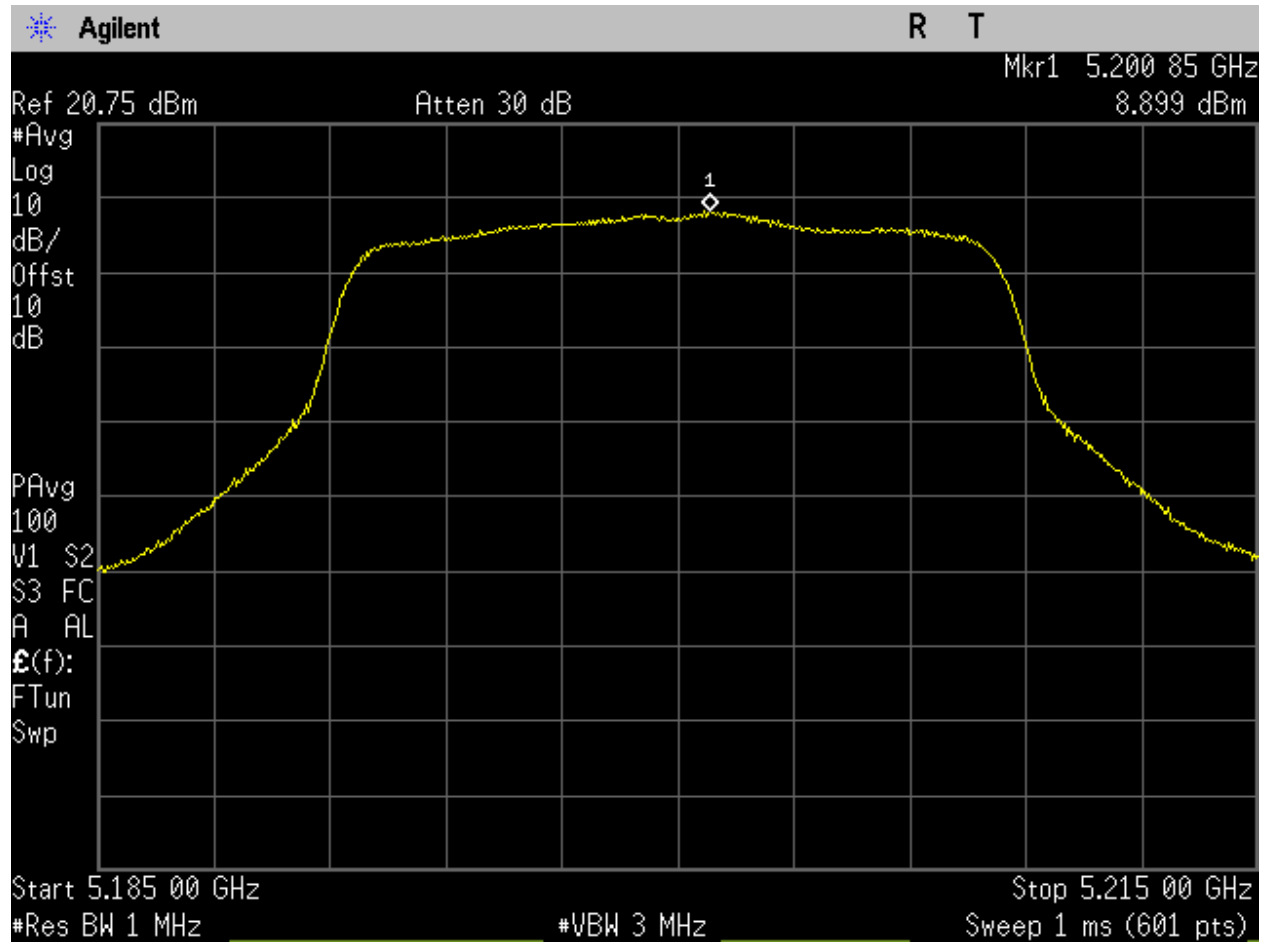


Figure 111: U-NII-1_5200MHz_low_Mid Ch_40_20MHz BW_a-mode_Spectral Density_Port 1.

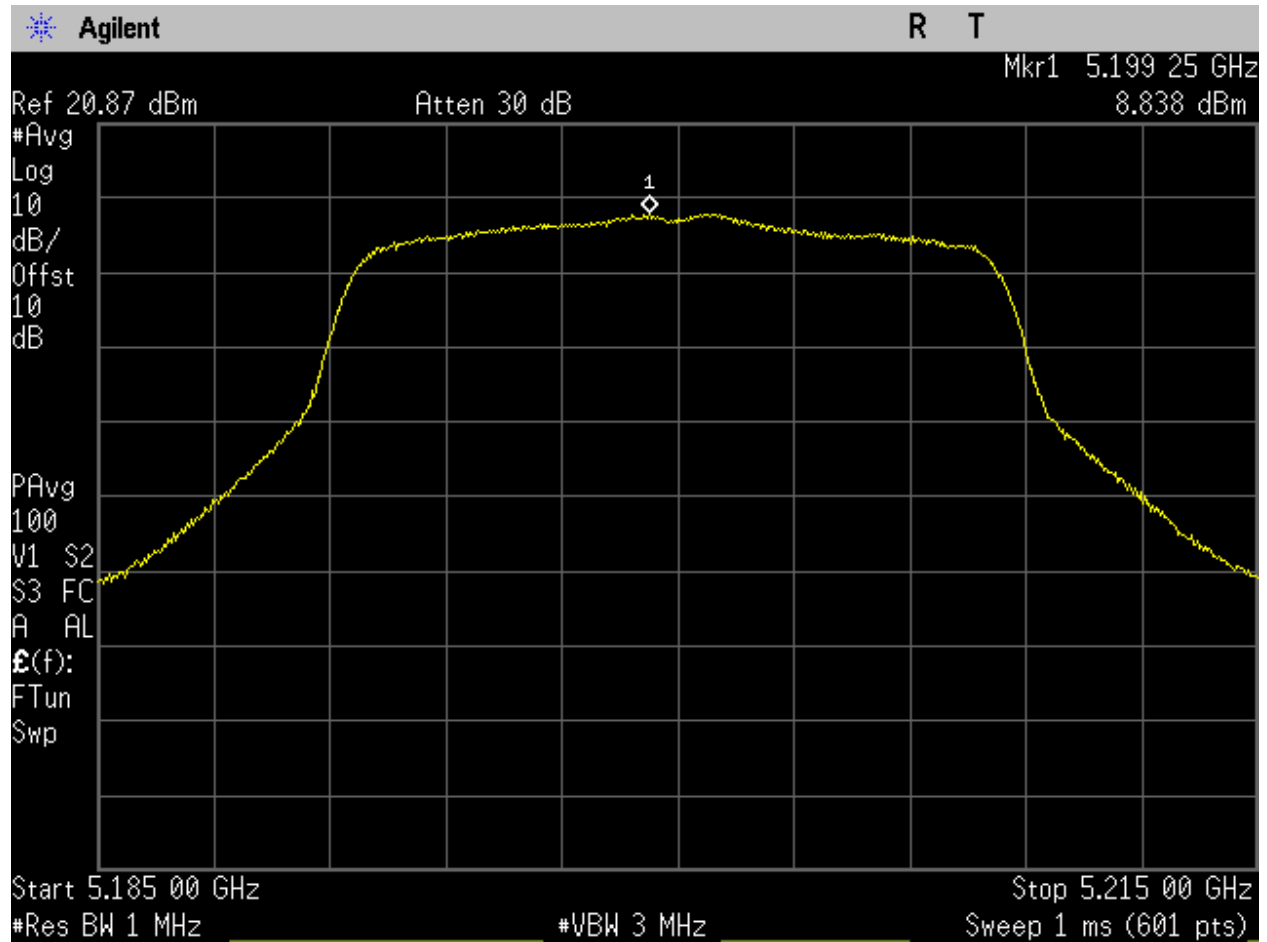


Figure 112: U-NII-1_5200MHz_low_Mid Ch_40_20MHz BW_a-mode_Spectral Density_Port 2.

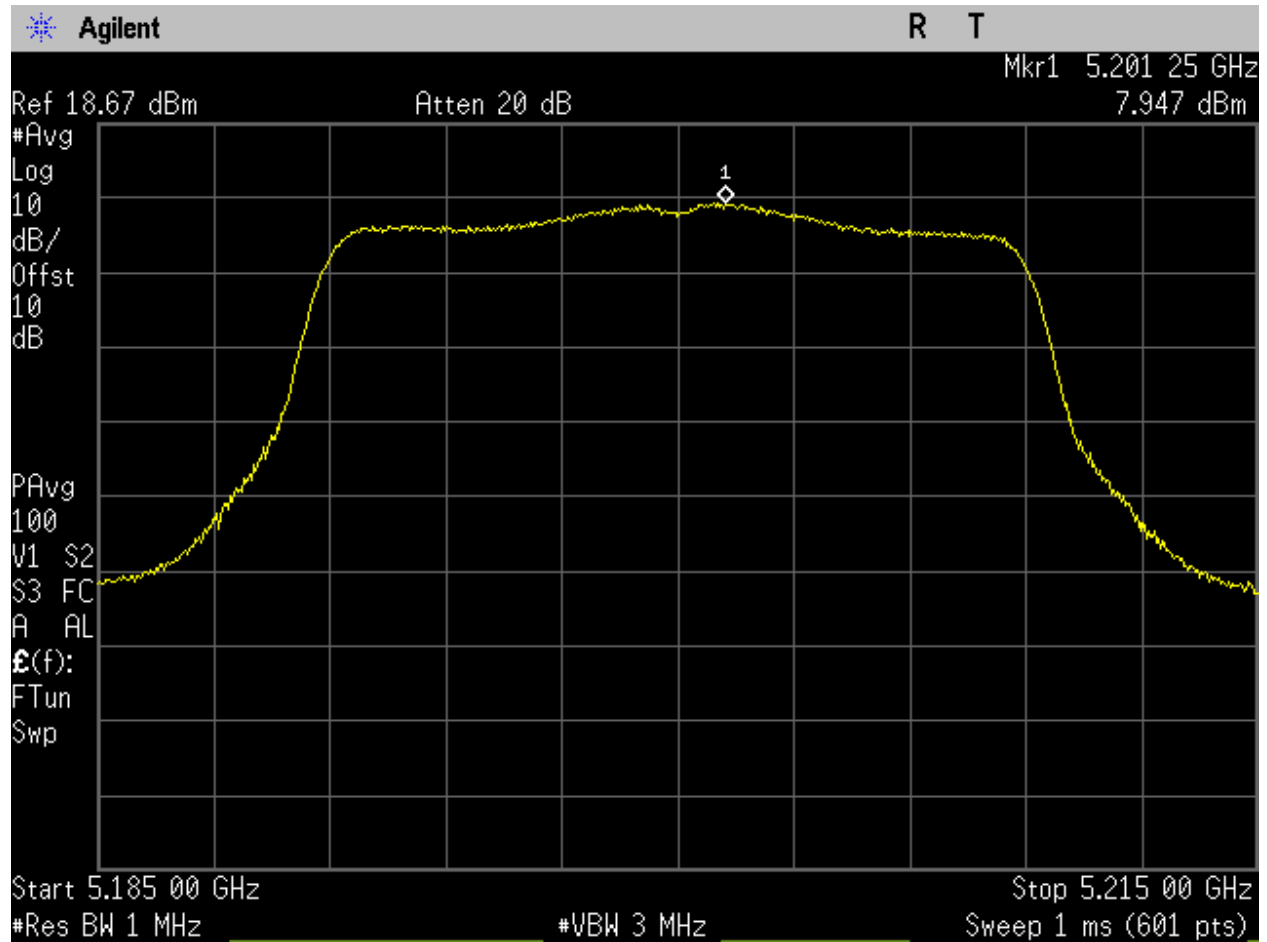


Figure 113: U-NII-1_5200MHz_low_Mid Ch_40_20MHz BW_ac-mode_Spectral Density_Port 1.

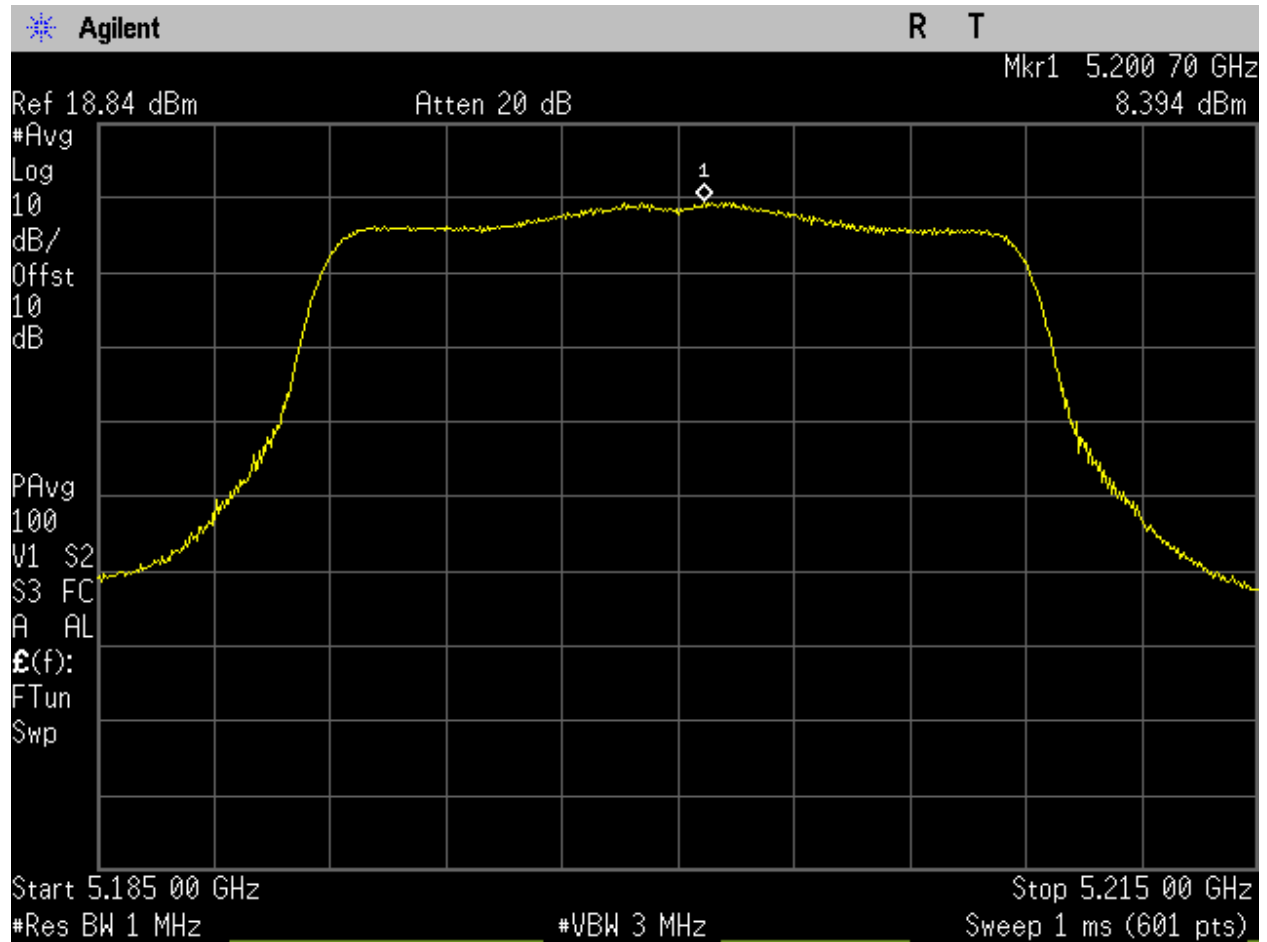


Figure 114: U-NII-1_5200MHz_low_Mid Ch_40_20MHz BW_ac-mode_Spectral Density_Port 2.

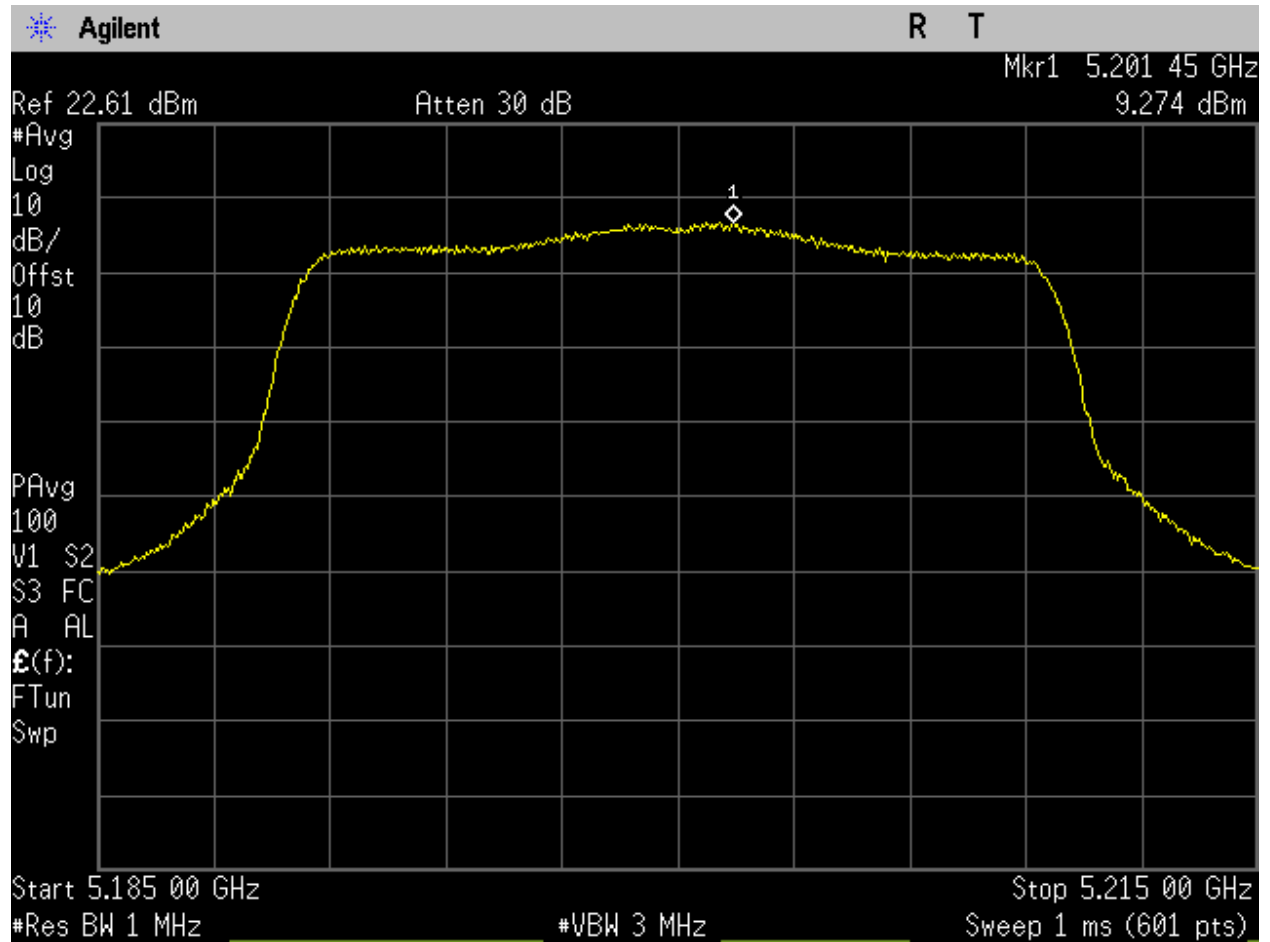


Figure 115: U-NII-1_5200MHz_low_Mid Ch_40_20MHz BW_ax-mode_Spectral Density_Port 1.

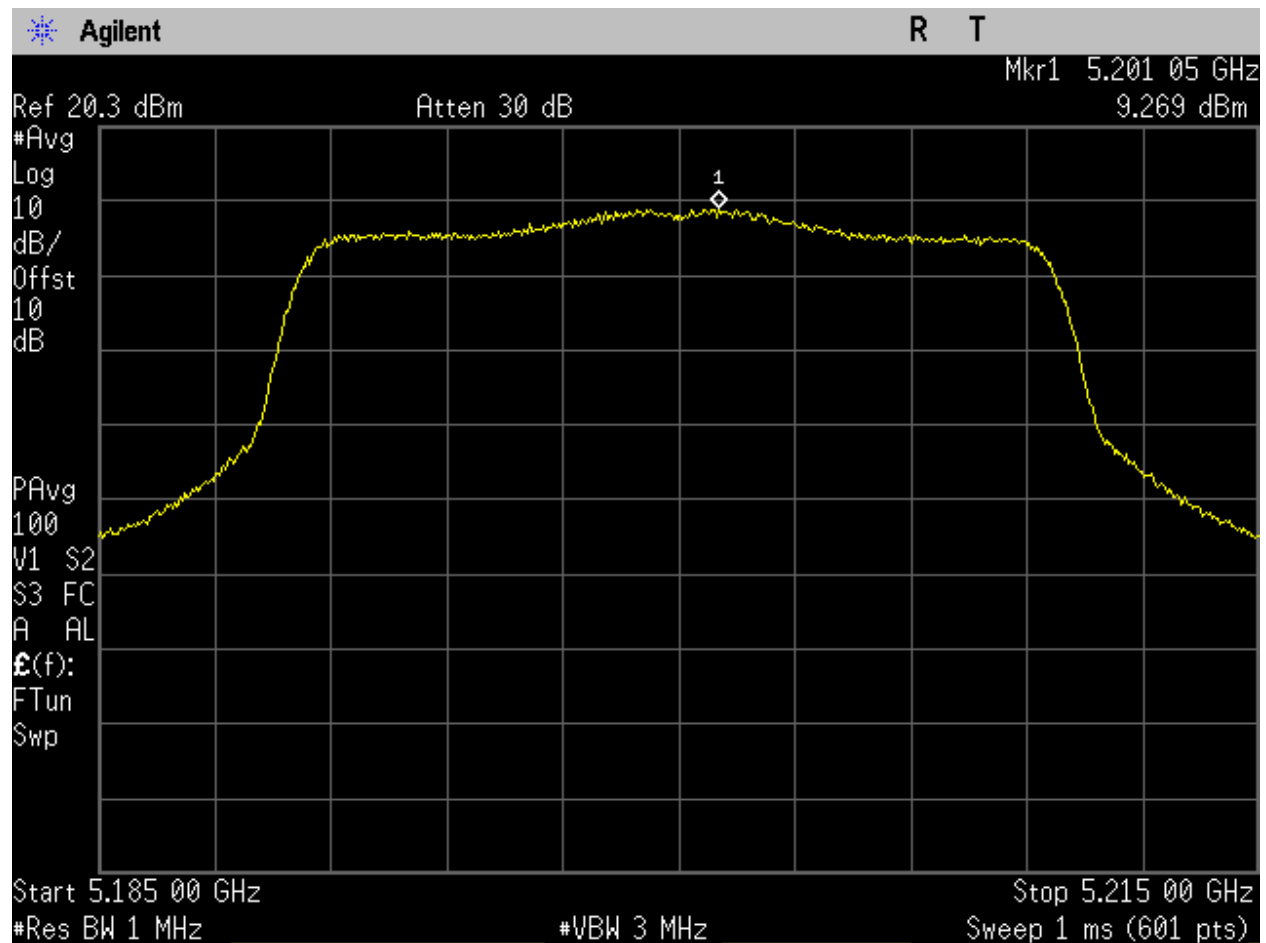


Figure 116: U-NII-1_5200MHz_low_Mid Ch_40_20MHz BW_ax-mode_Spectral Density_Port 2.

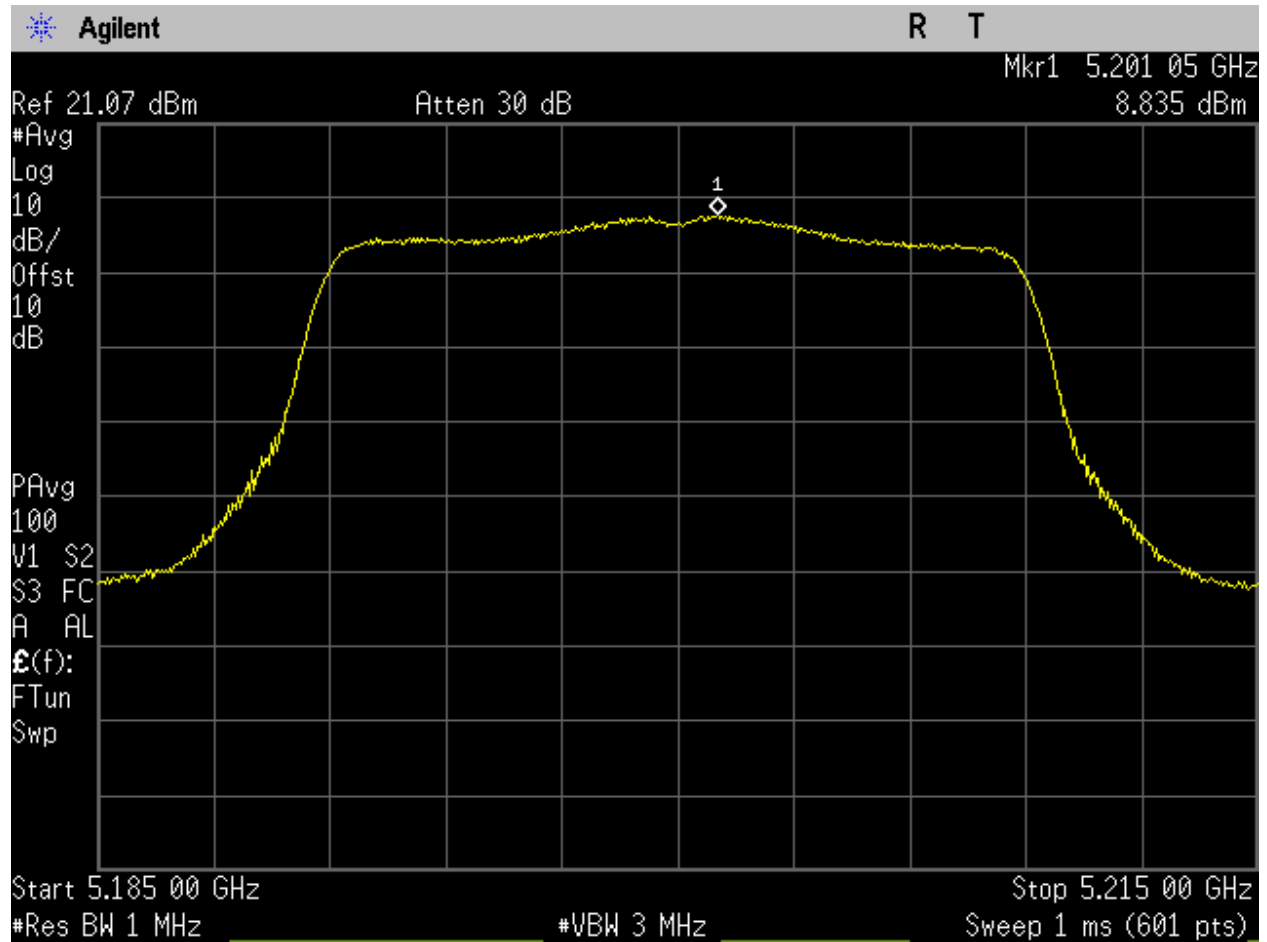


Figure 117: U-NII-1_5200MHz_low_Mid Ch_40_20MHz BW_n-mode_Spectral Density_Port 1.

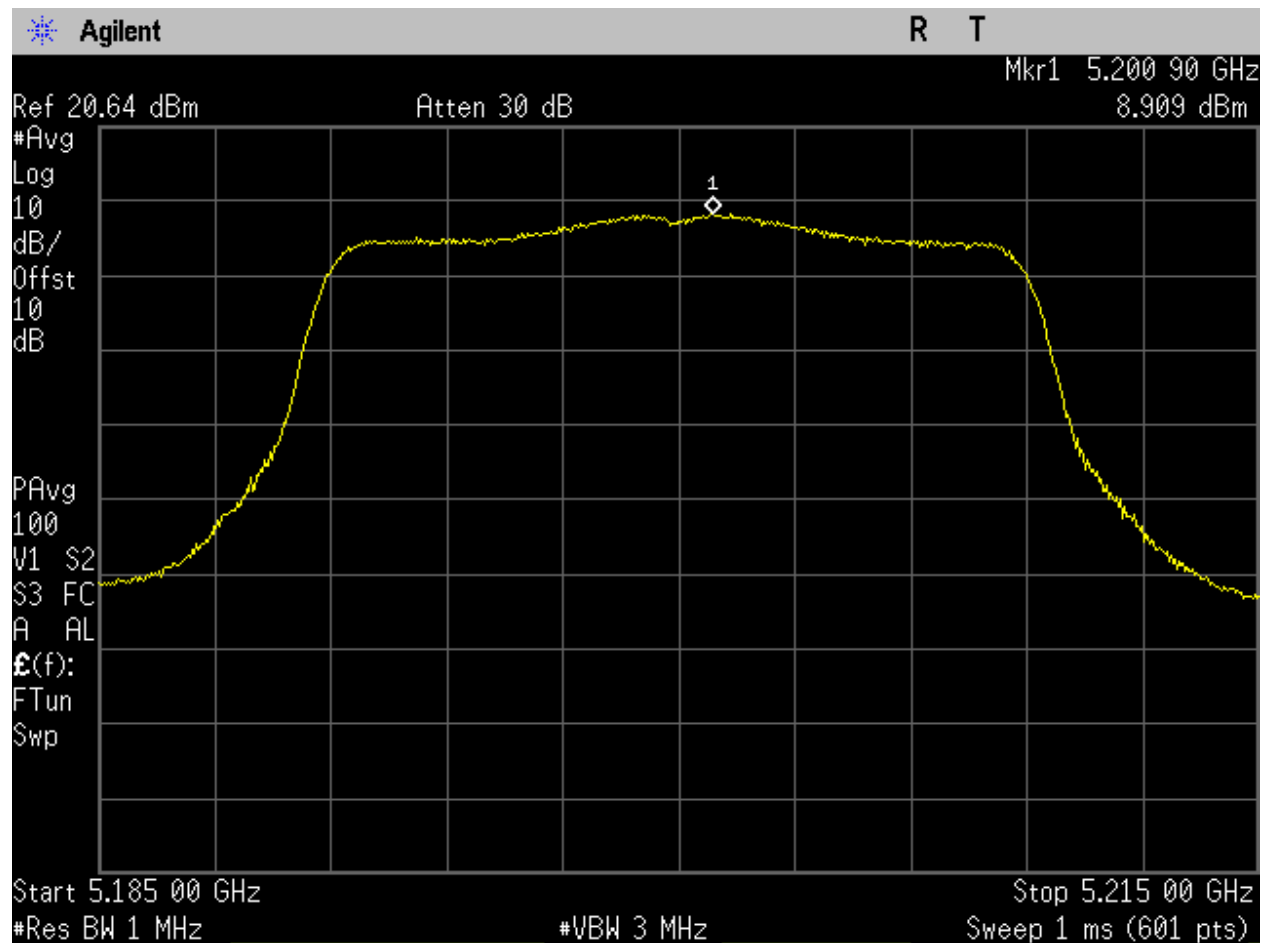


Figure 118: U-NII-1_5200MHz_low_Mid Ch_40_20MHz BW_n-mode_Spectral Density_Port 2.

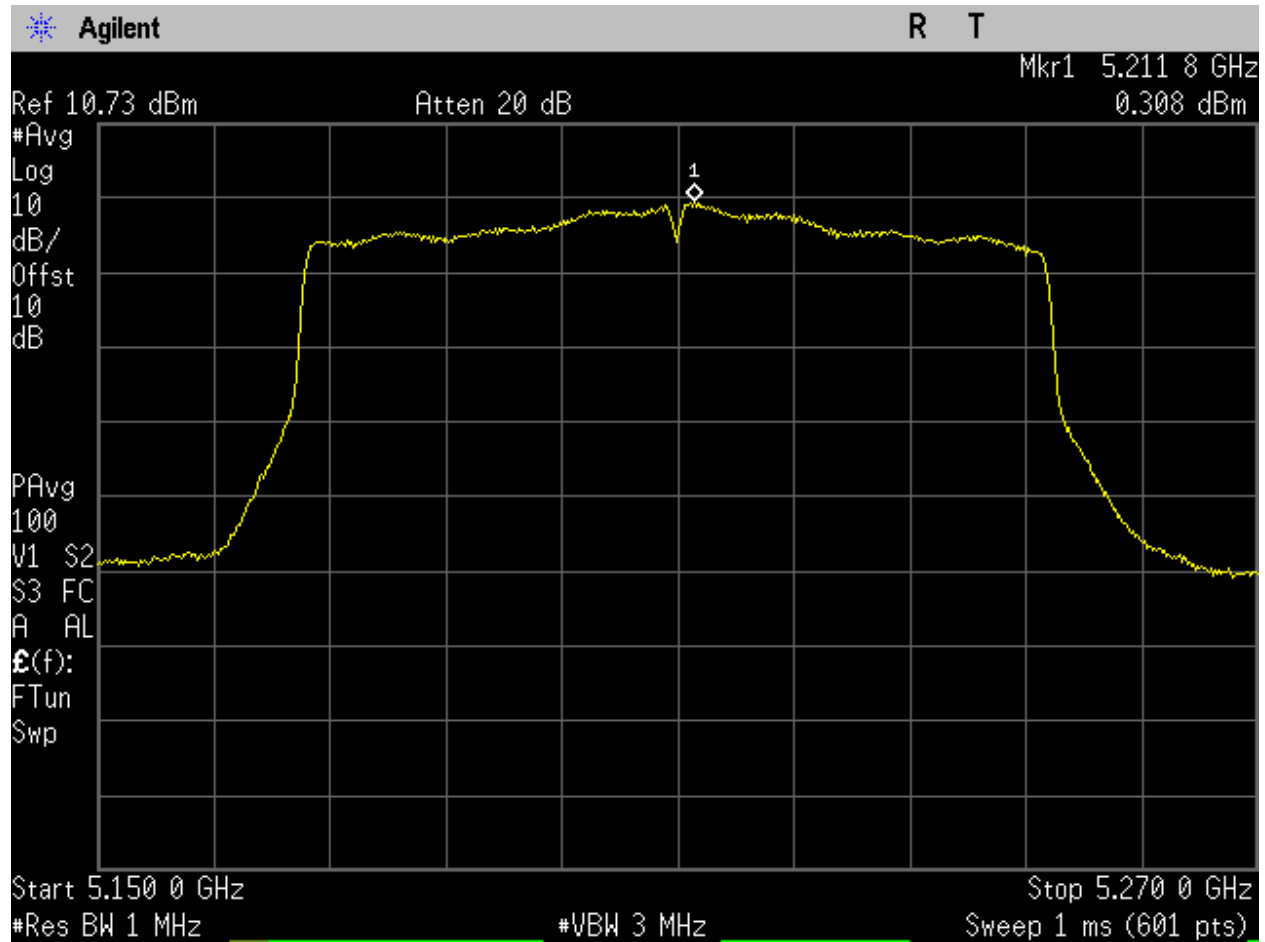


Figure 119: U-NII-1_5210MHz_Low Mid Ch_42_80MHz BW_ac-mode_Spectral Density_Port 1.

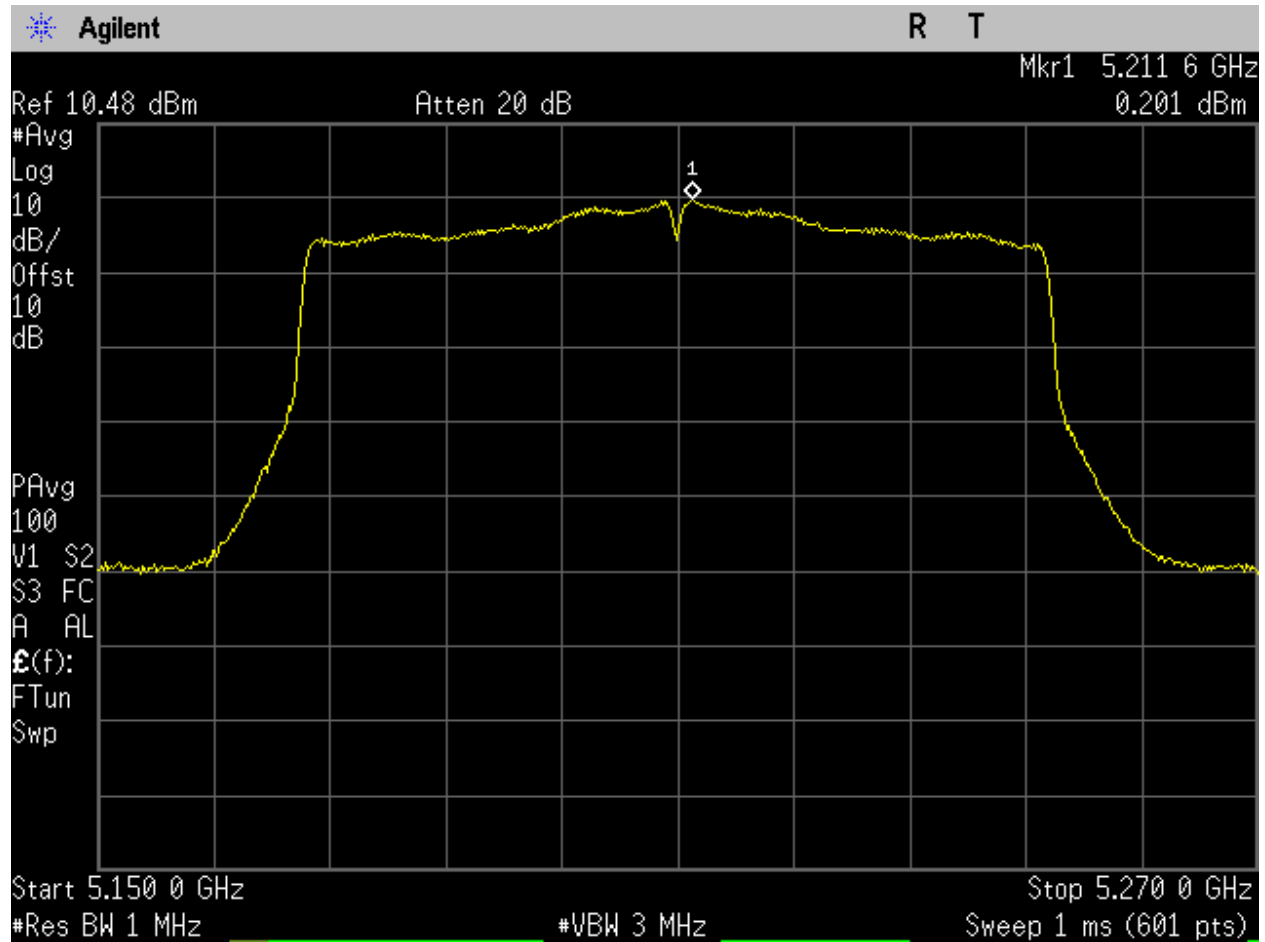


Figure 120: U-NII-1_5210MHz_Low Mid Ch_42_80MHz BW_ac-mode_Spectral Density_Port 2.

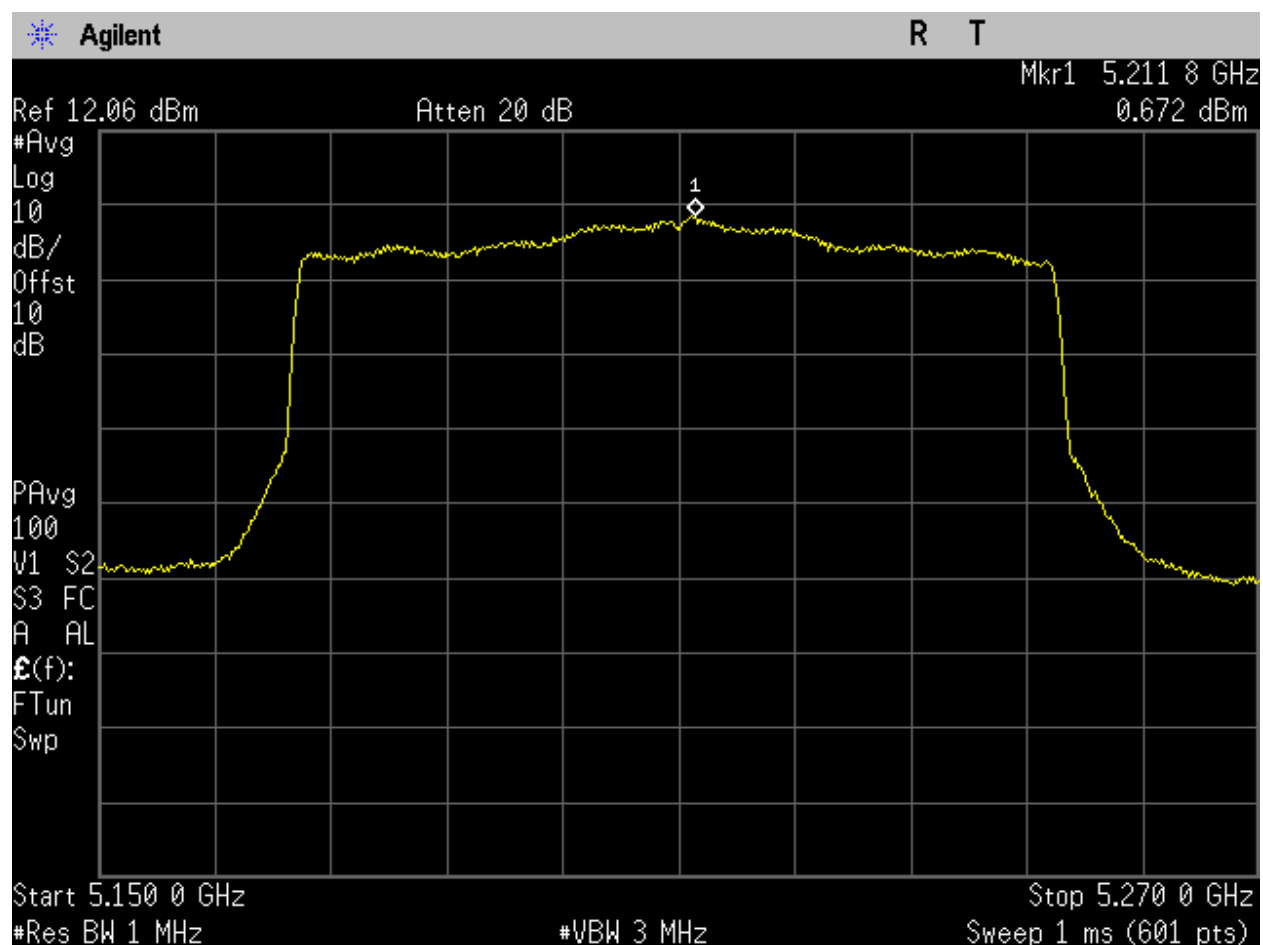


Figure 121: U-NII-1_5210MHz_Low Mid Ch_42_80MHz BW_ax-mode_Spectral Density_Port 1.

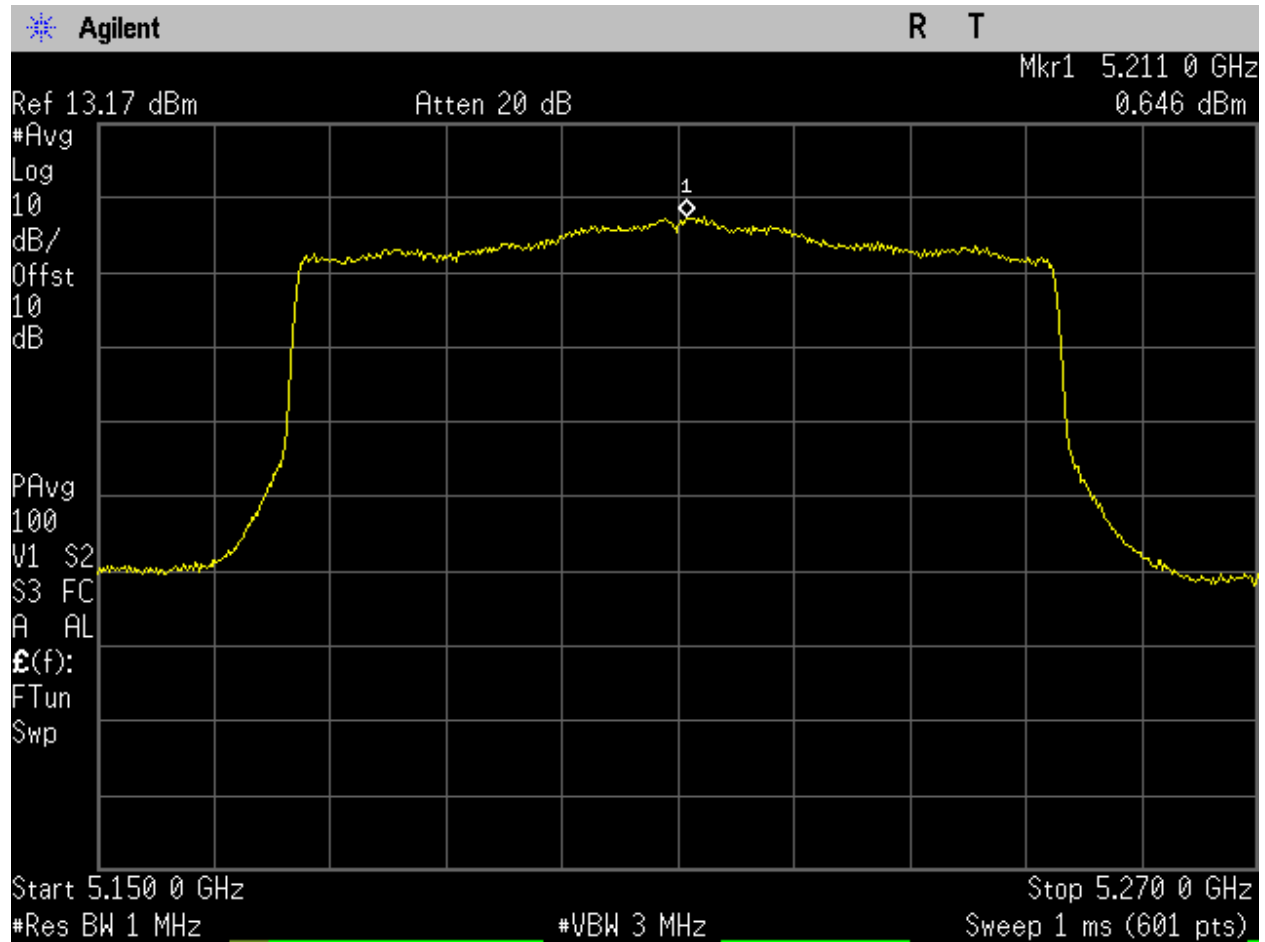


Figure 122: U-NII-1_5210MHz_Low Mid Ch_42_80MHz BW_ax-mode_Spectral Density_Port 2.

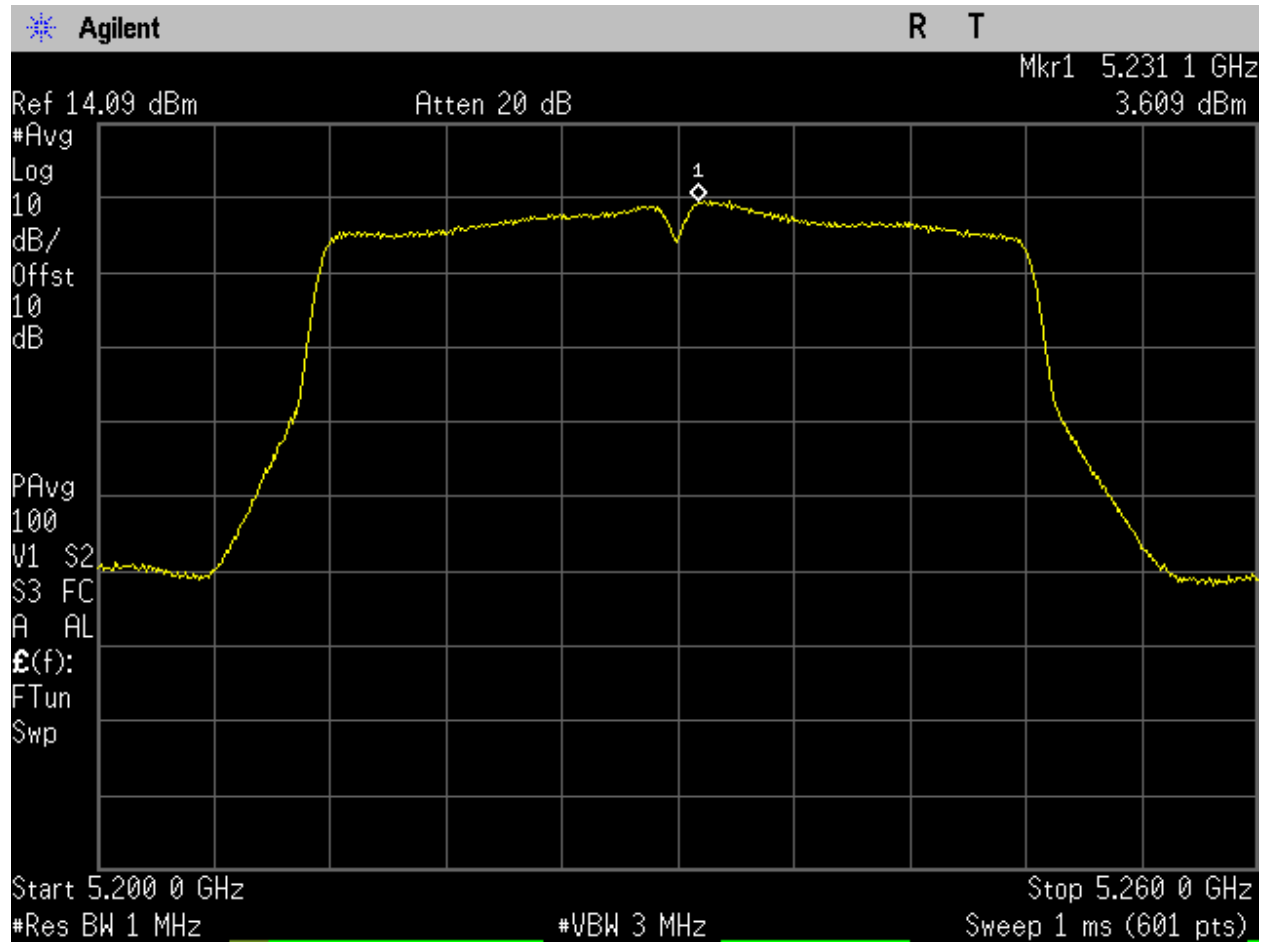


Figure 123: U-NII-1_5230MHz_high_Mid Ch_46_40MHz BW_ac-mode_Spectral Density_Port 1.

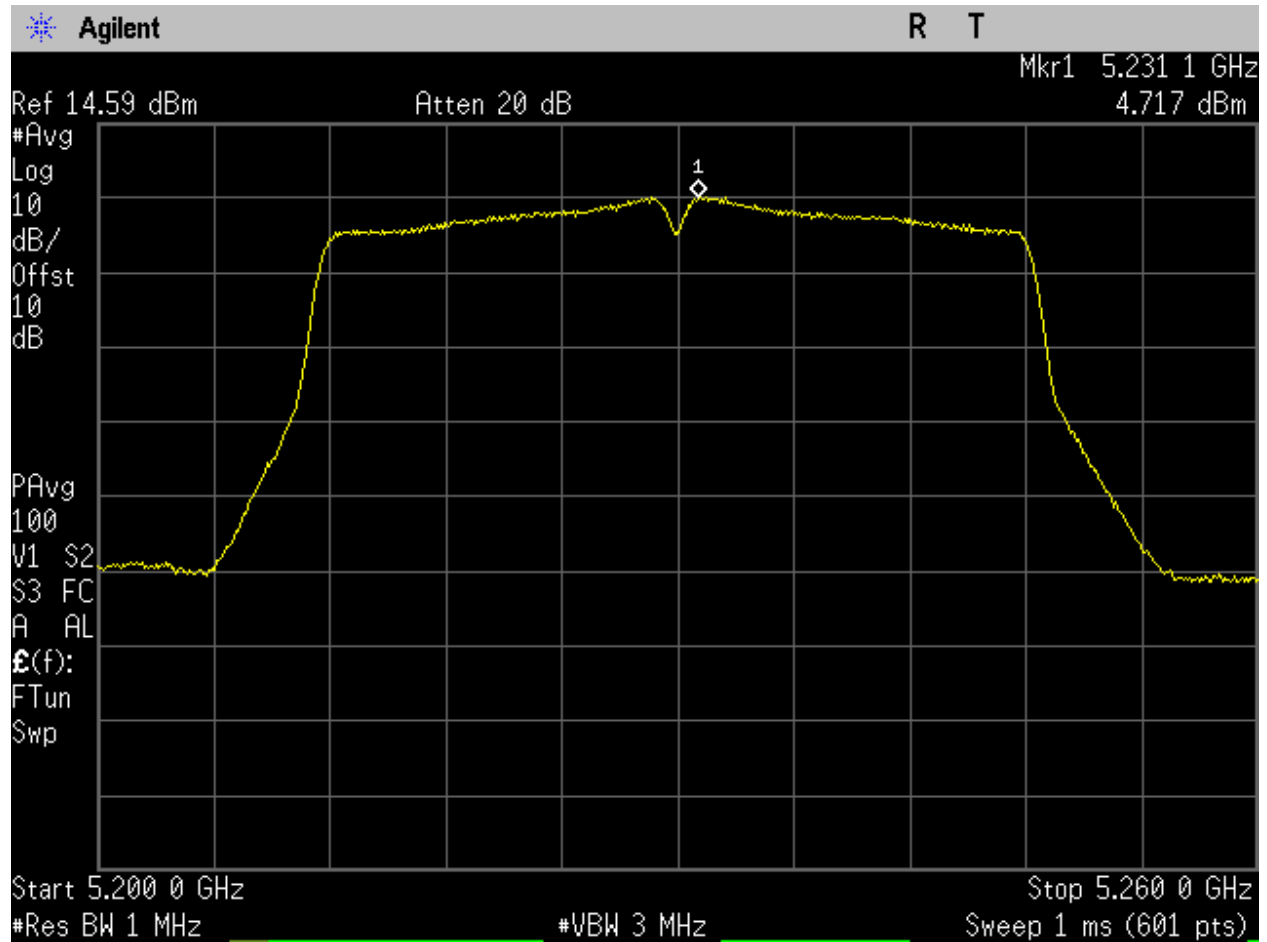


Figure 124: U-NII-1_5230MHz_high_Mid Ch_46_40MHz BW_ac-mode_Spectral Density_Port 2.

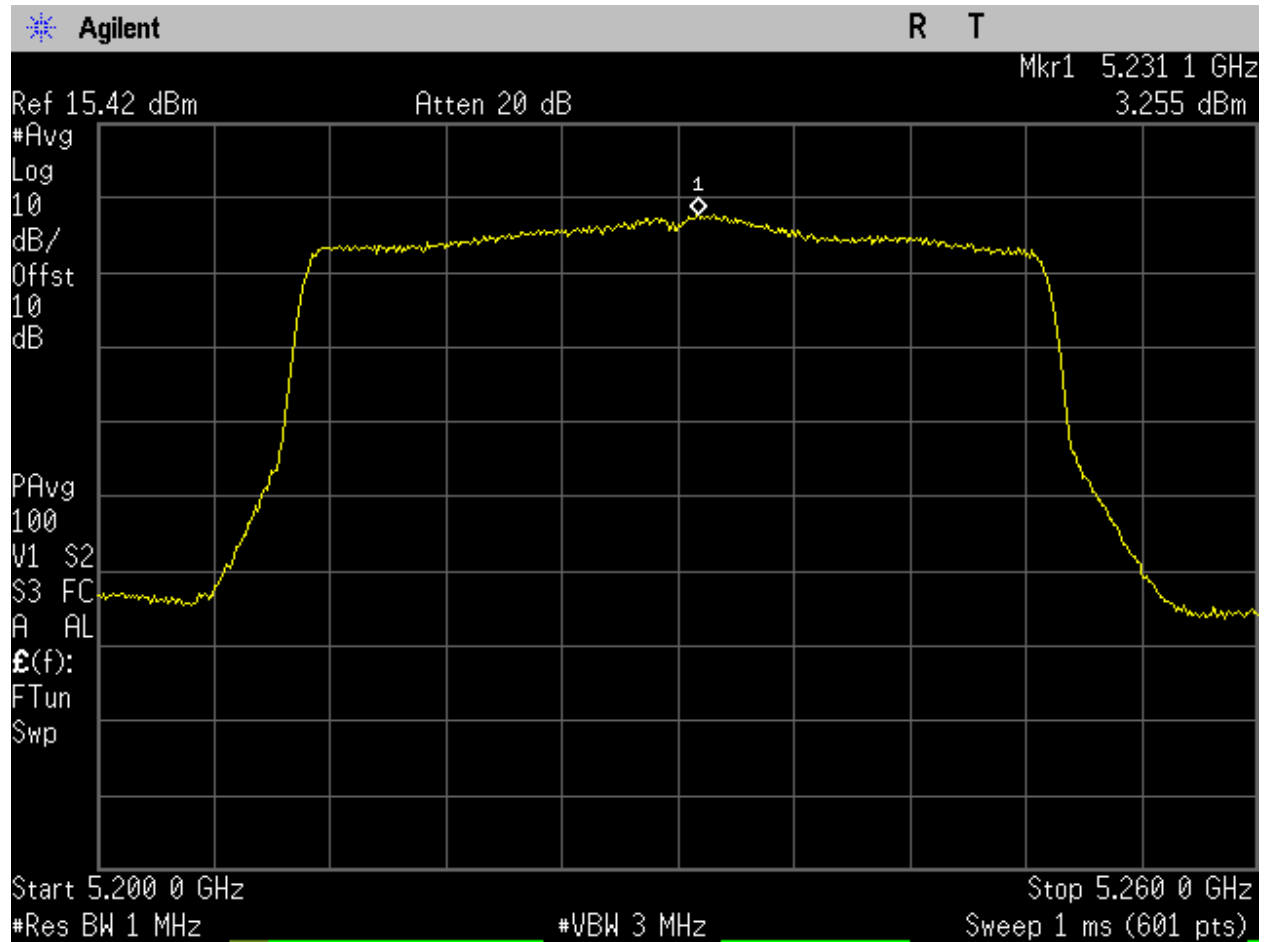


Figure 125: U-NII-1_5230MHz_high_Mid Ch_46_40MHz BW_ax-mode_Spectral Density_Port 1.

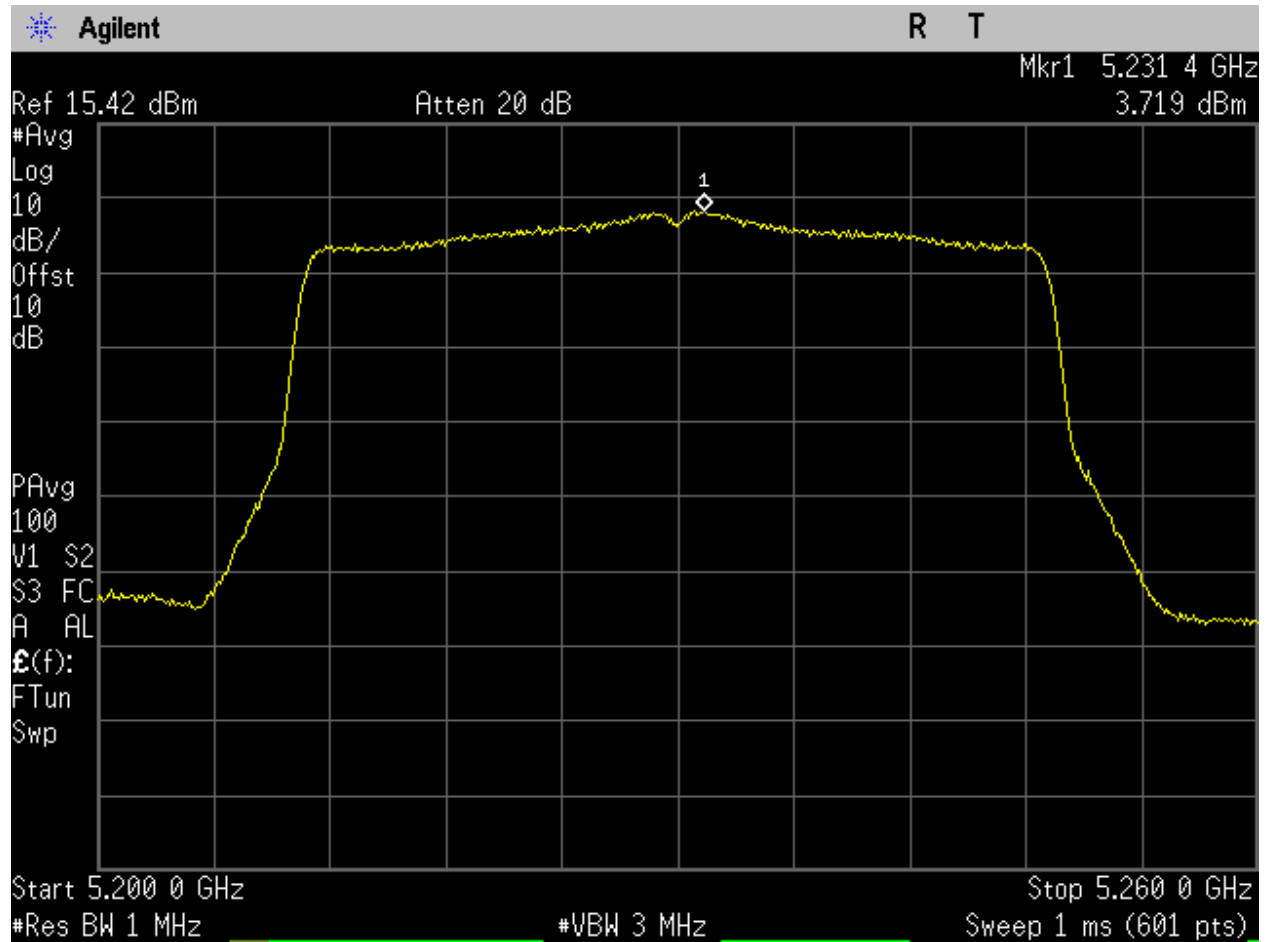


Figure 126: U-NII-1_5230MHz_high_Mid Ch_46_40MHz BW_ax-mode_Spectral Density_Port 2.

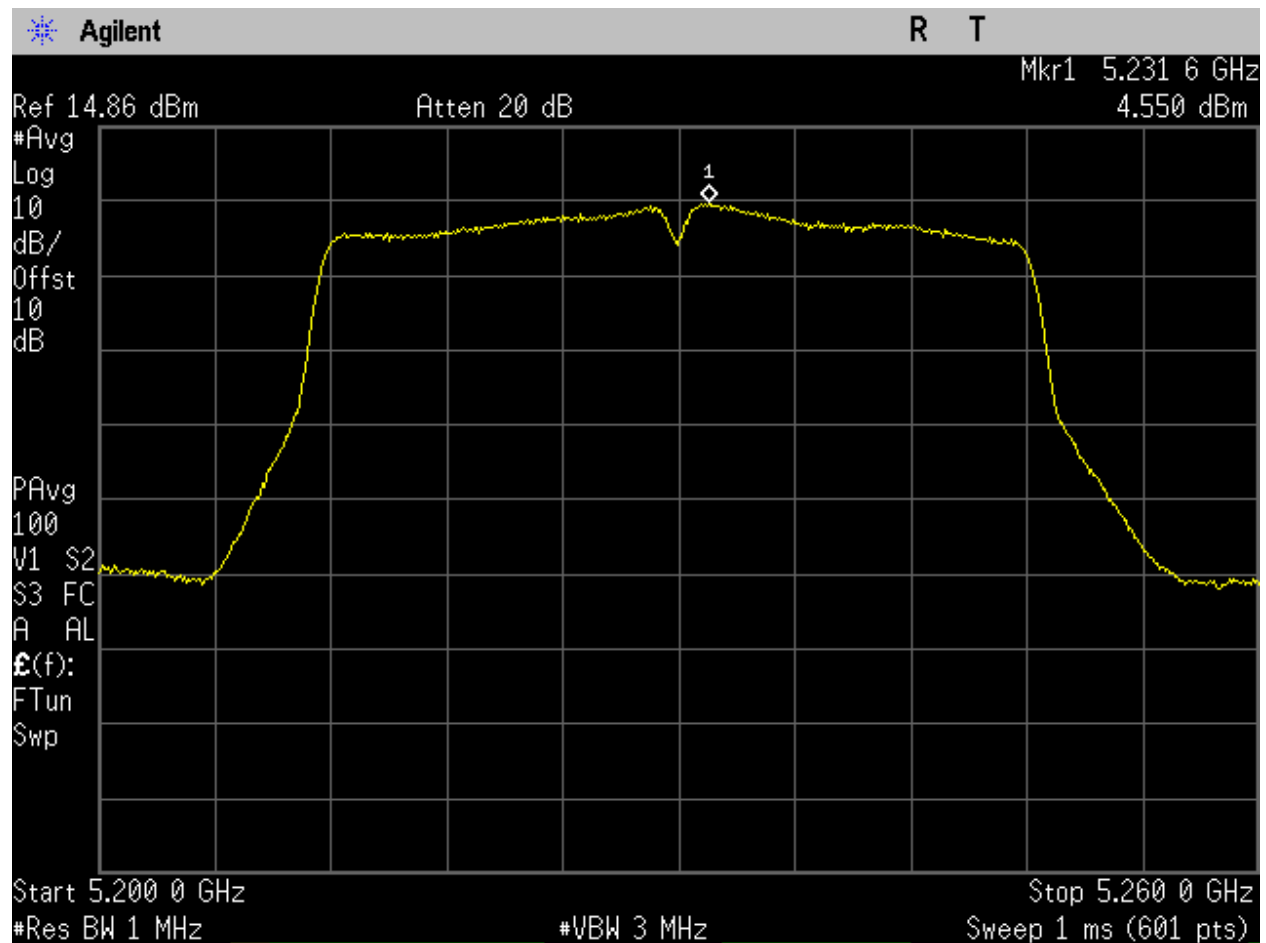


Figure 127: U-NII-1_5230MHz_high_Mid Ch_46_40MHz BW_n-mode_Spectral Density_Port 1.

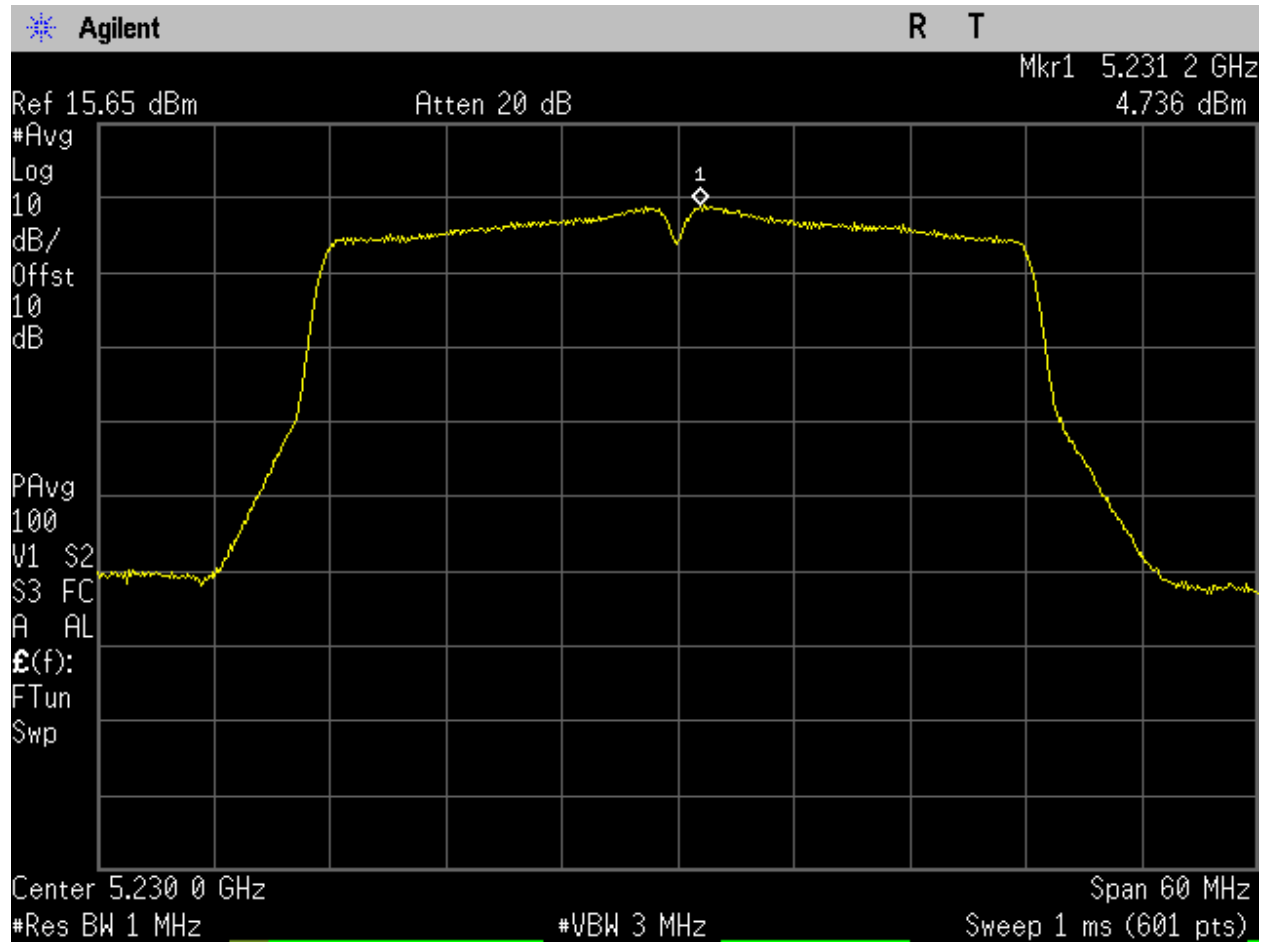


Figure 128: U-NII-1_5230MHz_high_Mid Ch_46_40MHz BW_n-mode_Spectral Density_Port 2.

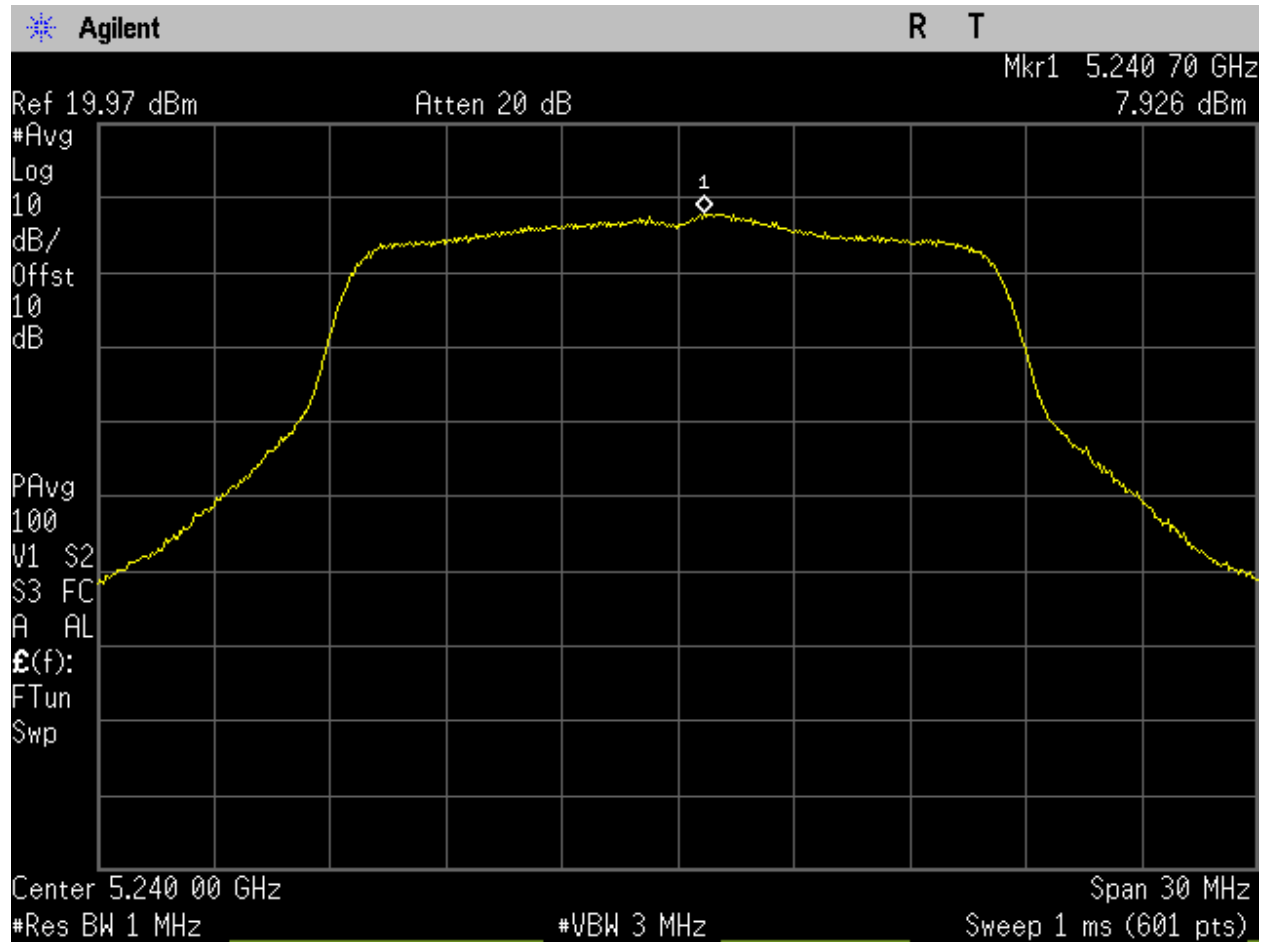


Figure 129: U-NII-1_5240MHz_high_mid Ch_48_20MHz BW_a-mode_Spectral Density_Port 1.

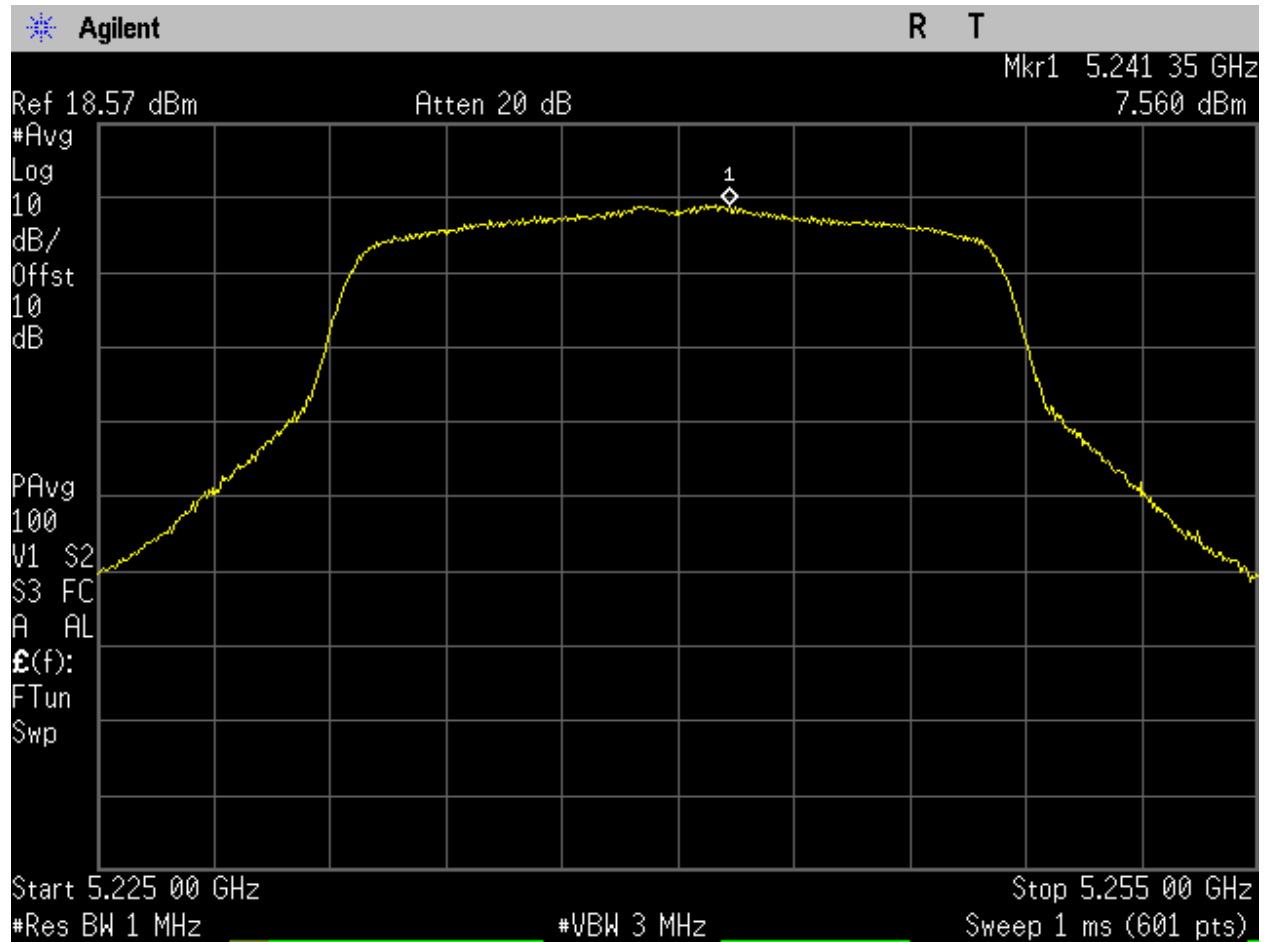


Figure 130: U-NII-1_5240MHz_high_mid Ch_48_20MHz BW_a-mode_Spectral Density_Port 2.

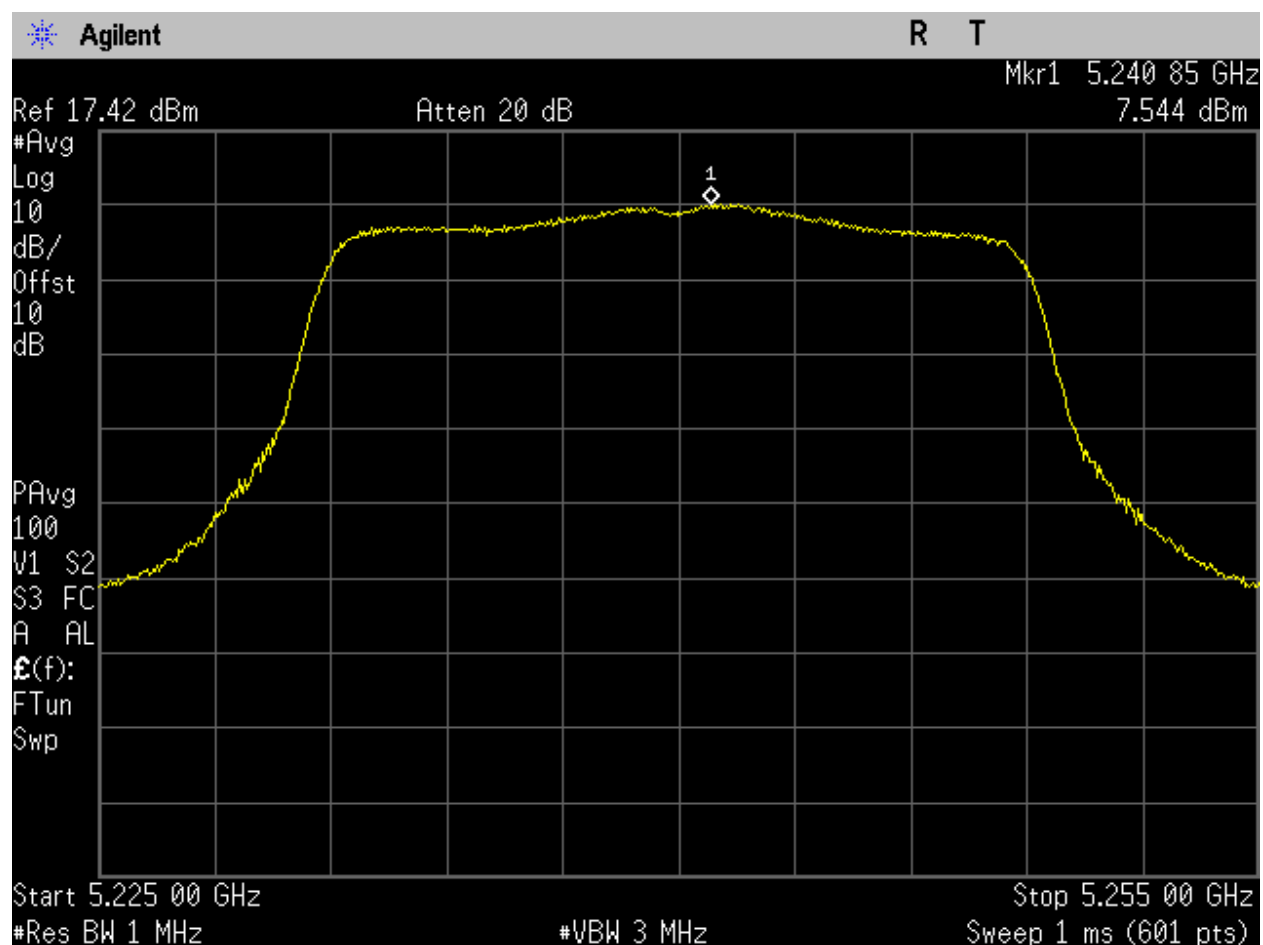


Figure 131: U-NII-1_5240MHz_high_mid Ch_48_20MHz BW_ac-mode_Spectral Density_Port 1.

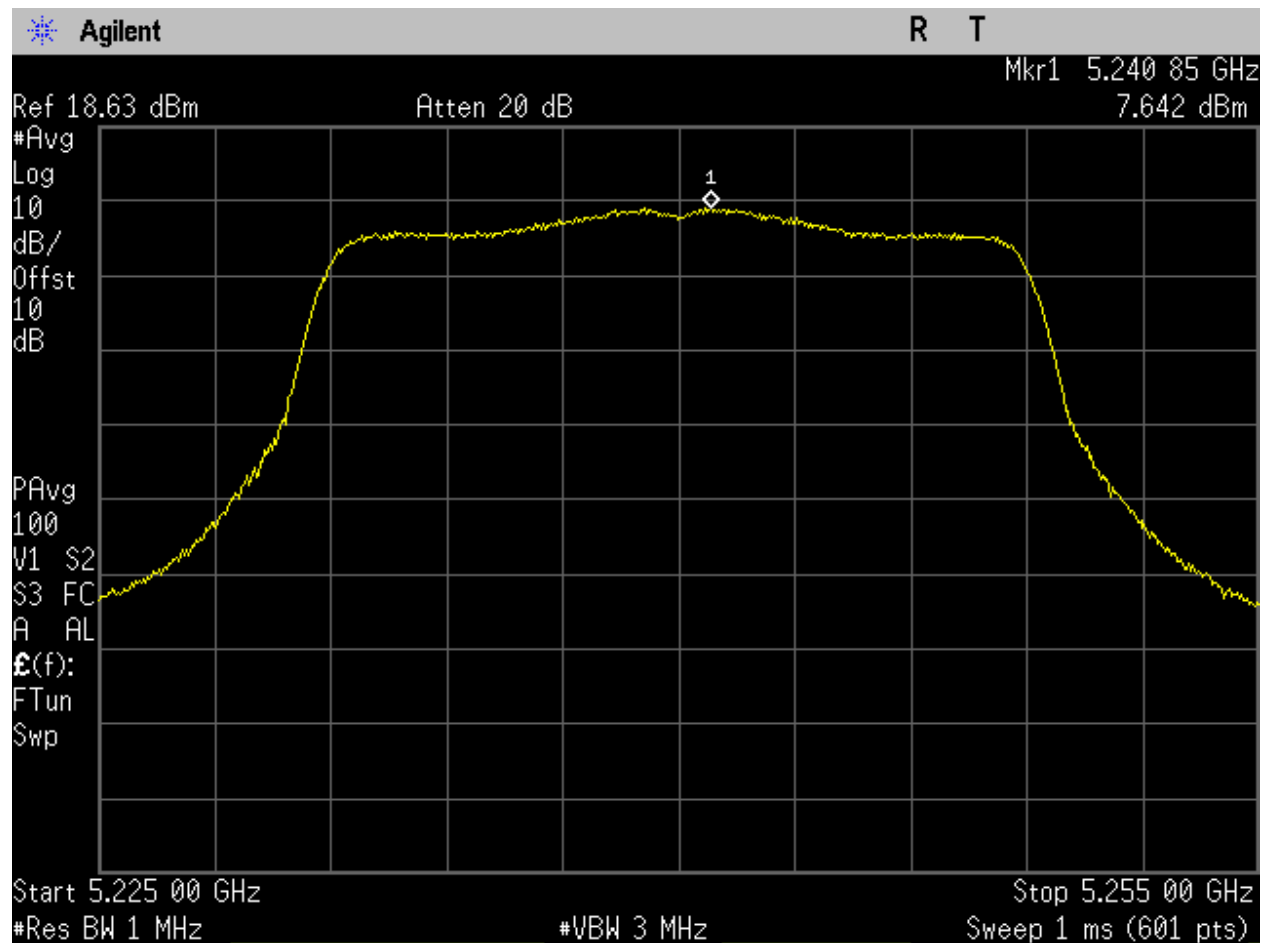


Figure 132: U-NII-1_5240MHz_high_mid Ch_48_20MHz BW_ac-mode_Spectral Density_Port 2.

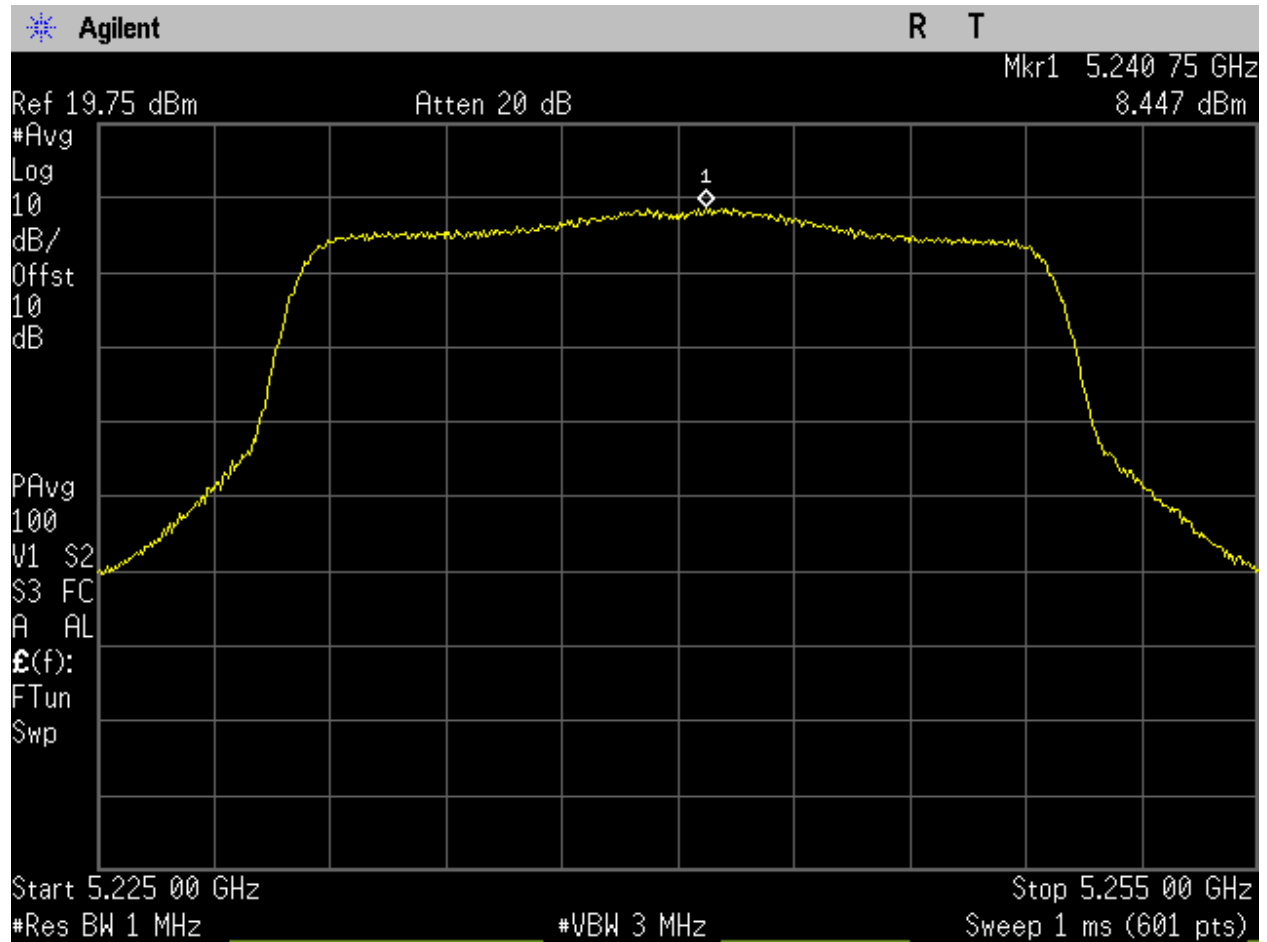


Figure 133: U-NII-1_5240MHz_high_mid Ch_48_20MHz BW_ax-mode_Spectral Density_Port 1.

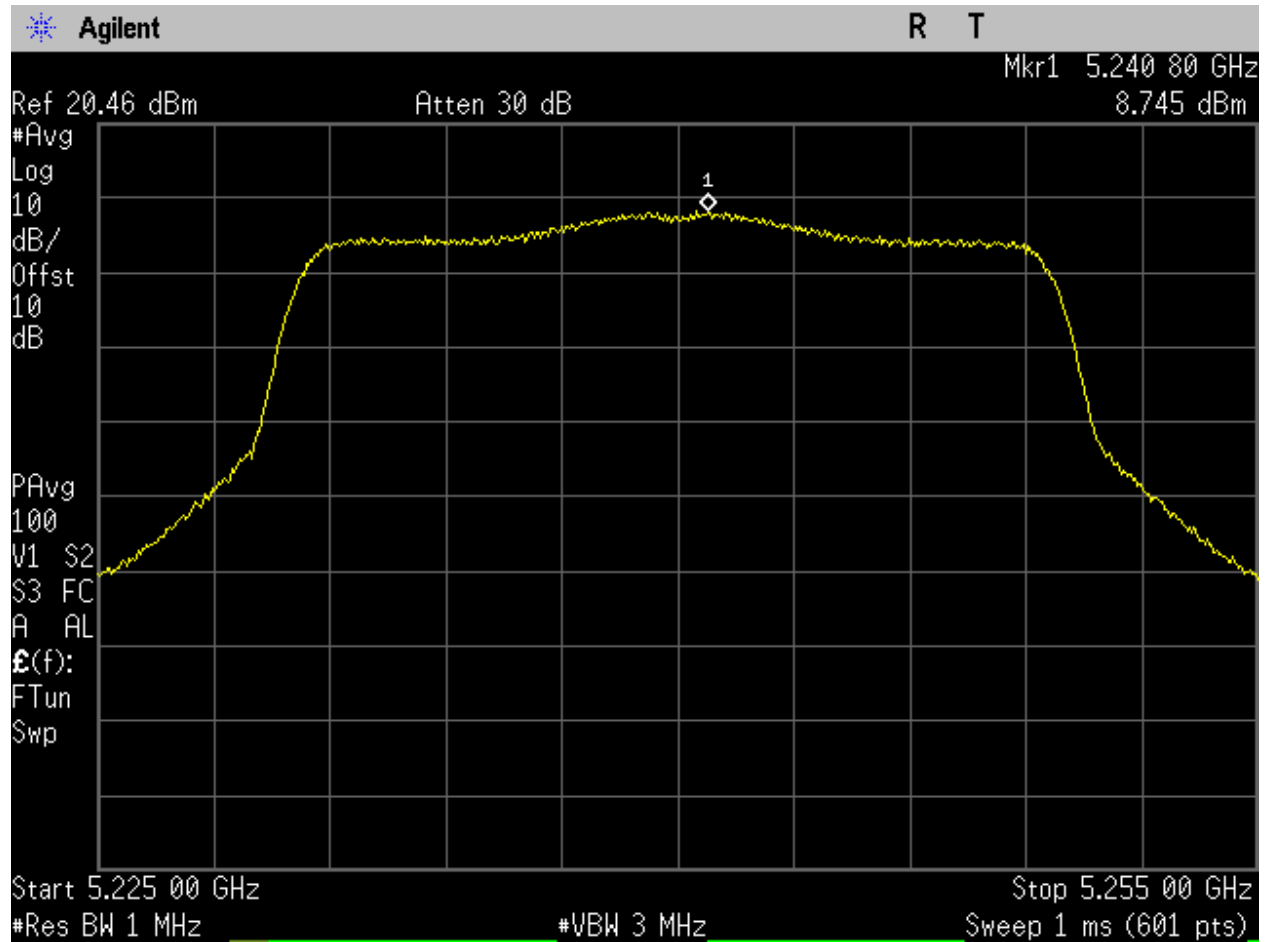


Figure 134: U-NII-1_5240MHz_high_mid Ch_48_20MHz BW_ax-mode_Spectral Density_Port 2.

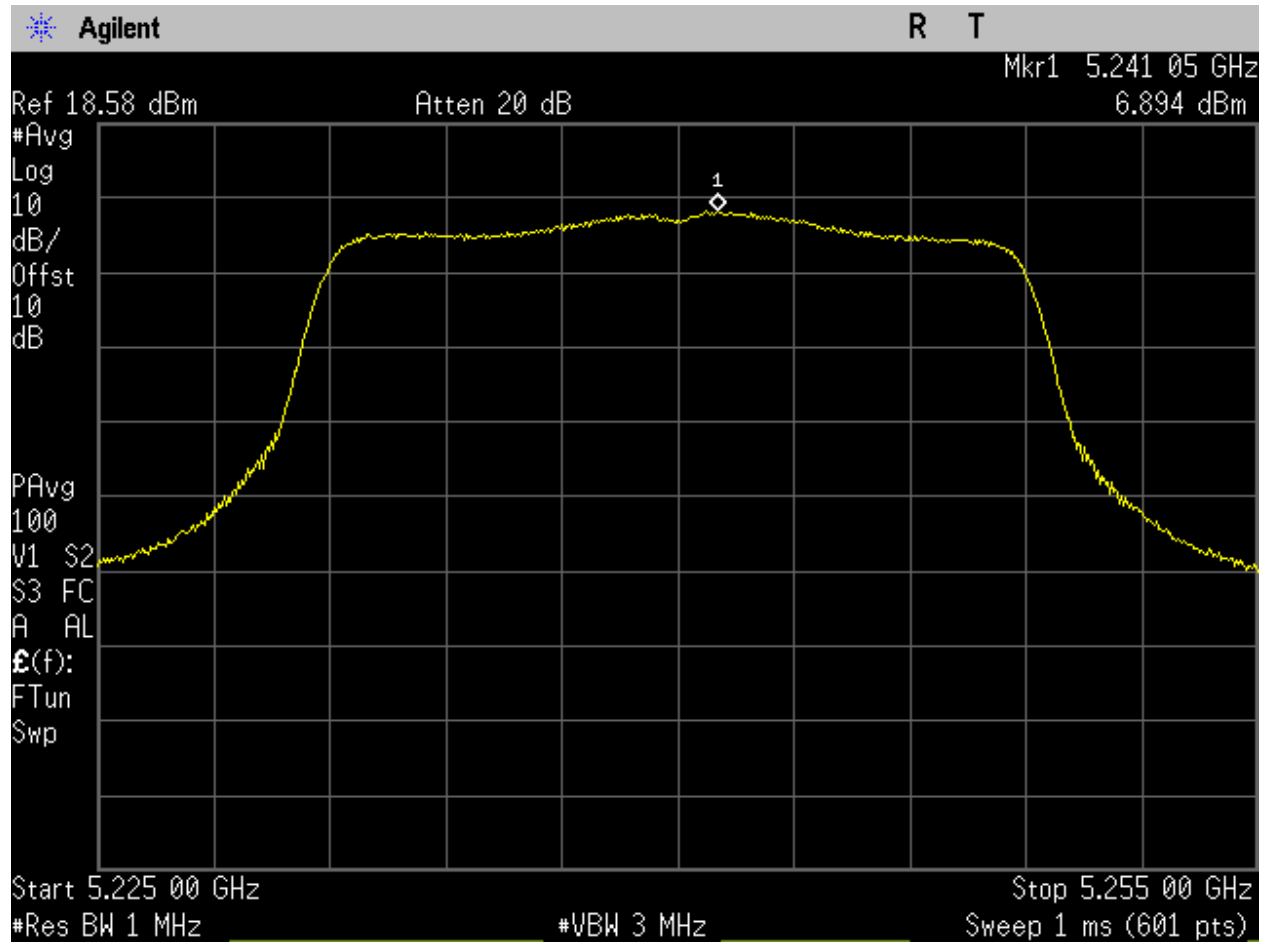


Figure 135: U-NII-1_5240MHz_high_mid Ch_48_20MHz BW_n-mode_Spectral Density_Port 1.

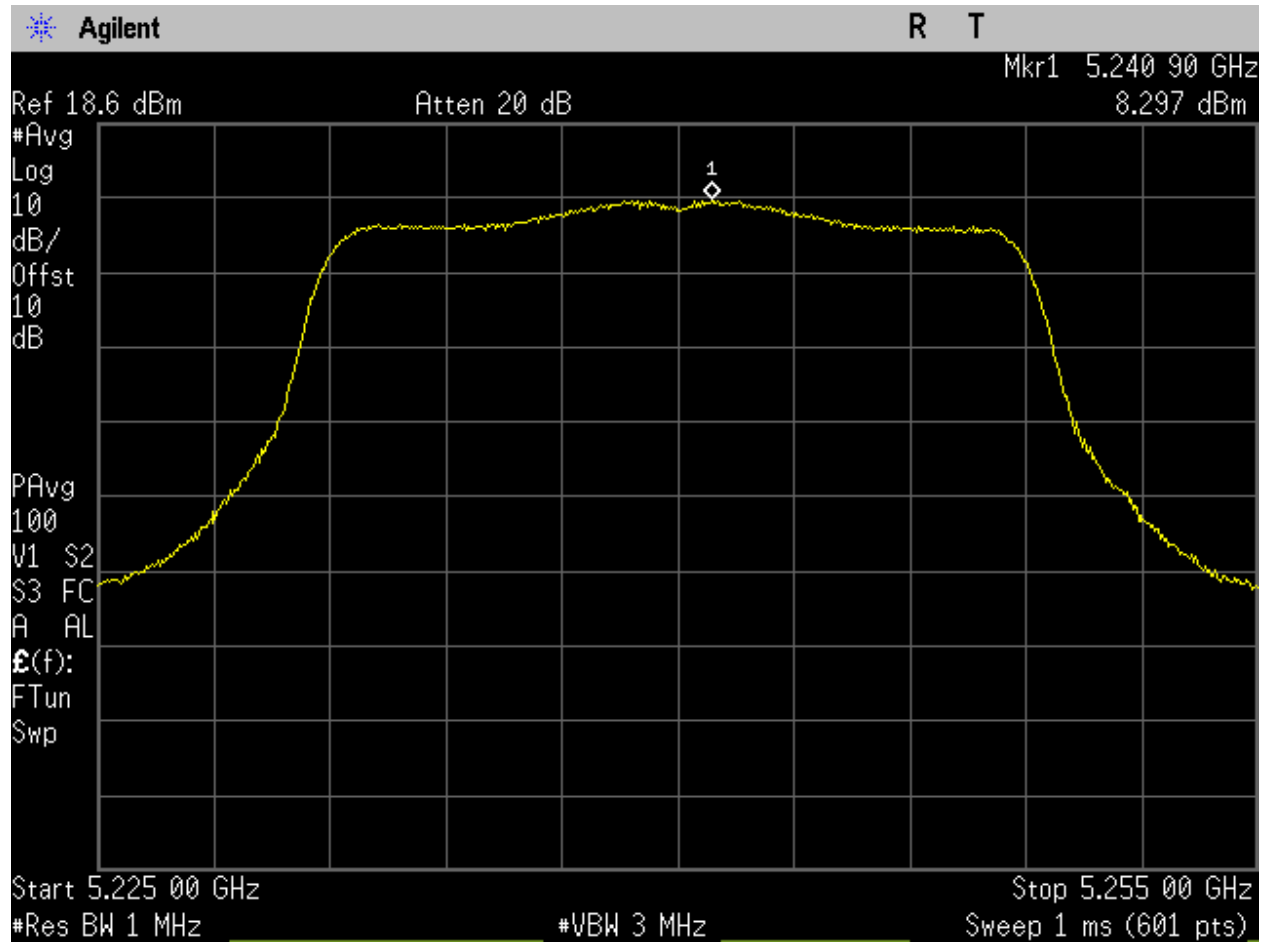


Figure 136: U-NII-1_5240MHz_high_mid Ch_48_20MHz BW_n-mode_Spectral Density_Port 2.

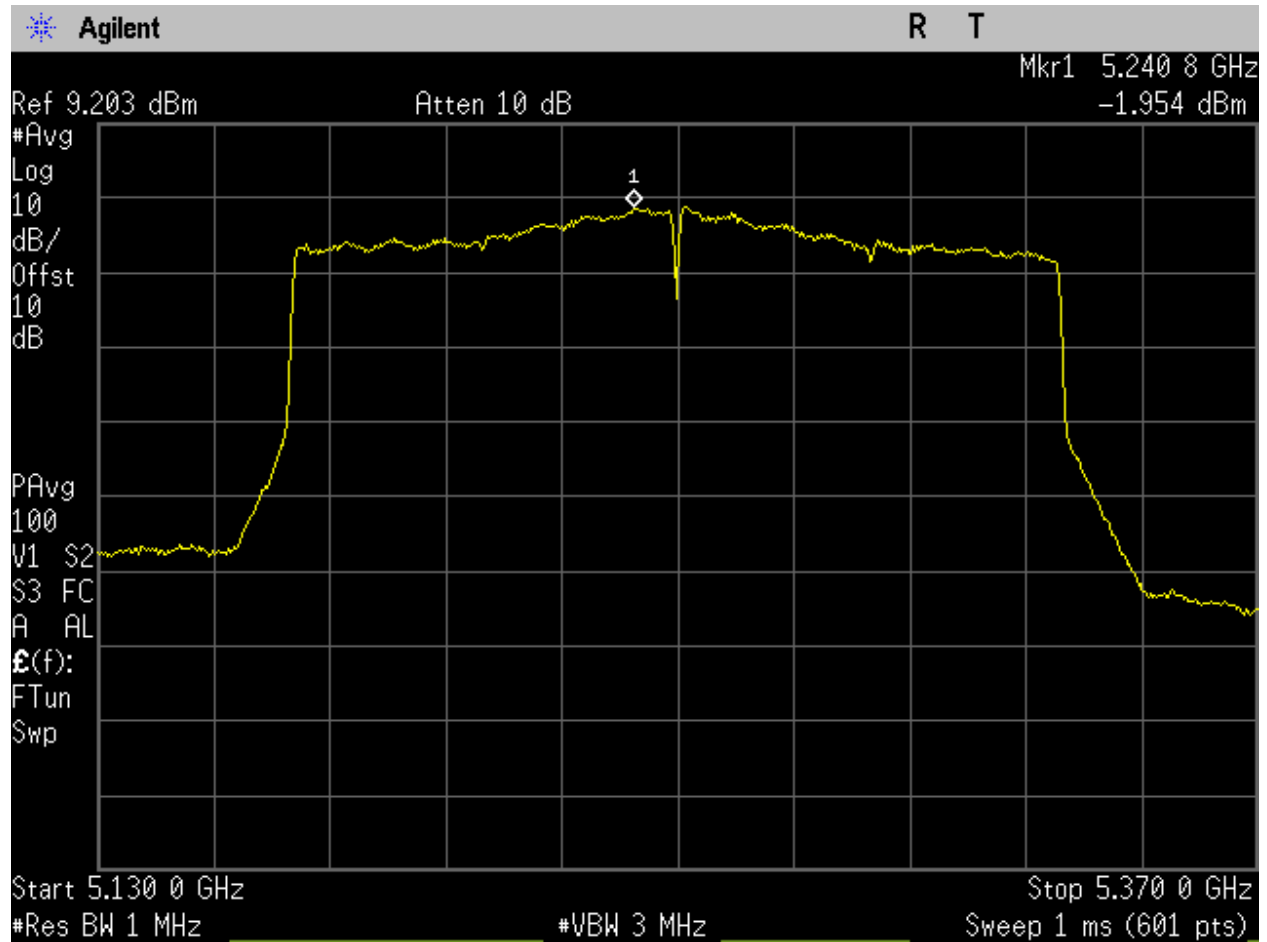


Figure 137: U-NII-2A_5250MHz_High_Mid Ch_50_160MHz BW_ac-mode_Spectral Density_Port 1.

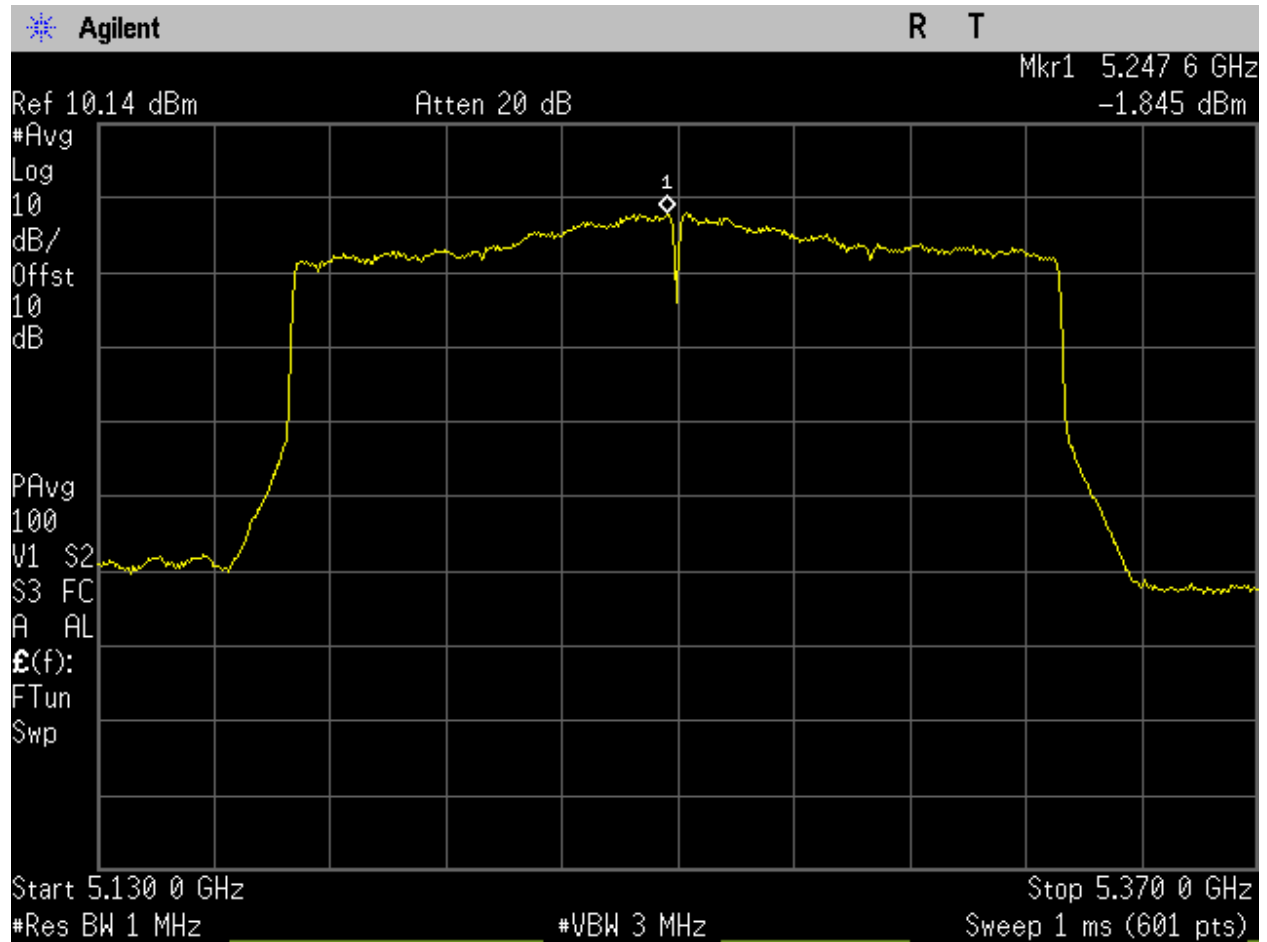


Figure 138: U-NII-2A_5250MHz_High_Mid Ch_50_160MHz BW_ac-mode_Spectral Density_Port 2.

§15.407(b)(1) & (9 – 10) Undesirable Emissions

FCC 15.209 Conducted

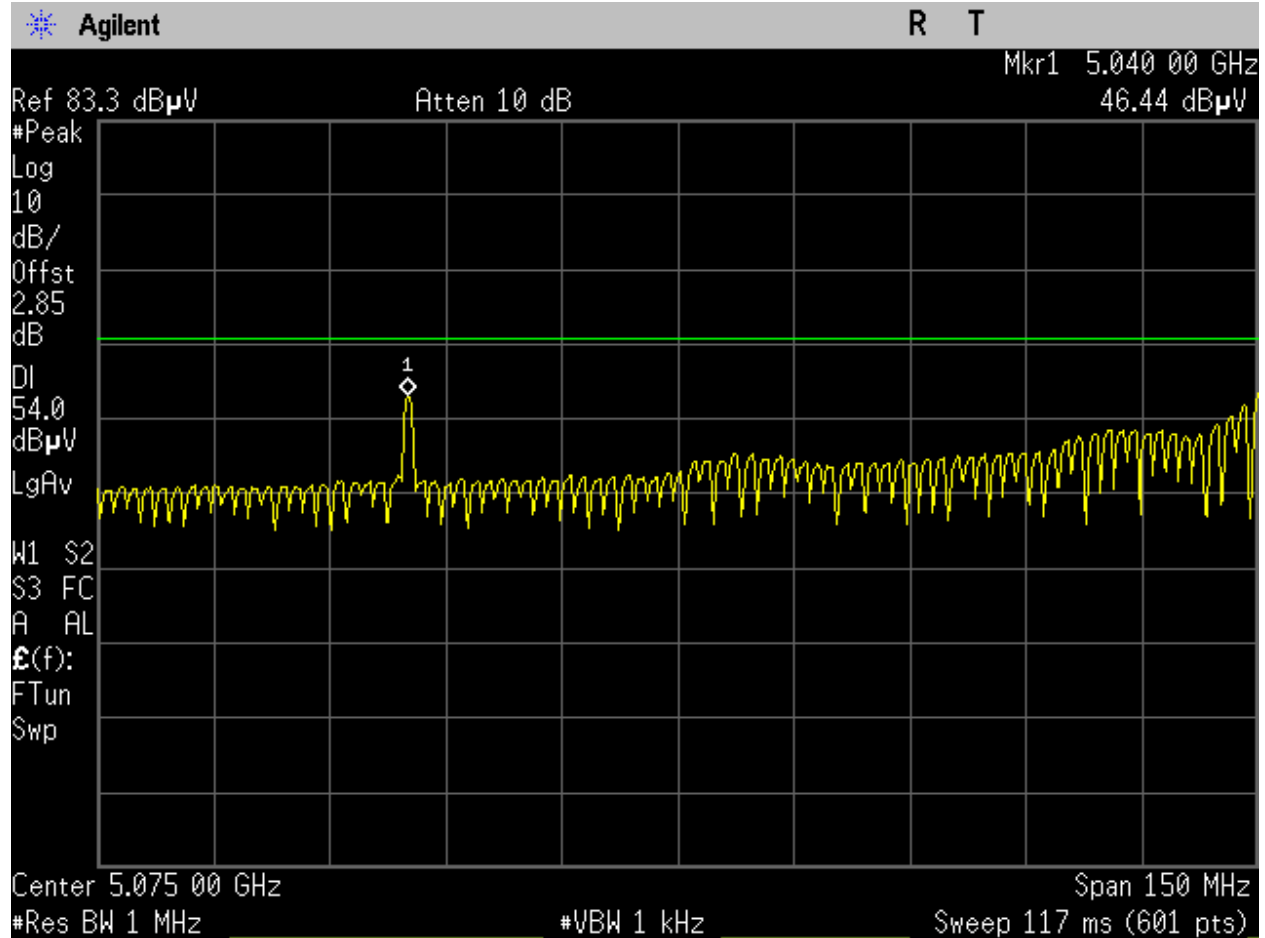


Figure 139: U-NII-1_5180MHz_Low Ch_36_20MHz BW_a-mode_15.209_Lower Band Edge Avg_Port 1.

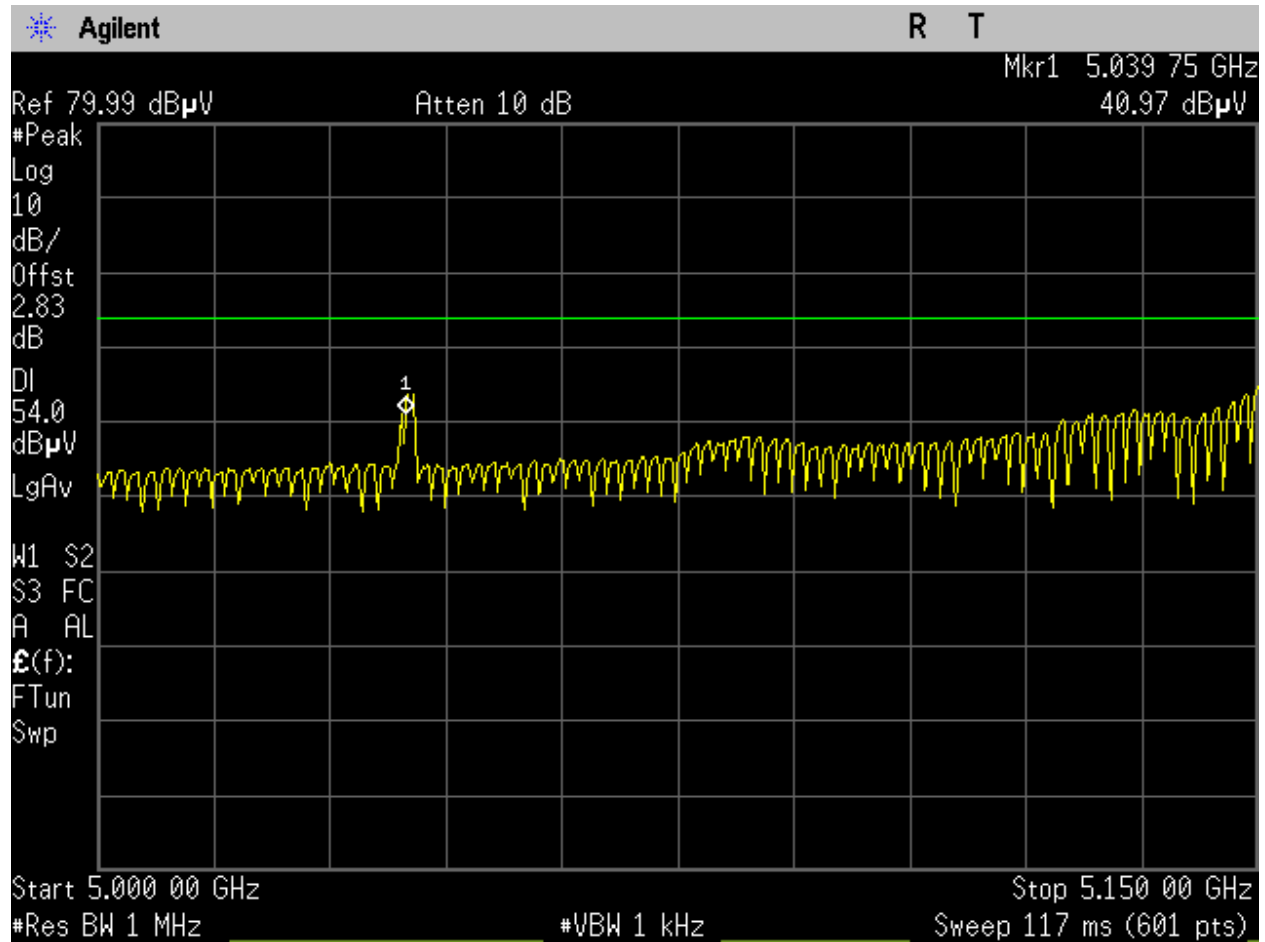


Figure 140: U-NII-1_5180MHz_Low Ch_36_20MHz BW_a-mode_15.209_Lower Band Edge Avg_Port 2.

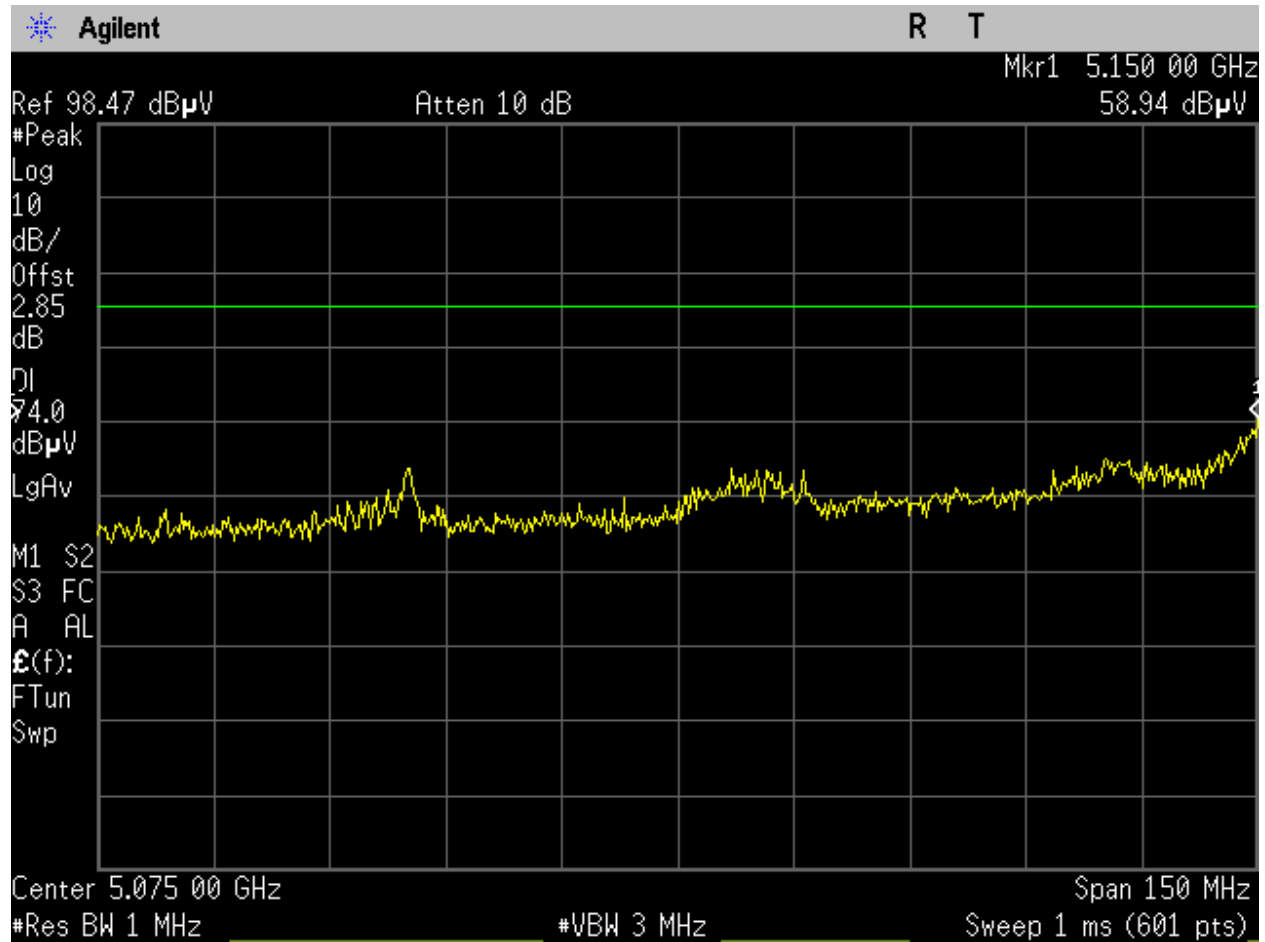


Figure 141: U-NII-1_5180MHz_Low Ch_36_20MHz BW_a-mode_15.209_Lower Band Edge Peak_Port 1.

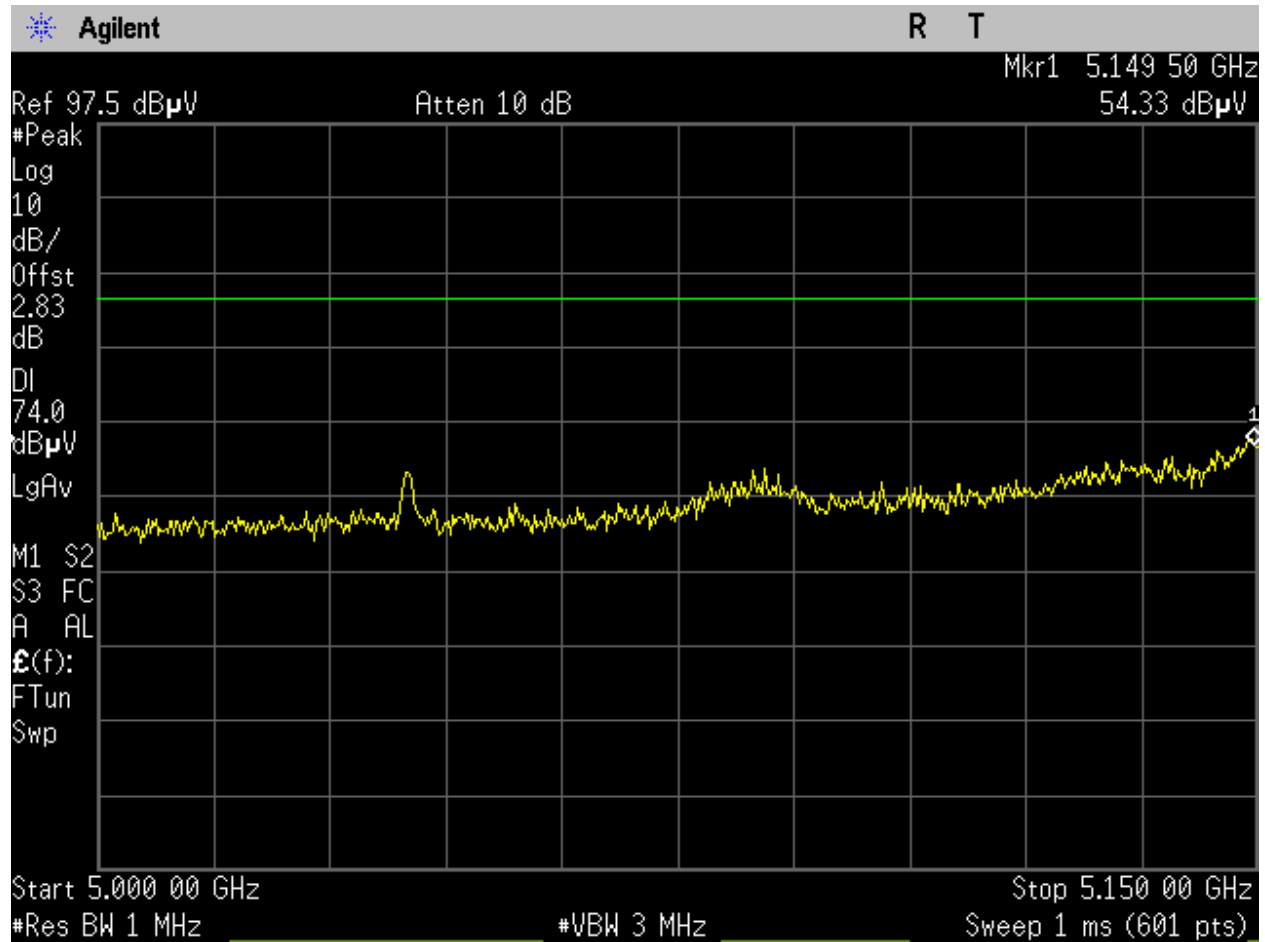


Figure 142: U-NII-1_5180MHz_Low Ch_36_20MHz BW_a-mode_15.209_Lower Band Edge Peak_Port 2.

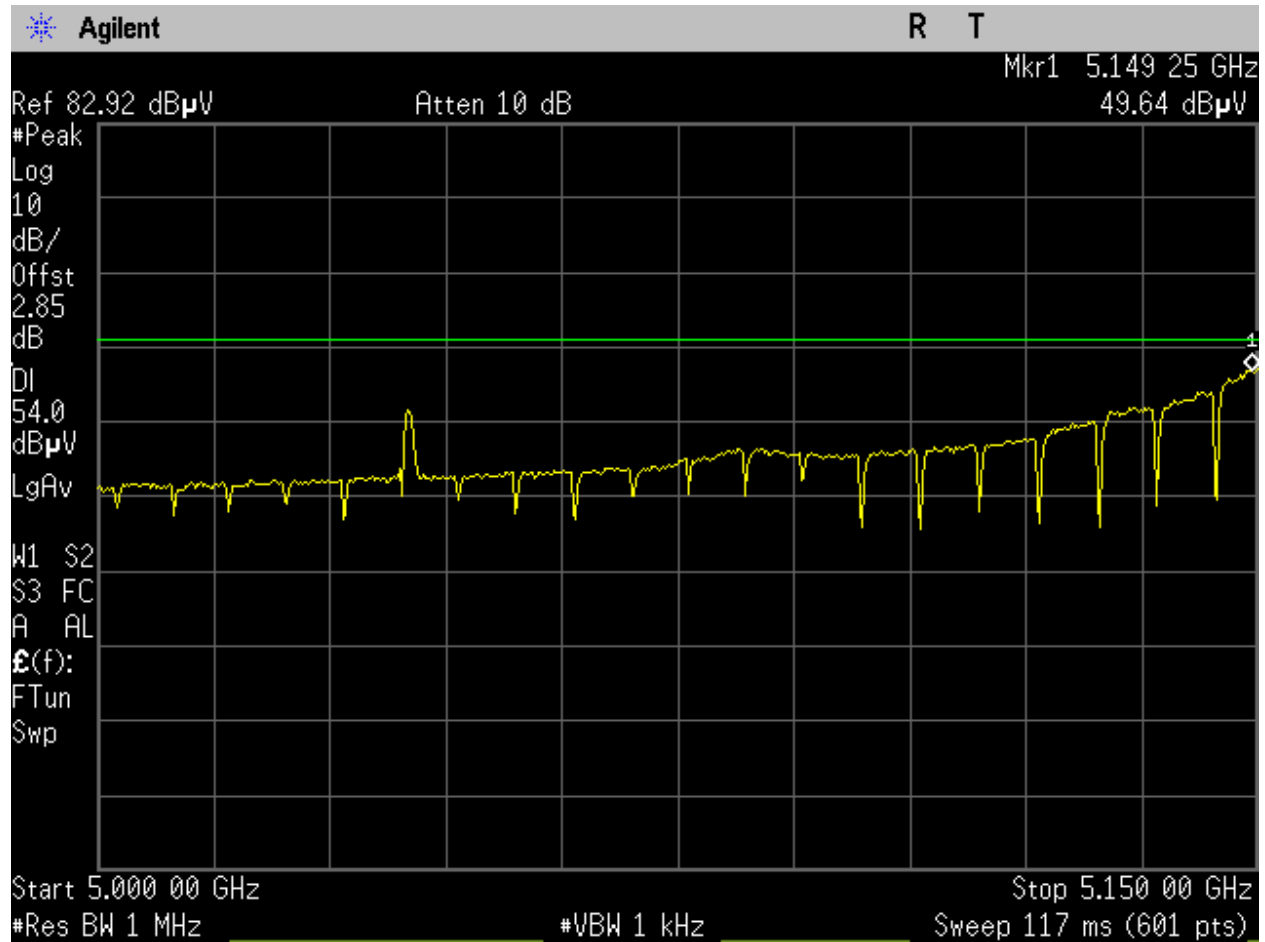


Figure 143: U-NII-1_5180MHz_Low Ch_36_20MHz BW_ac-mode_15.209_Lower Band Edge Avg_Port 1.

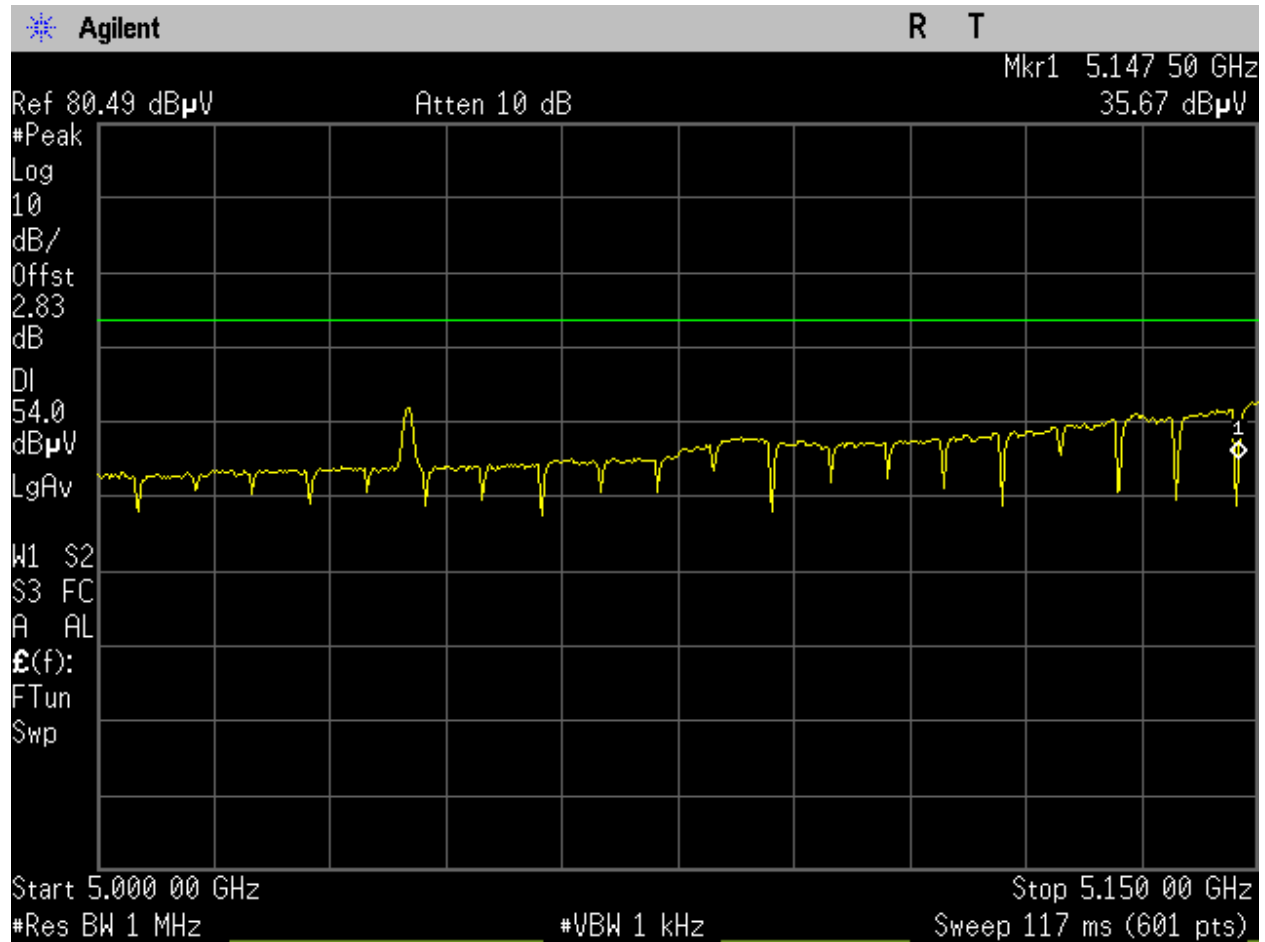


Figure 144: U-NII-1_5180MHz_Low Ch_36_20MHz BW_ac-mode_15.209_Lower Band Edge Avg_Port 2.

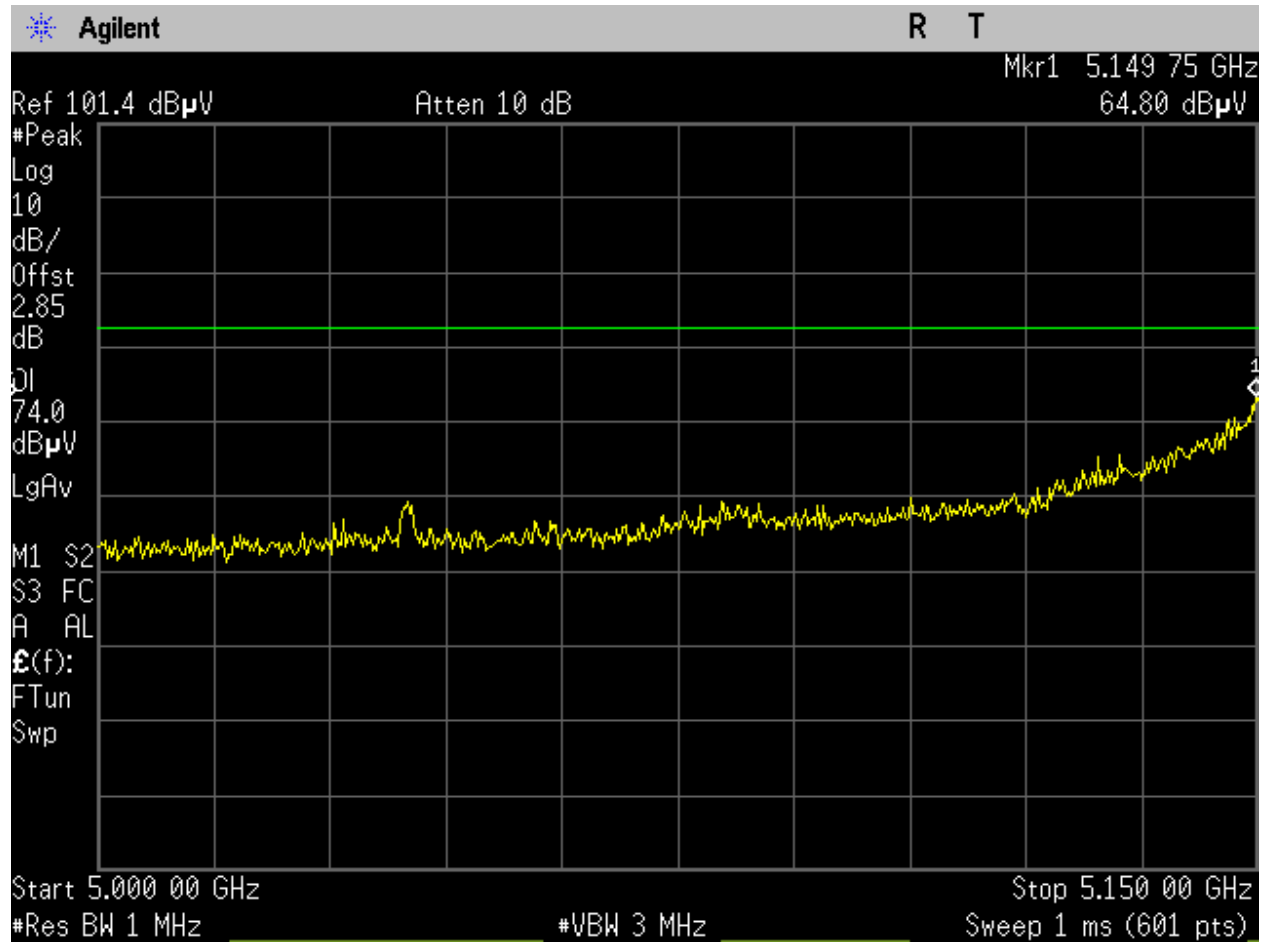


Figure 145: U-NII-1_5180MHz_Low Ch_36_20MHz BW_ac-mode_15.209_Lower Band Edge Peak_Port 1.

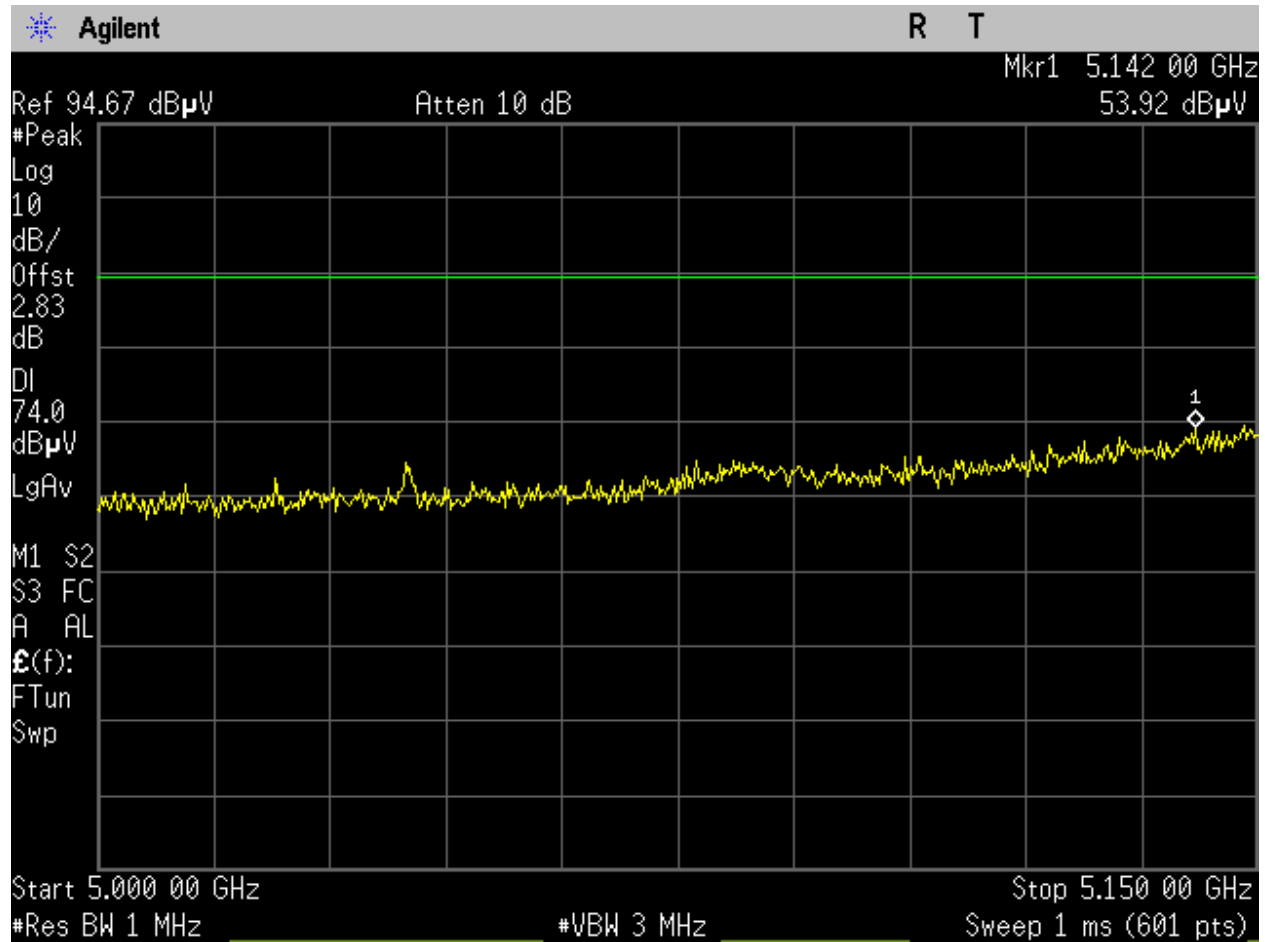


Figure 146: U-NII-1_5180MHz_Low Ch_36_20MHz BW_ac-mode_15.209_Lower Band Edge Peak_Port 2.

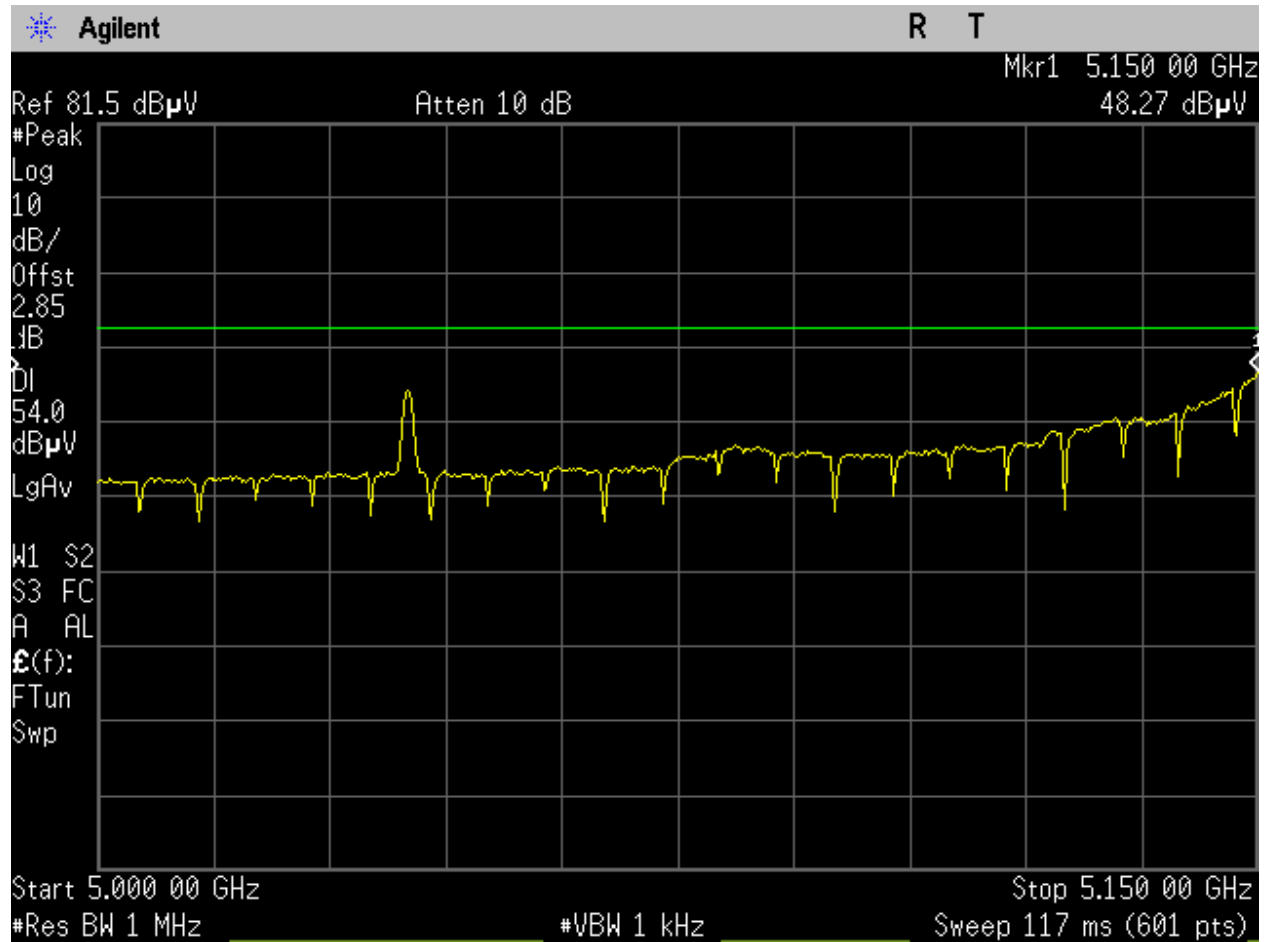


Figure 147: U-NII-1_5180MHz_Low Ch_36_20MHz BW_ax-mode_15.209_Lower Band Edge Avg_Port 1.

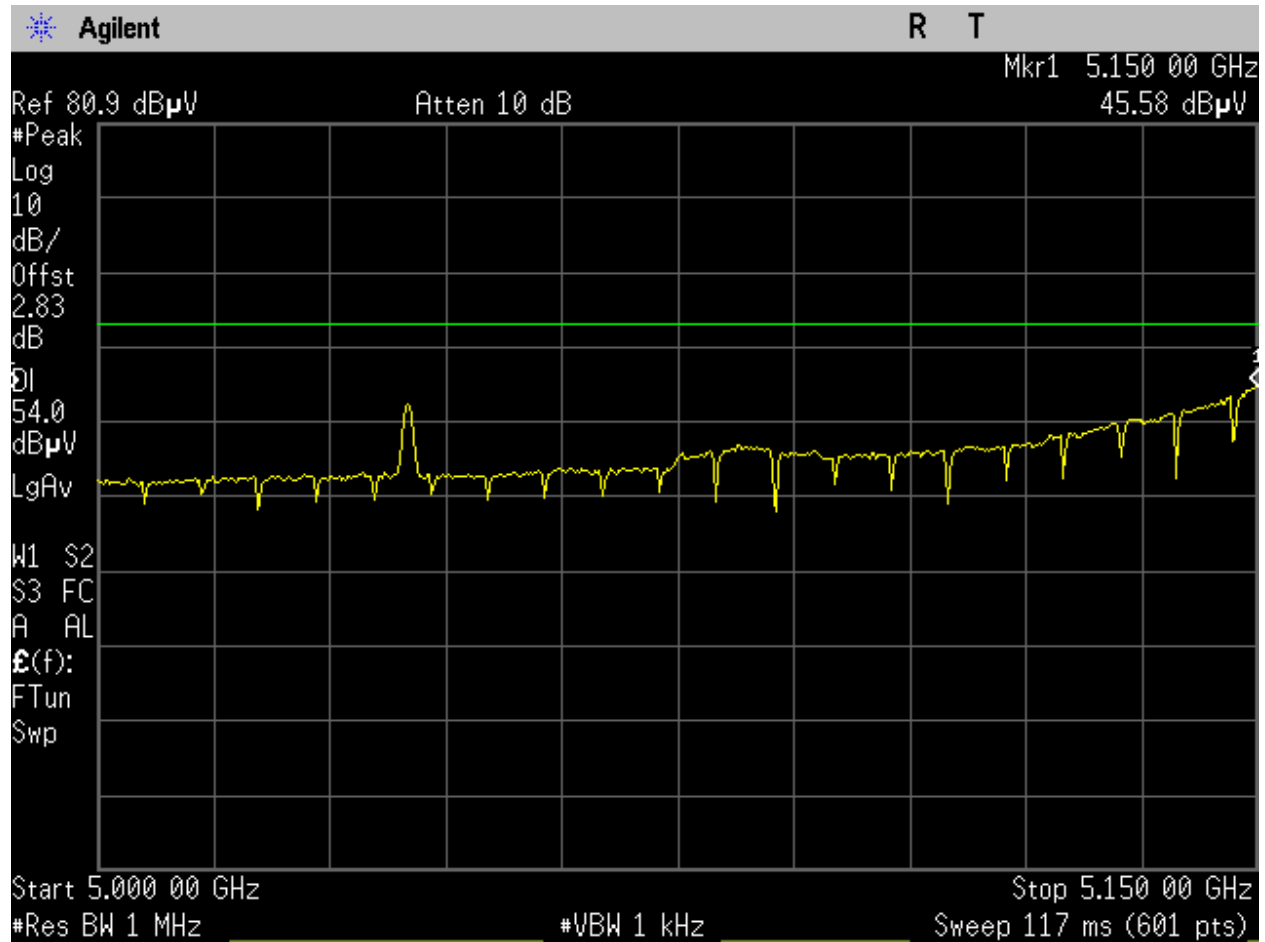


Figure 148: U-NII-1_5180MHz_Low Ch_36_20MHz BW_ax-mode_15.209_Lower Band Edge Avg_Port 2.

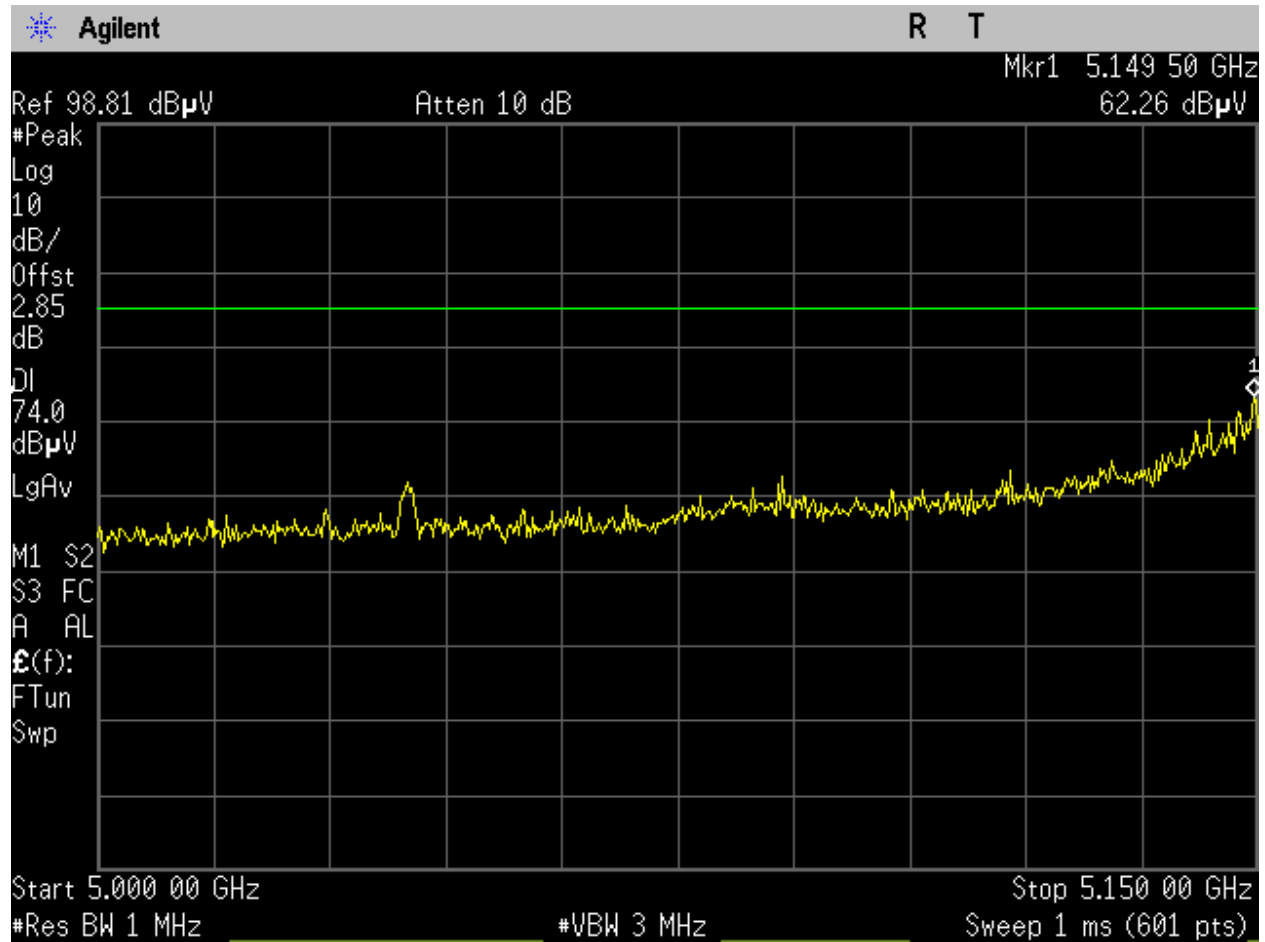


Figure 149: U-NII-1_5180MHz_Low Ch_36_20MHz BW_ax-mode_15.209_Lower Band Edge Peak_Port 1.

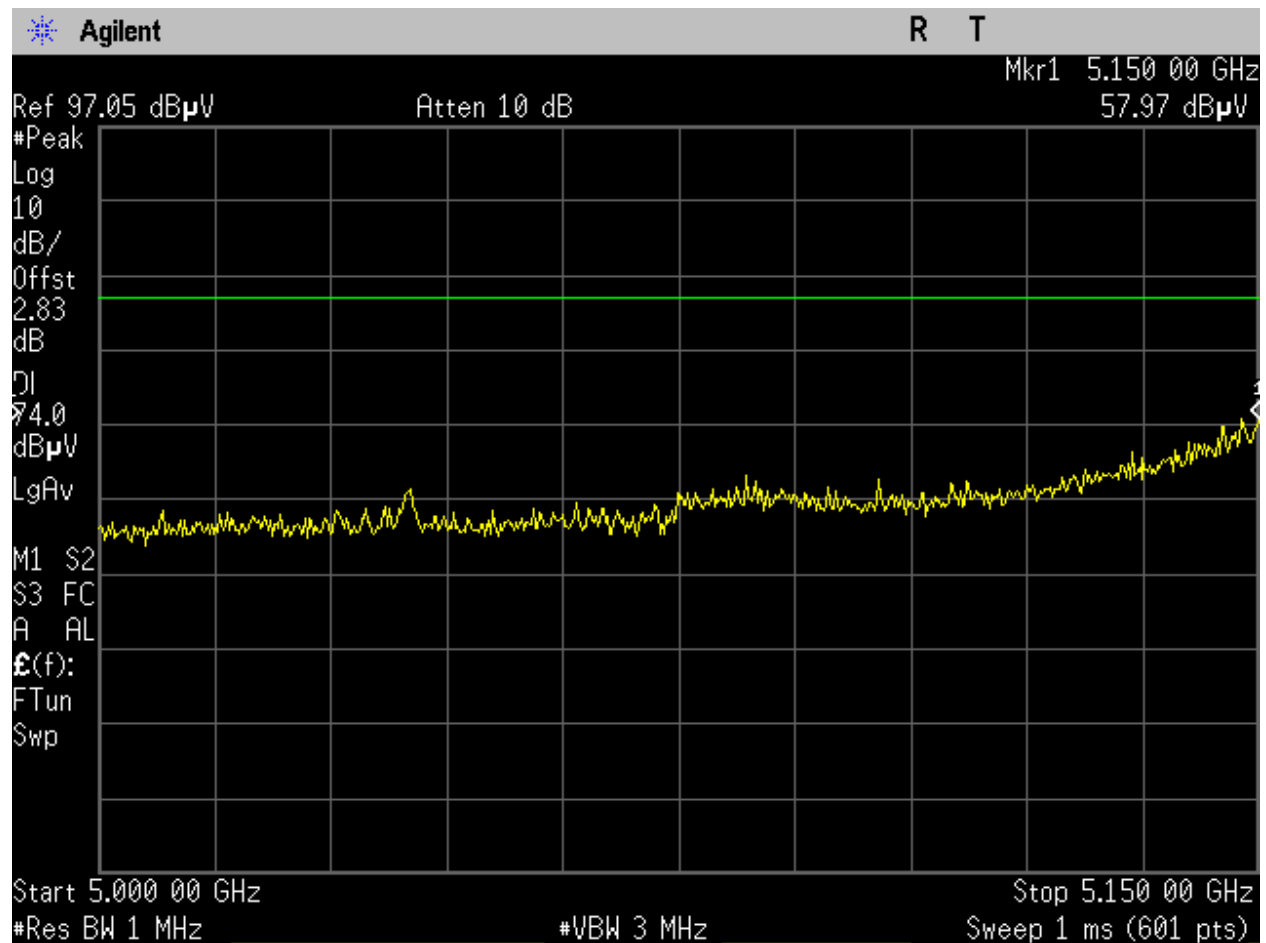


Figure 150: U-NII-1_5180MHz_Low Ch_36_20MHz BW_ax-mode_15.209_Lower Band Edge Peak_Port 2.

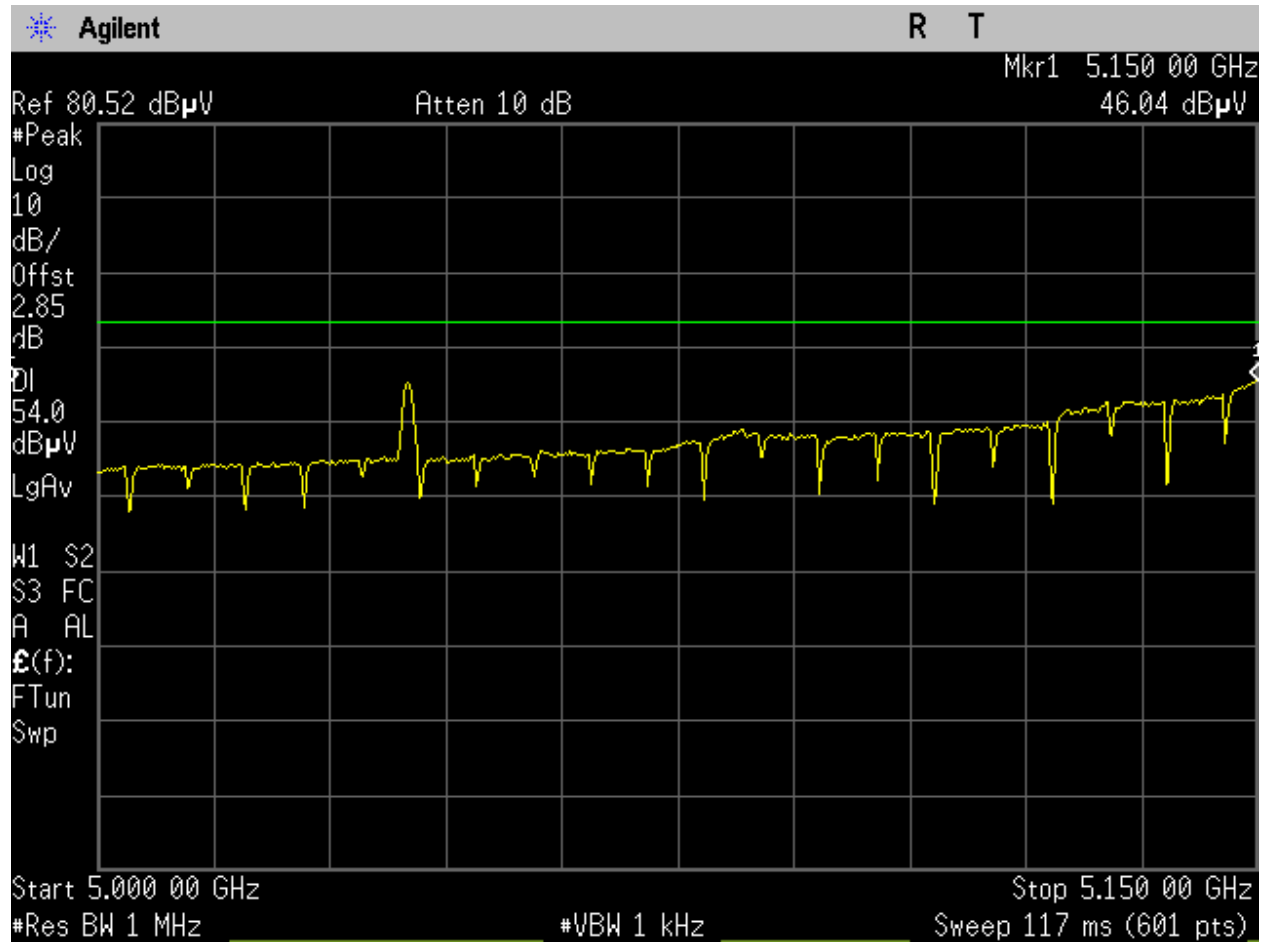


Figure 151: U-NII-1_5180MHz_Low Ch_36_20MHz BW_n-mode_15.209_Lower Band Edge Avg_Port 1.

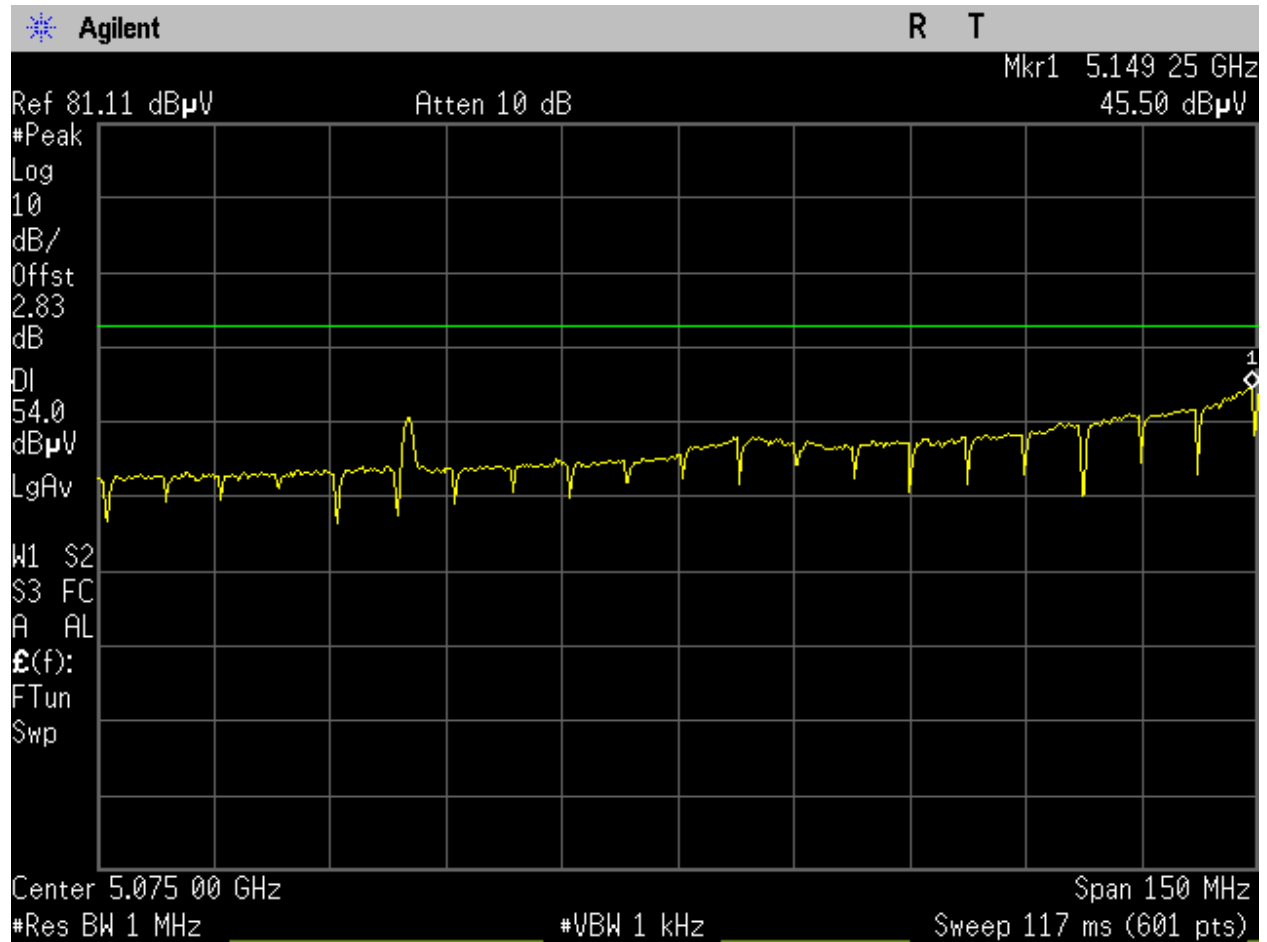


Figure 152: U-NII-1_5180MHz_Low Ch_36_20MHz BW_n-mode_15.209_Lower Band Edge Avg_Port 2.

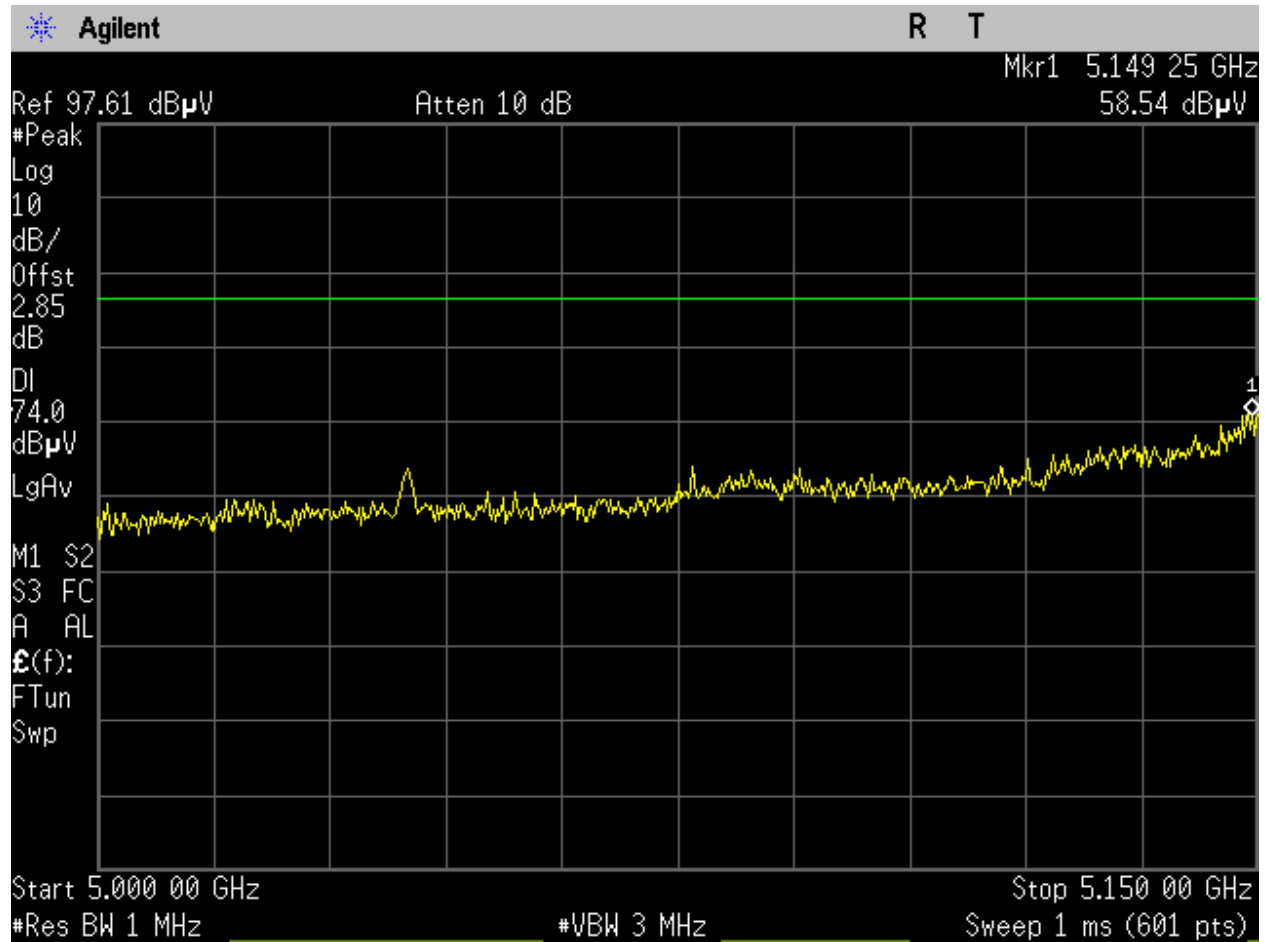


Figure 153: U-NII-1_5180MHz_Low Ch_36_20MHz BW_n-mode_15.209_Lower Band Edge Peak_Port 1.

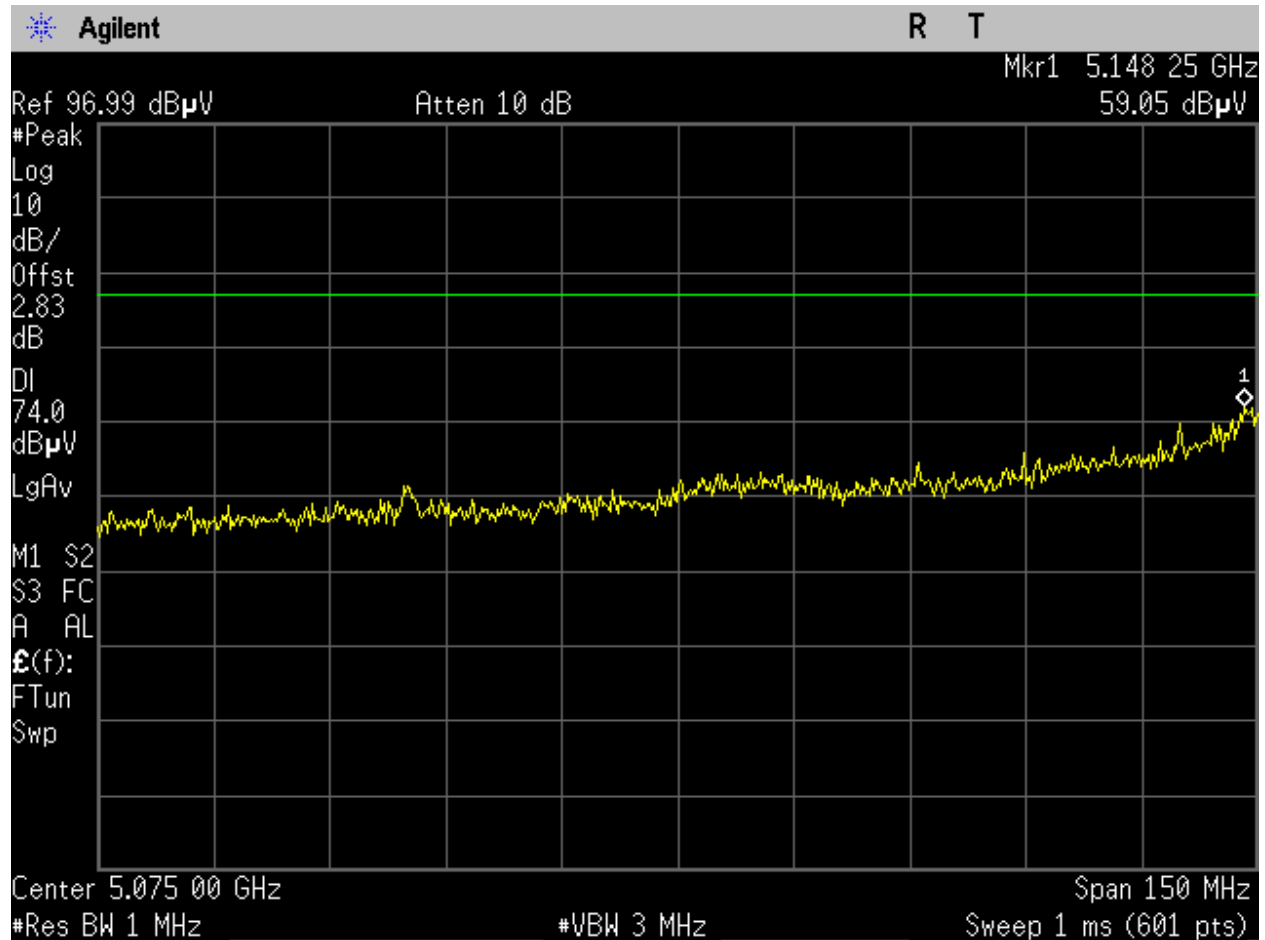


Figure 154: U-NII-1_5180MHz_Low Ch_36_20MHz BW_n-mode_15.209_Lower Band Edge Peak_Port 2.

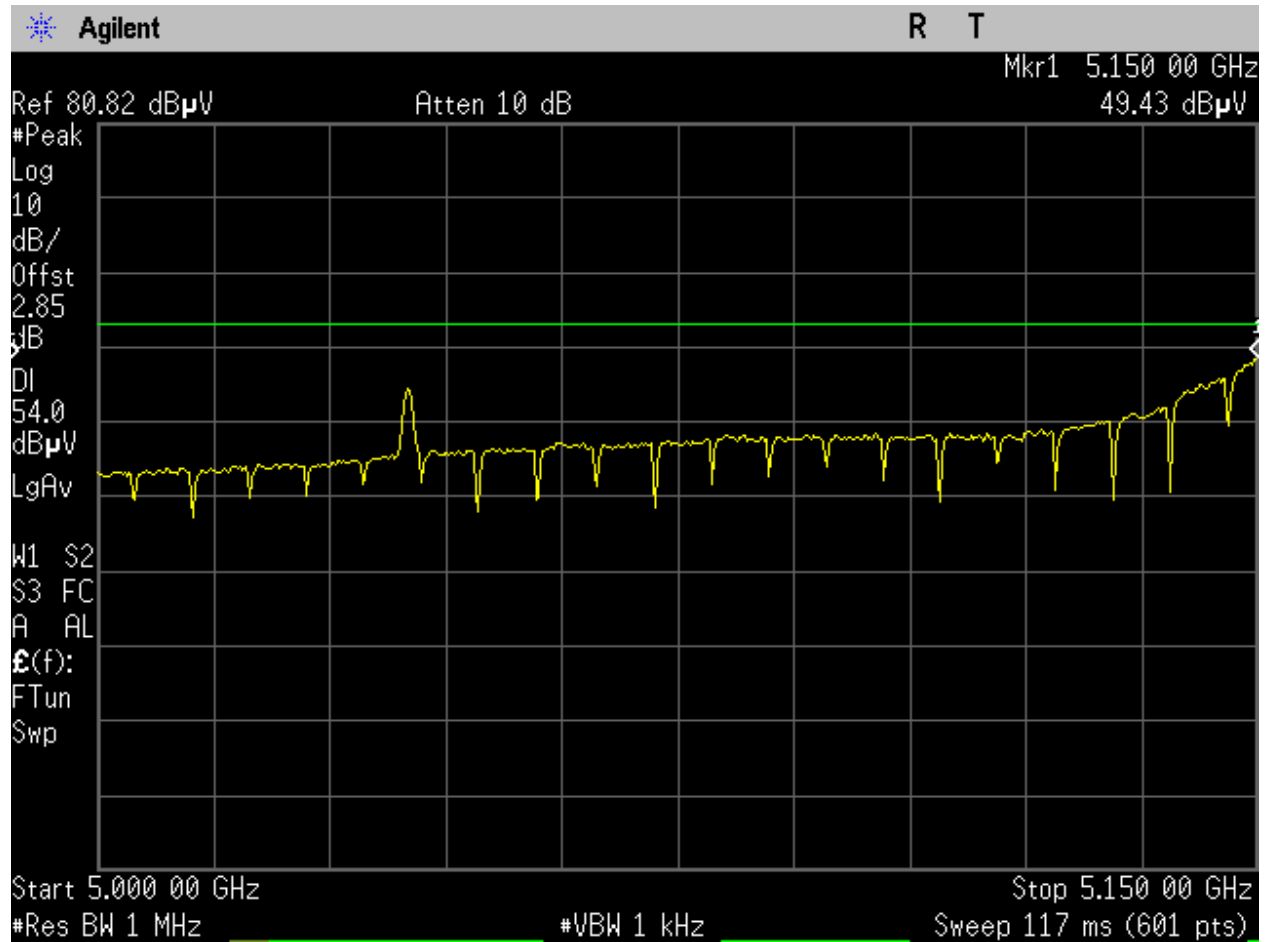


Figure 155: U-NII-1_5190MHz_Low Ch_38_40MHz BW_ac-mode_15.209_Lower Band Edge Avg_Port 1.

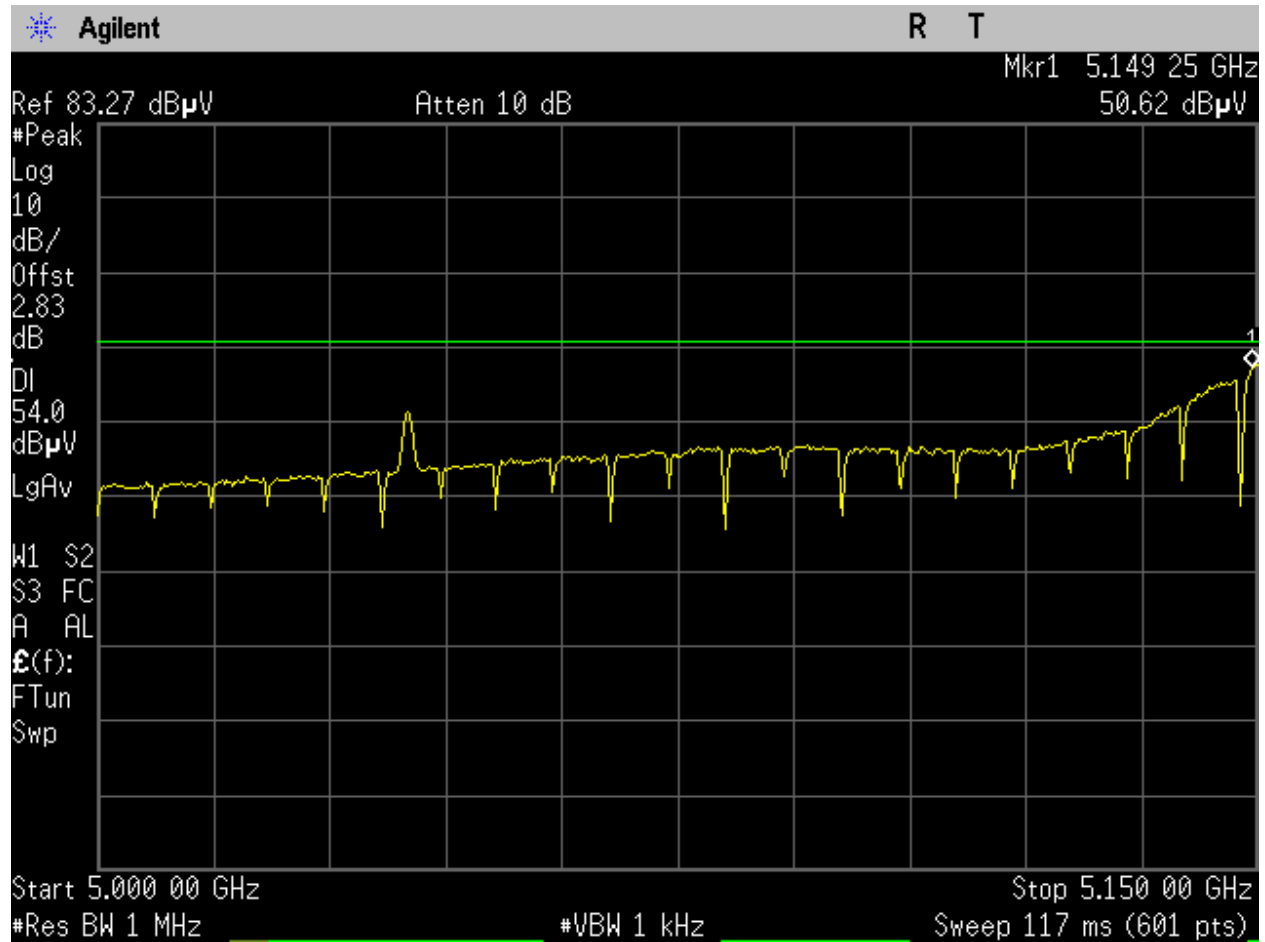


Figure 156: U-NII-1_5190MHz_Low Ch_38_40MHz BW_ac-mode_15.209_Lower Band Edge Avg_Port 2.

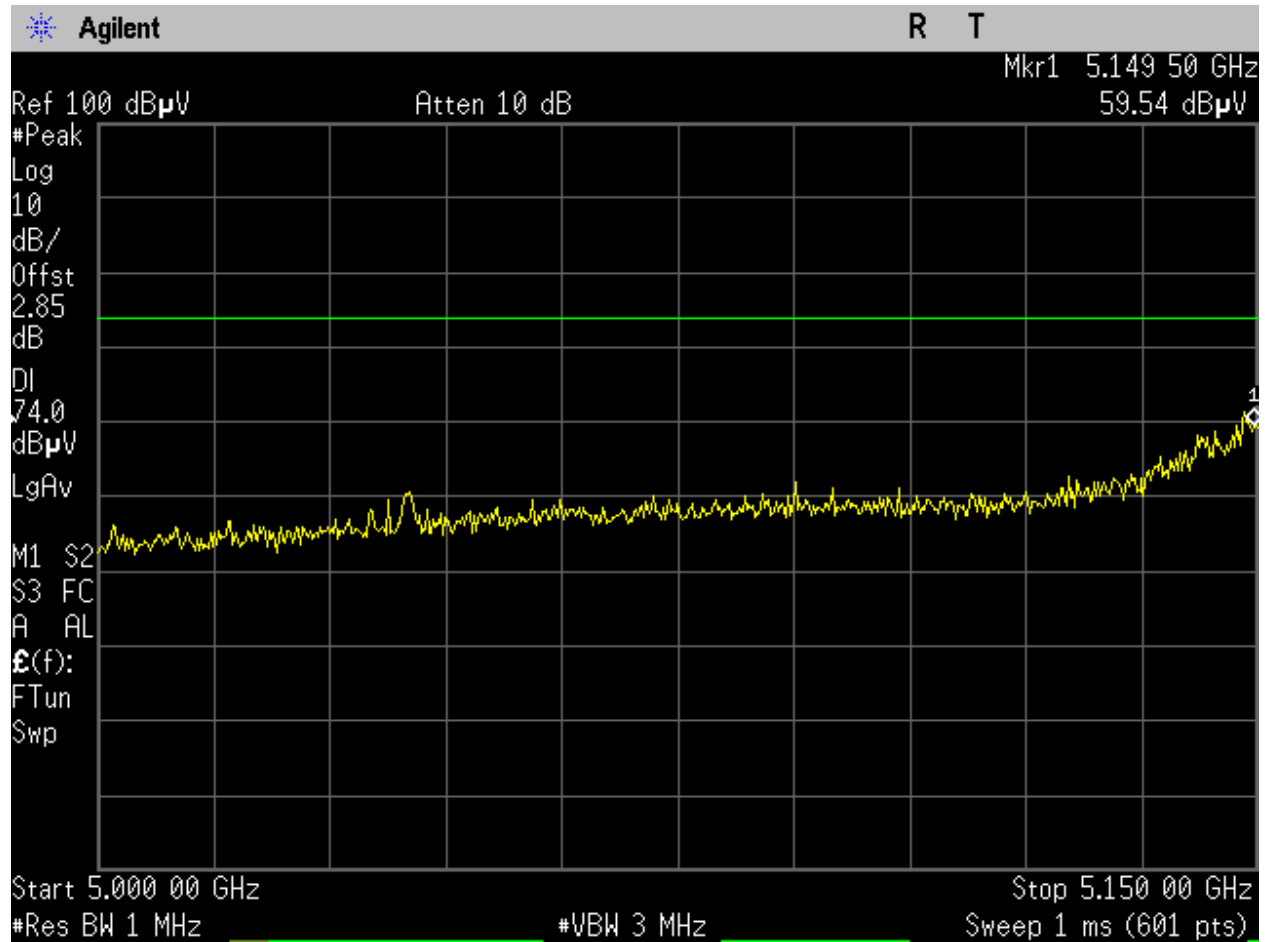


Figure 157: U-NII-1_5190MHz_Low Ch_38_40MHz BW_ac-mode_15.209_Lower Band Edge Peak_Port 1.

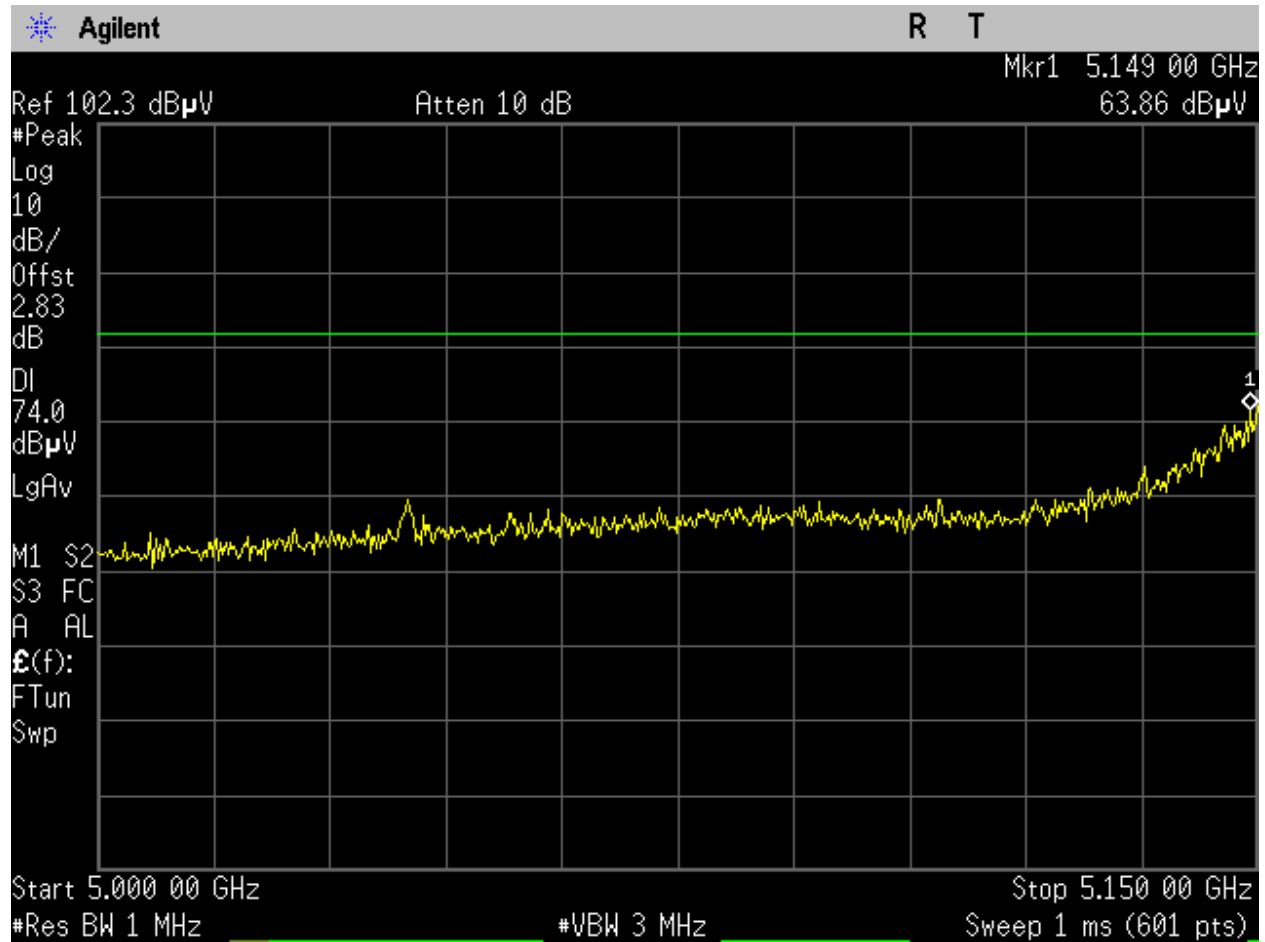


Figure 158: U-NII-1_5190MHz_Low Ch_38_40MHz BW_ac-mode_15.209_Lower Band Edge Peak_Port 2.

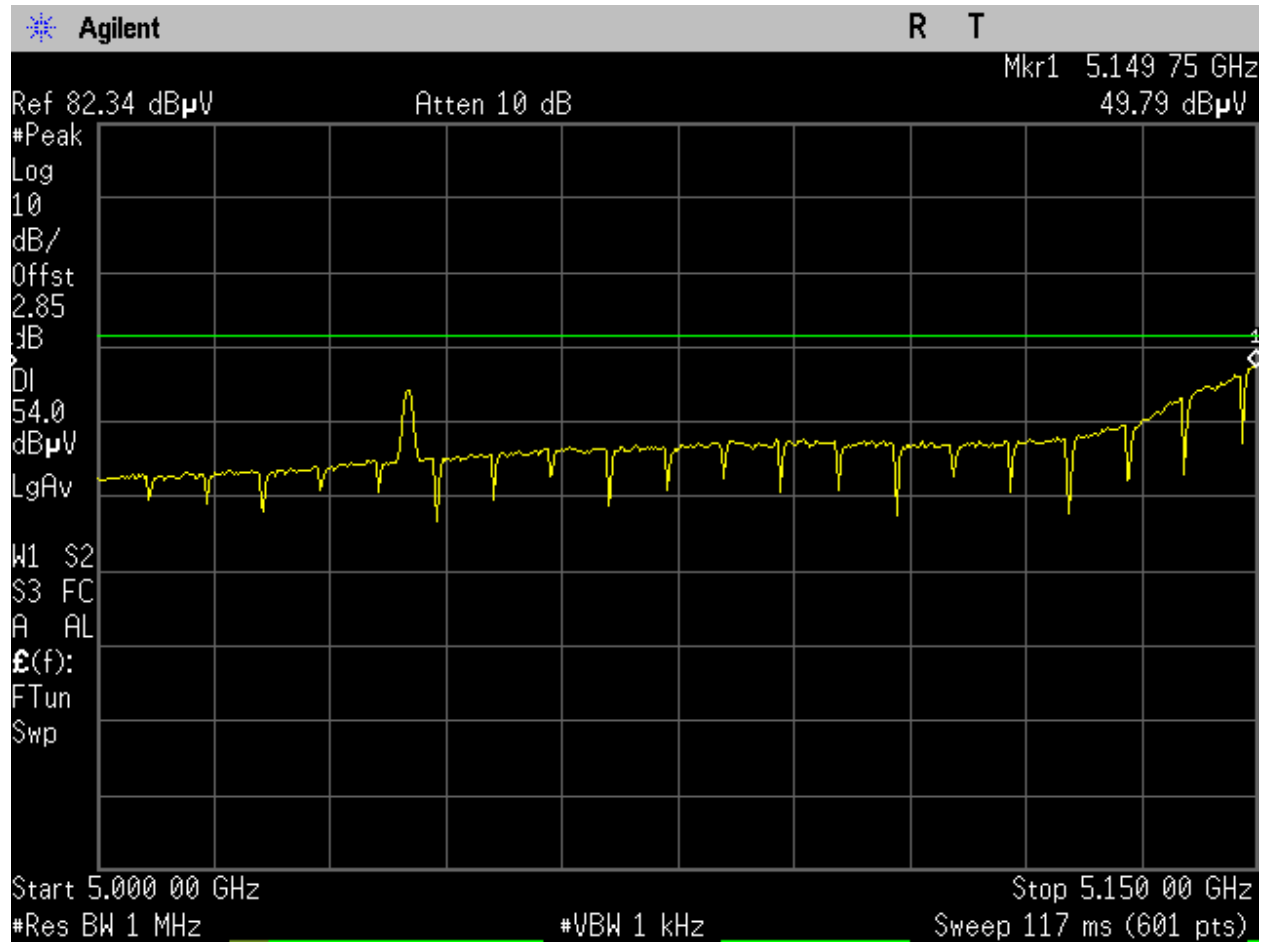


Figure 159: U-NII-1_5190MHz_Low Ch_38_40MHz BW_ax-mode_15.209_Lower Band Edge Avg_Port 1.

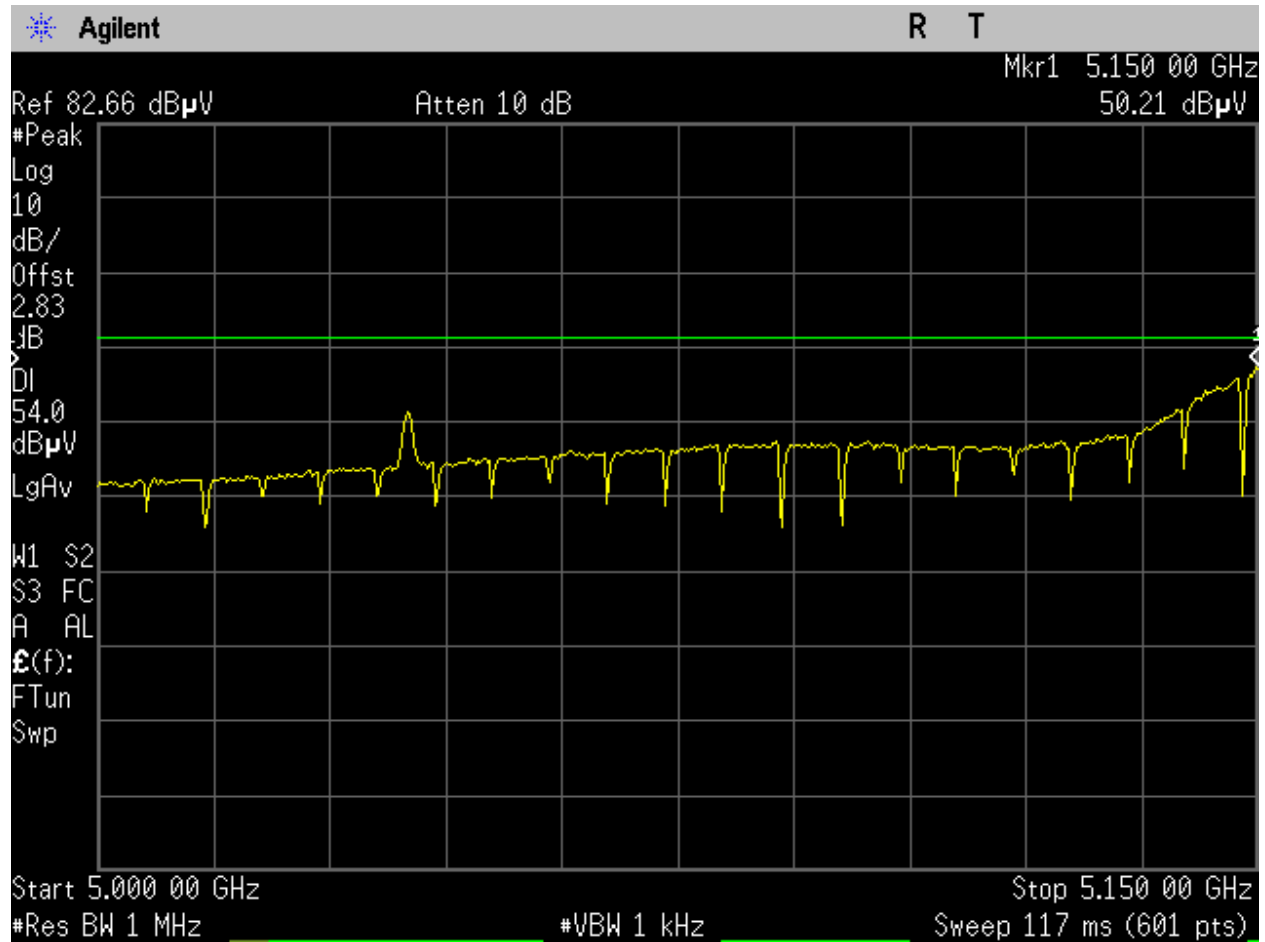


Figure 160: U-NII-1_5190MHz_Low Ch_38_40MHz BW_ax-mode_15.209_Lower Band Edge Avg_Port 2.

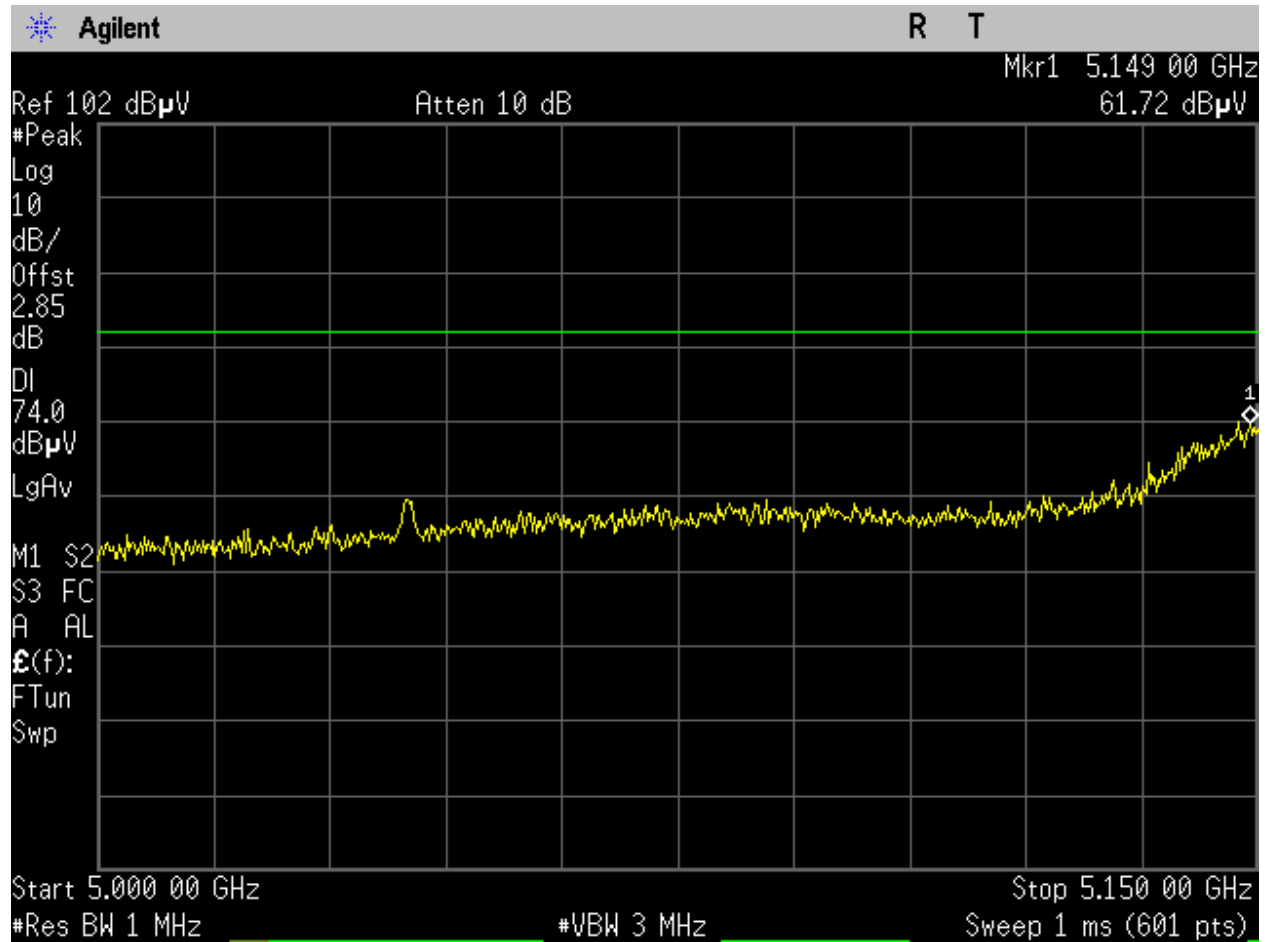


Figure 161: U-NII-1_5190MHz_Low Ch_38_40MHz BW_ax-mode_15.209_Lower Band Edge Peak_Port 1.

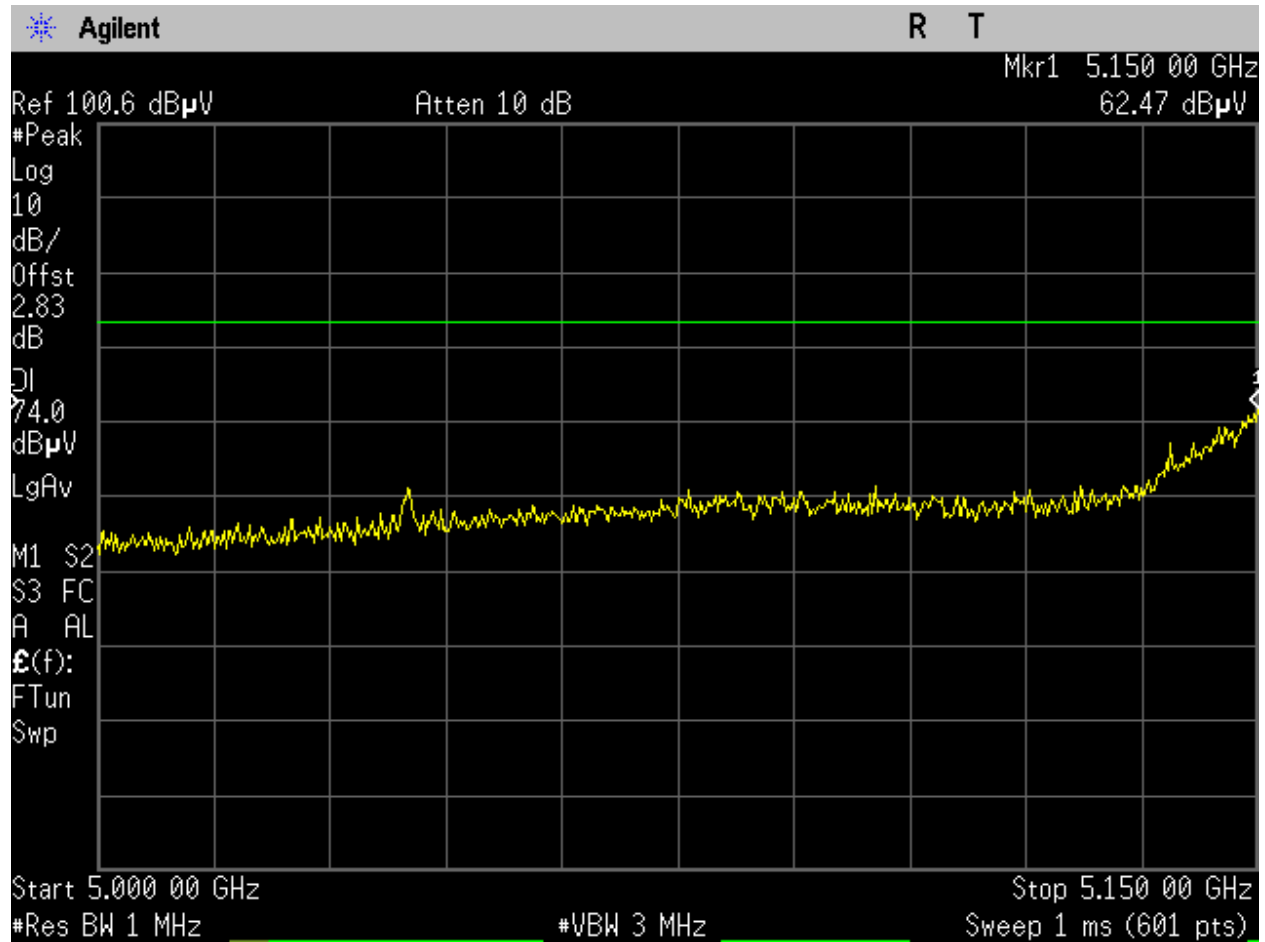


Figure 162: U-NII-1_5190MHz_Low Ch_38_40MHz BW_ax-mode_15.209_Lower Band Edge Peak_Port 2.

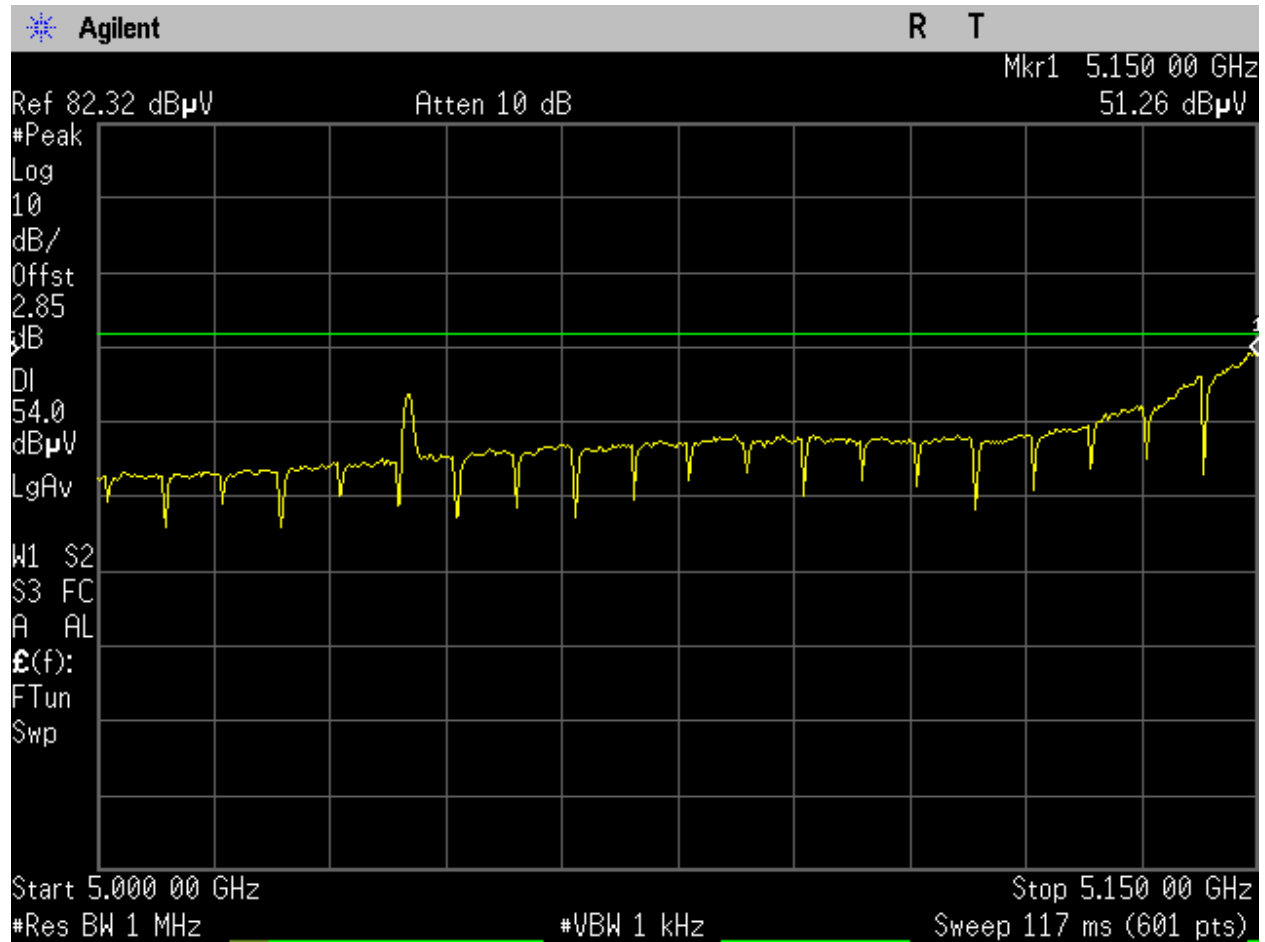


Figure 163: U-NII-1_5190MHz_Low Ch_38_40MHz BW_n-mode_15.209_Lower Band Edge Avg_Port 1.

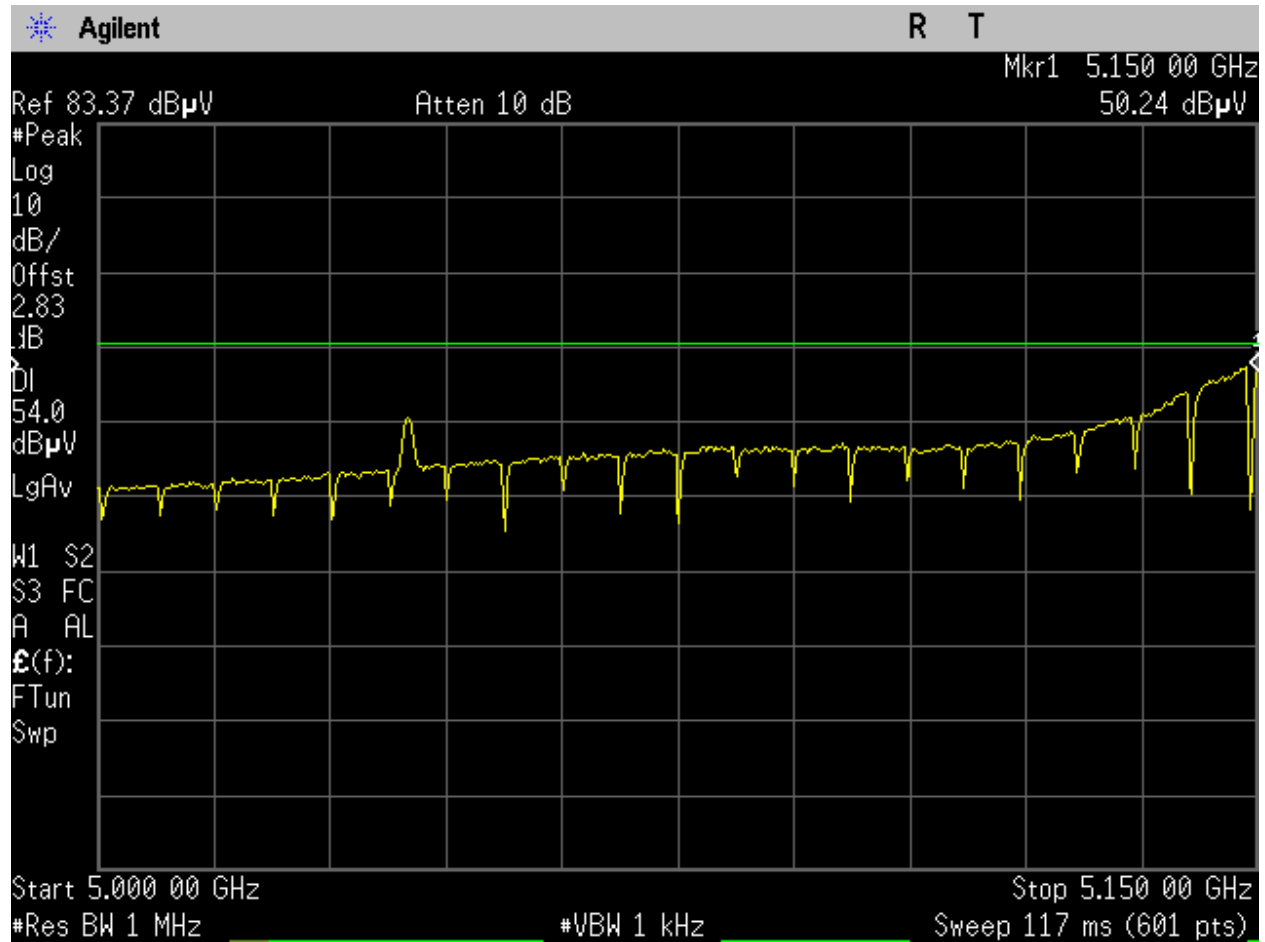


Figure 164: U-NII-1_5190MHz_Low Ch_38_40MHz BW_n-mode_15.209_Lower Band Edge Avg_Port 2.

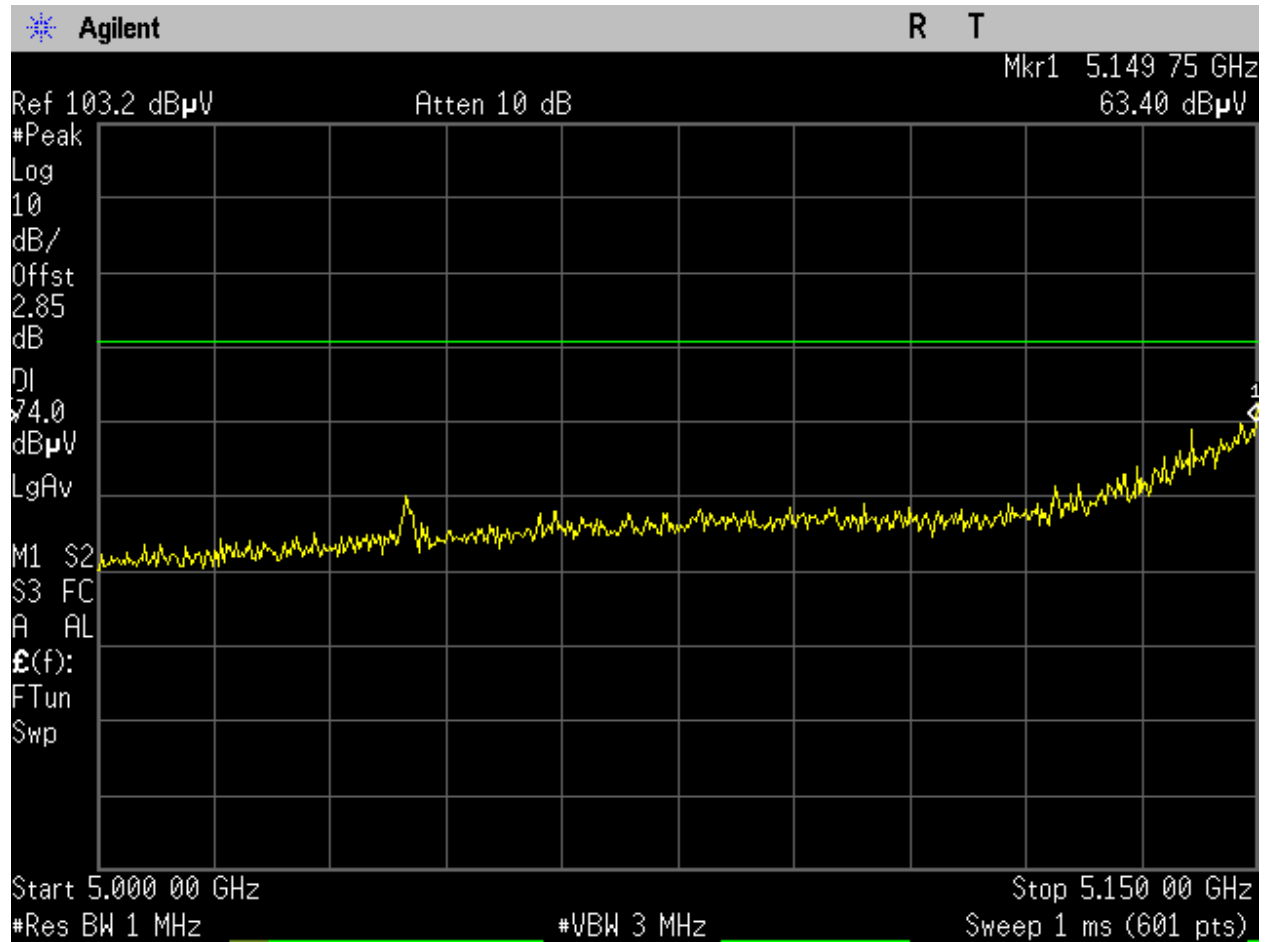


Figure 165: U-NII-1_5190MHz_Low Ch_38_40MHz BW_n-mode_15.209_Lower Band Edge Peak_Port 1.

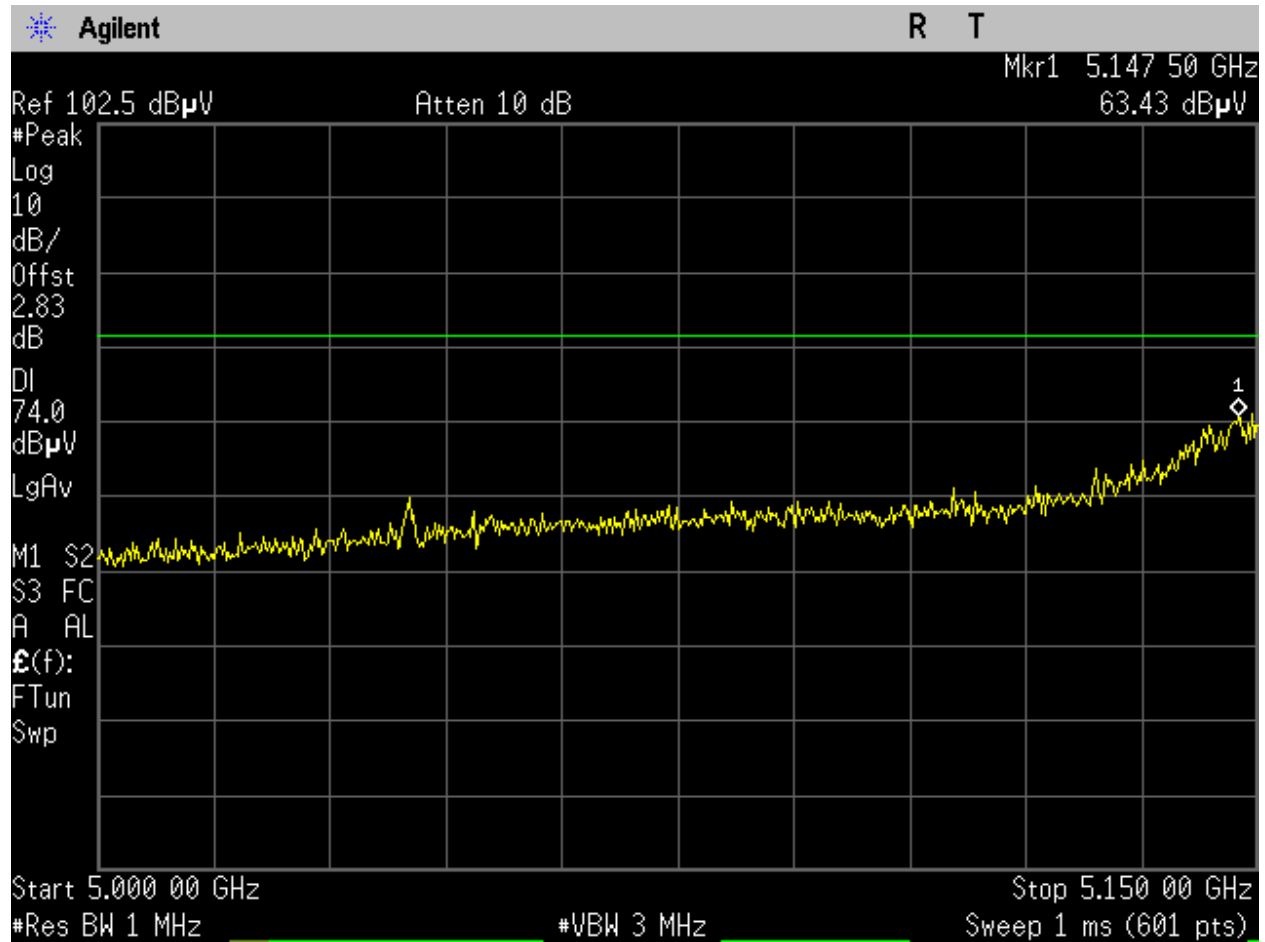


Figure 166: U-NII-1_5190MHz_Low Ch_38_40MHz BW_n-mode_15.209_Lower Band Edge Peak_Port 2.

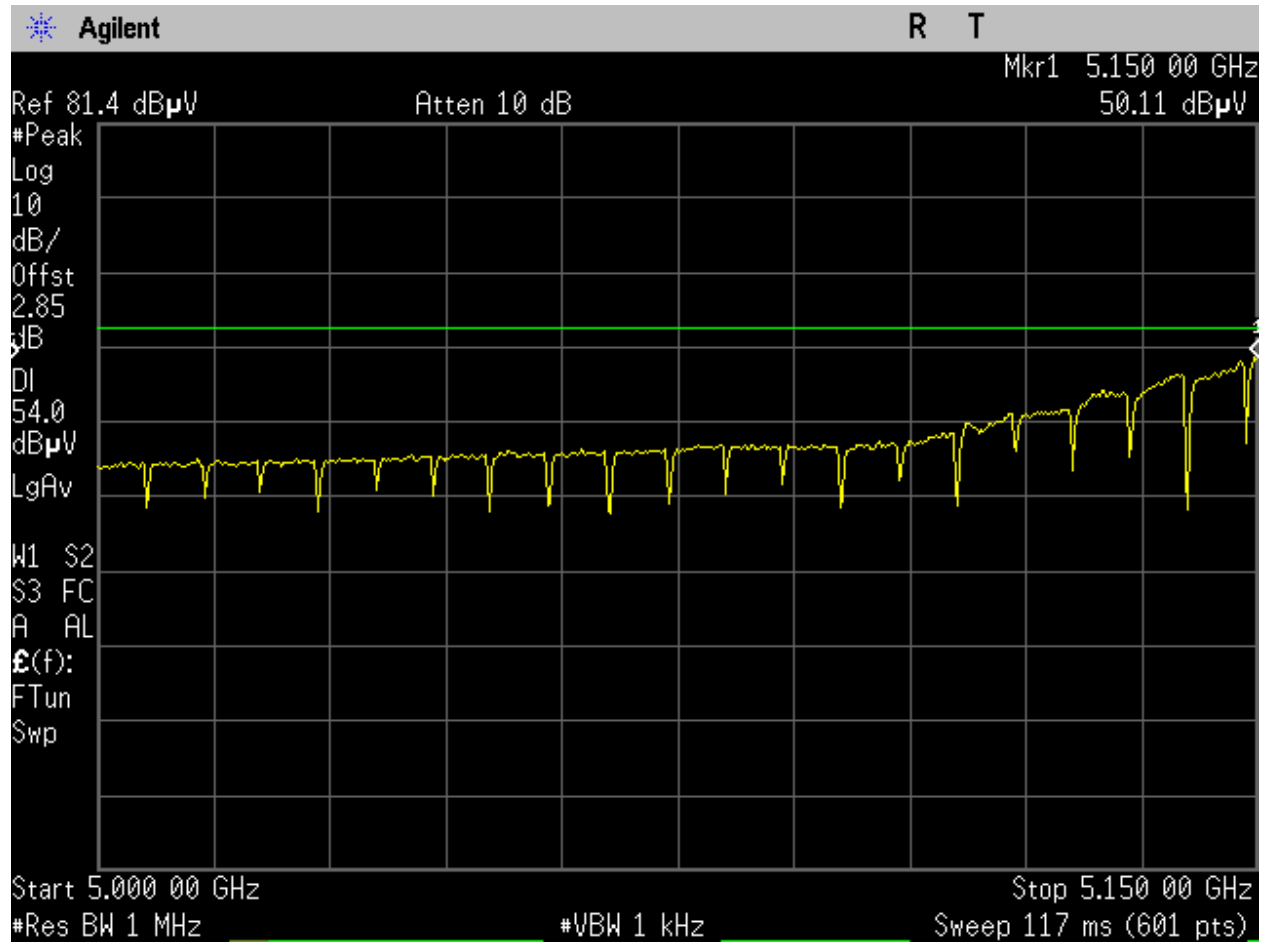


Figure 167: U-NII-1_5210MHz_Low Mid Ch_42_80MHz BW_ac-mode_15.209_Lower Band Edge Avg_Port 1.

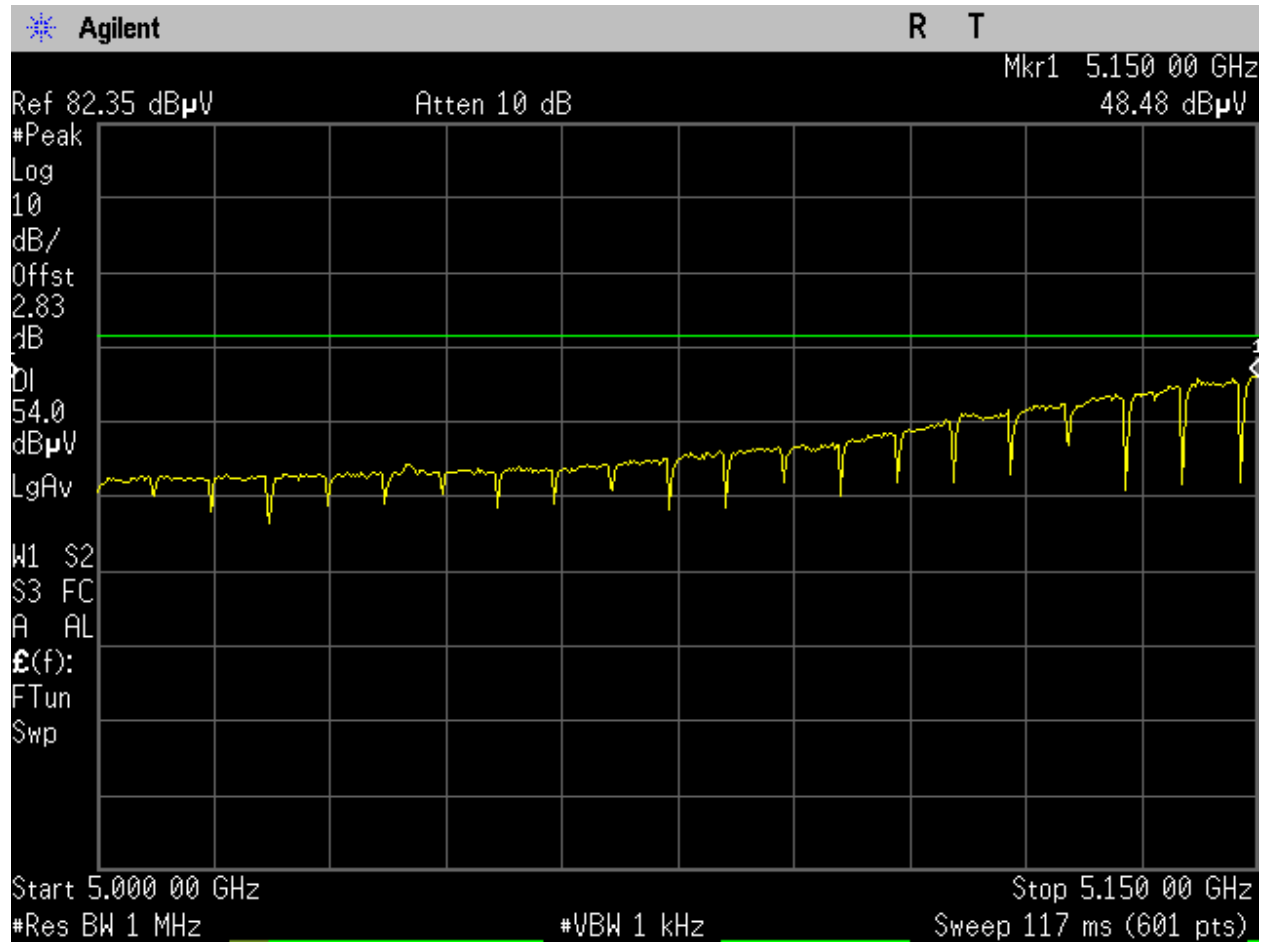


Figure 168: U-NII-1_5210MHz_Low Mid Ch_42_80MHz BW_ac-mode_15.209_Lower Band Edge Avg_Port 2.

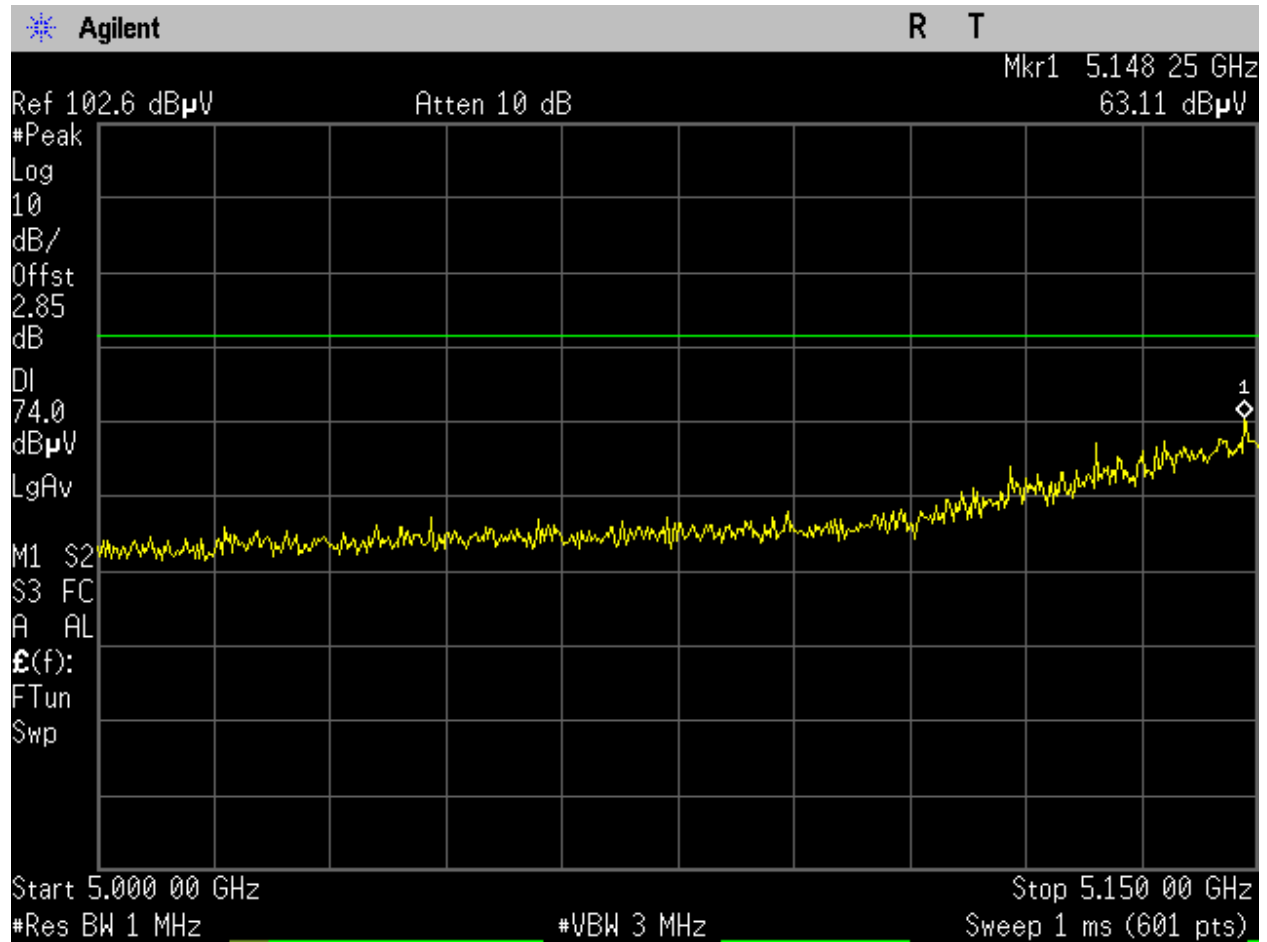


Figure 169: U-NII-1_5210MHz_Low Mid Ch_42_80MHz BW_ac-mode_15.209_Lower Band Edge Peak_Port 1.

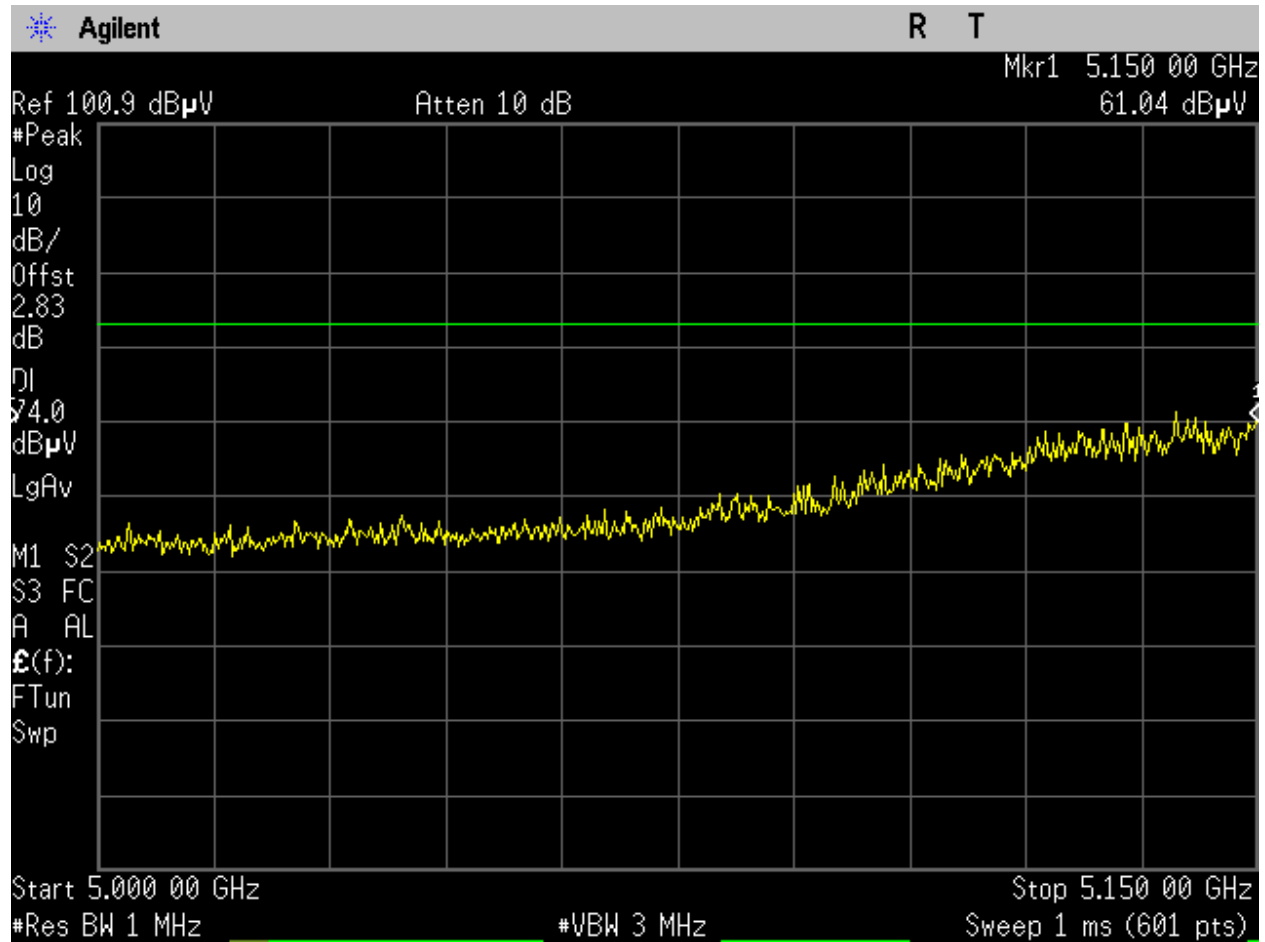


Figure 170: U-NII-1_5210MHz_Low Mid Ch_42_80MHz BW_ac-mode_15.209_Lower Band Edge Peak_Port 2.

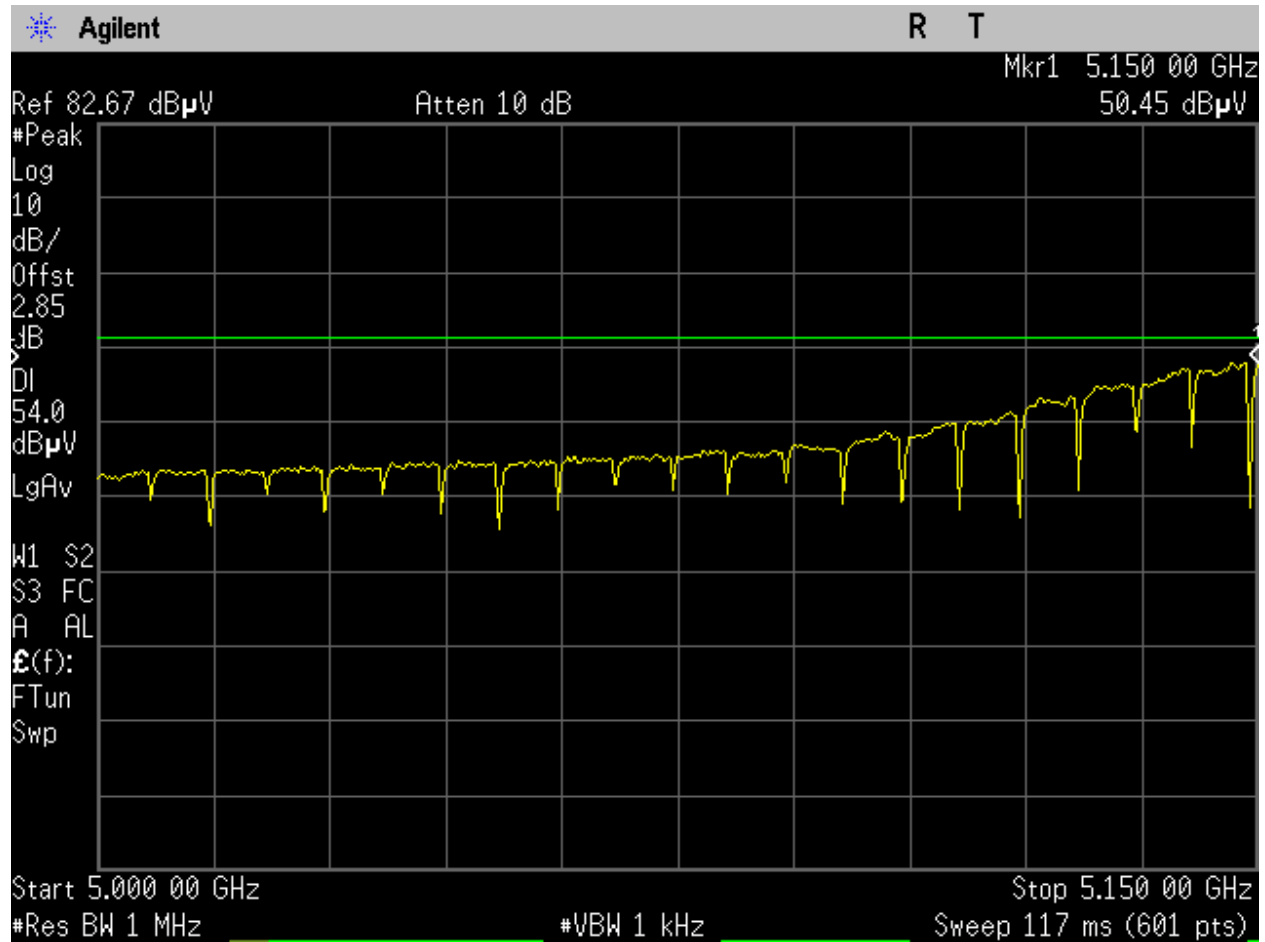


Figure 171: U-NII-1_5210MHz_Low Mid Ch_42_80MHz BW_ax-mode_15.209_Lower Band Edge Avg_Port 1.

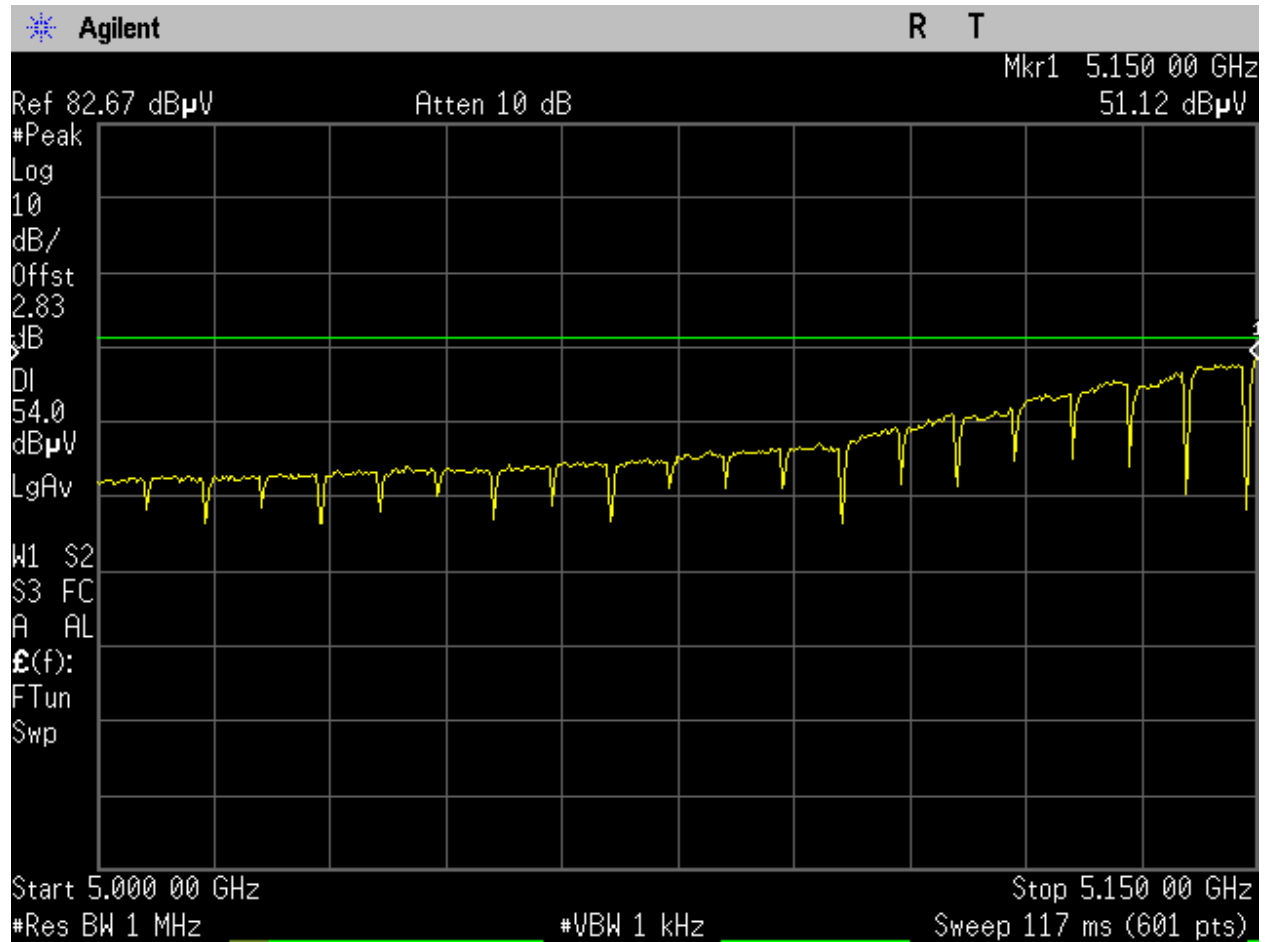


Figure 172: U-NII-1_5210MHz_Low Mid Ch_42_80MHz BW_ax-mode_15.209_Lower Band Edge Avg_Port 2.

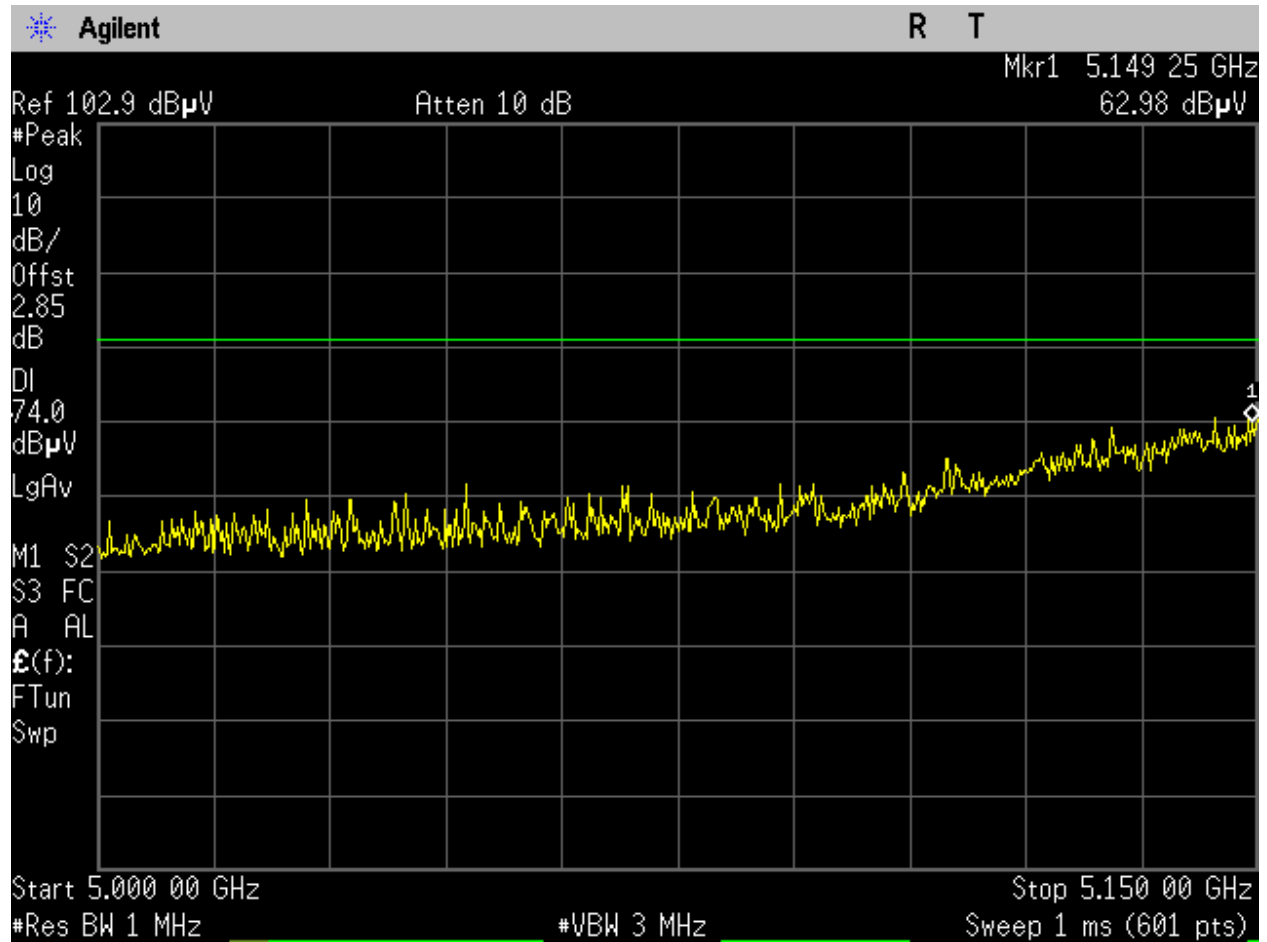


Figure 173: U-NII-1_5210MHz_Low Mid Ch_42_80MHz BW_ax-mode_15.209_Lower Band Edge Peak_Port 1.

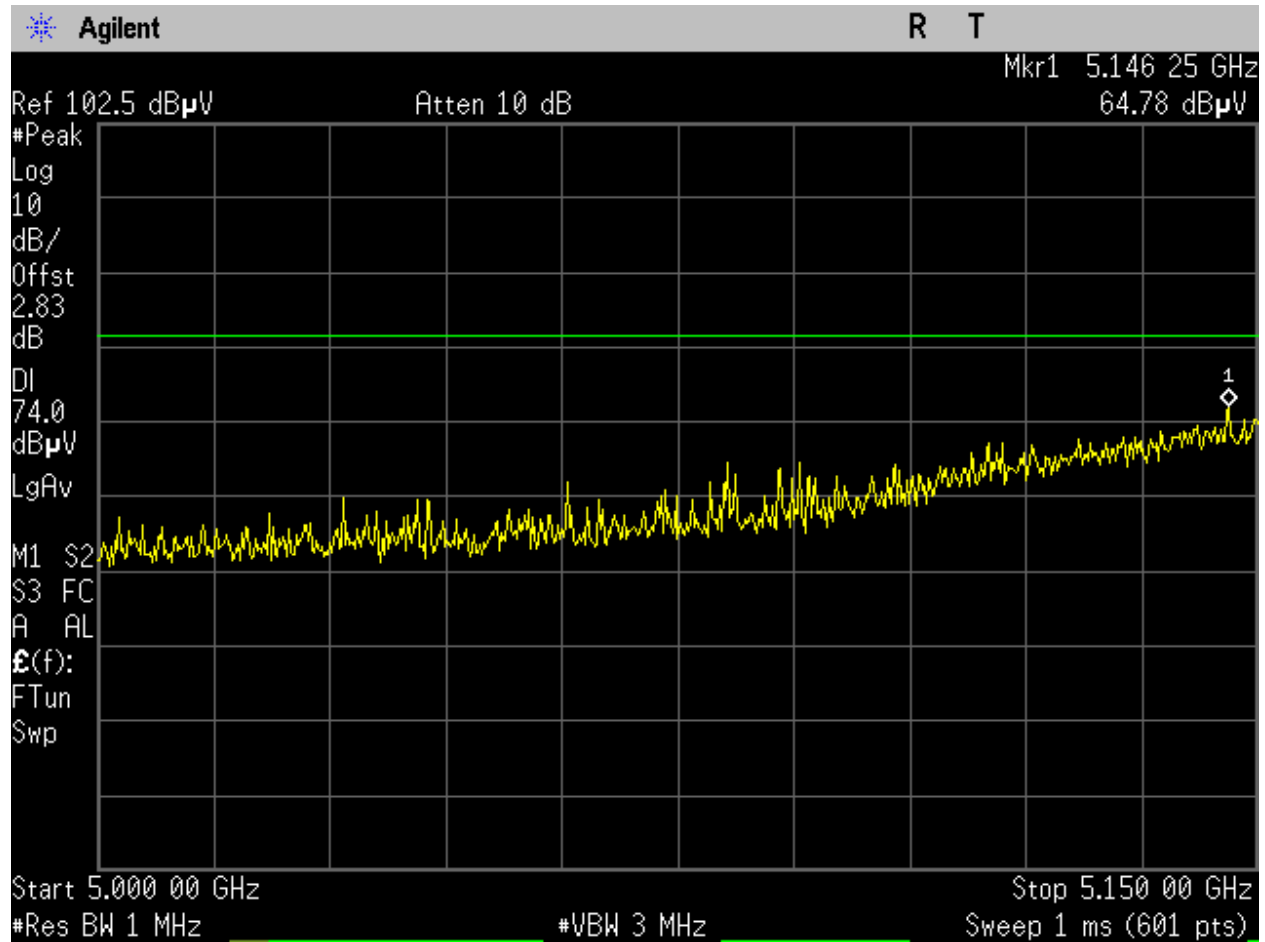


Figure 174: U-NII-1_5210MHz_Low Mid Ch_42_80MHz BW_ax-mode_15.209_Lower Band Edge Peak_Port 2.

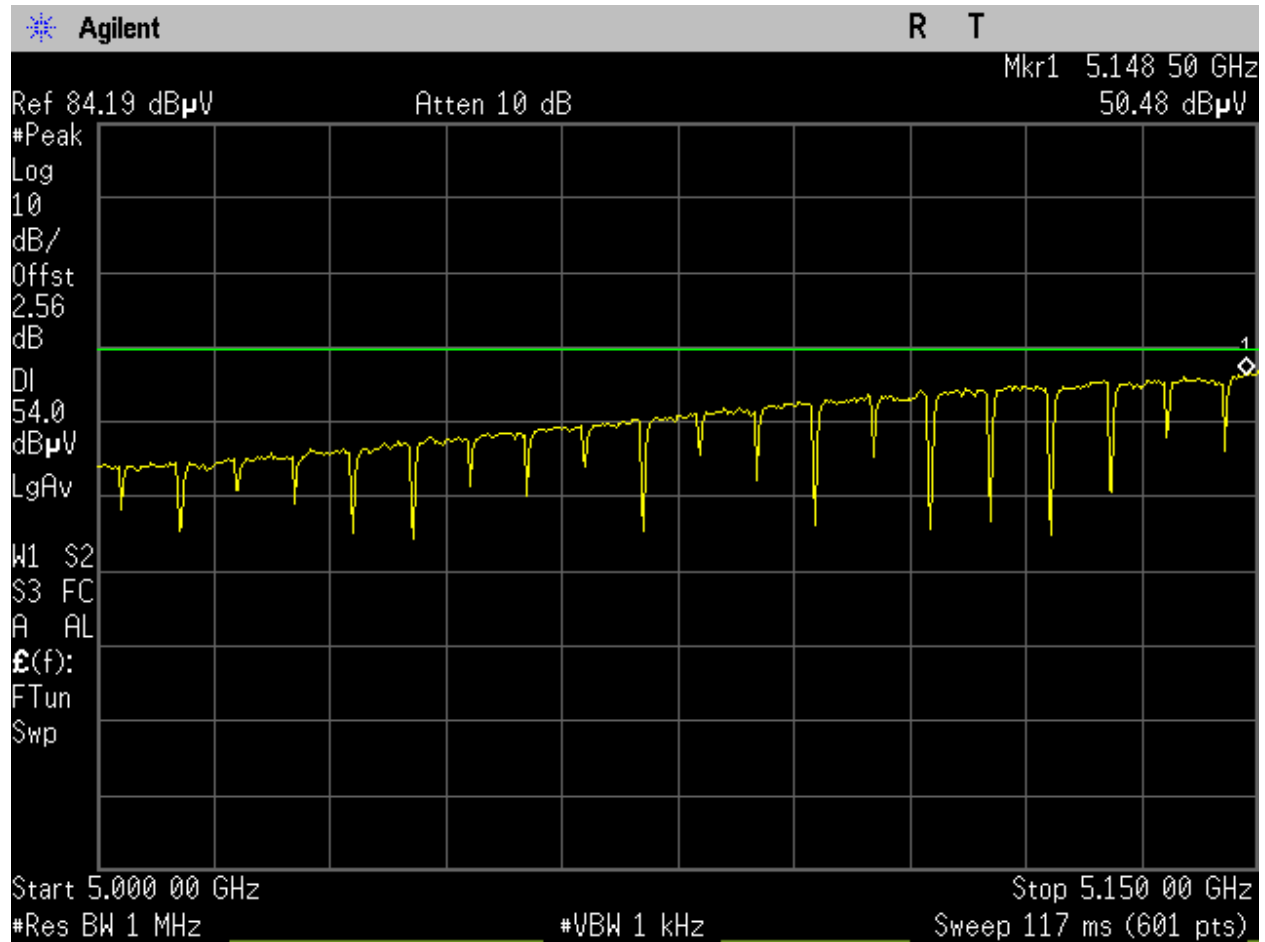


Figure 175: U-NII-2A_5250MHz_Mid Ch_50_160MHz BW_ac-mode_15.209_Lower Band Edge Avg_Port 1.

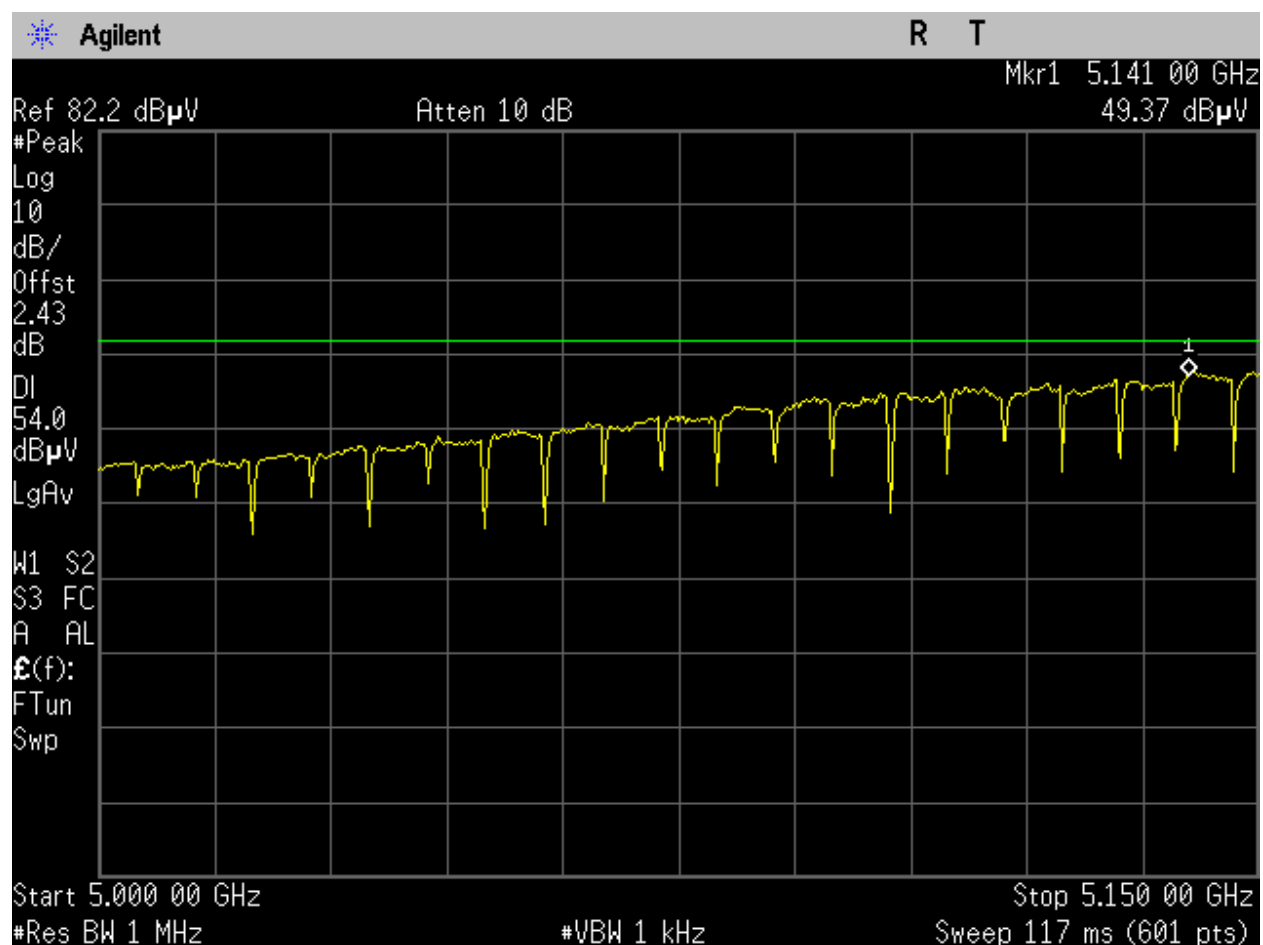


Figure 176: U-NII-2A_5250MHz_Mid Ch_50_160MHz BW_ac-mode_15.209_Lower Band Edge Avg_Port 2.

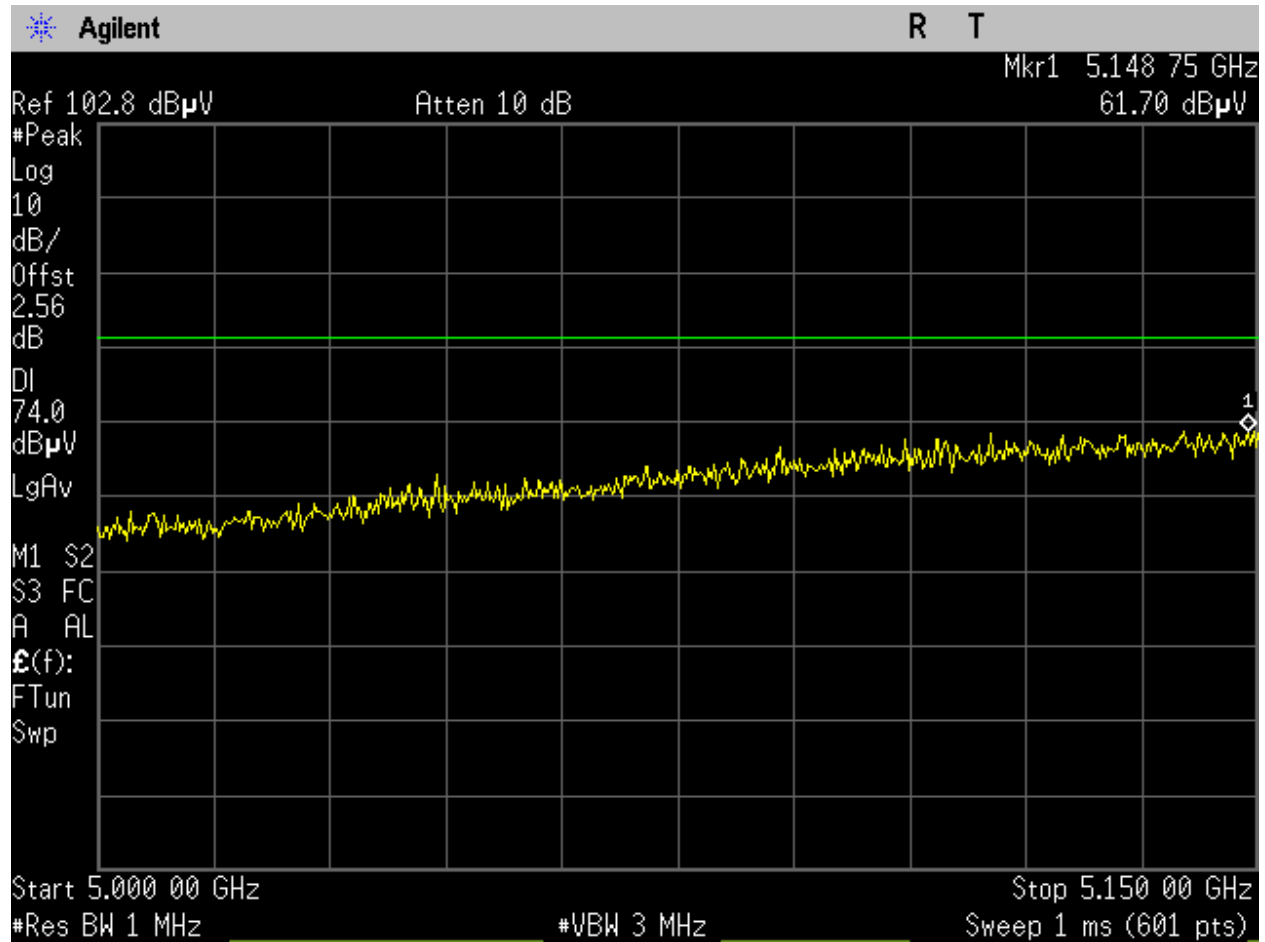


Figure 177: U-NII-2A_5250MHz_Mid Ch_50_160MHz BW_ac-mode_15.209_Lower Band Edge Peak_Port 1.

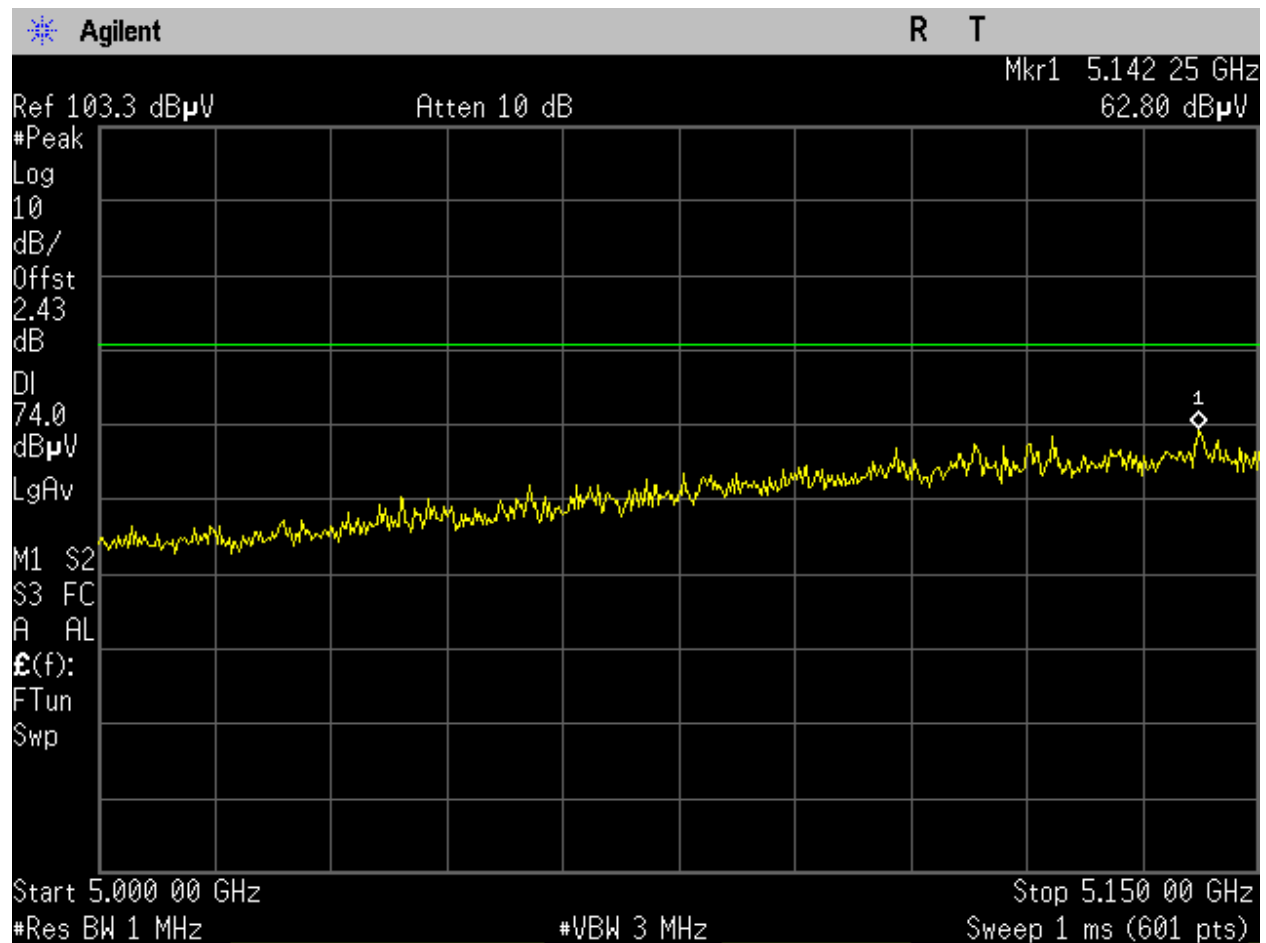


Figure 178: U-NII-2A_5250MHz_Mid Ch_50_160MHz BW_ac-mode_15.209_Lower Band Edge Peak_Port 2.

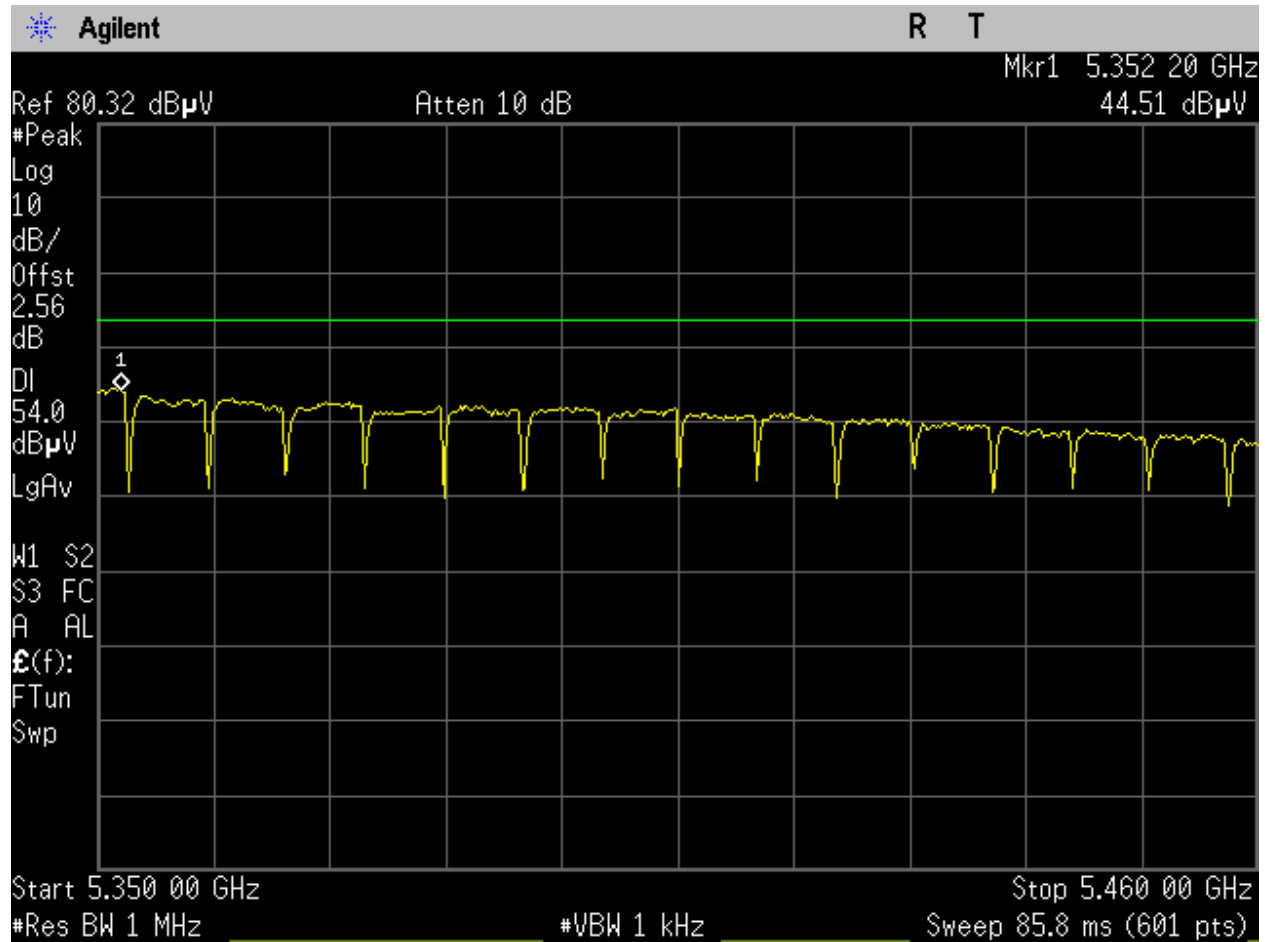


Figure 179: U-NII-2A_5250MHz_Mid Ch_50_160MHz BW_ac-mode_15.209_Upper Band Edge Avg_Port 1.

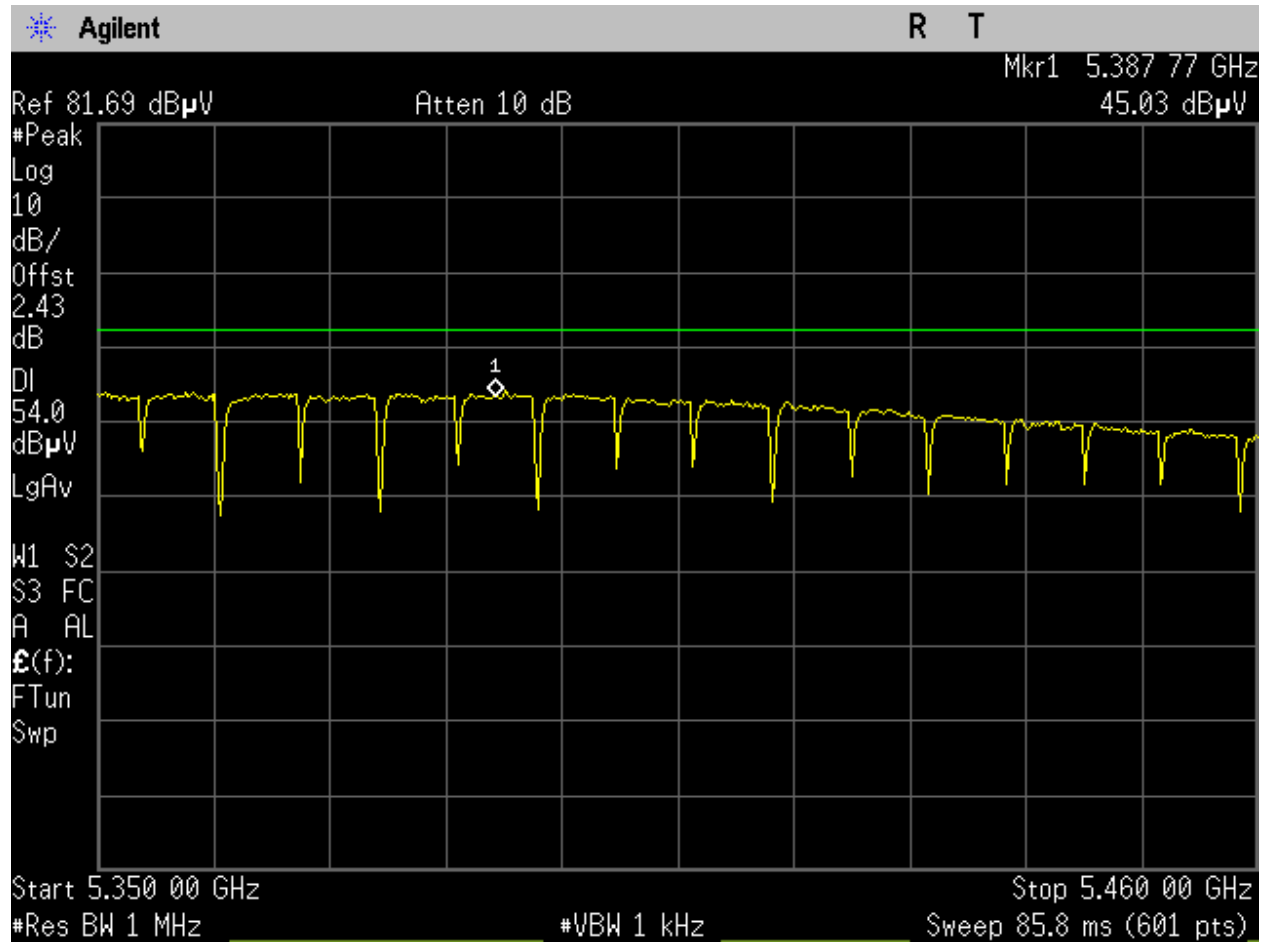


Figure 180: U-NII-2A_5250MHz_Mid Ch_50_160MHz BW_ac-mode_15.209_Upper Band Edge Avg_Port 2.

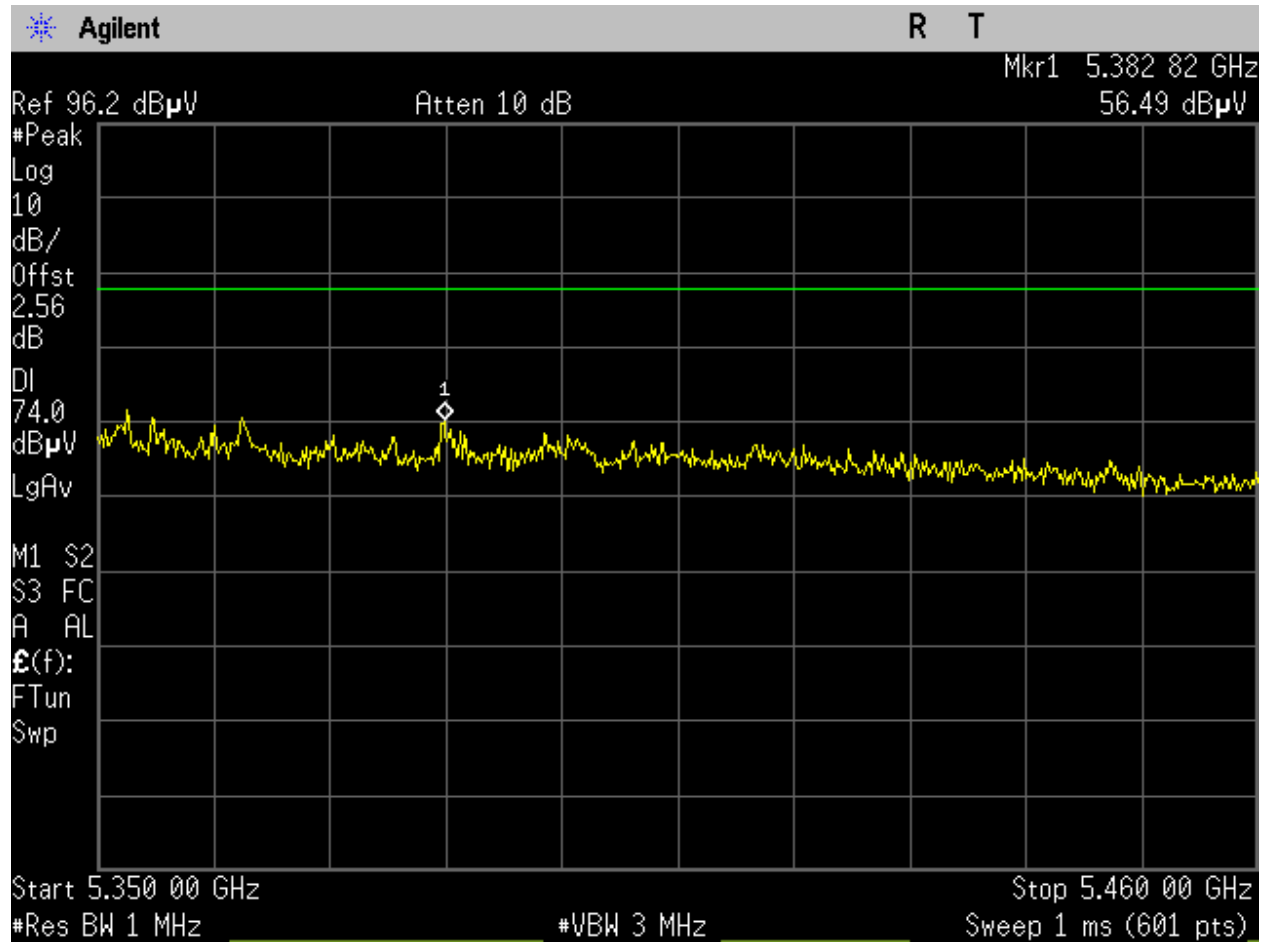


Figure 181: U-NII-2A_5250MHz_Mid Ch_50_160MHz BW_ac-mode_15.209_Upper Band Edge Peak_Port 1.

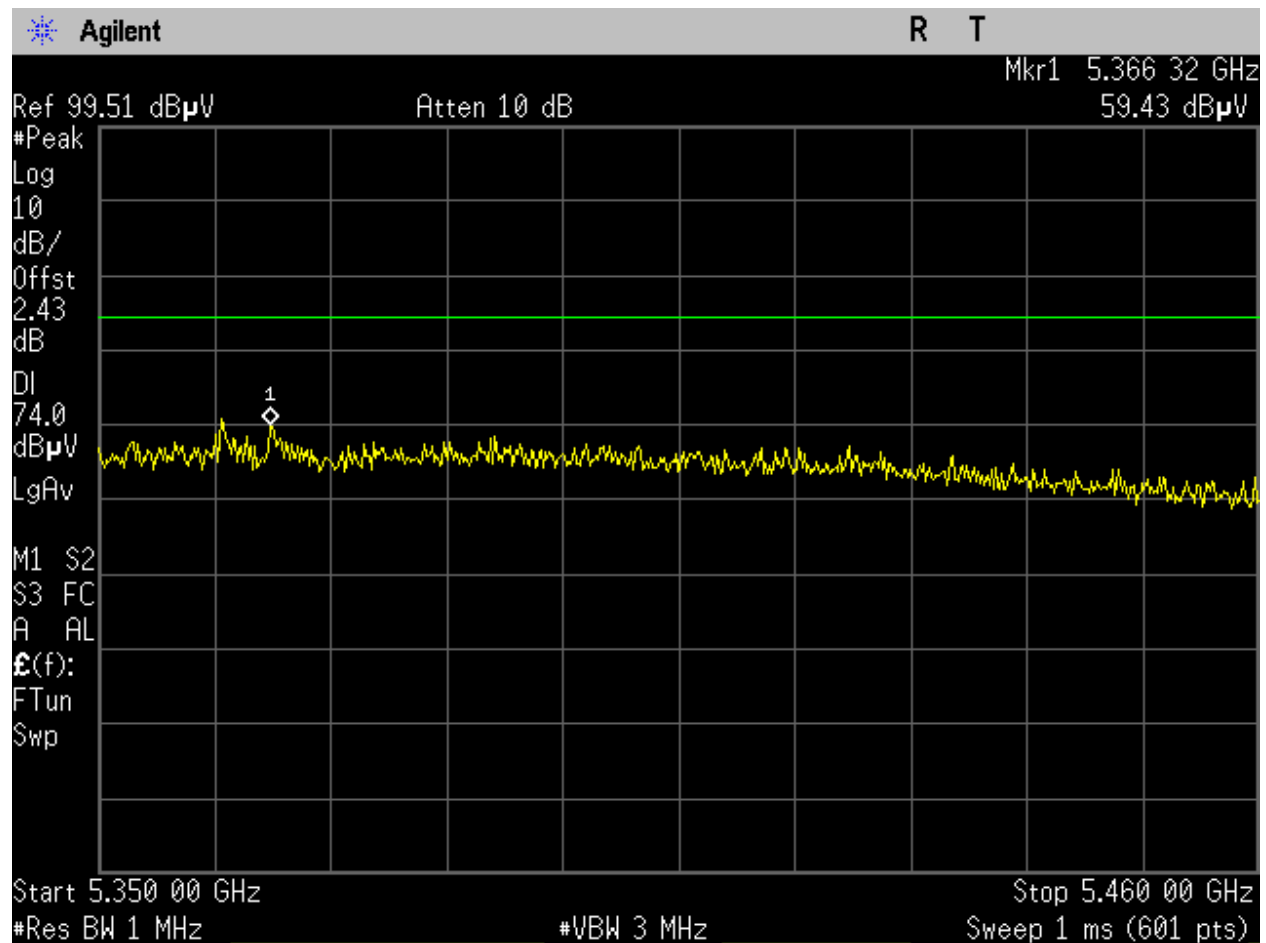


Figure 182: U-NII-2A_5250MHz_Mid Ch_50_160MHz BW_ac-mode_15.209_Upper Band Edge Peak_Port 2.

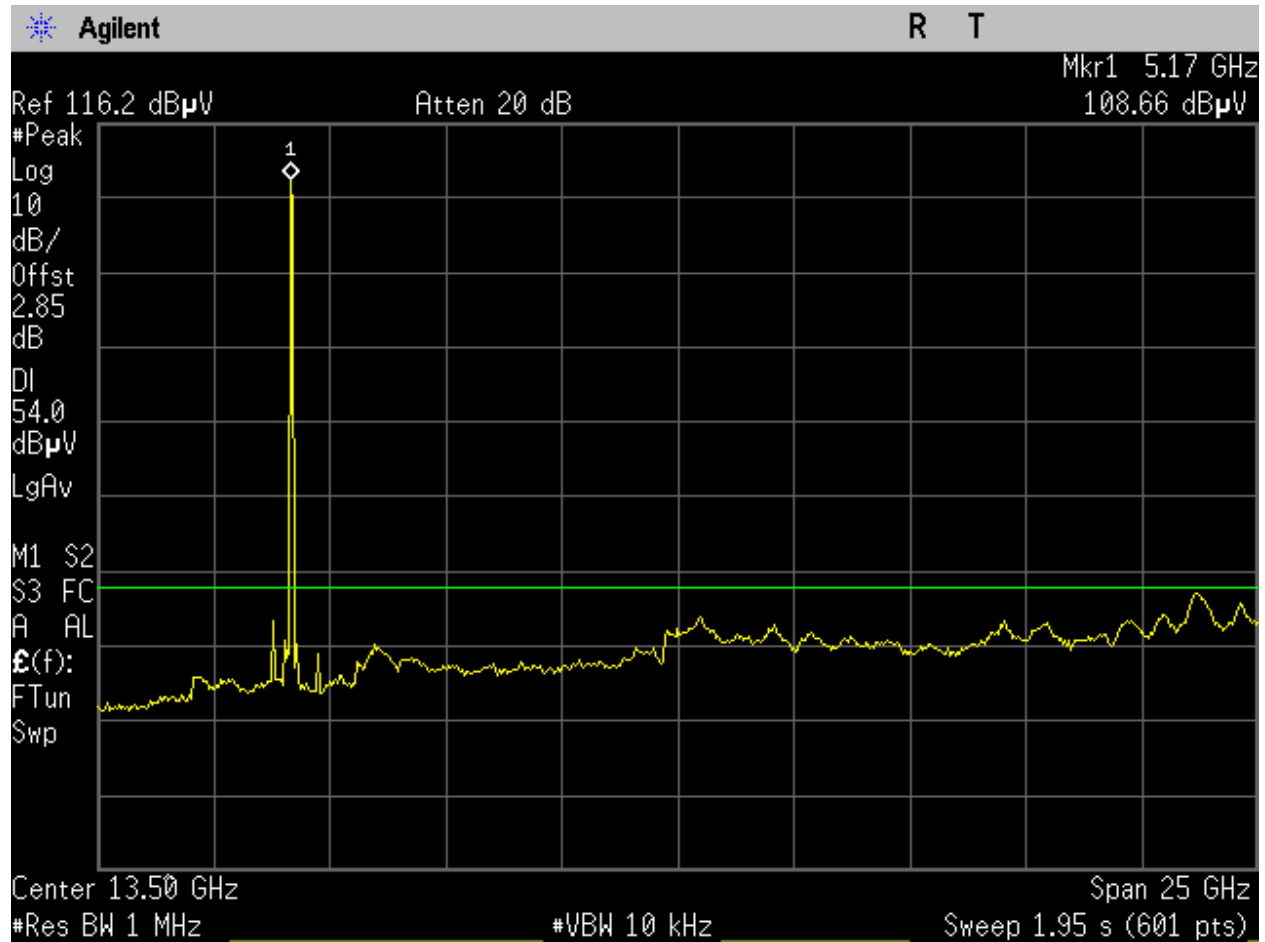


Figure 183: U-NII-1_5180MHz_Low Ch_36_20MHz BW_a-mode_15.209_1-26GHz avg_Port 1.

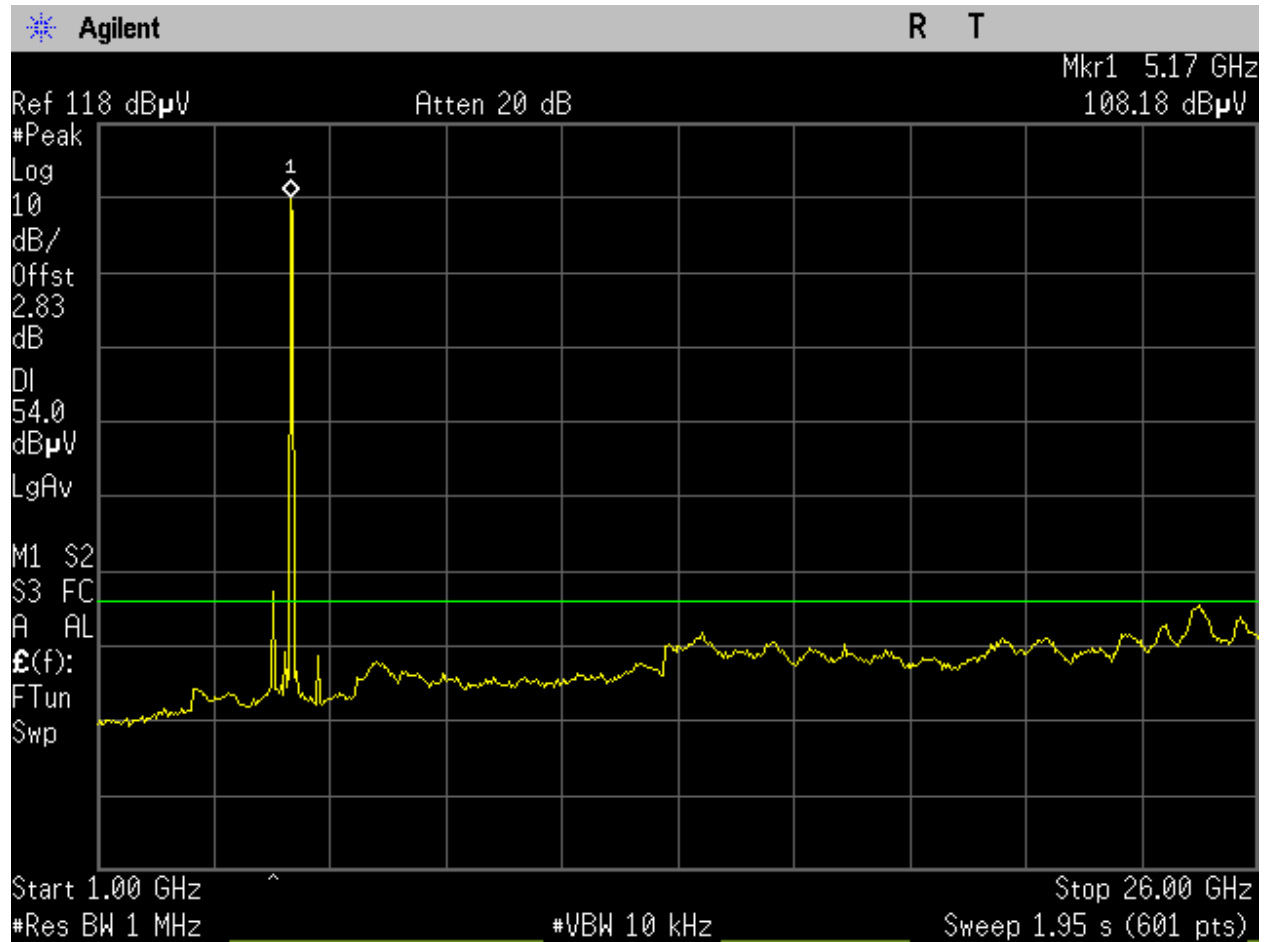


Figure 184: U-NII-1_5180MHz_Low Ch_36_20MHz BW_a-mode_15.209_1-26GHz avg_Port 2.

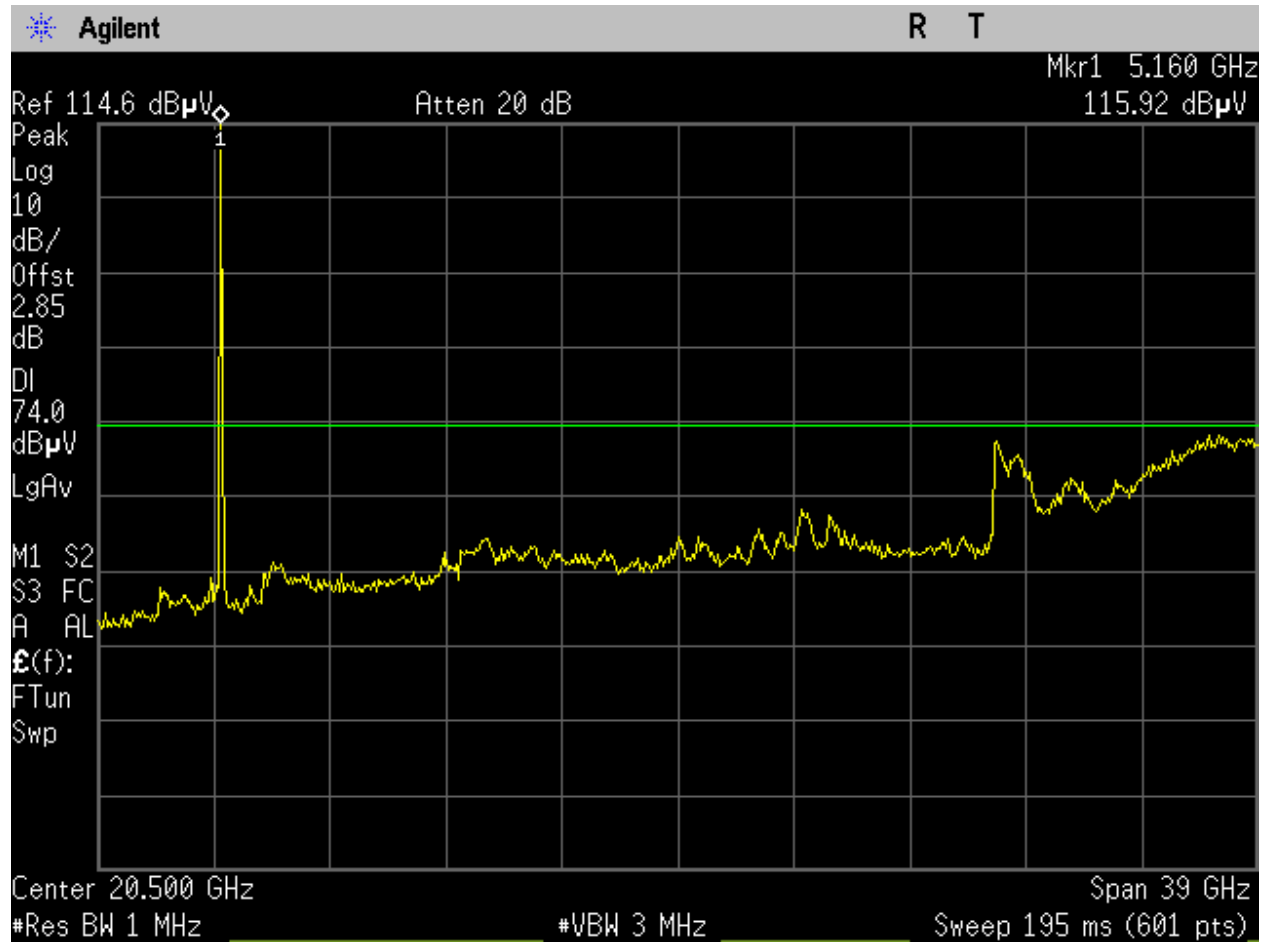


Figure 185: U-NII-1_5180MHz_Low Ch_36_20MHz BW_a-mode_15.209_1-40GHz _Peak_Port 1.

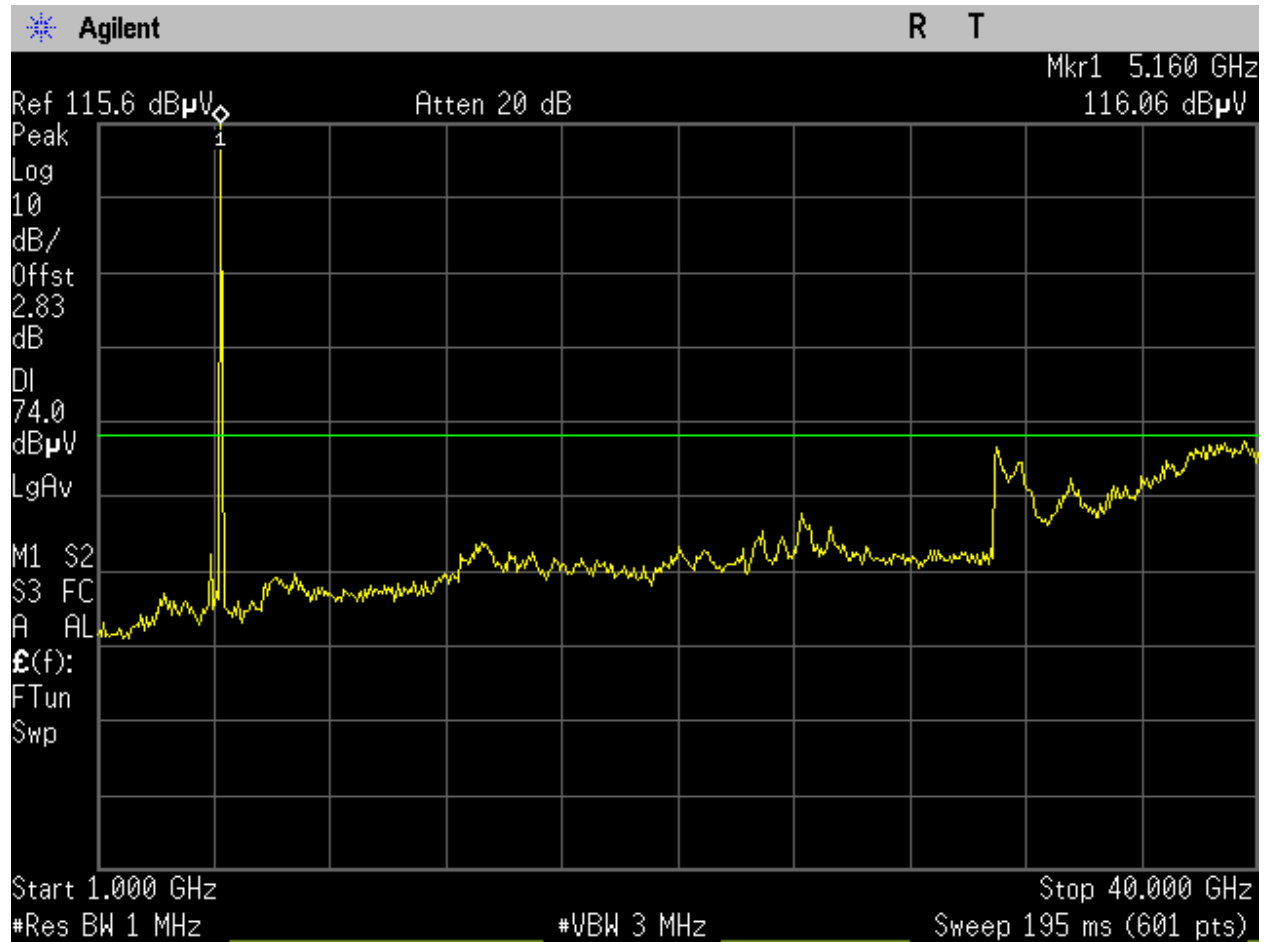


Figure 186: U-NII-1_5180MHz_Low Ch_36_20MHz BW_a-mode_15.209_1-40GHz _Peak_Port 2.

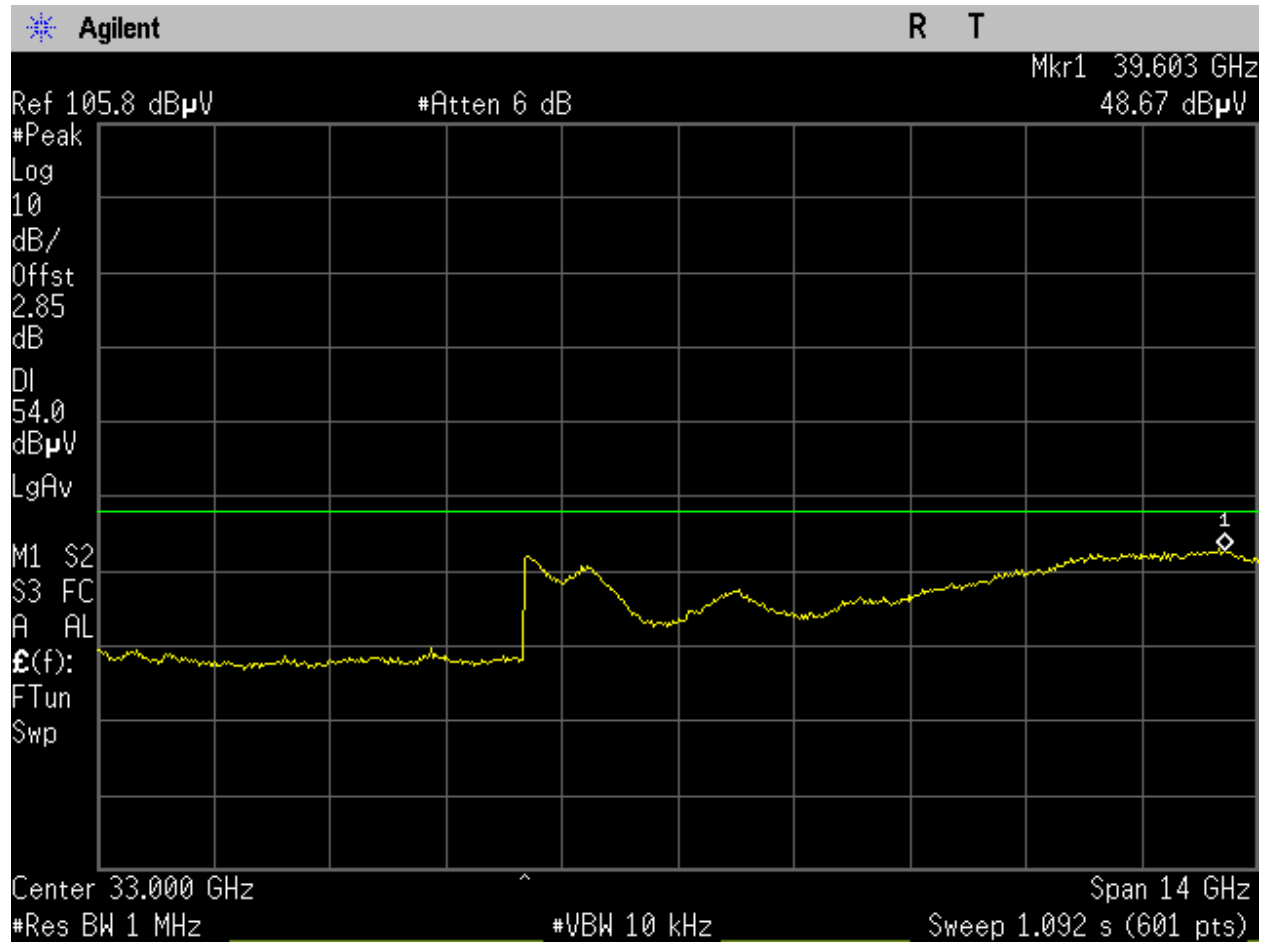


Figure 187: U-NII-1_5180MHz_Low Ch_36_20MHz BW_a-mode_15.209_26-40GHz_Avg_Port 1.

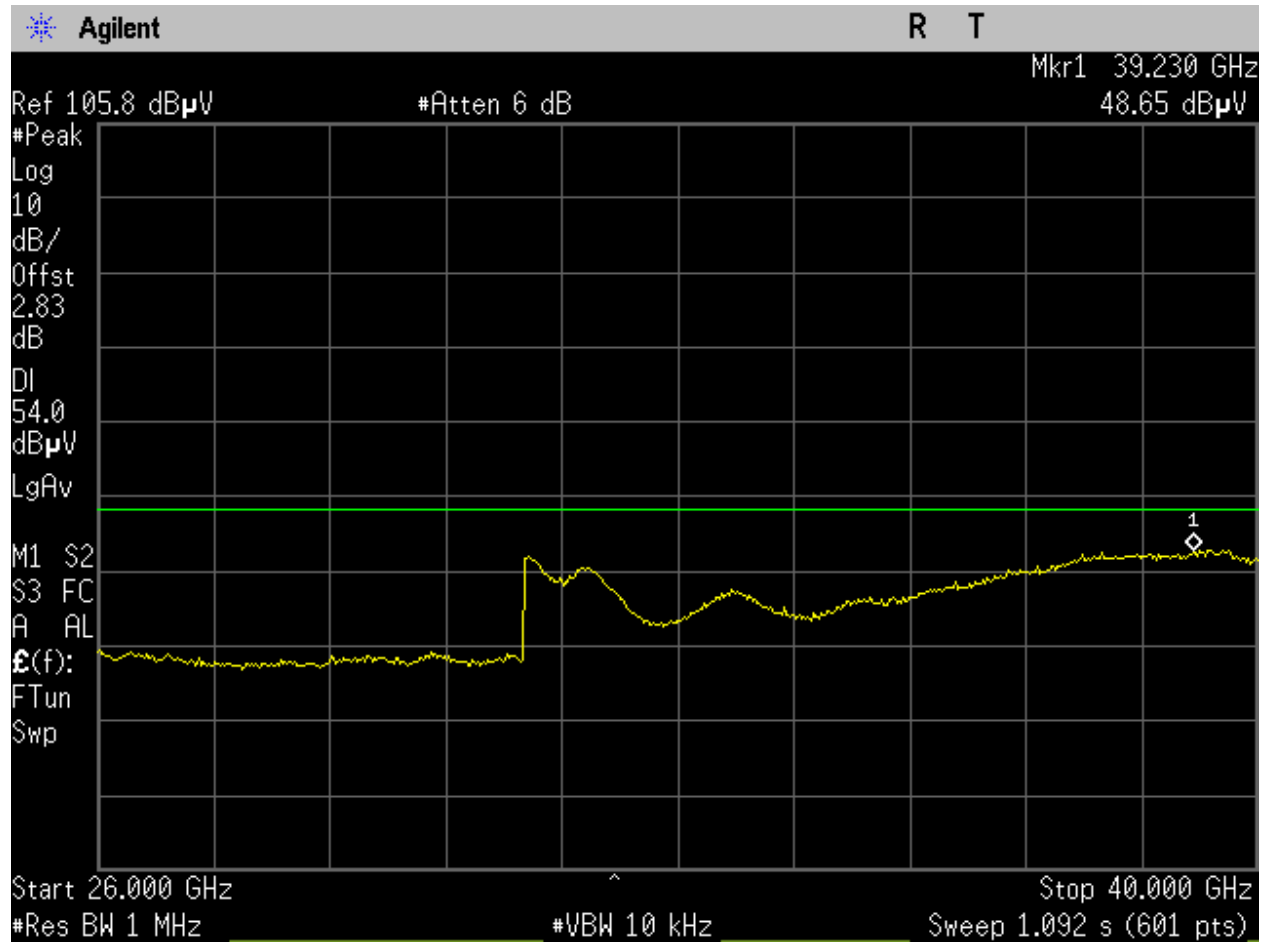


Figure 188: U-NII-1_5180MHz_Low Ch_36_20MHz BW_a-mode_15.209_26-40GHz_Avg_Port 2.

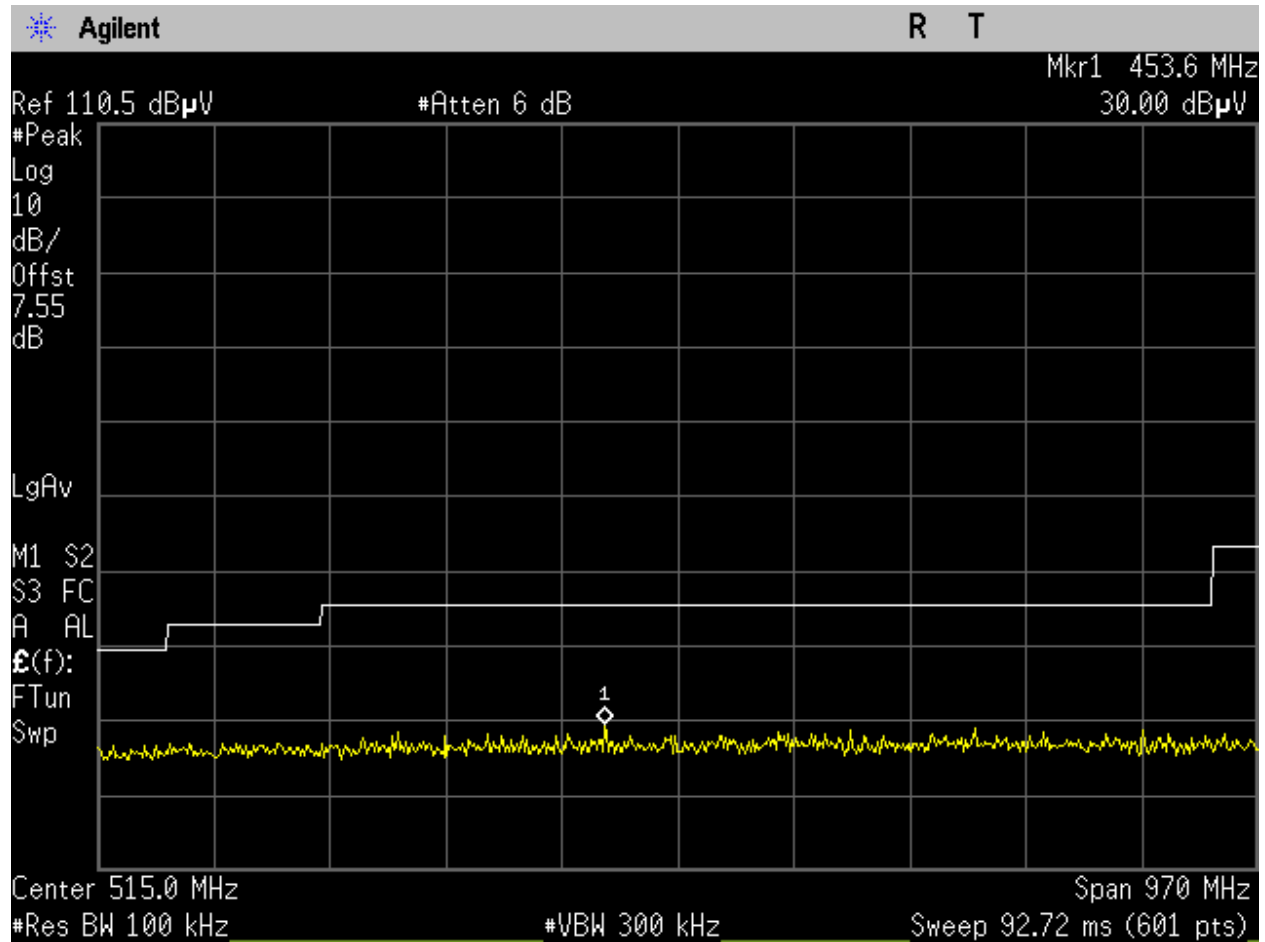


Figure 189: U-NII-1_5180MHz_Low Ch_36_20MHz BW_a-mode_15.209_30-1000MHz_Peak_Port 1.

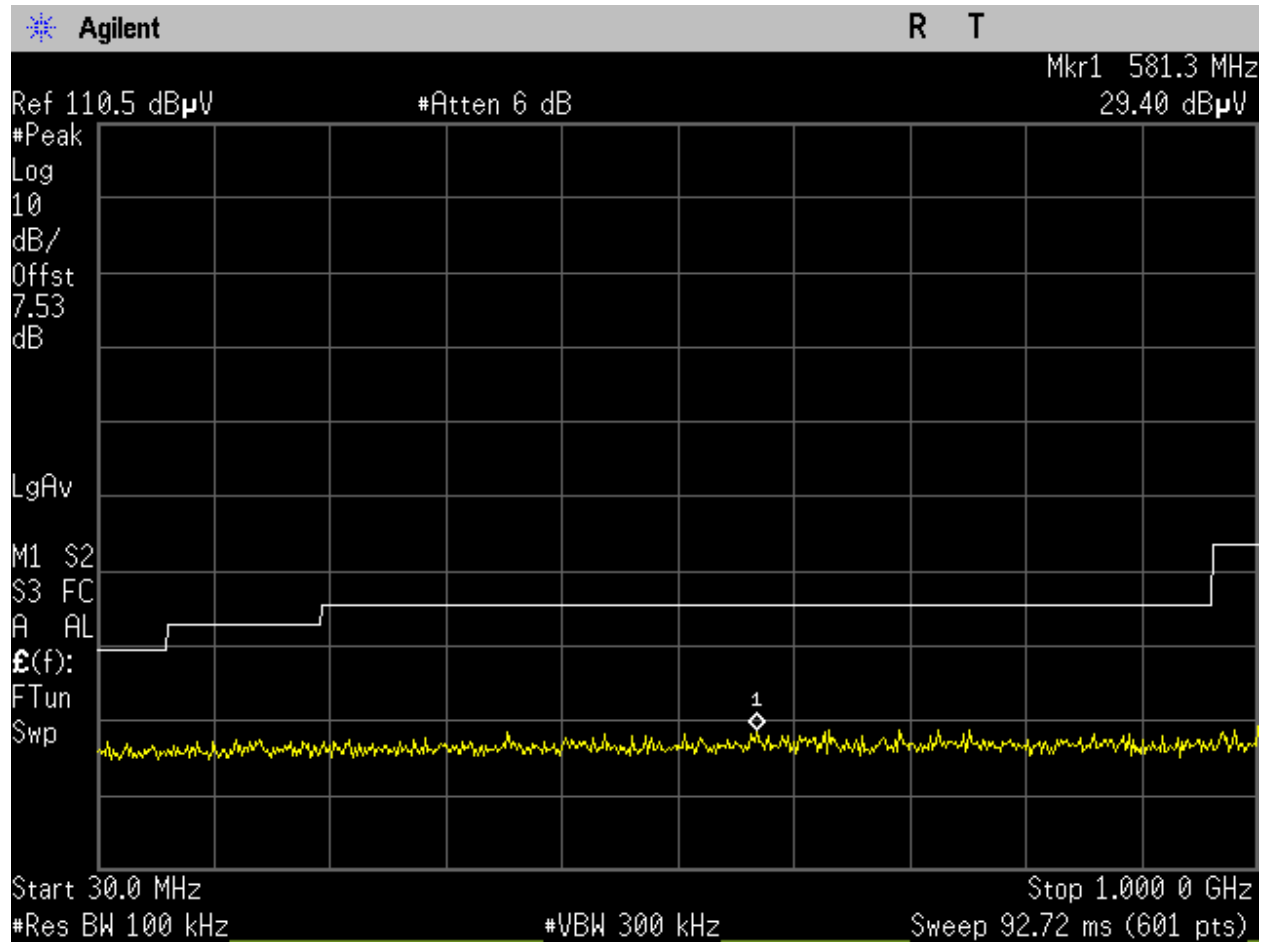


Figure 190: U-NII-1_5180MHz_Low Ch_36_20MHz BW_a-mode_15.209_30-1000MHz_Peak_Port 2.

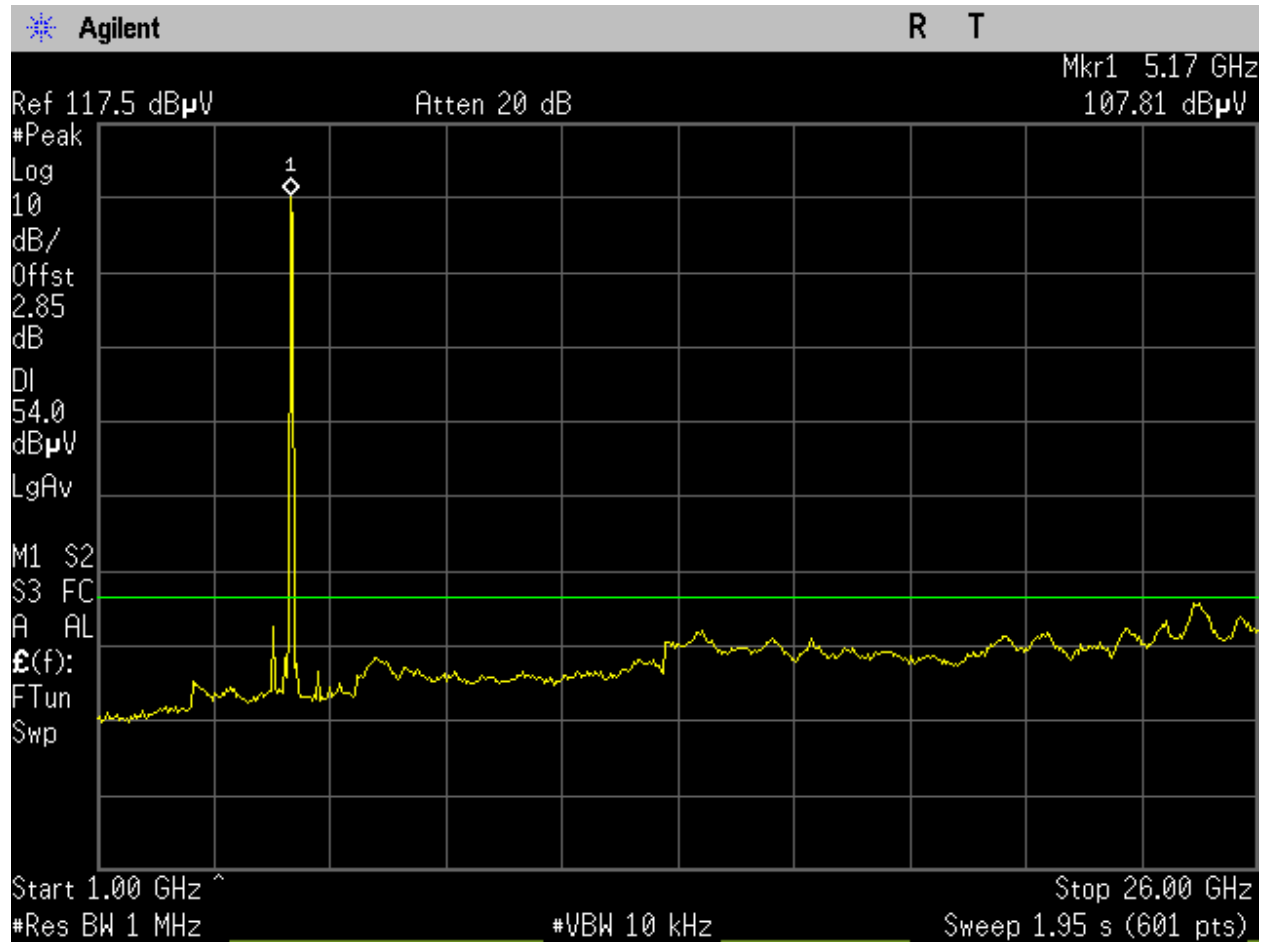


Figure 191: U-NII-1_5180MHz_Low Ch_36_20MHz BW_ac-mode_15.209_1-26GHz avg_Port 1.

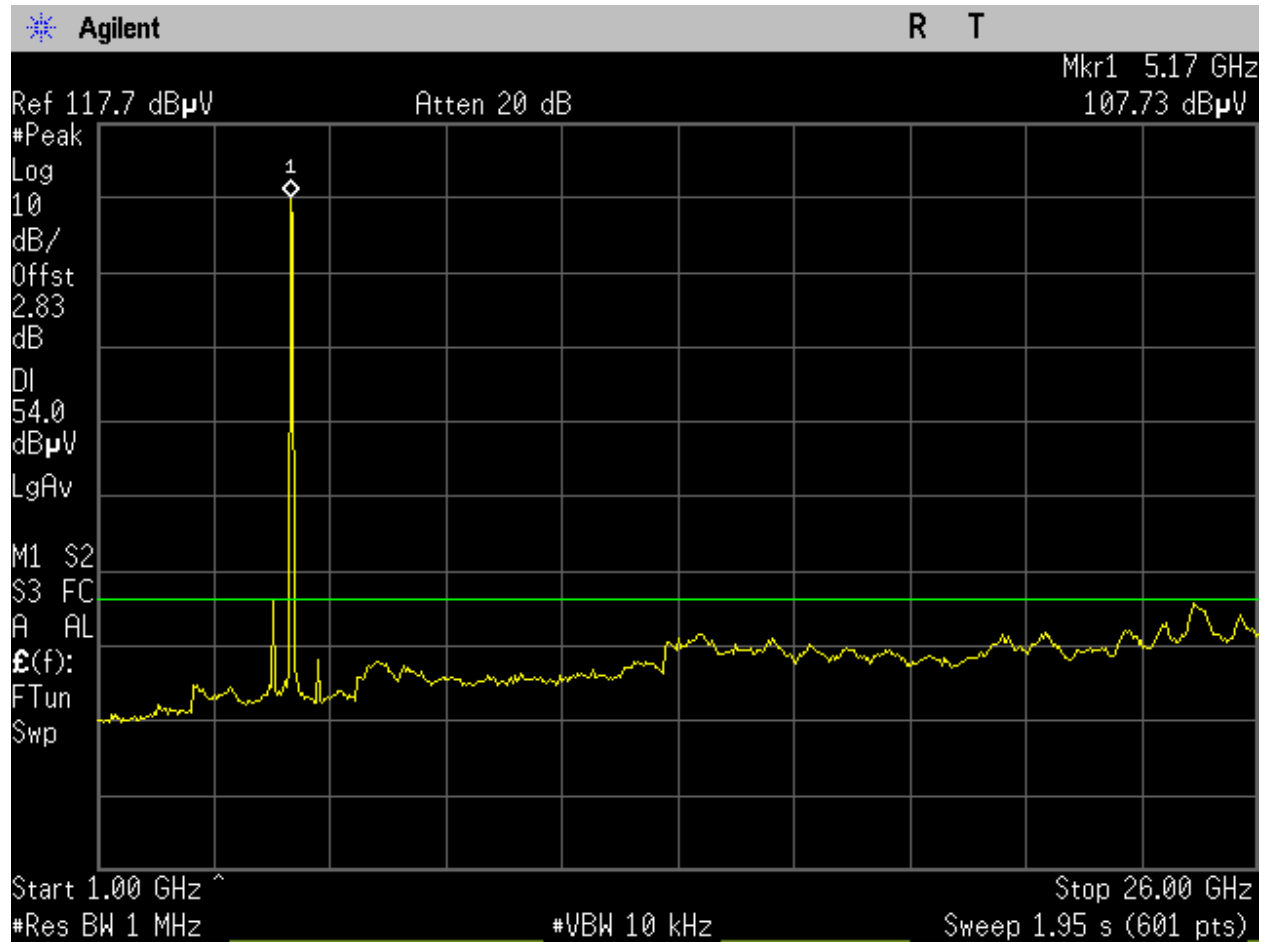


Figure 192: U-NII-1_5180MHz_Low Ch_36_20MHz BW_ac-mode_15.209_1-26GHz avg_Port 2.

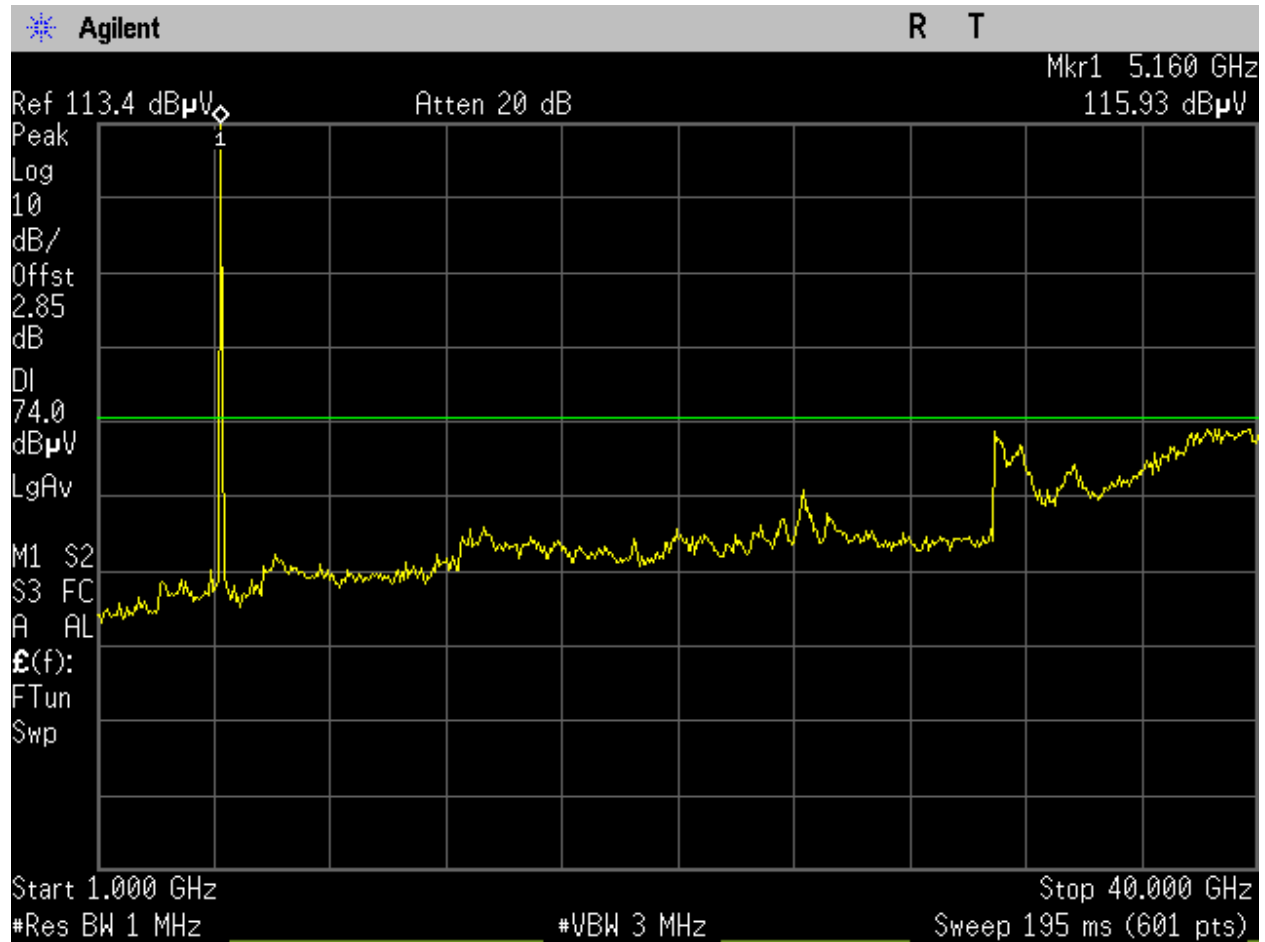


Figure 193: U-NII-1_5180MHz_Low Ch_36_20MHz BW_ac-mode_15.209_1-40GHz _Peak_Port 1.



Figure 194: U-NII-1_5180MHz_Low Ch_36_20MHz BW_ac-mode_15.209_1-40GHz _Peak_Port 2.

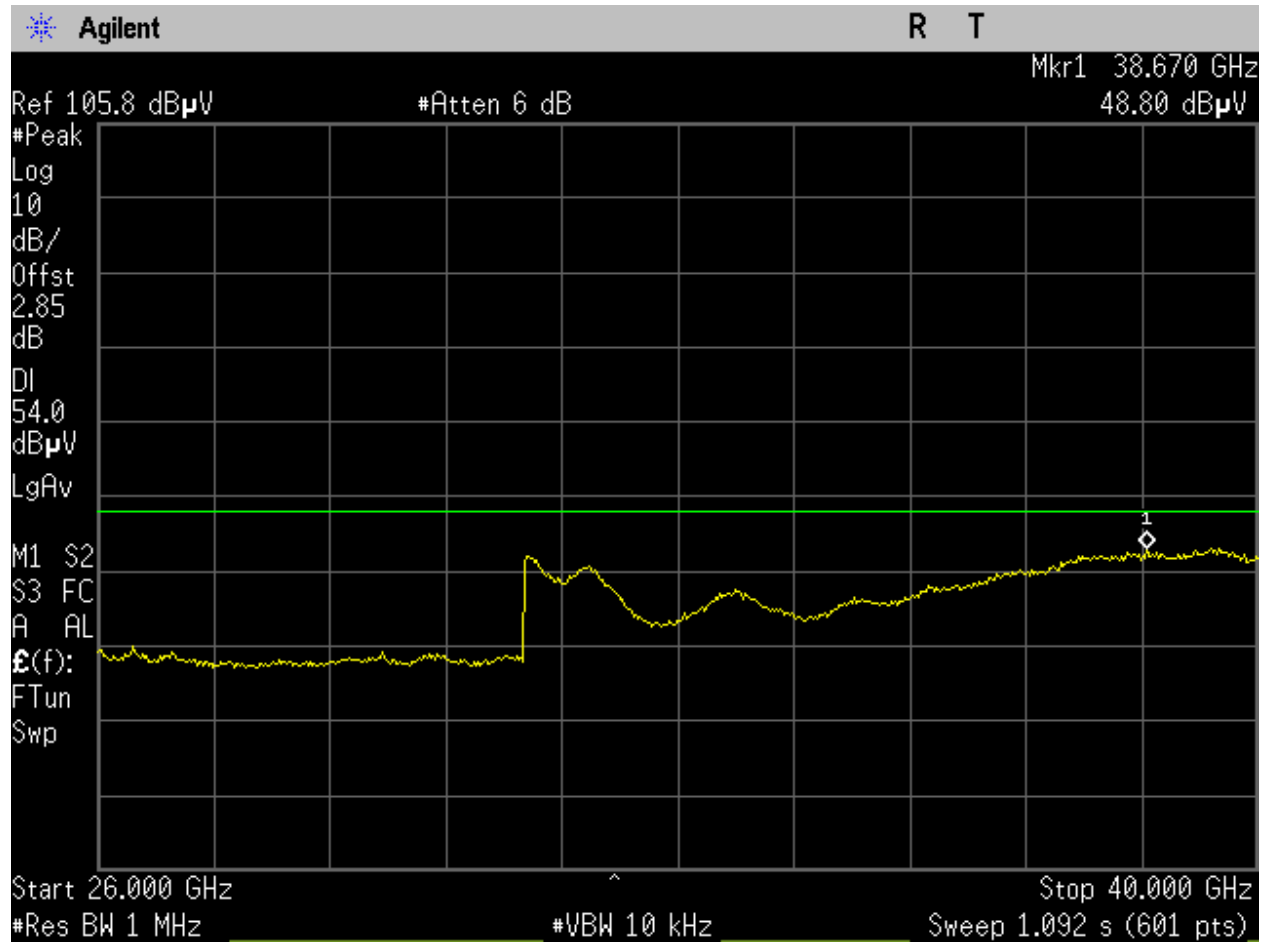


Figure 195: U-NII-1_5180MHz_Low Ch_36_20MHz BW_ac-mode_15.209_26-40GHz_Avg_Port 1.

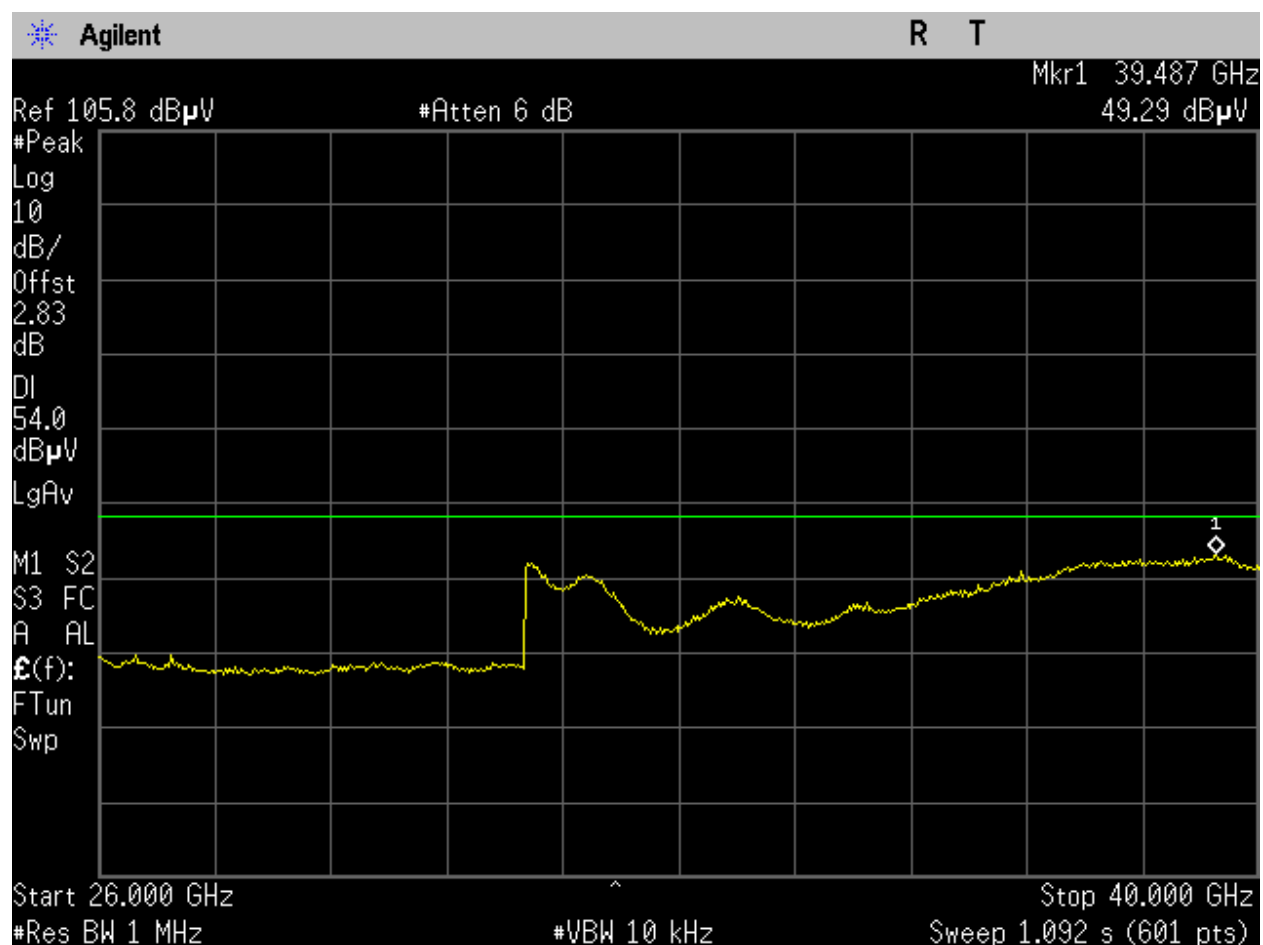


Figure 196: U-NII-1_5180MHz_Low Ch_36_20MHz BW_ac-mode_15.209_26-40GHz_Avg_Port 2.

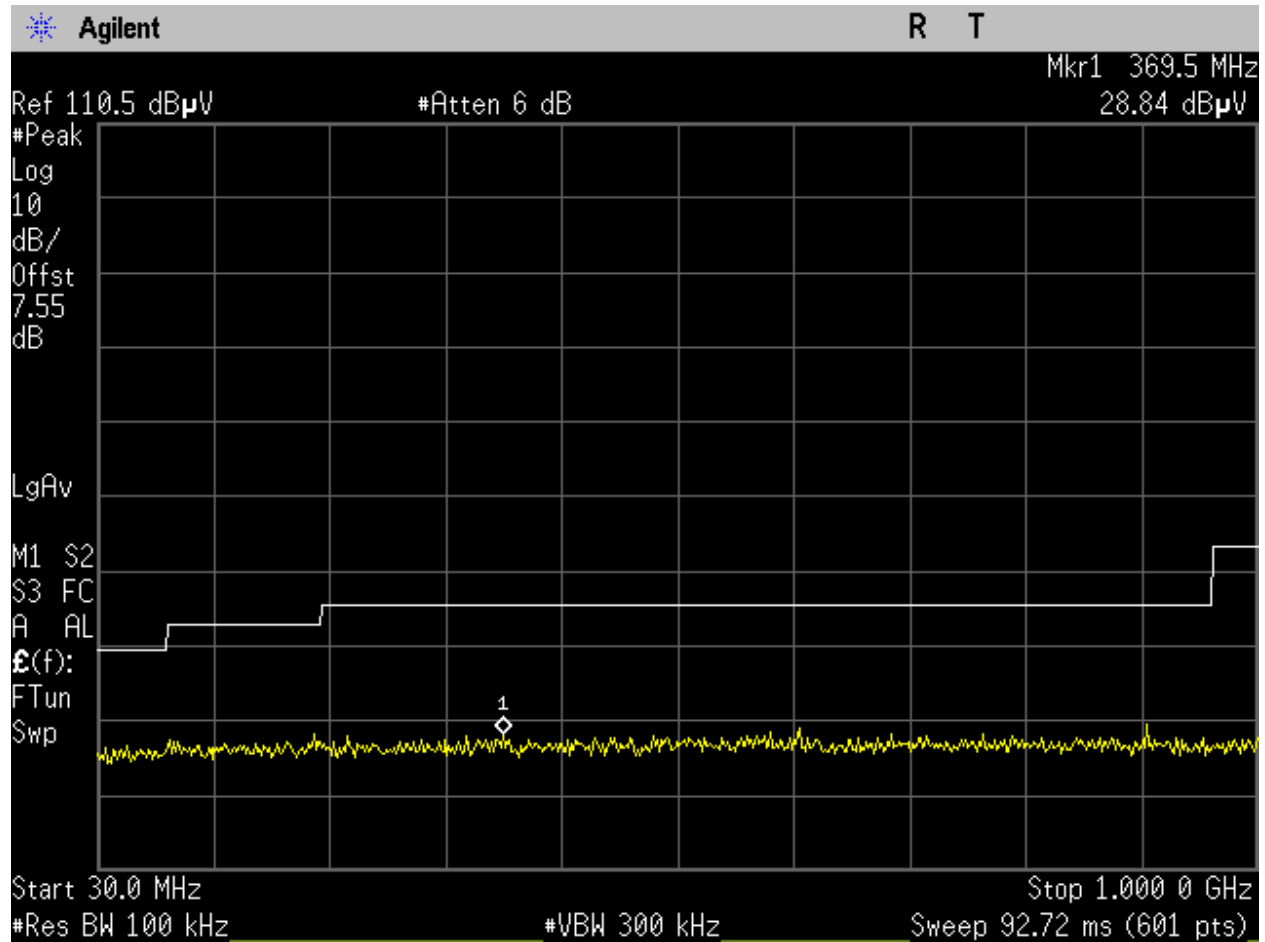


Figure 197: U-NII-1_5180MHz_Low Ch_36_20MHz BW_ac-mode_15.209_30-1000MHz_Peak_Port 1.